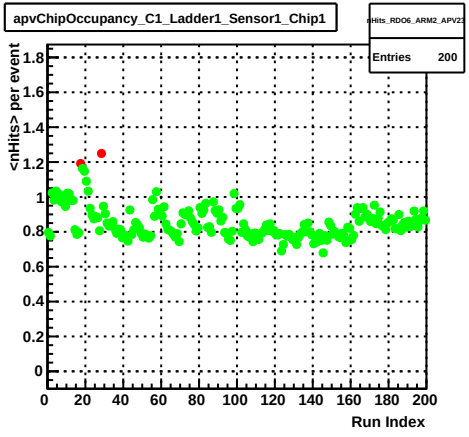
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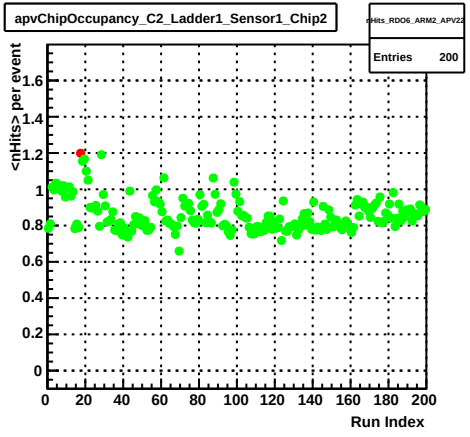
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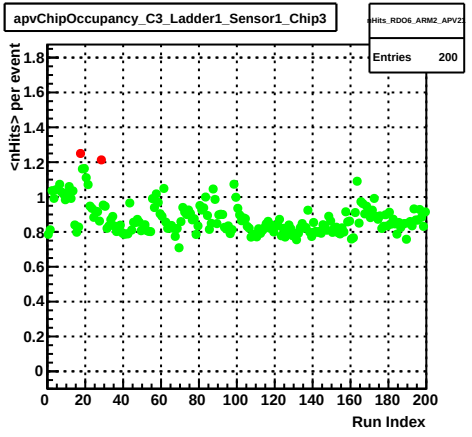
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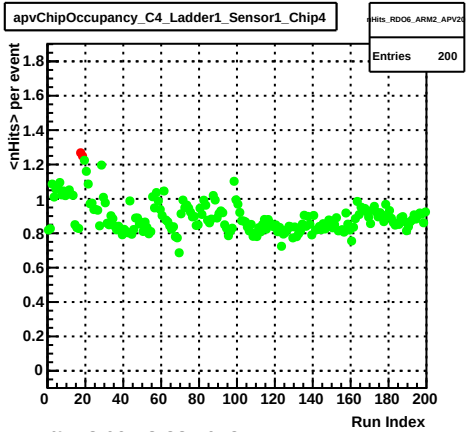
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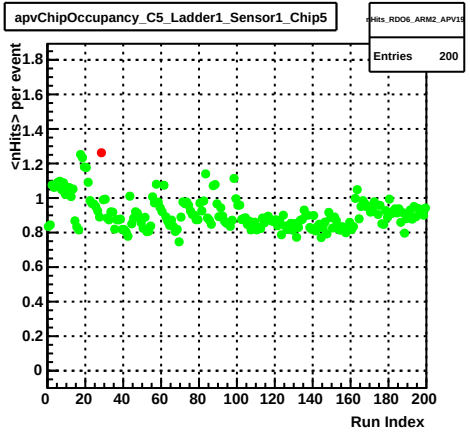
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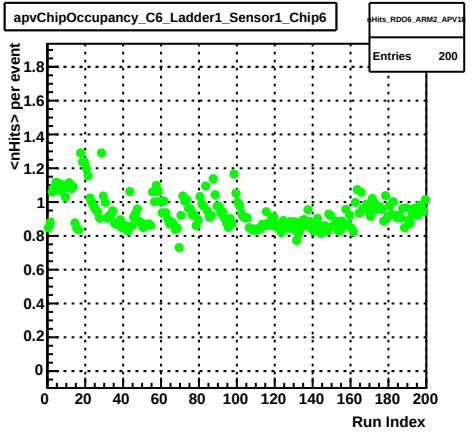
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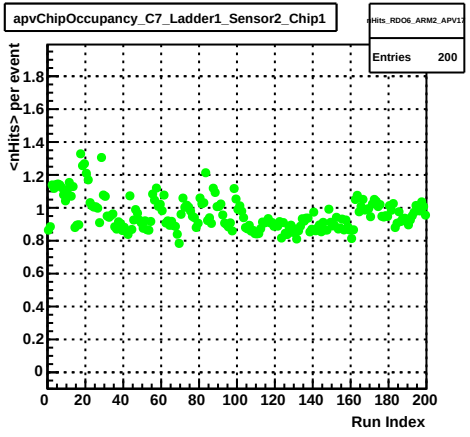
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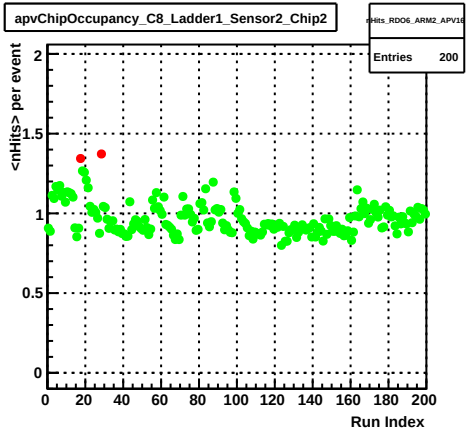
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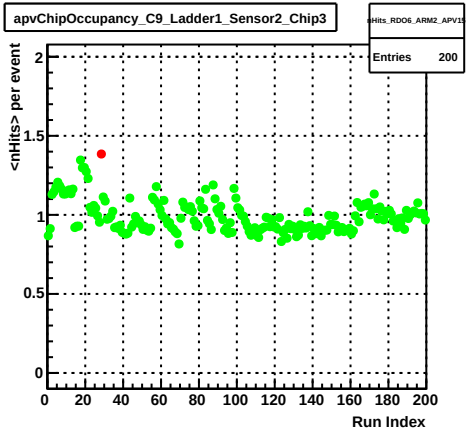
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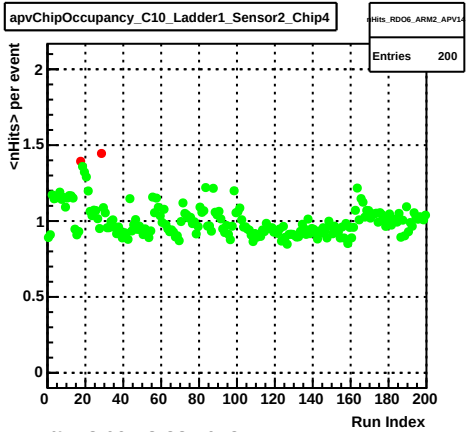
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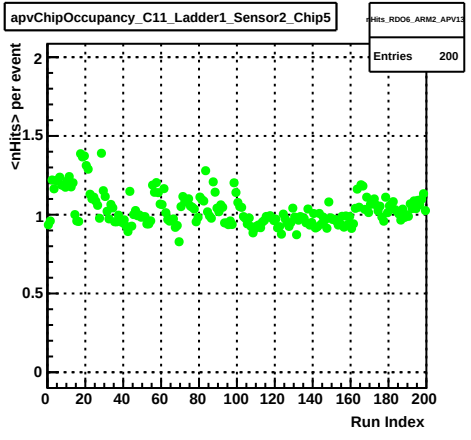
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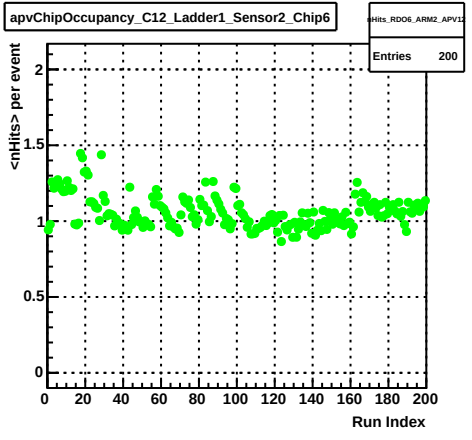
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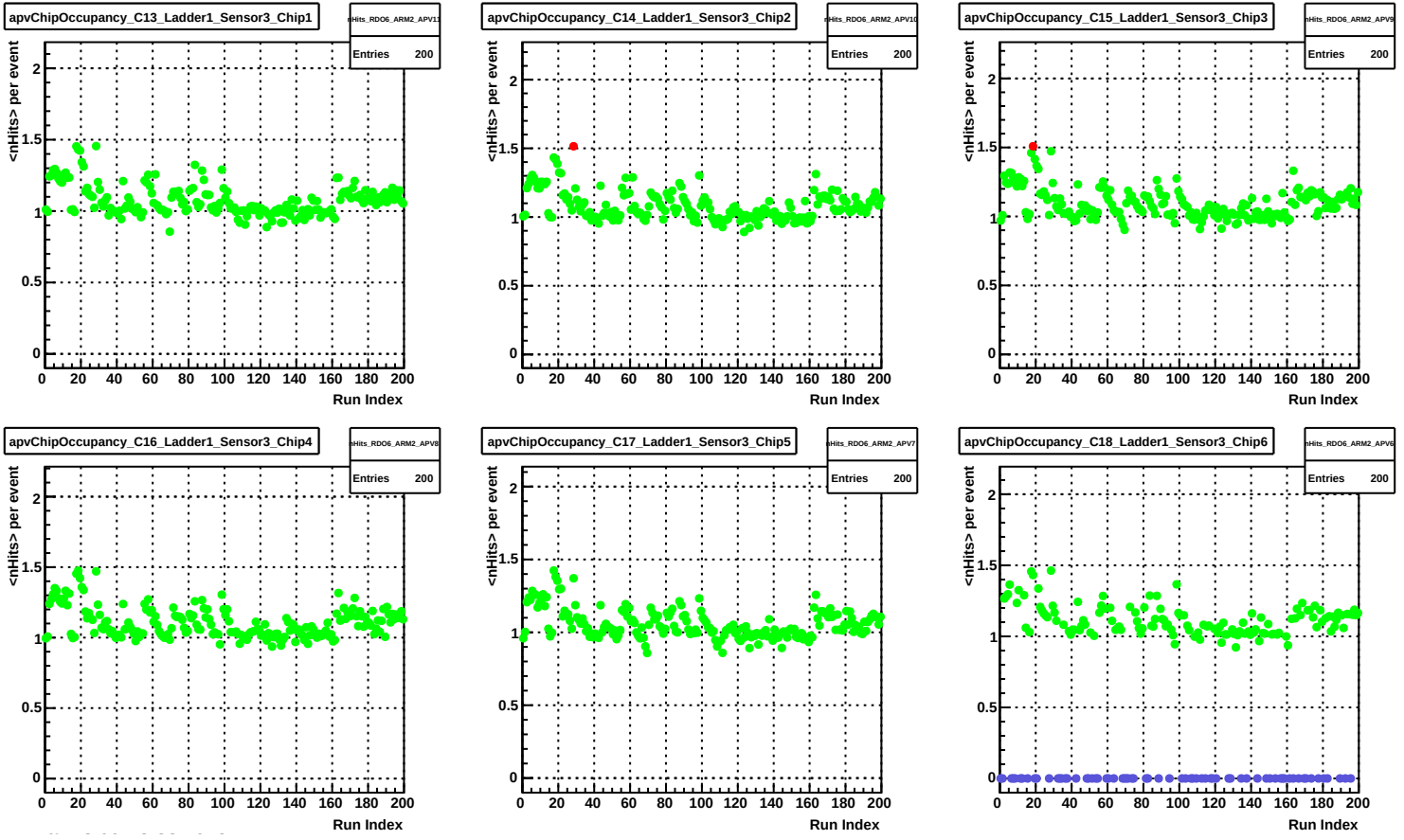
apvChipOccupancy_C13_Ladder1_Sensor3_Chip1
nHits_RDO6_ARM2_APV13
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apvChipOccupancy_C14_Ladder1_Sensor3_Chip2
nHits_RDO6_ARM2_APV10
Entries 200
apvChipOccupancy_C15_Ladder1_Sensor3_Chip3
nHits_RDO6_ARM2_APV9
Entries 200
apvChipOccupancy_C16_Ladder1_Sensor3_Chip4
nHits_RDO6_ARM2_APV8
Entries 200
apvChipOccupancy_C17_Ladder1_Sensor3_Chip5
nHits_RDO6_ARM2_APV7
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nHits_RDO6_ARM2_APV6
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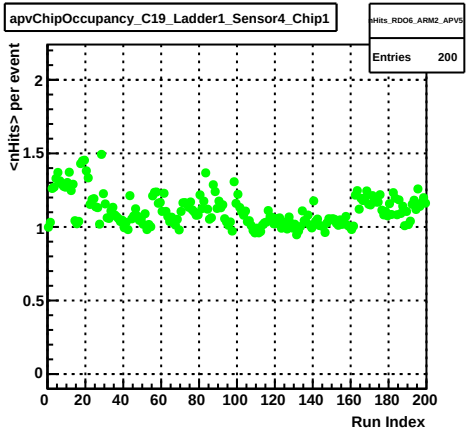
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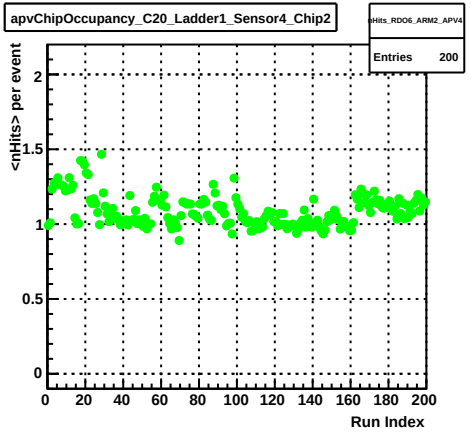
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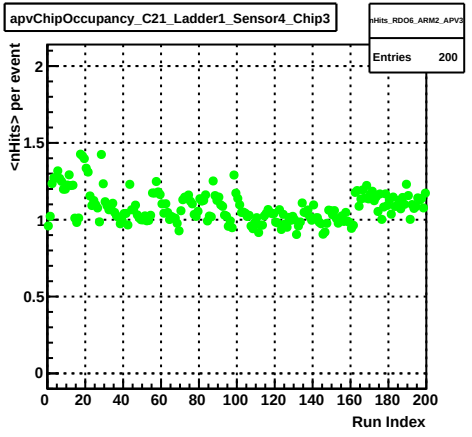
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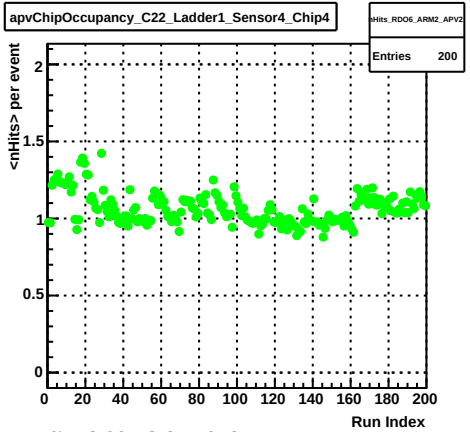
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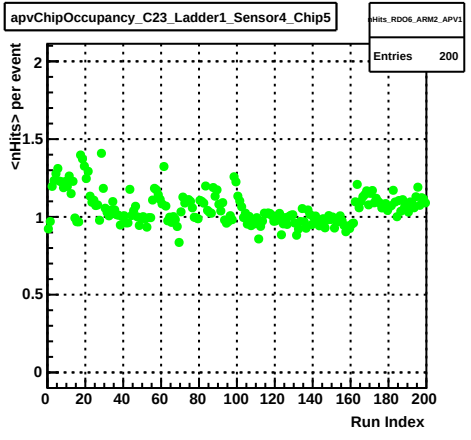
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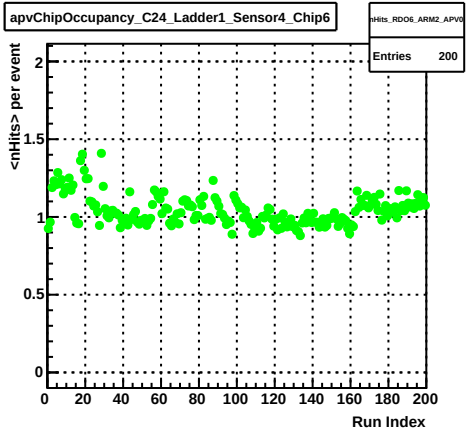
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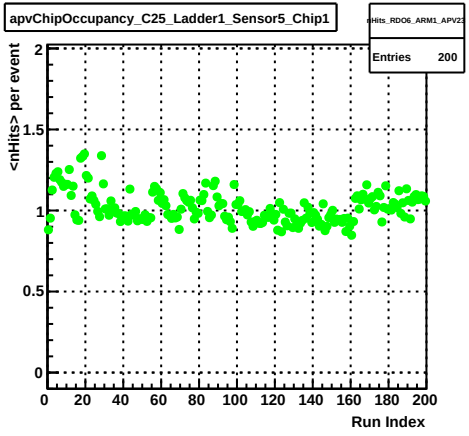
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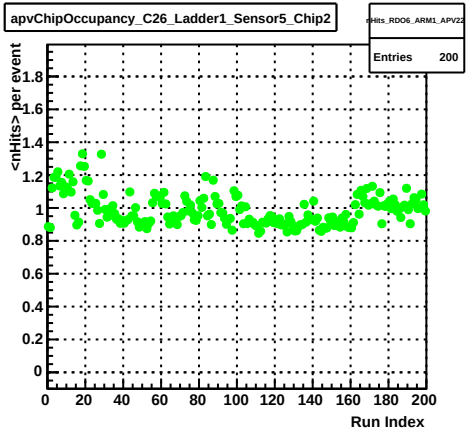
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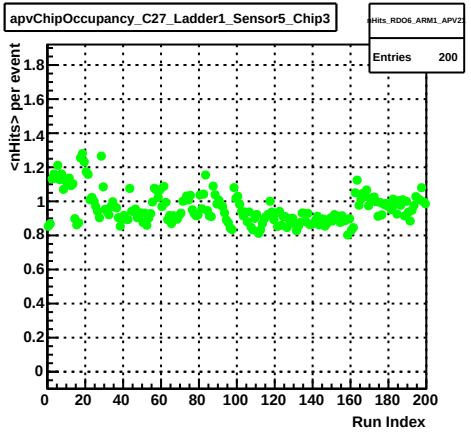
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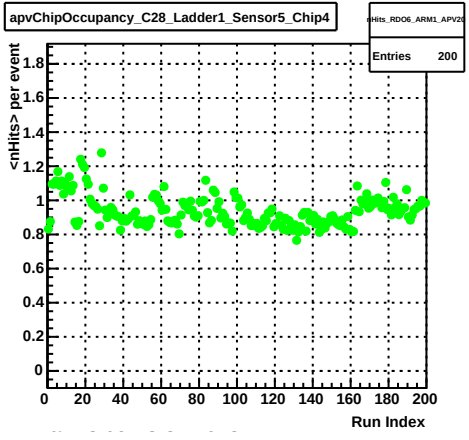
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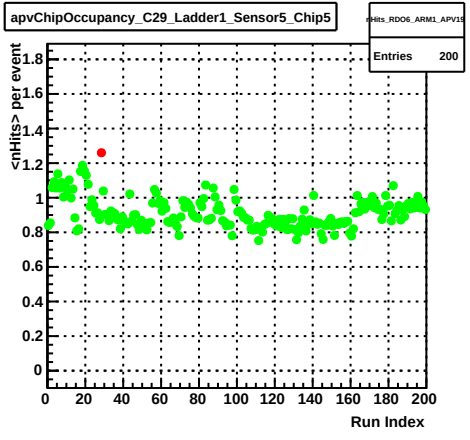
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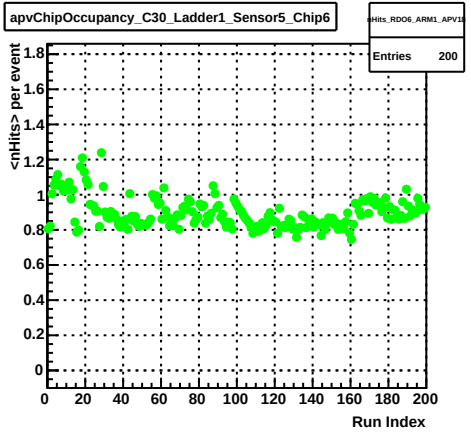
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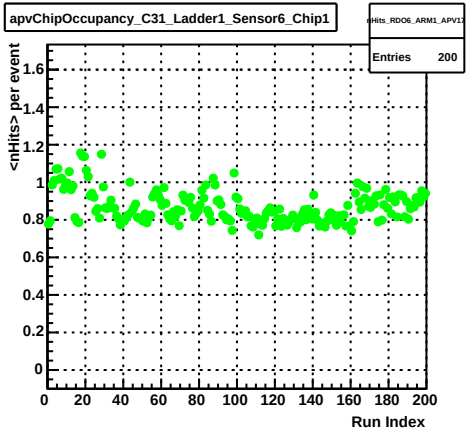
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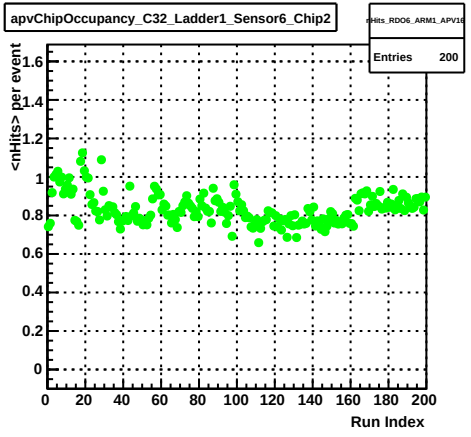
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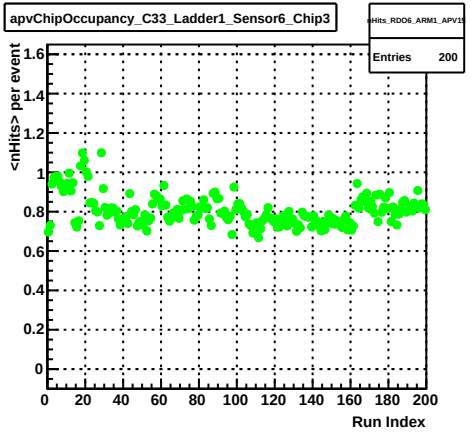
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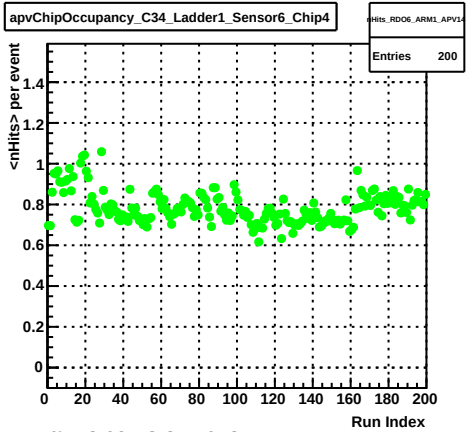
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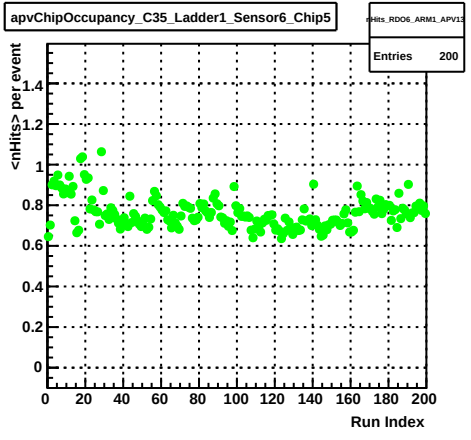
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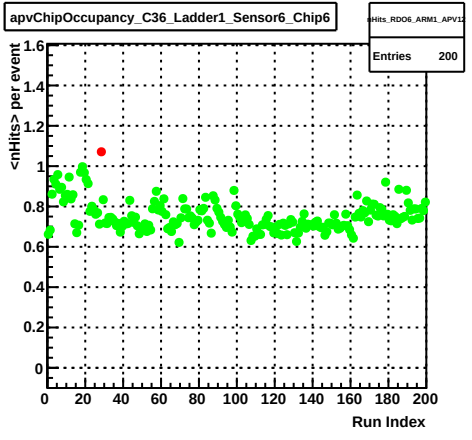
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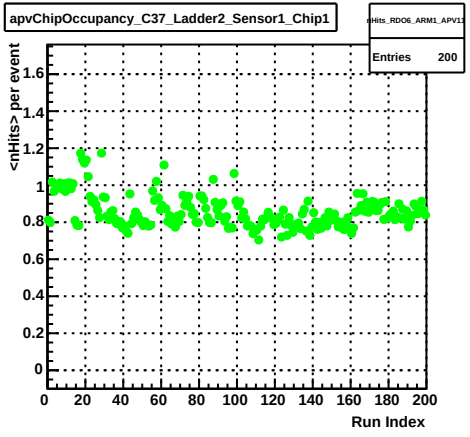
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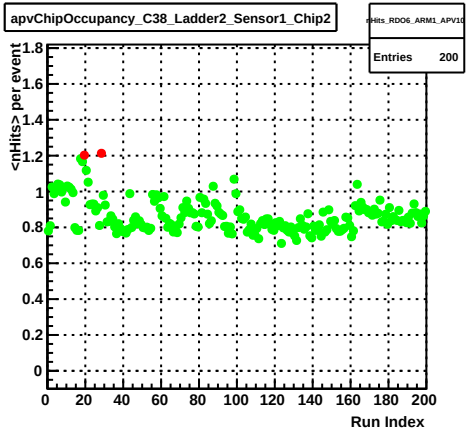
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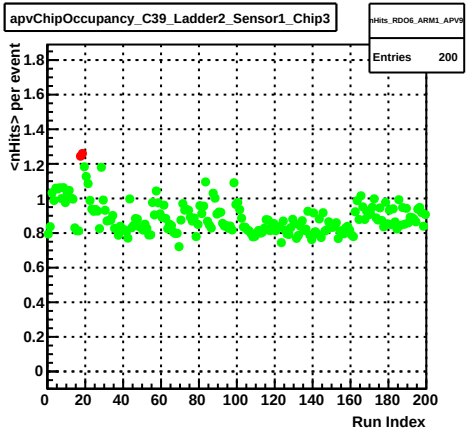
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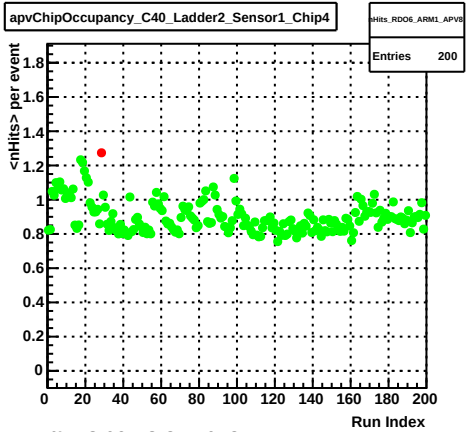
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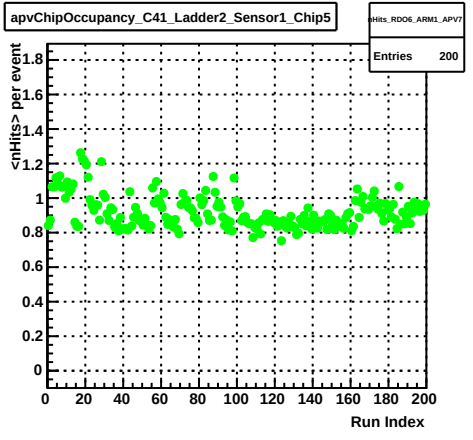
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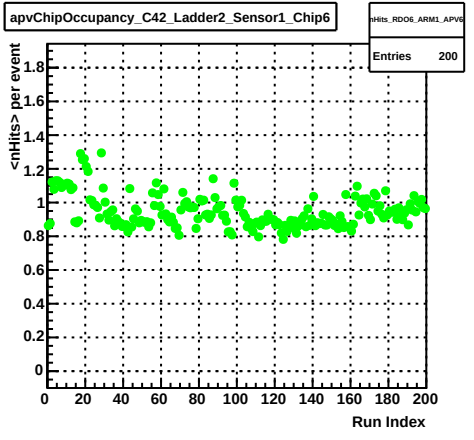
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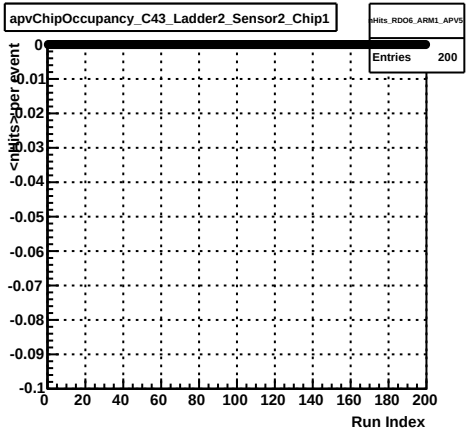
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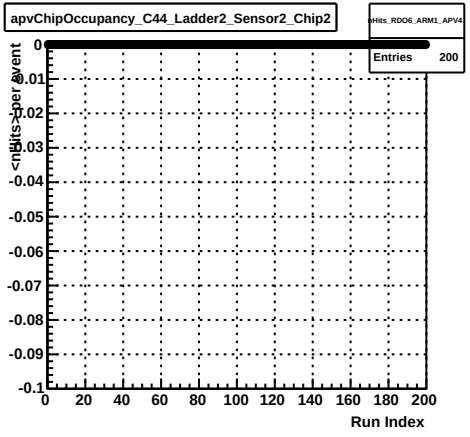
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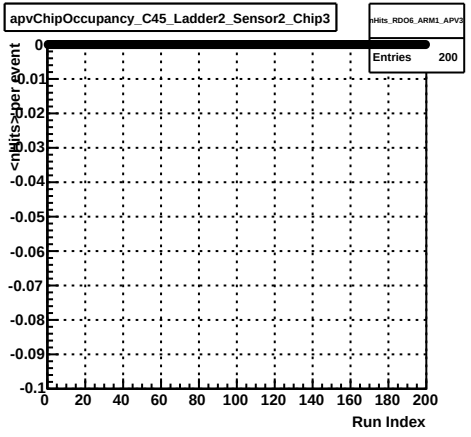
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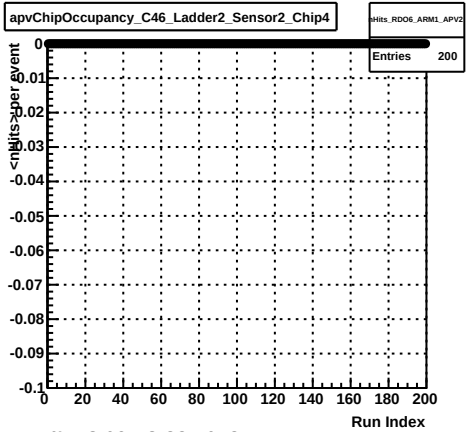
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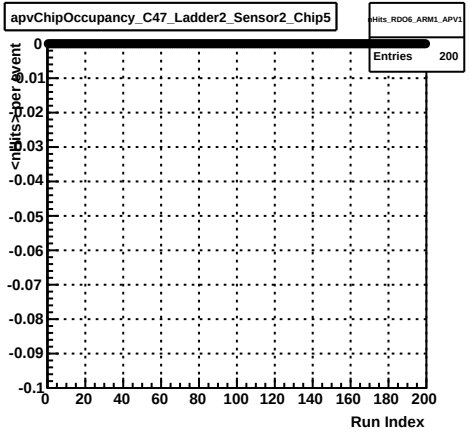
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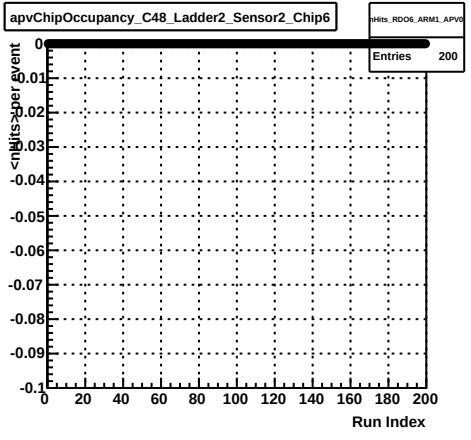
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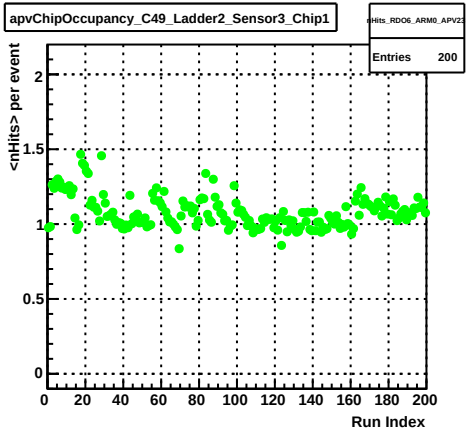
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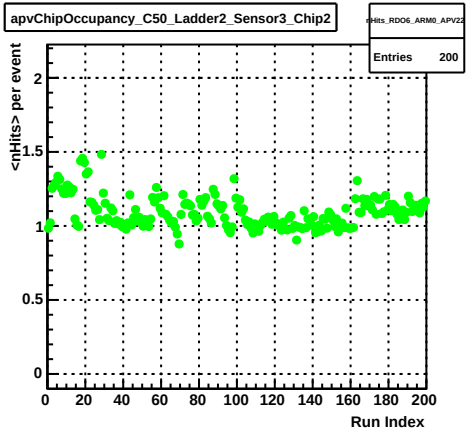
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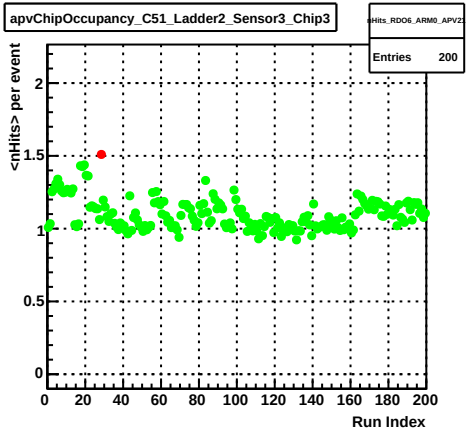
Run Index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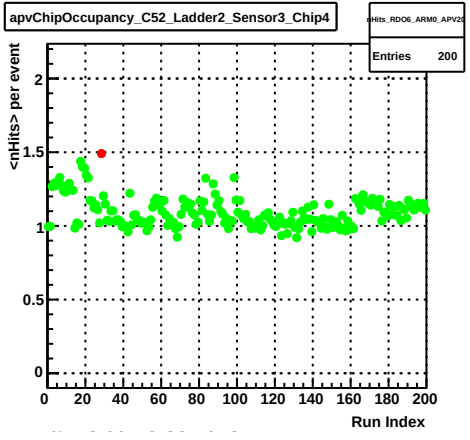
Run Index


apvChipOccupancy_C52_Ladder2_Sensor3_Chip4
nHits_RDO6_ARM0_APV20
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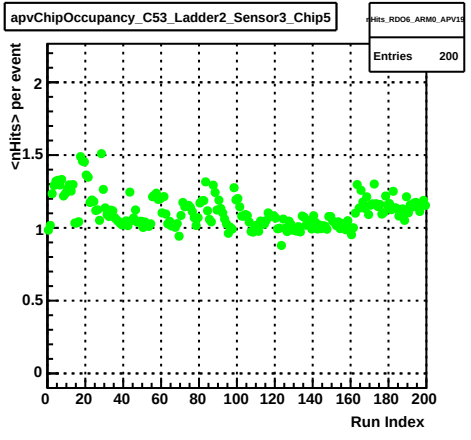
apvChipOccupancy_C53_Ladder2_Sensor3_Chip5
nHits_RDO6_ARM0_APV19
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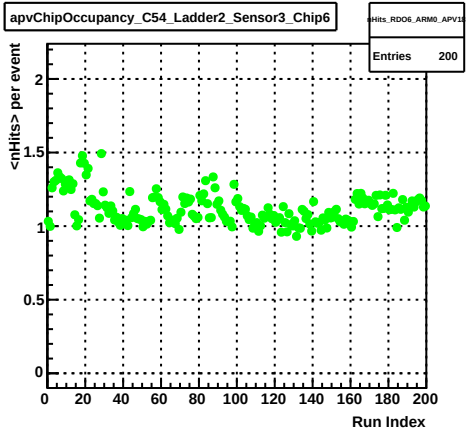
Run Index


apvChipOccupancy_C54_Ladder2_Sensor3_Chip6
nHits_RDO6_ARM0_APV18
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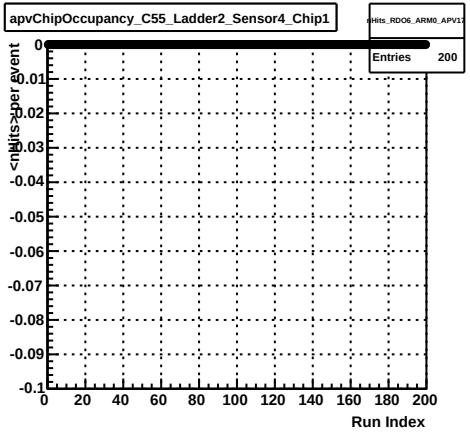
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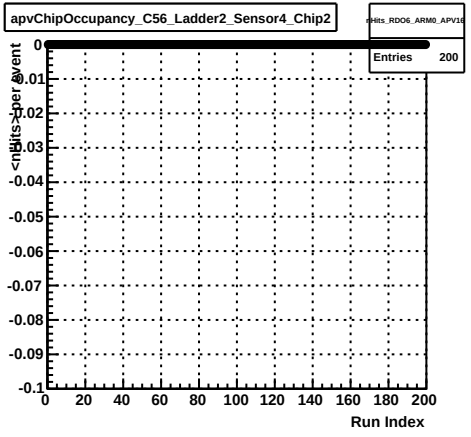
apvChipOccupancy_C56_Ladder2_Sensor4_Chip2
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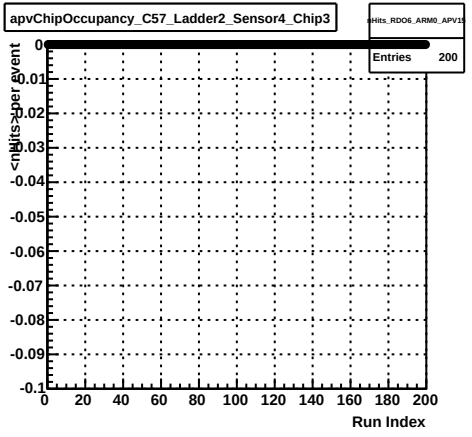
apvChipOccupancy_C57_Ladder2_Sensor4_Chip3
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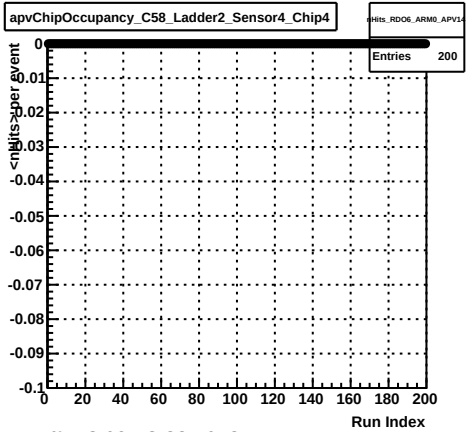
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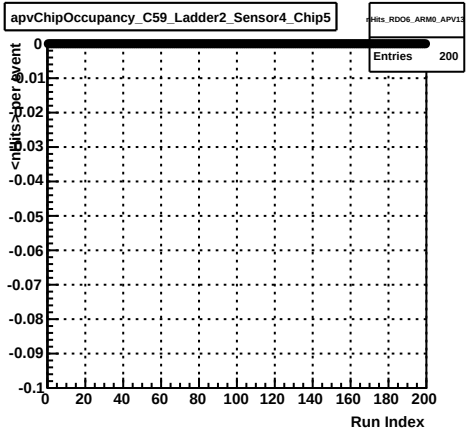
apvChipOccupancy_C59_Ladder2_Sensor4_Chip5
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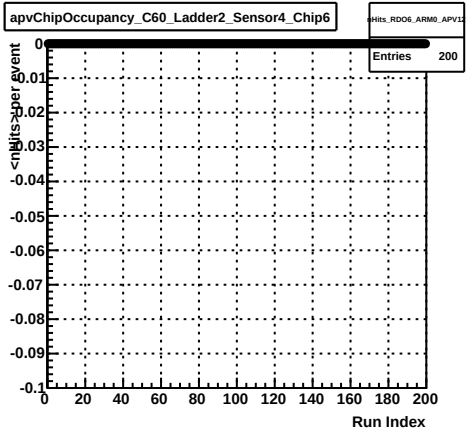
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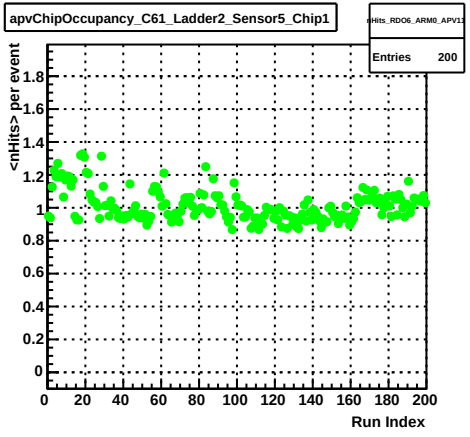
apvChipOccupancy_C61_Ladder2_Sensor5_Chip1
Entries 200
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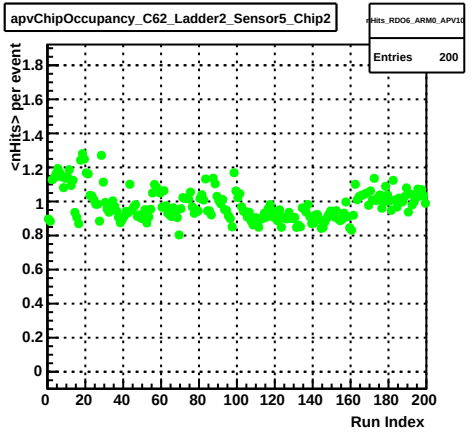
Run Index


apvChipOccupancy_C62_Ladder2_Sensor5_Chip2
Entries 200
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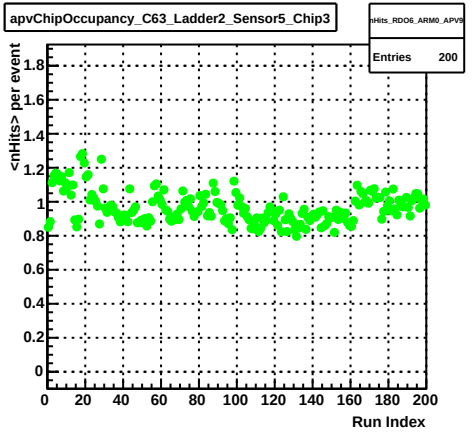
Run Index


apvChipOccupancy_C63_Ladder2_Sensor5_Chip3
Entries 200
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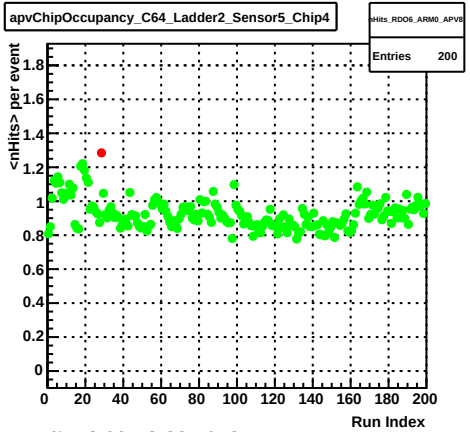
Run Index


apvChipOccupancy_C64_Ladder2_Sensor5_Chip4
Entries 200
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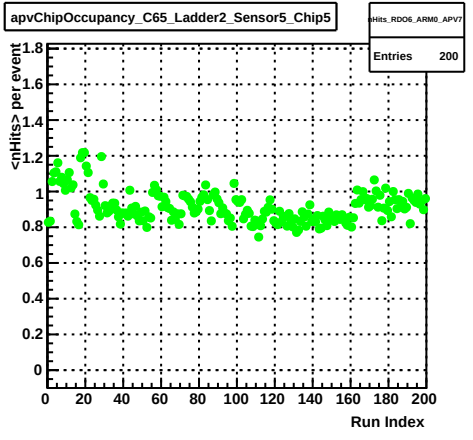
Run Index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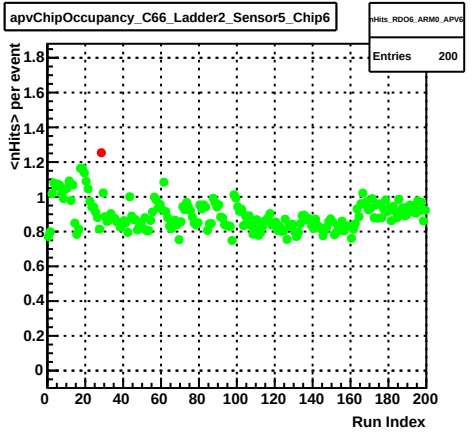
Run Index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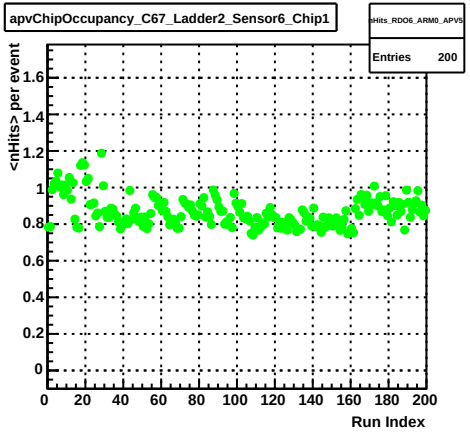
Run Index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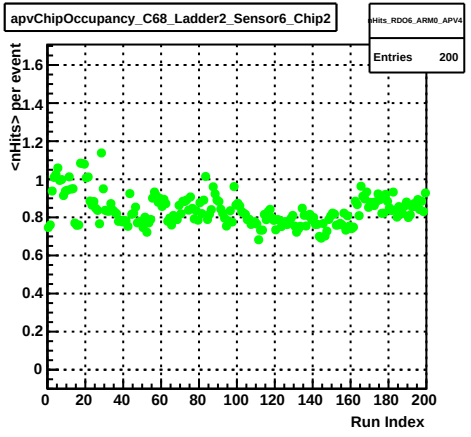
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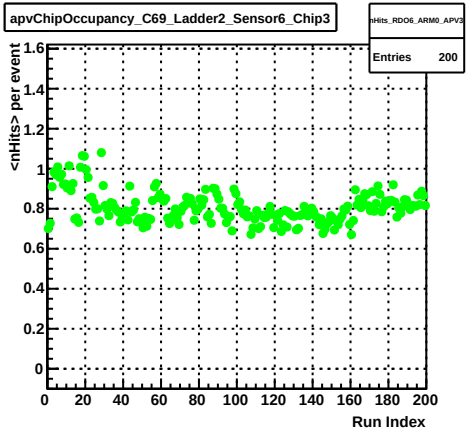
Run Index


apvChipOccupancy_C69_Ladder2_Sensor6_Chip3
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Entries 200
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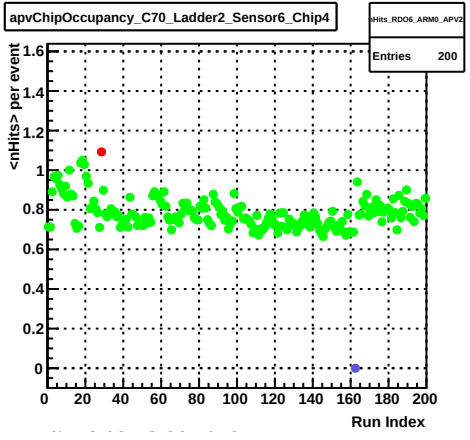
Run Index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nHits_RDO6_ARM0_APV2
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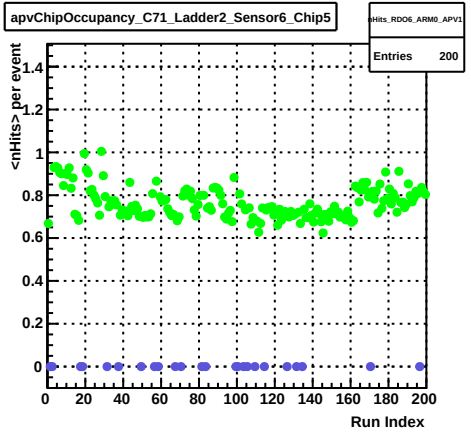
Run Index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Entries 200
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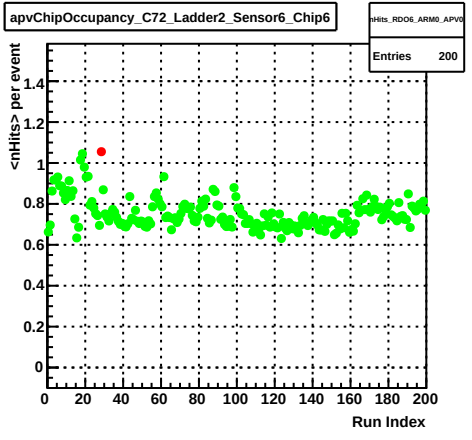
Run Index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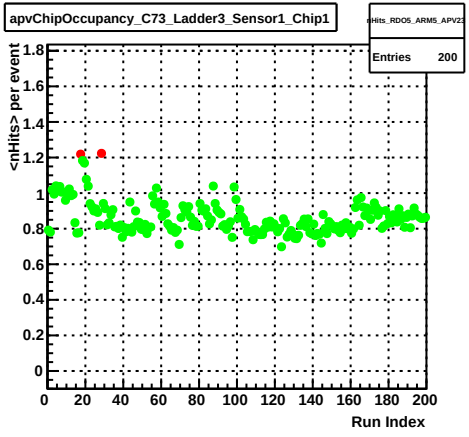
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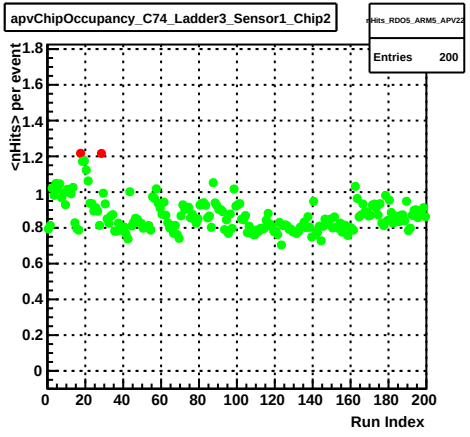
Run Index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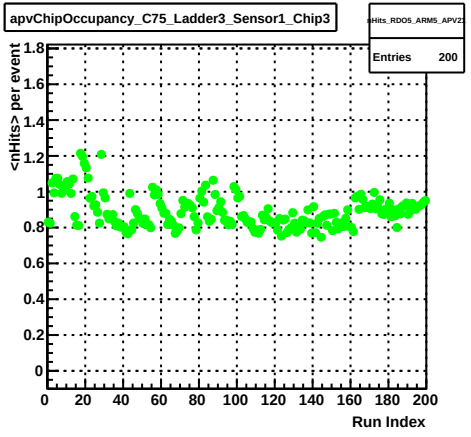
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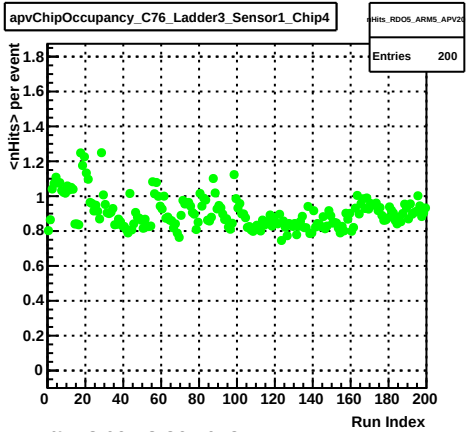
Run Index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Entries 200
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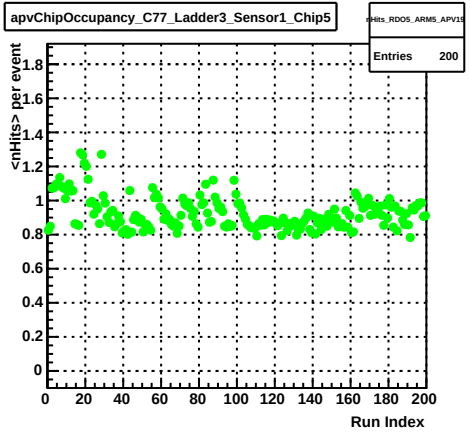
Run Index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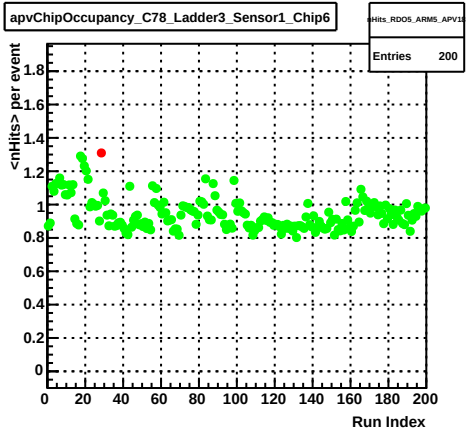
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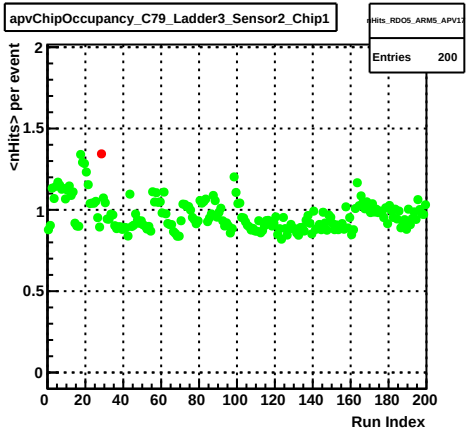
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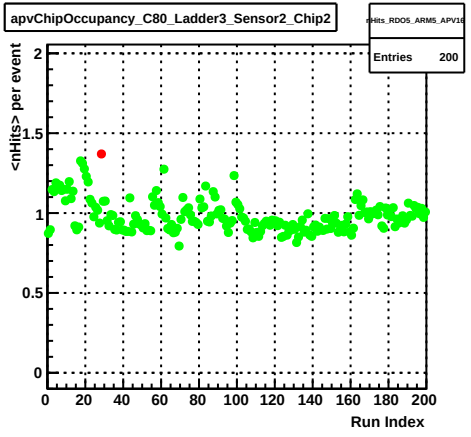
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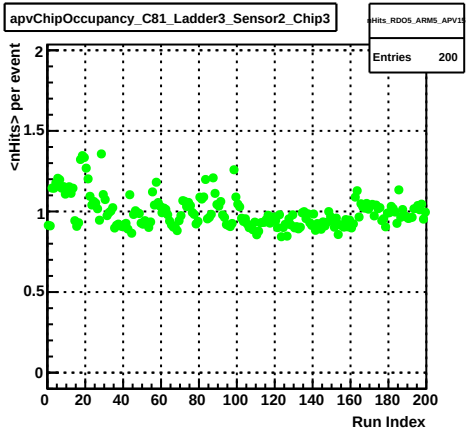
Run Index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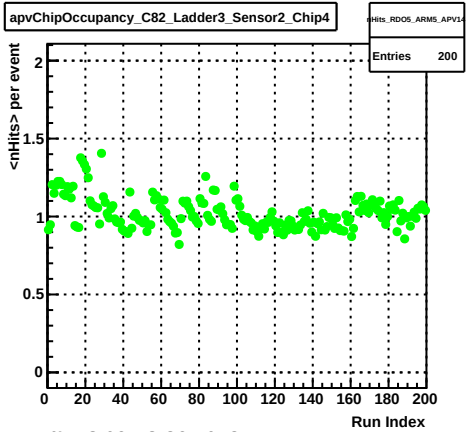
Run Index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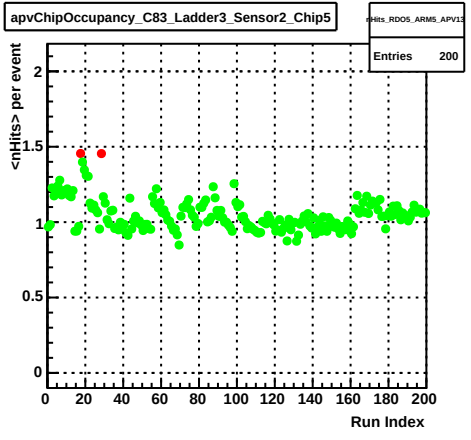
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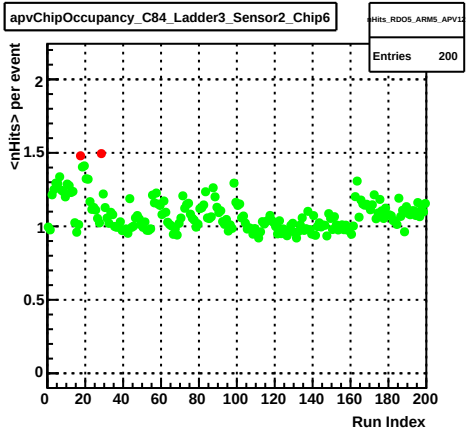
apvChipOccupancy_C84_Ladder3_Sensor2_Chip6
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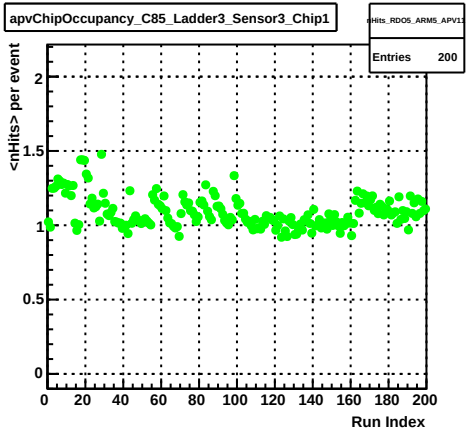
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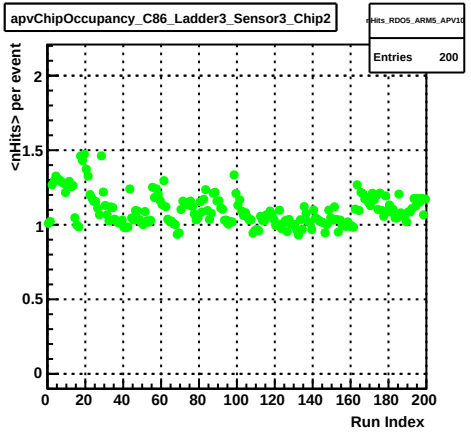
Run Index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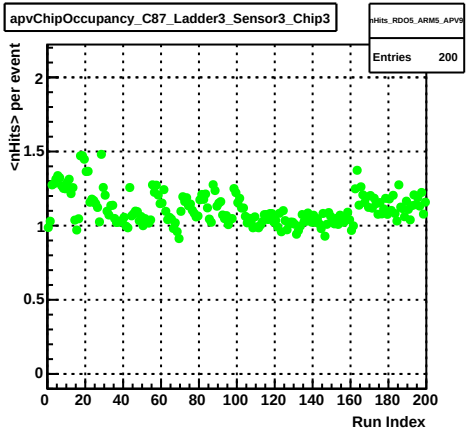
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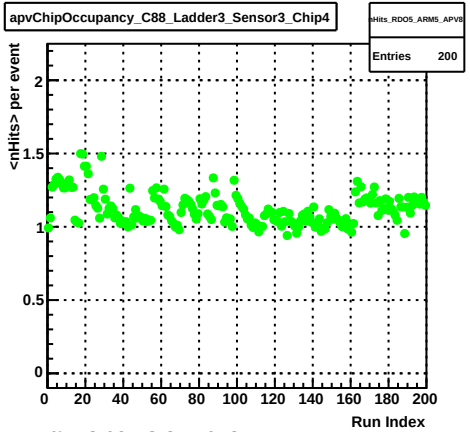
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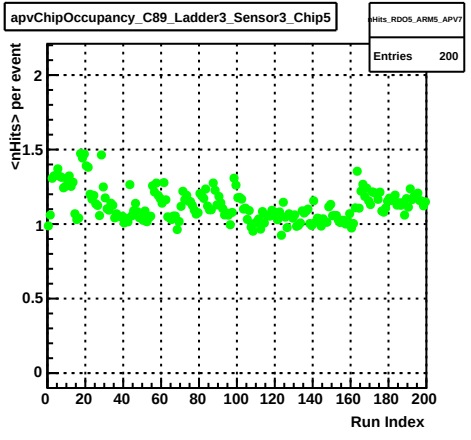
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nHits_RDO5_ARM5_APV7
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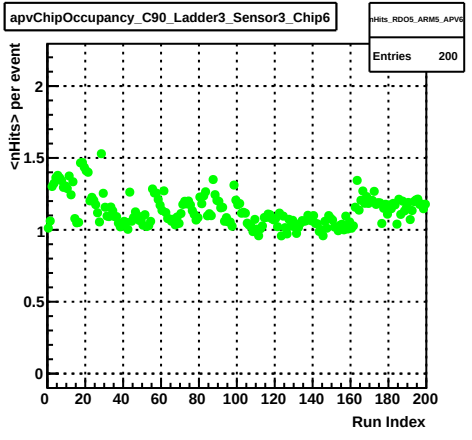
Run Index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nHits_RDO5_ARM5_APV6
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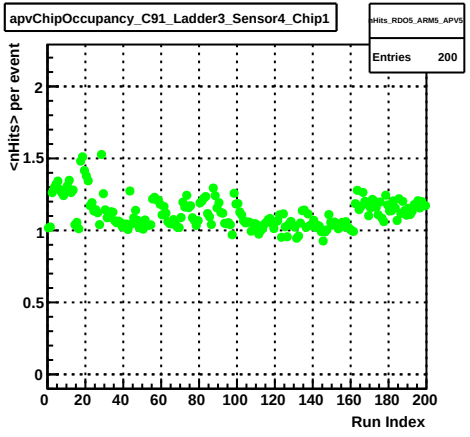
Run Index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Entries 200
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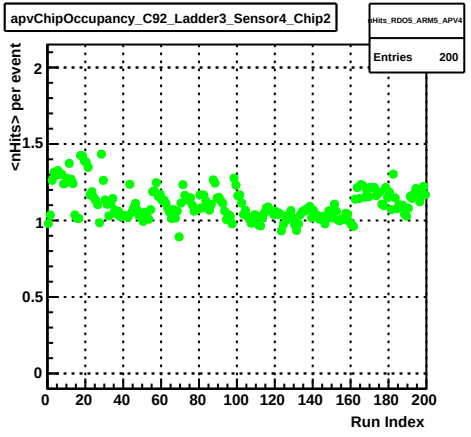
Run Index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Entries 200
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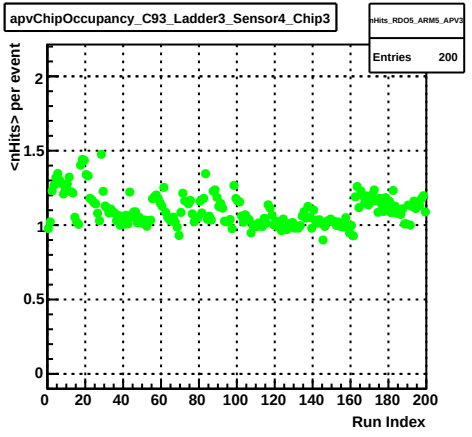
Run Index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Entries 200
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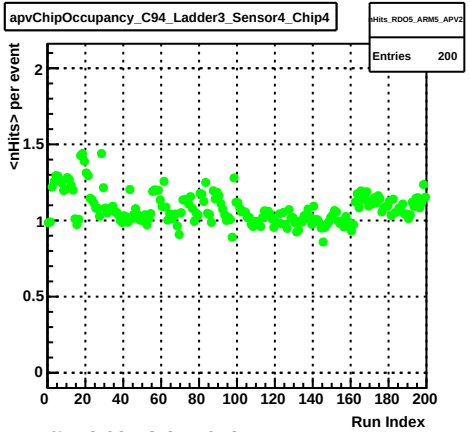
Run Index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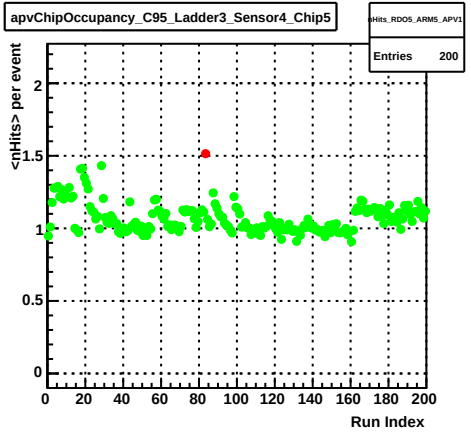
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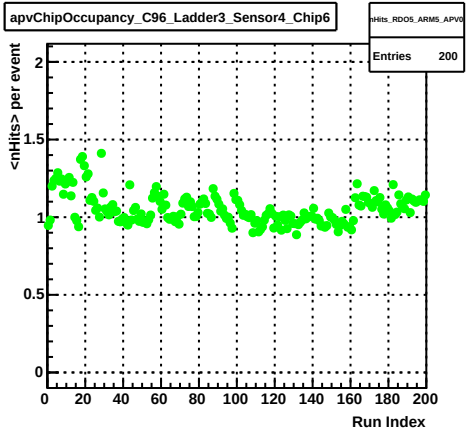
apvChipOccupancy_C96_Ladder3_Sensor4_Chip6
nHits_RDO5_ARM5_APV0
Entries 200
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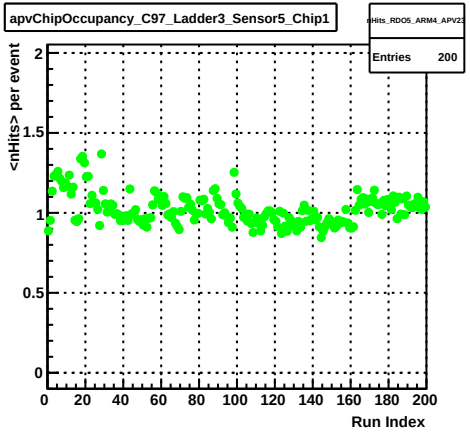
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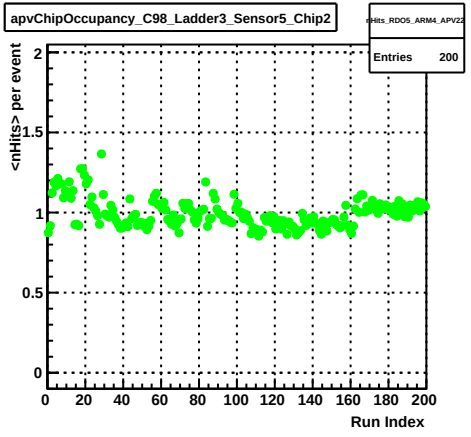
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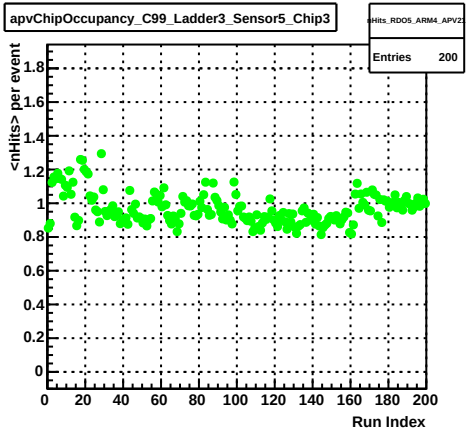
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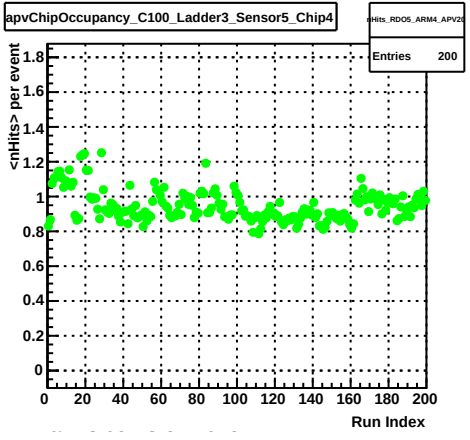
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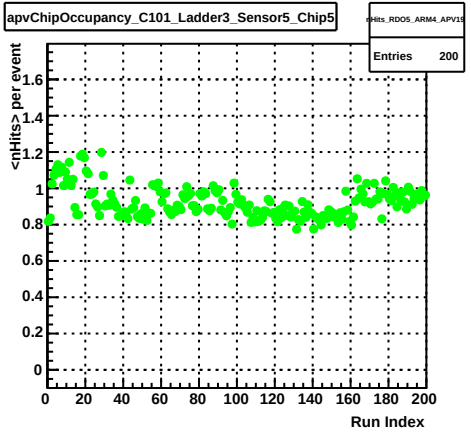
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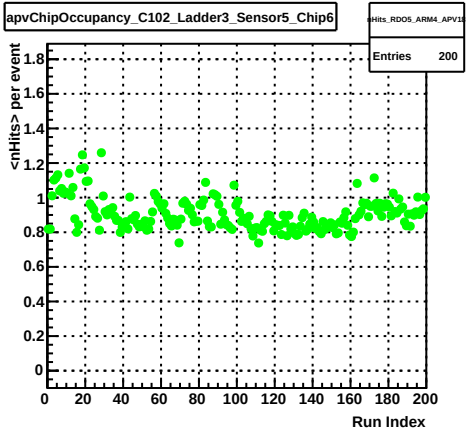
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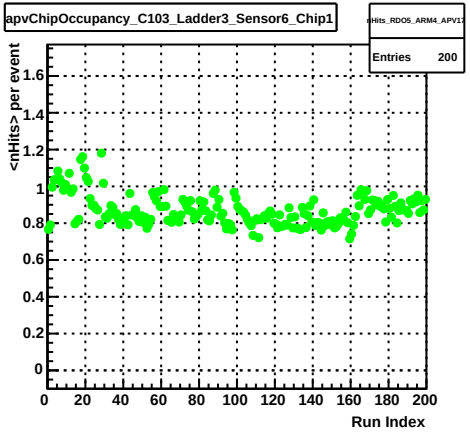
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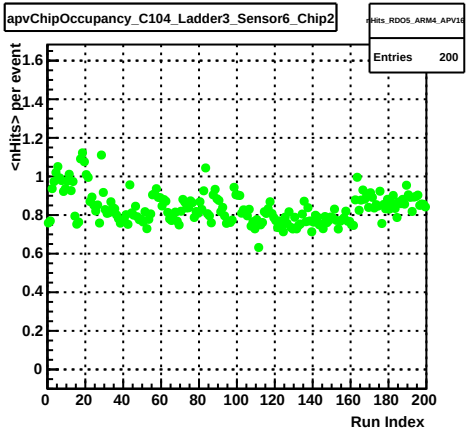
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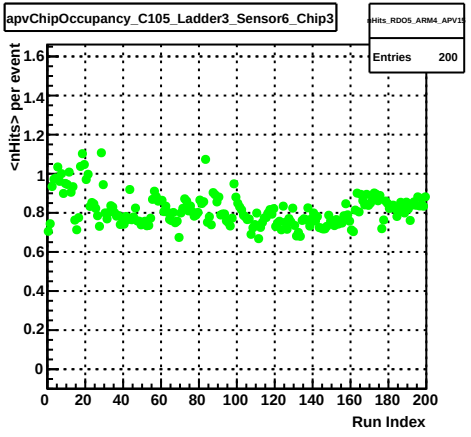
apvChipOccupancy_C105_Ladder3_Sensor6_Chip3
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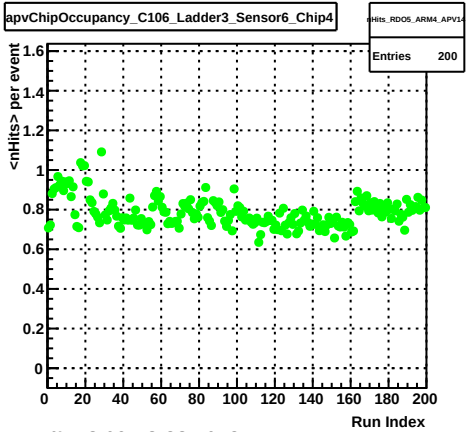
apvChipOccupancy_C106_Ladder3_Sensor6_Chip4
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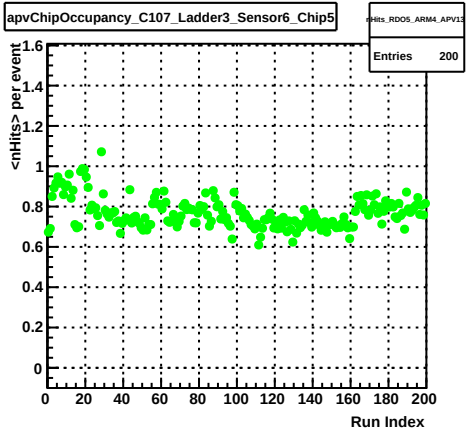
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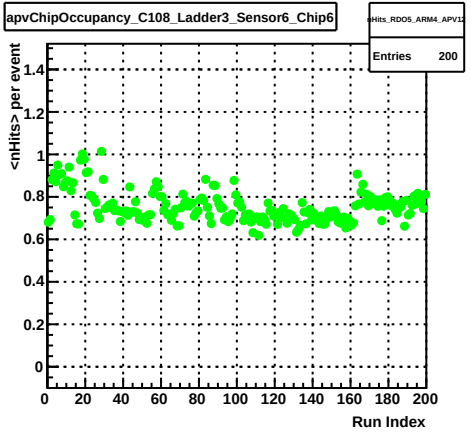
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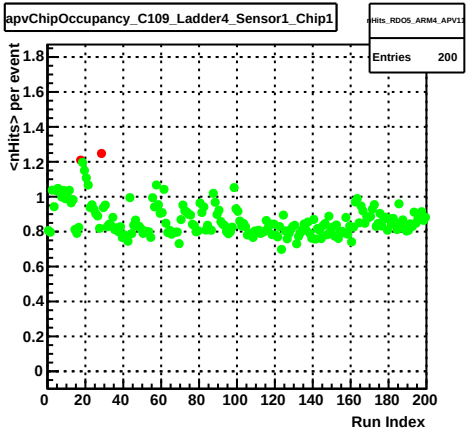
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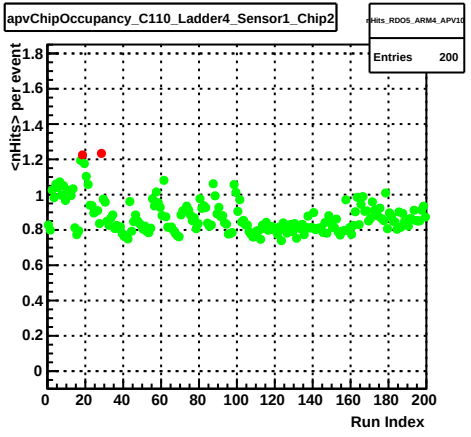
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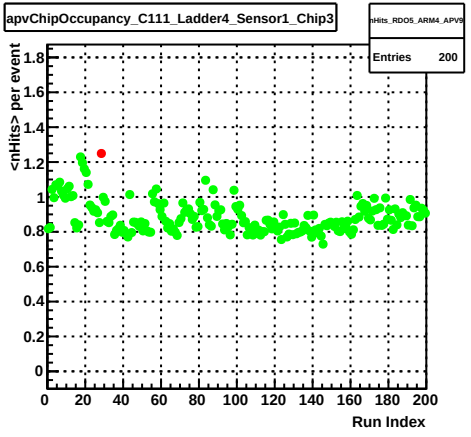
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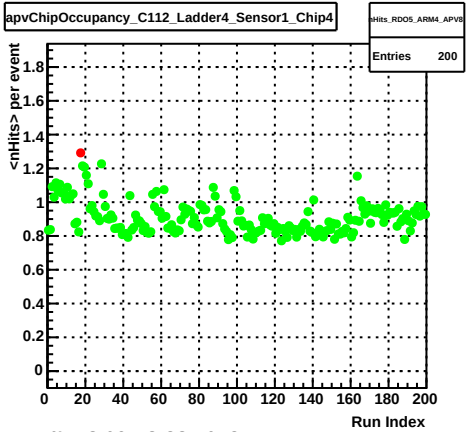
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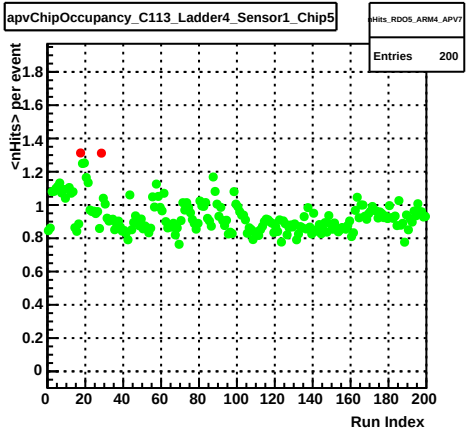
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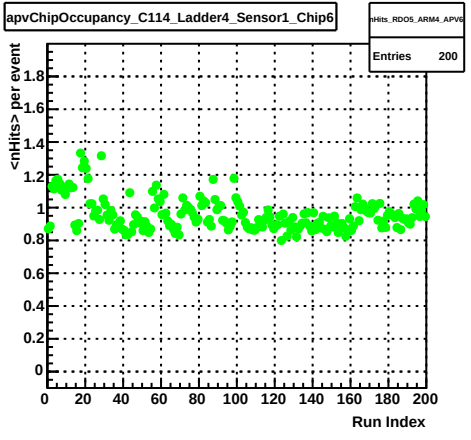
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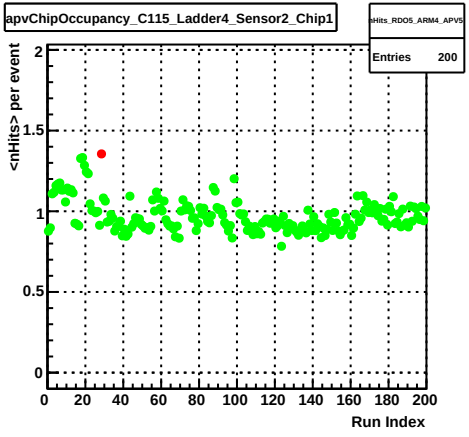
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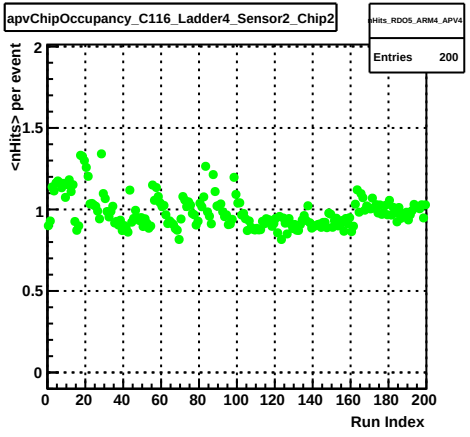
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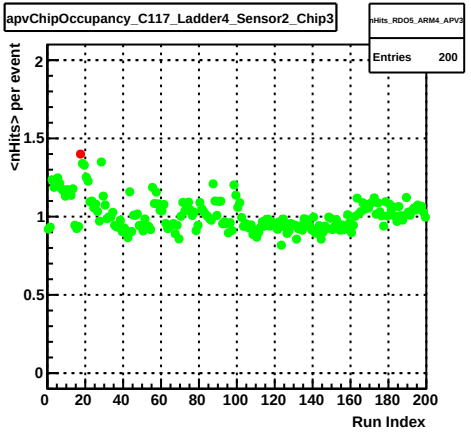
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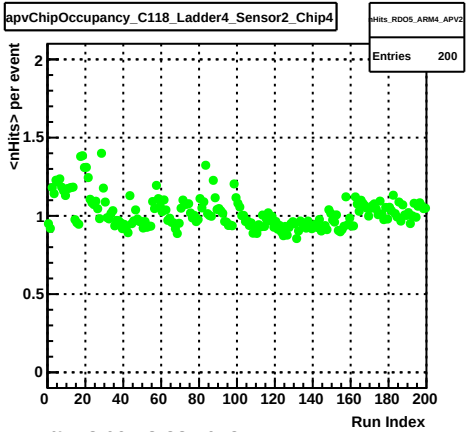
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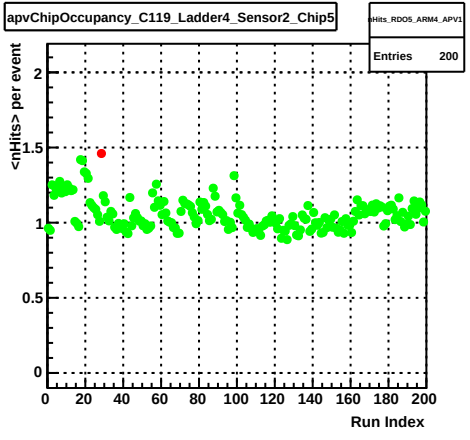
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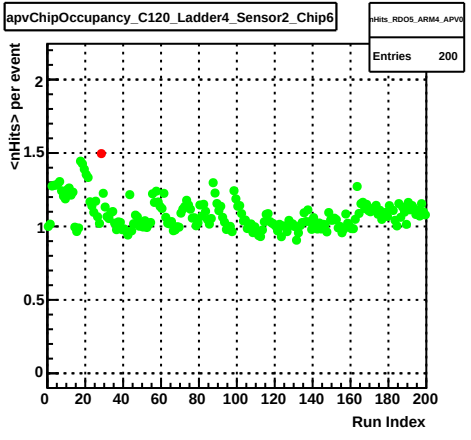
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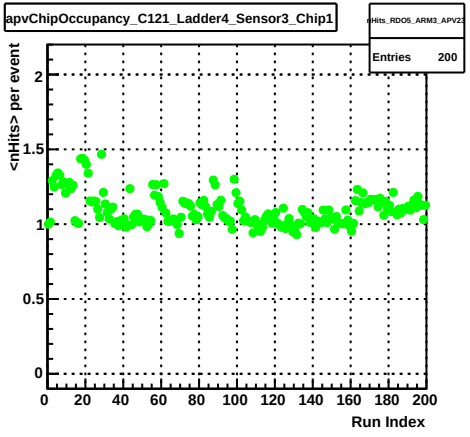
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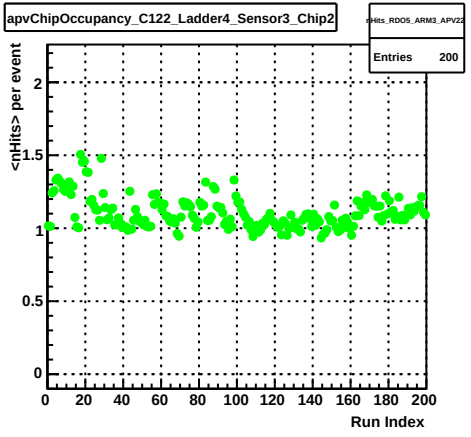
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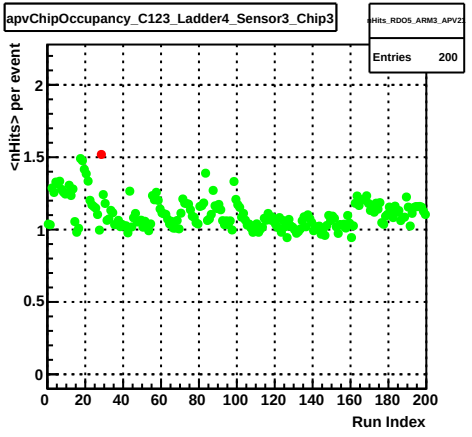
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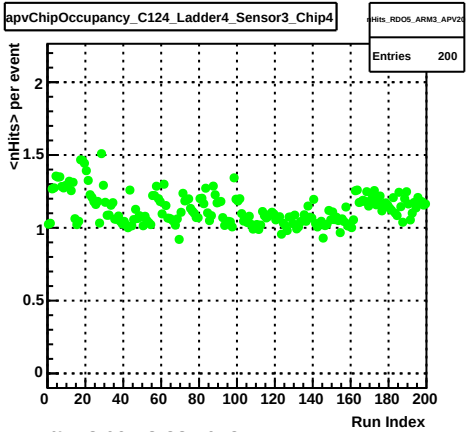
Run Index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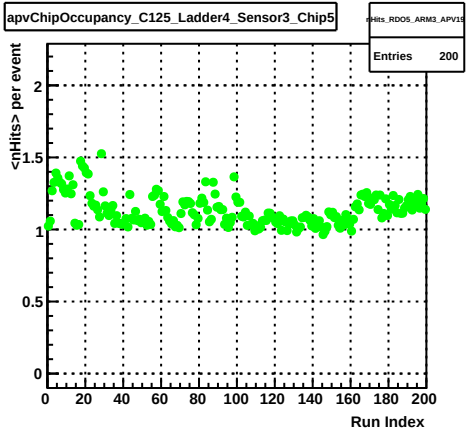
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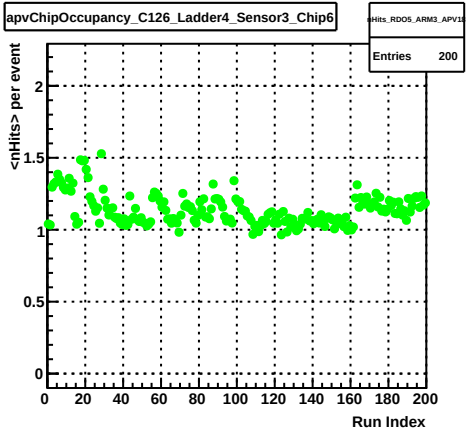
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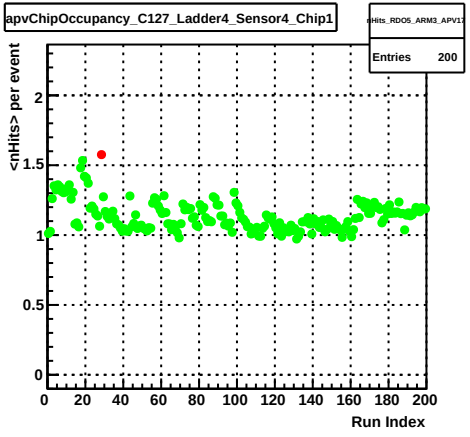
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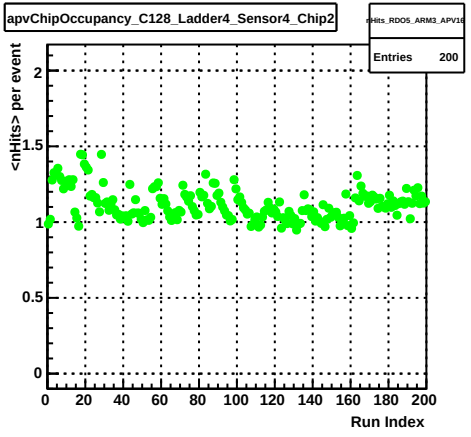
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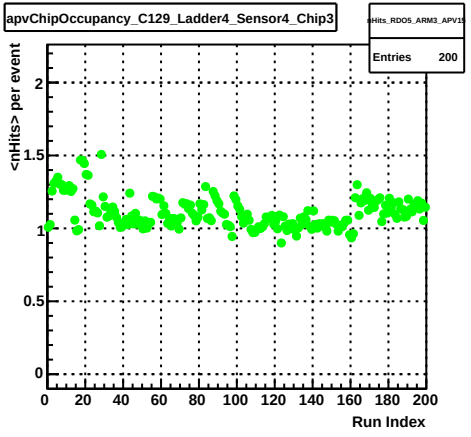
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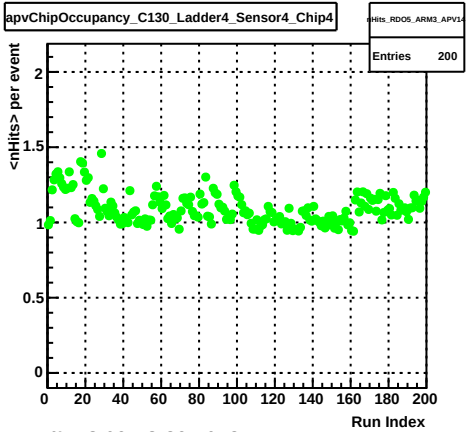
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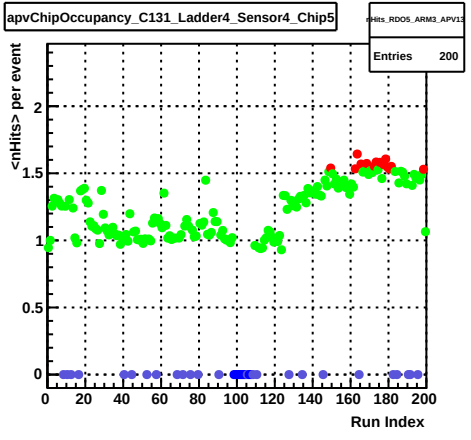
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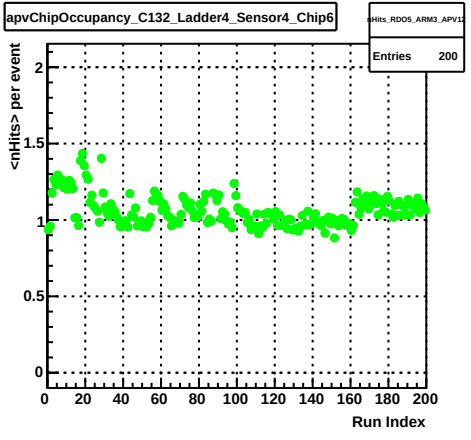
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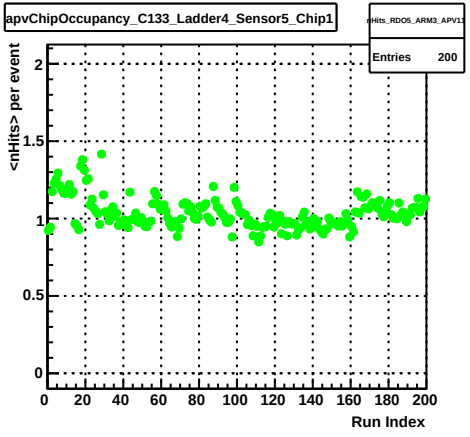
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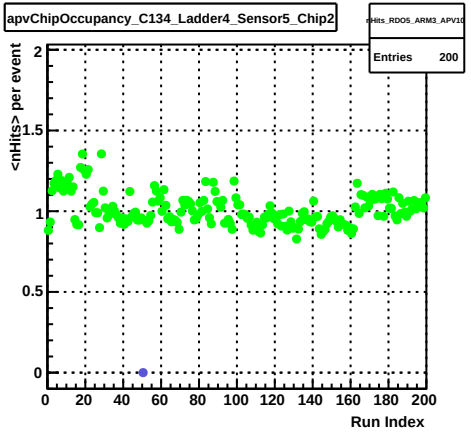
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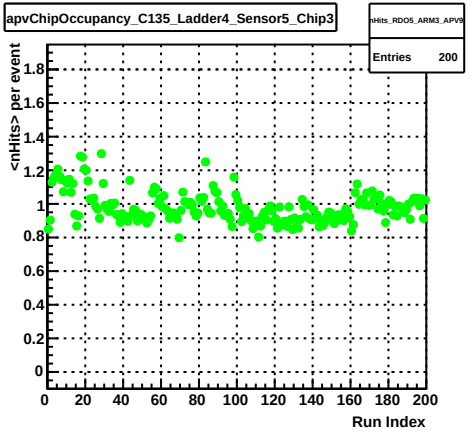
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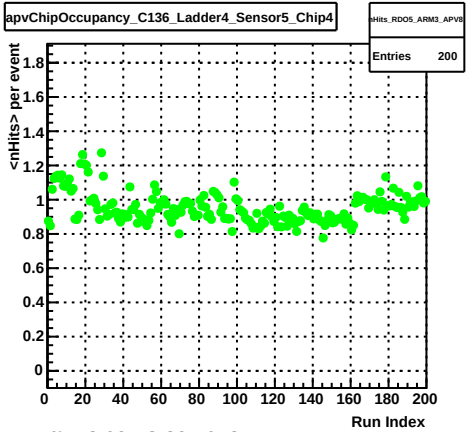
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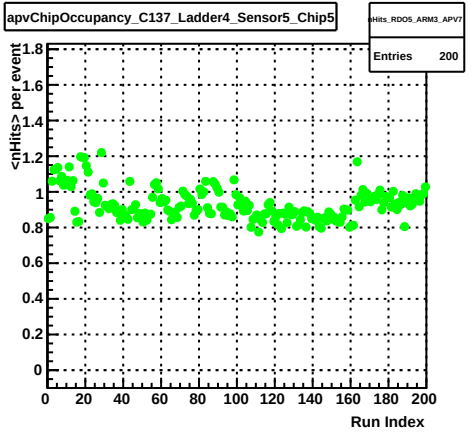
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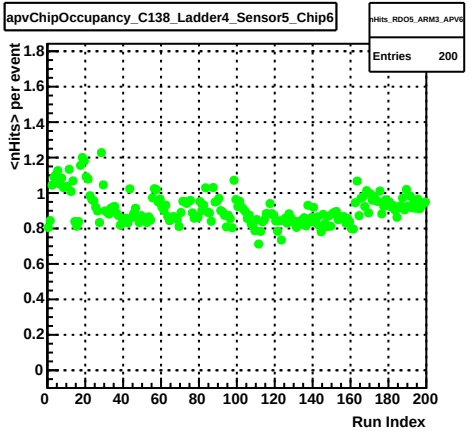
apvChipOccupancy_C138_Ladder4_Sensor5_Chip6
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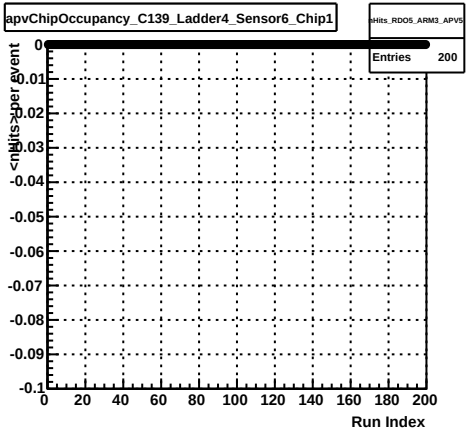
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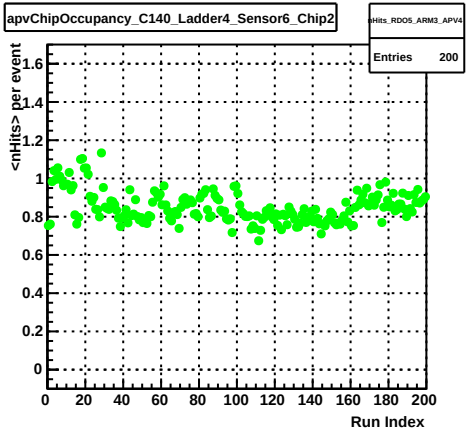
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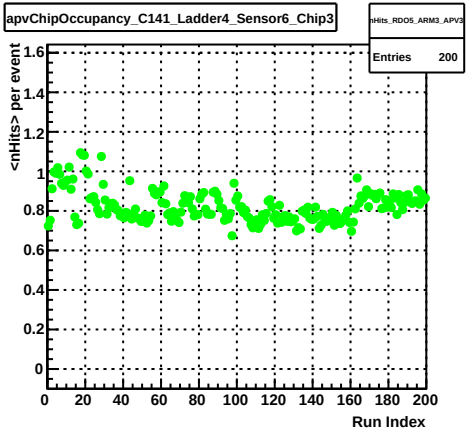
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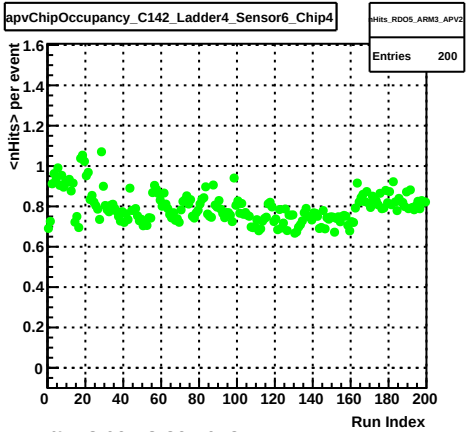
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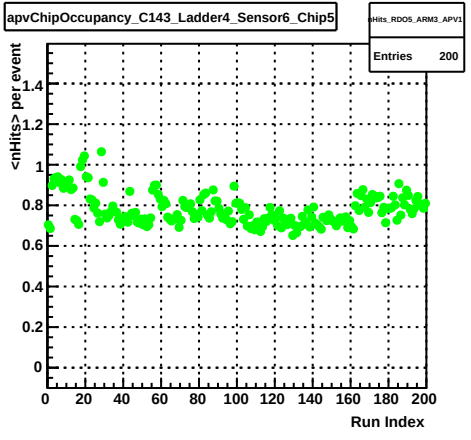
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Entries 200
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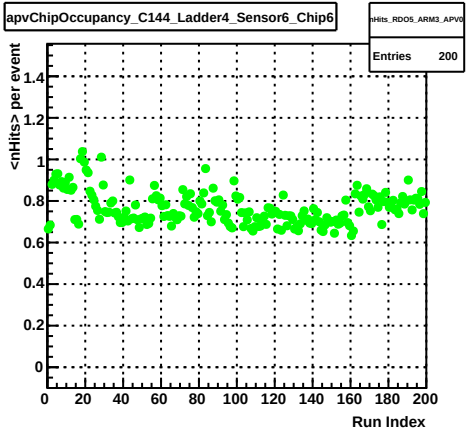
Run Index


apvChipOccupancy_C144_Ladder4_Sensor6_Chip6
nHits_RD05_ARM3_APV0
Entries 200
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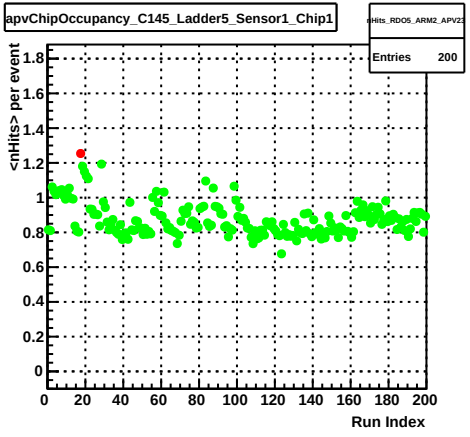
Run Index


apvChipOccupancy_C145_Ladder5_Sensor1_Chip1
Entries 200
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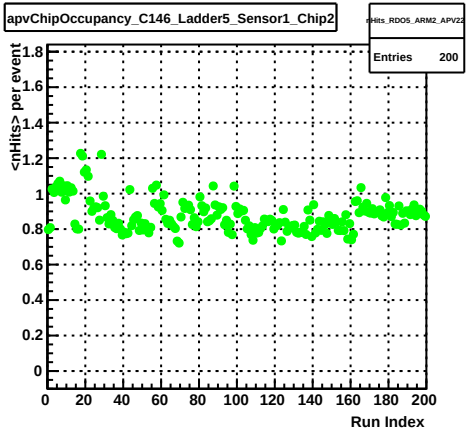
Run Index


apvChipOccupancy_C146_Ladder5_Sensor1_Chip2
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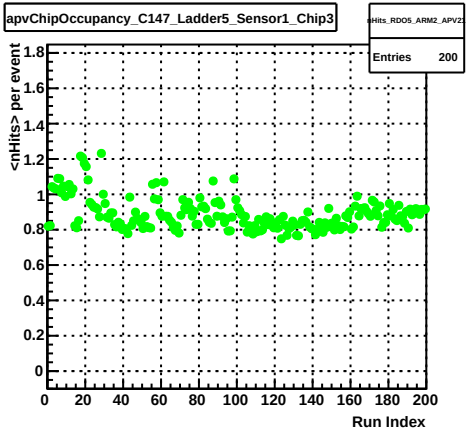
Run Index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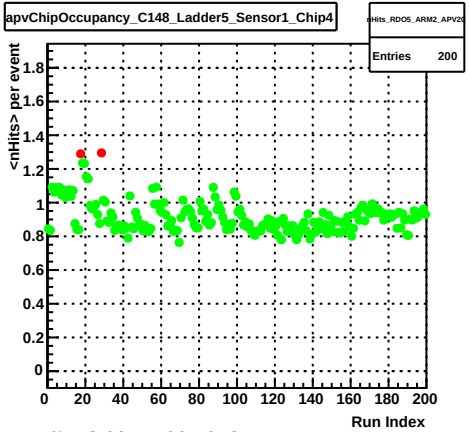
Run Index


apvChipOccupancy_C148_Ladder5_Sensor1_Chip4
Entries 200
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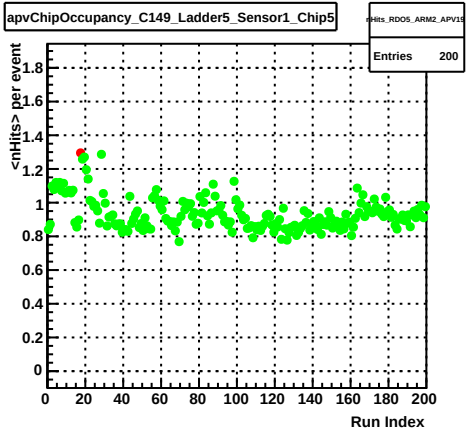
Run Index


apvChipOccupancy_C149_Ladder5_Sensor1_Chip5
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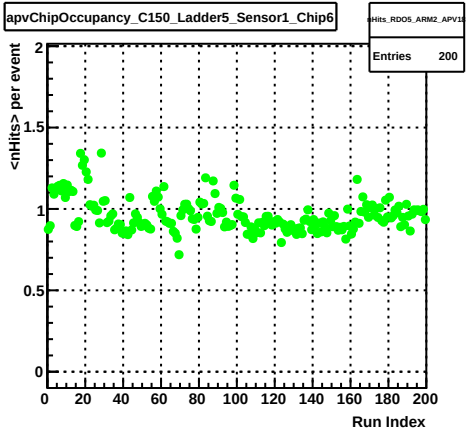
Run Index


apvChipOccupancy_C150_Ladder5_Sensor1_Chip6
Entries 200
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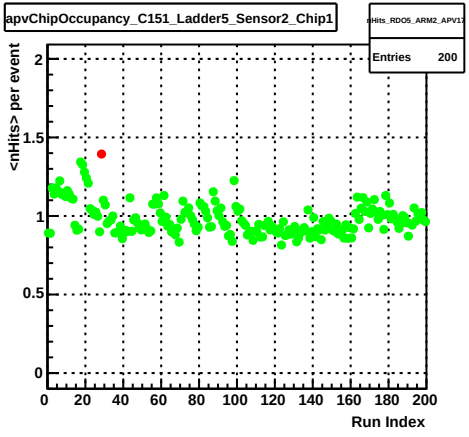
Run Index


apvChipOccupancy_C151_Ladder5_Sensor2_Chip1
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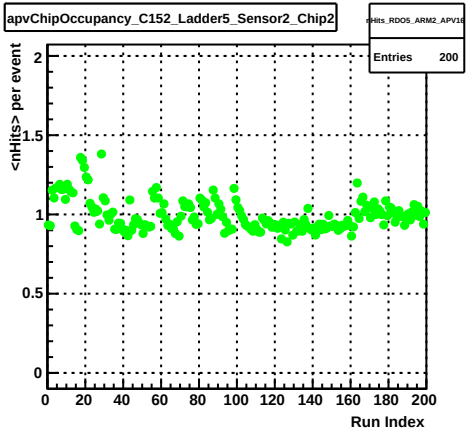
Run Index


apvChipOccupancy_C152_Ladder5_Sensor2_Chip2
Entries 200
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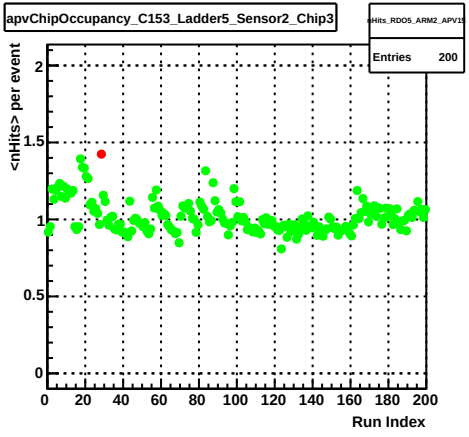
Run Index


apvChipOccupancy_C153_Ladder5_Sensor2_Chip3
Entries 200
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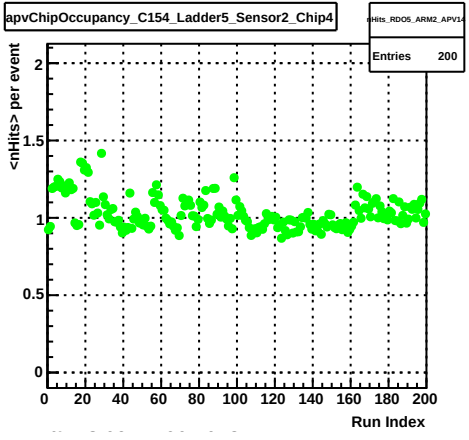
Run Index


apvChipOccupancy_C154_Ladder5_Sensor2_Chip4
Entries 200
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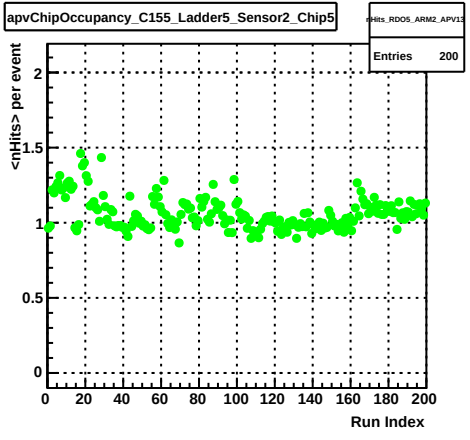
Run Index


apvChipOccupancy_C155_Ladder5_Sensor2_Chip5
Entries 200
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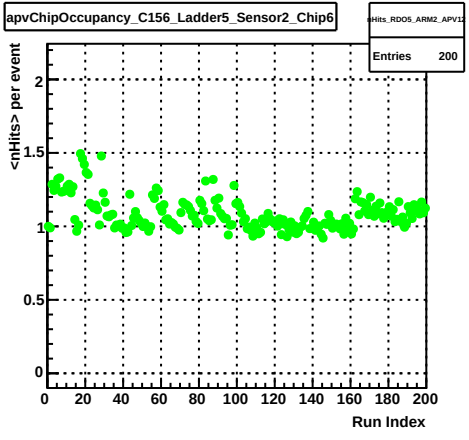
Run Index


apvChipOccupancy_C156_Ladder5_Sensor2_Chip6
Entries 200
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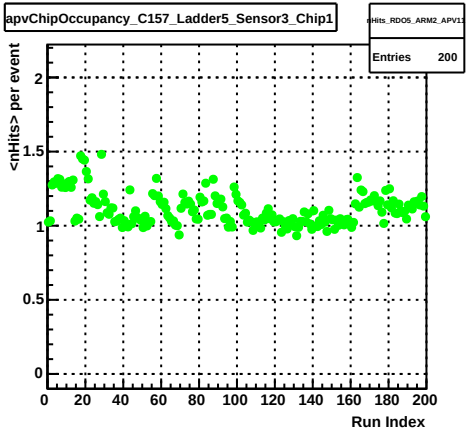
Run Index


apvChipOccupancy_C157_Ladder5_Sensor3_Chip1
Entries 200
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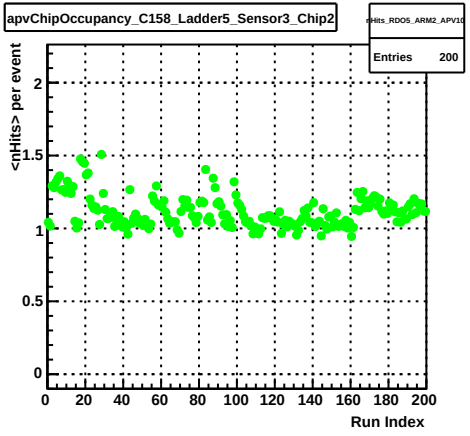
Run Index


apvChipOccupancy_C158_Ladder5_Sensor3_Chip2
Entries 200
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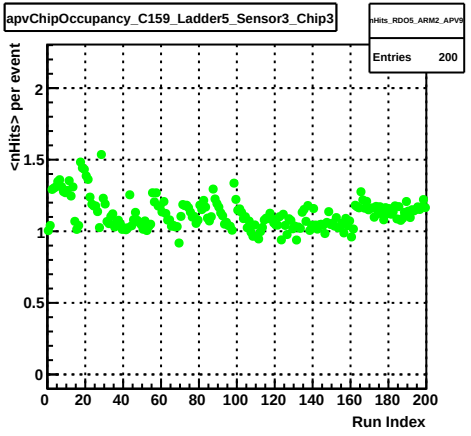
Run Index


apvChipOccupancy_C159_Ladder5_Sensor3_Chip3
Entries 200
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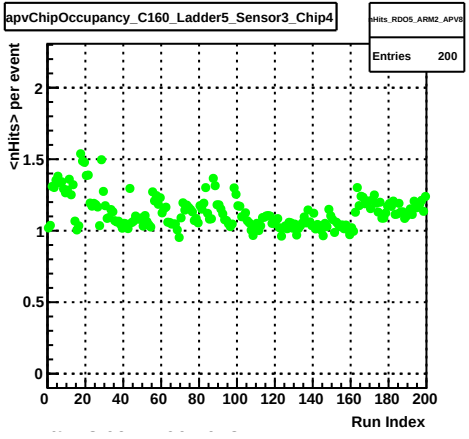
Run Index


apvChipOccupancy_C160_Ladder5_Sensor3_Chip4
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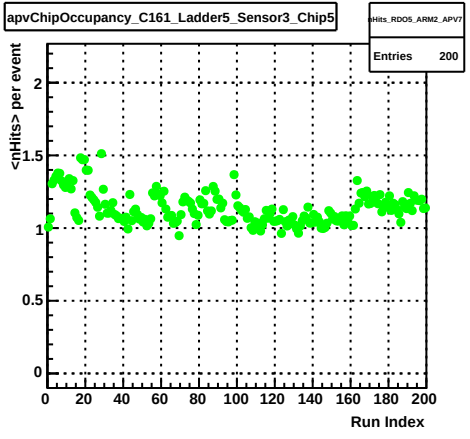
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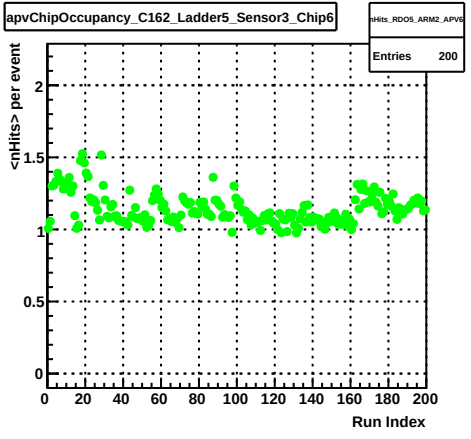
apvChipOccupancy_C162_Ladder5_Sensor3_Chip6
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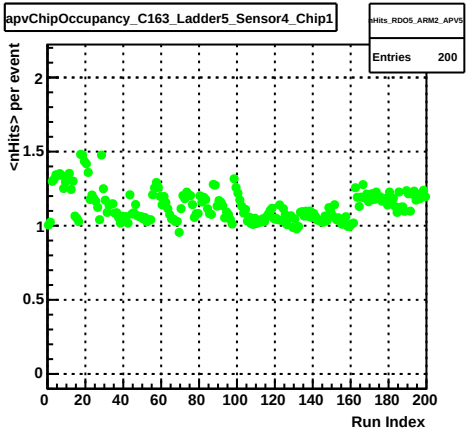
apvChipOccupancy_C163_Ladder5_Sensor4_Chip1
nHits_RDO5_ARM2_APV5
Entries 200
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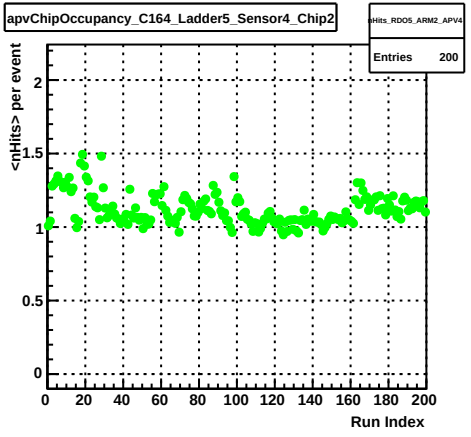
Run Index


apvChipOccupancy_C164_Ladder5_Sensor4_Chip2
nHits_RDO5_ARM2_APV4
Entries 200
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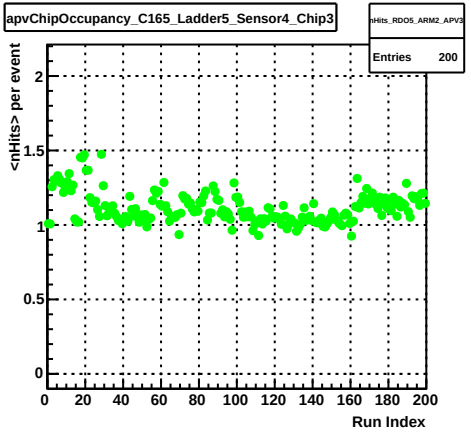
Run Index


apvChipOccupancy_C165_Ladder5_Sensor4_Chip3
nHits_RDO5_ARM2_APV3
Entries 200
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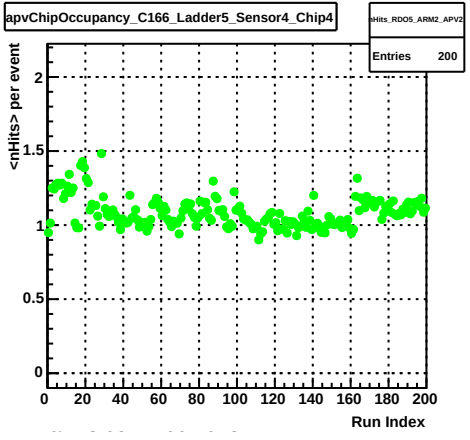
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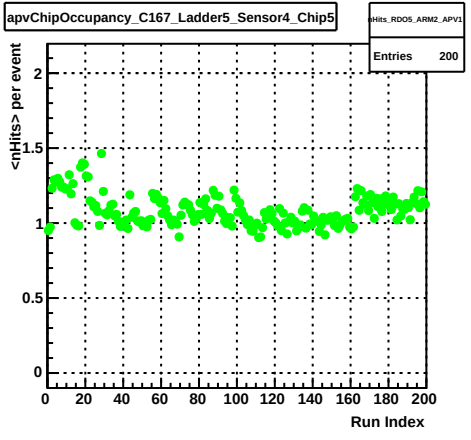
Run Index


apvChipOccupancy_C167_Ladder5_Sensor4_Chip5
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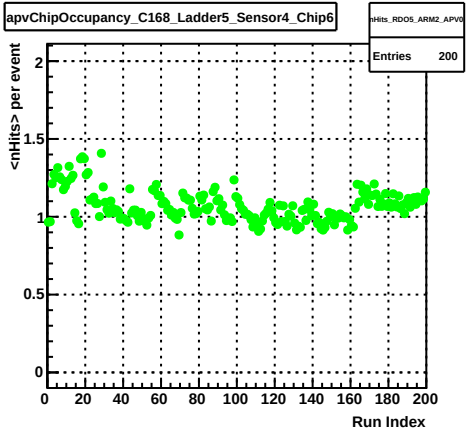
apvChipOccupancy_C168_Ladder5_Sensor4_Chip6
nHits_RDO5_ARM2_APV0
Entries 200
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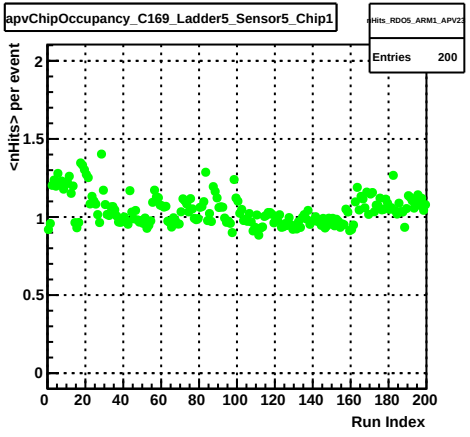
Run Index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Entries 200
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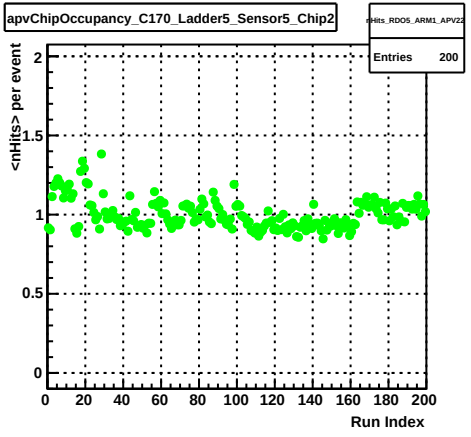
Run Index


apvChipOccupancy_C170_Ladder5_Sensor5_Chip2
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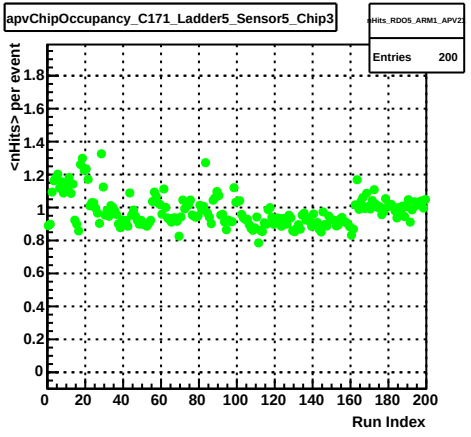
Run Index


apvChipOccupancy_C171_Ladder5_Sensor5_Chip3
Entries 200
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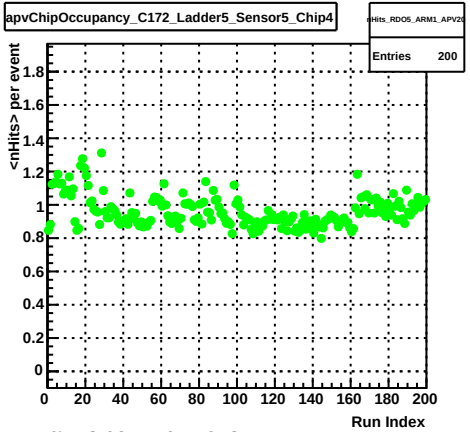
Run Index


apvChipOccupancy_C172_Ladder5_Sensor5_Chip4
Entries 200
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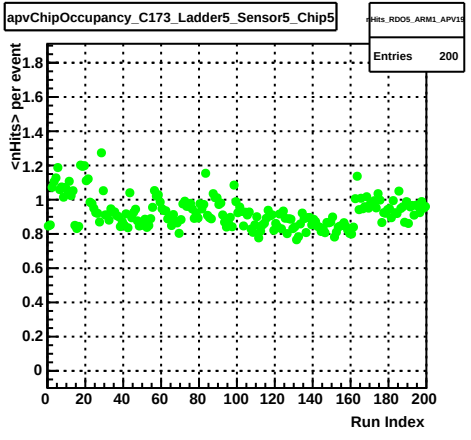
Run Index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Entries 200
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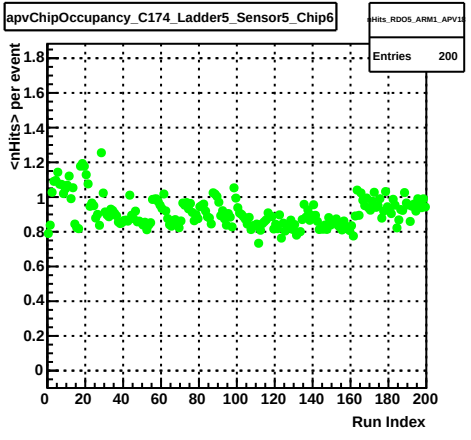
Run Index


apvChipOccupancy_C174_Ladder5_Sensor5_Chip6
Entries 200
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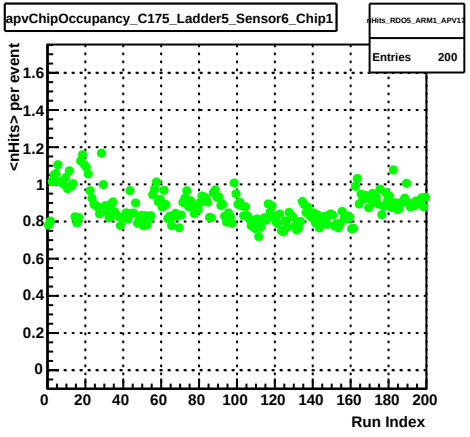
Run Index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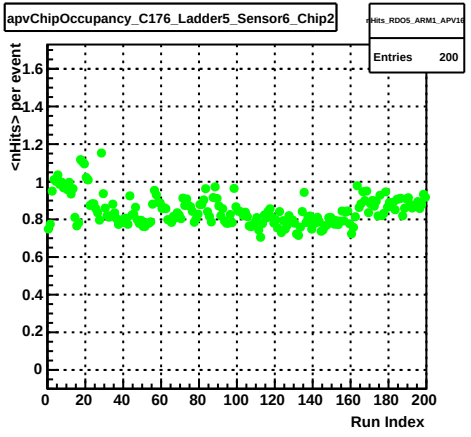
Run Index


apvChipOccupancy_C176_Ladder5_Sensor6_Chip2
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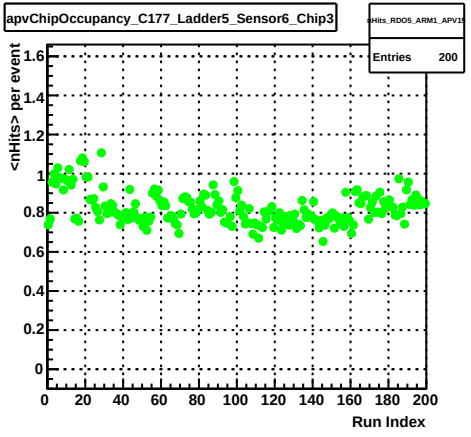
Run Index


apvChipOccupancy_C177_Ladder5_Sensor6_Chip3
Entries 200
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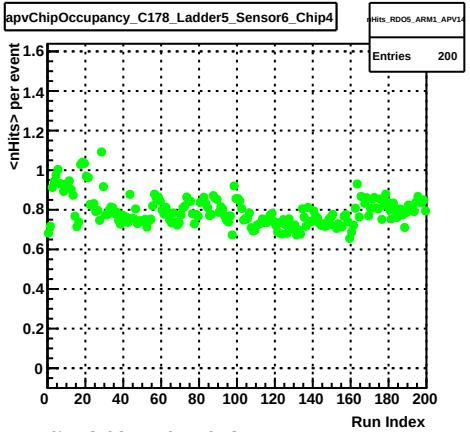
Run Index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Entries 200
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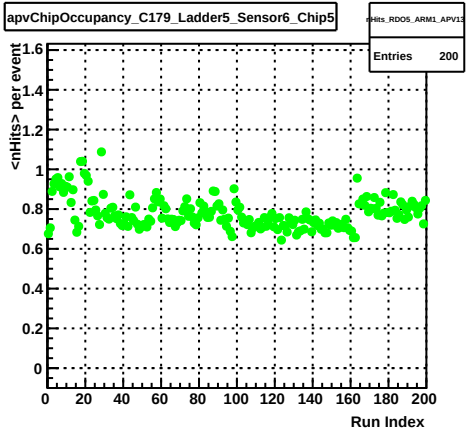
Run Index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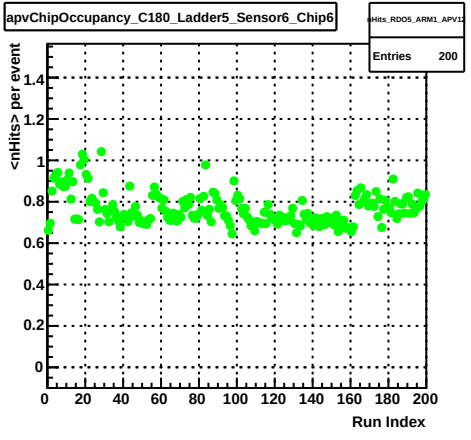
apvChipOccupancy_C180_Ladder5_Sensor6_Chip6
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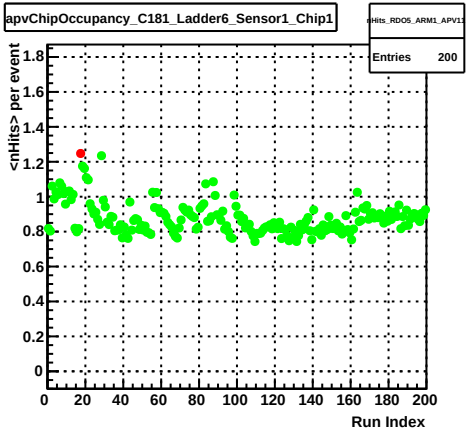
Run Index


apvChipOccupancy_C181_Ladder6_Sensor1_Chip1
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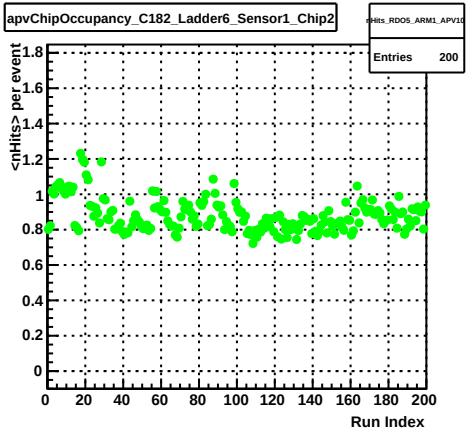
Run Index


apvChipOccupancy_C182_Ladder6_Sensor1_Chip2
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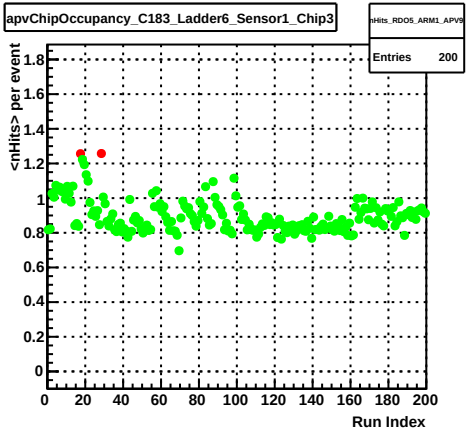
Run Index


apvChipOccupancy_C183_Ladder6_Sensor1_Chip3
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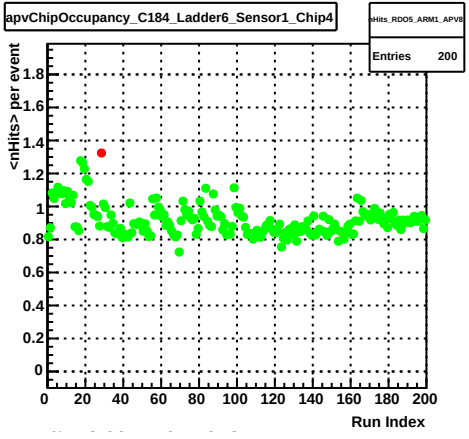
Run Index


apvChipOccupancy_C184_Ladder6_Sensor1_Chip4
Entries 200
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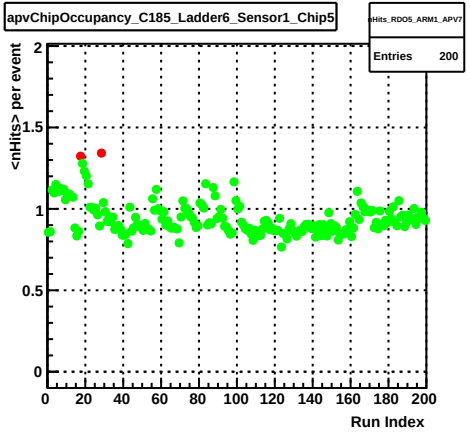
Run Index


apvChipOccupancy_C185_Ladder6_Sensor1_Chip5
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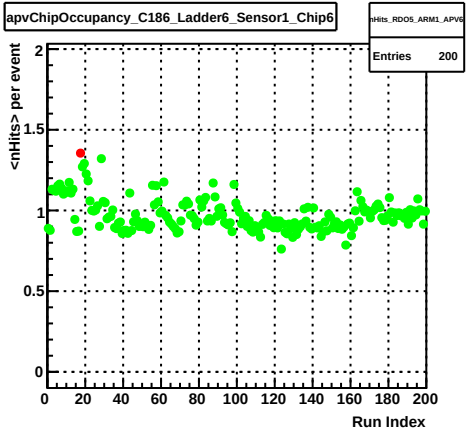
Run Index


apvChipOccupancy_C186_Ladder6_Sensor1_Chip6
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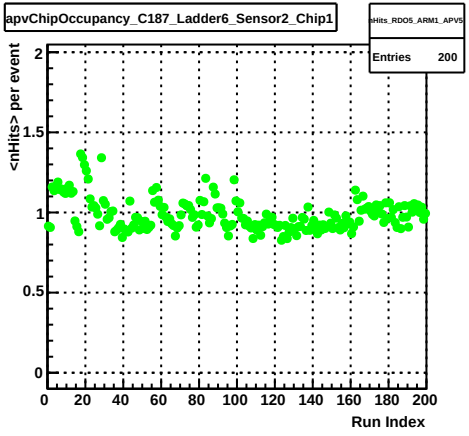
Run Index


apvChipOccupancy_C187_Ladder6_Sensor2_Chip1
nHits_RD05_ARM1_APV5
Entries 200
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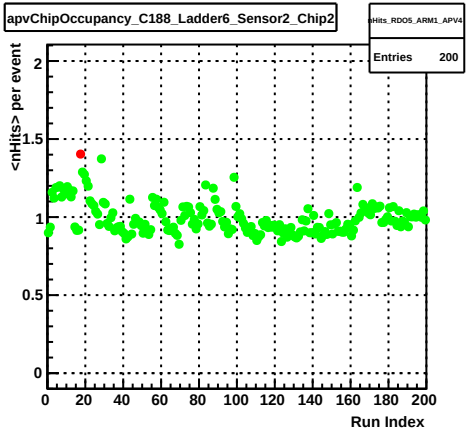
Run Index


apvChipOccupancy_C188_Ladder6_Sensor2_Chip2
nHits_RD05_ARM1_APV4
Entries 200
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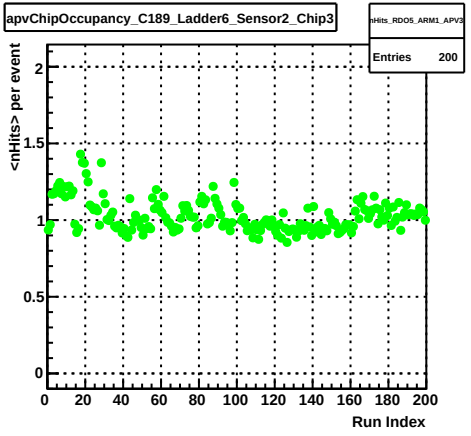
Run Index


apvChipOccupancy_C189_Ladder6_Sensor2_Chip3
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Entries 200
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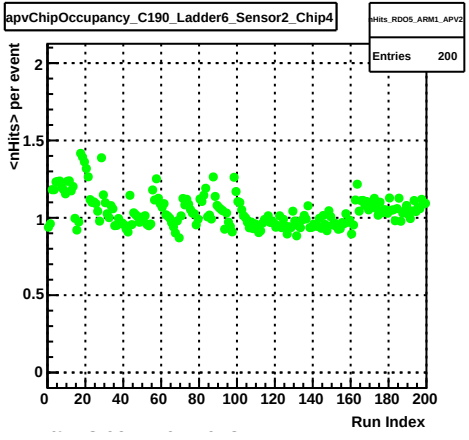
Run Index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nHits_RD05_ARM1_APV2
Entries 200
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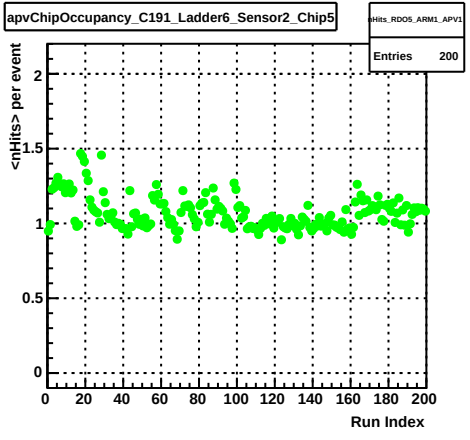
Run Index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nHits_RD05_ARM1_APV1
Entries 200
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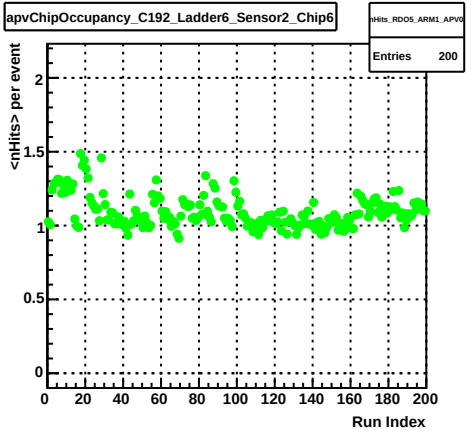
Run Index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Entries 200
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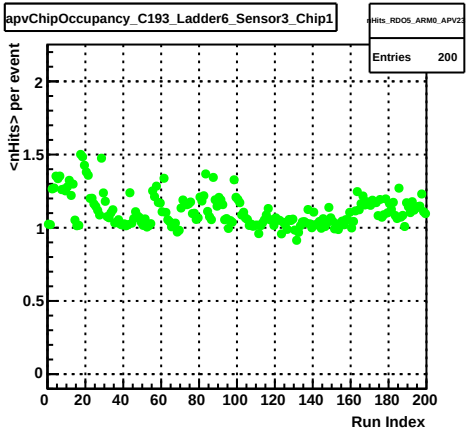
Run Index


apvChipOccupancy_C193_Ladder6_Sensor3_Chip1
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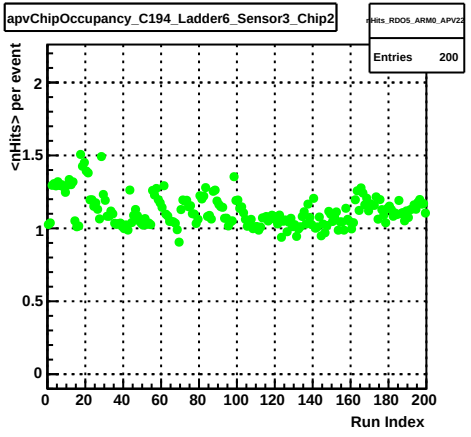
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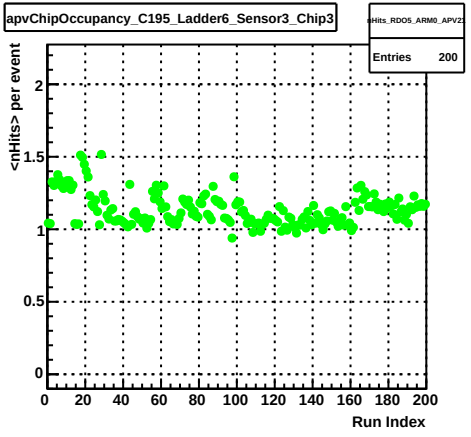
apvChipOccupancy_C195_Ladder6_Sensor3_Chip3
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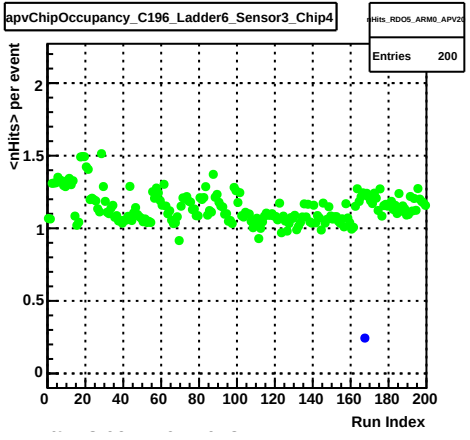
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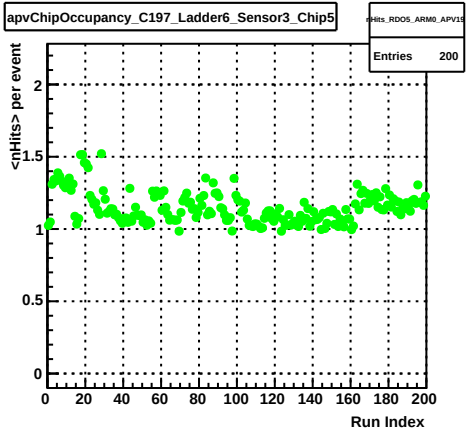
apvChipOccupancy_C197_Ladder6_Sensor3_Chip5
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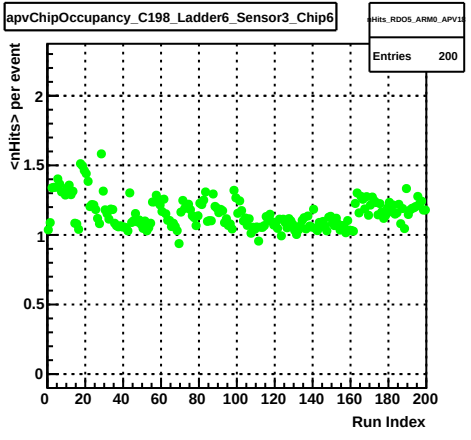
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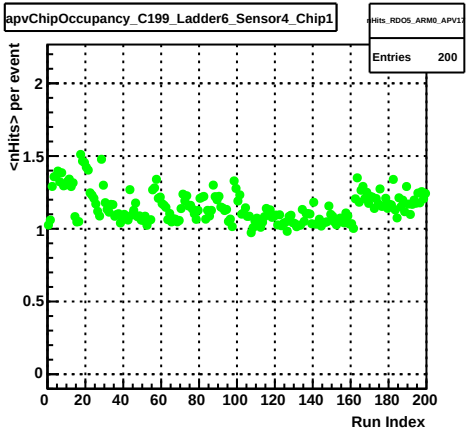
apvChipOccupancy_C199_Ladder6_Sensor4_Chip1
nHits_RDO5_ARM0_APV13
Entries 200
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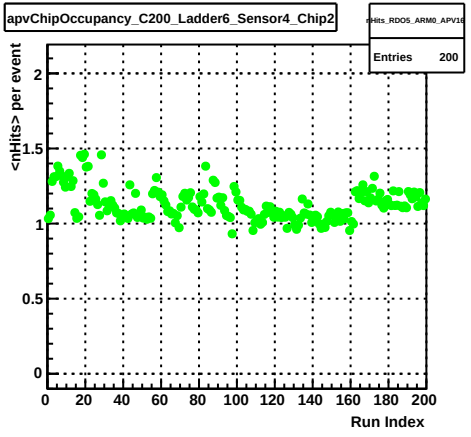
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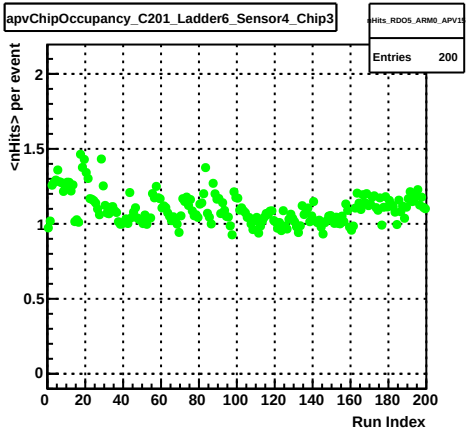
apvChipOccupancy_C201_Ladder6_Sensor4_Chip3
nHits_RDO5_ARM0_APV19
Entries 200
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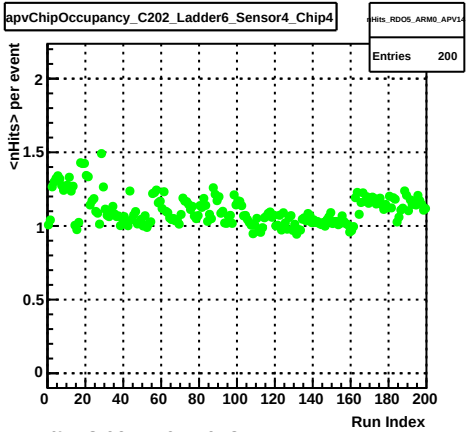
apvChipOccupancy_C202_Ladder6_Sensor4_Chip4
nHits_RDO5_ARM0_APV14
Entries 200
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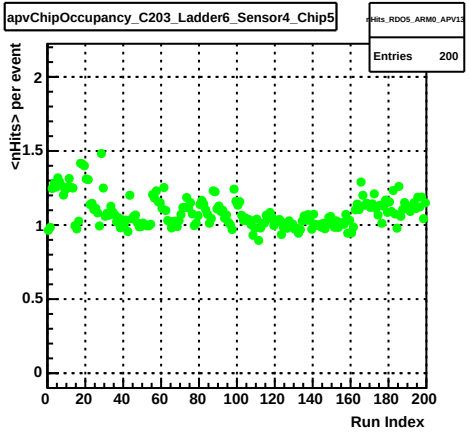
apvChipOccupancy_C203_Ladder6_Sensor4_Chip5
nHits_RDO5_ARM0_APV17
Entries 200
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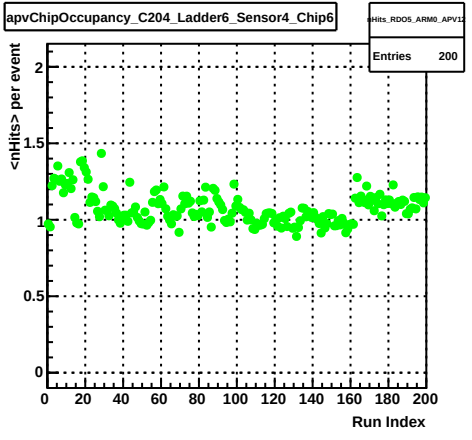
Run Index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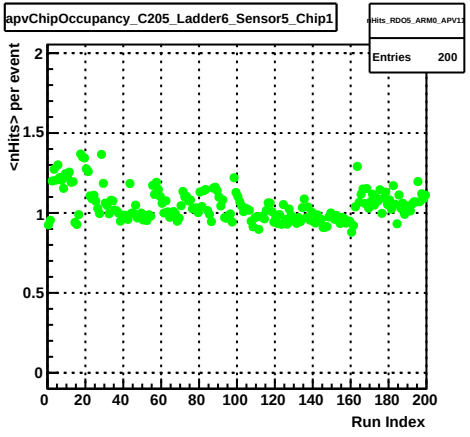
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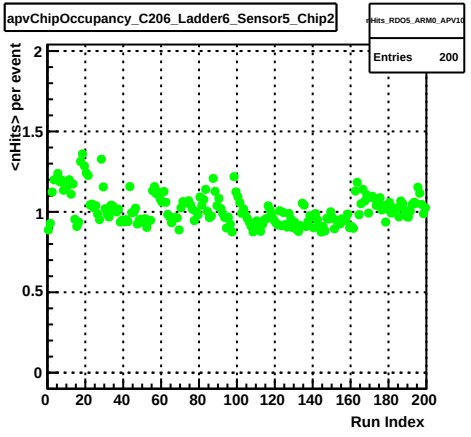
Run Index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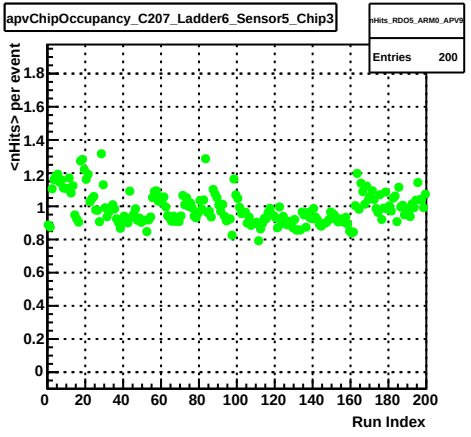
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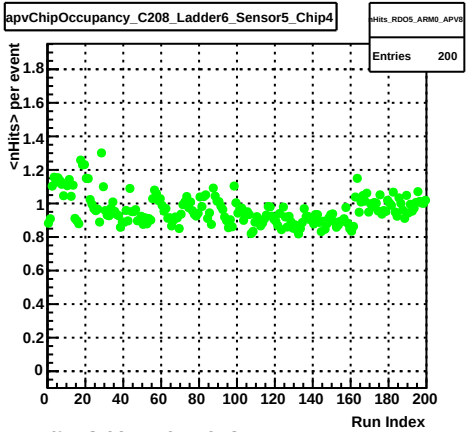
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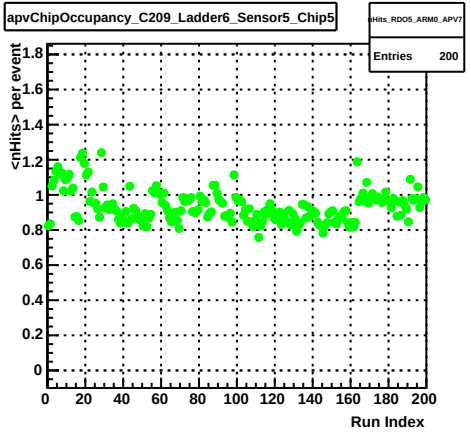
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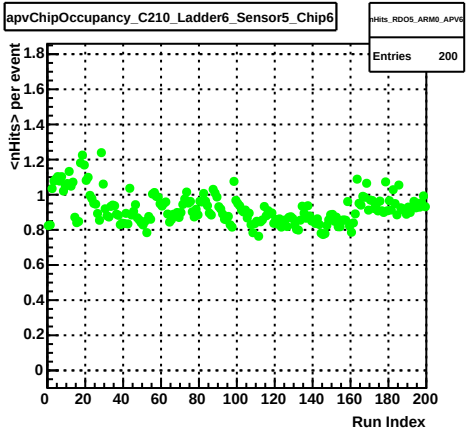
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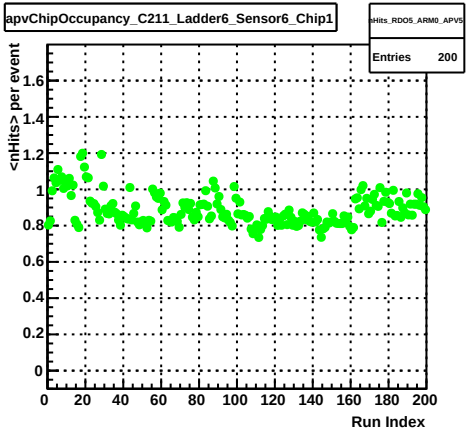
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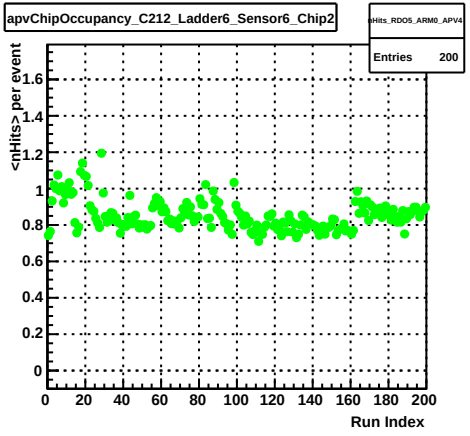
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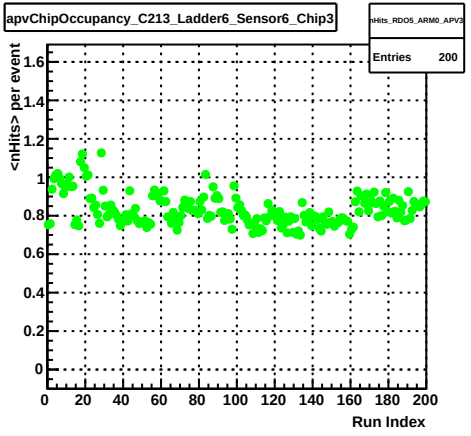
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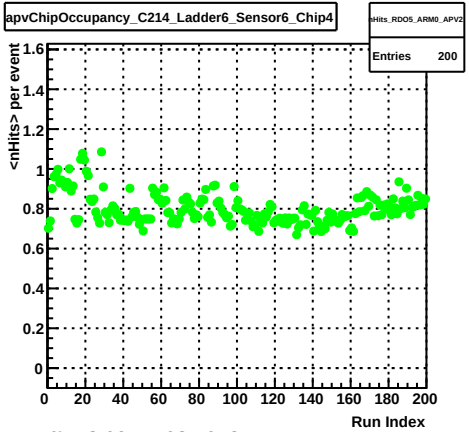
Run Index


apvChipOccupancy_C214_Ladder6_Sensor6_Chip4
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Entries 200
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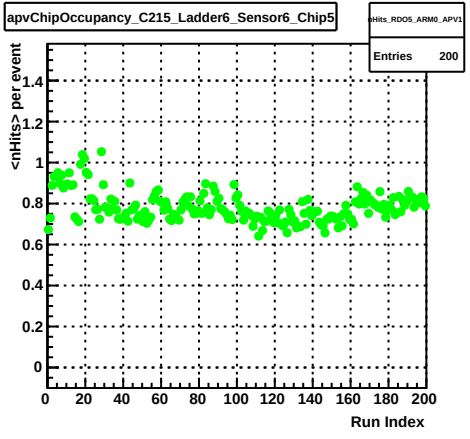
Run Index


apvChipOccupancy_C215_Ladder6_Sensor6_Chip5
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Entries 200
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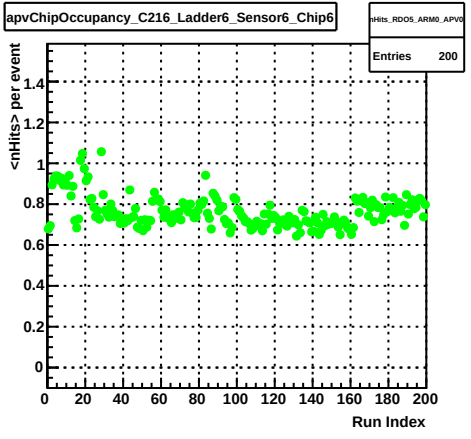
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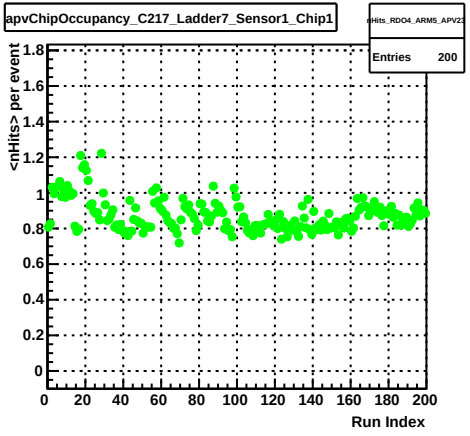
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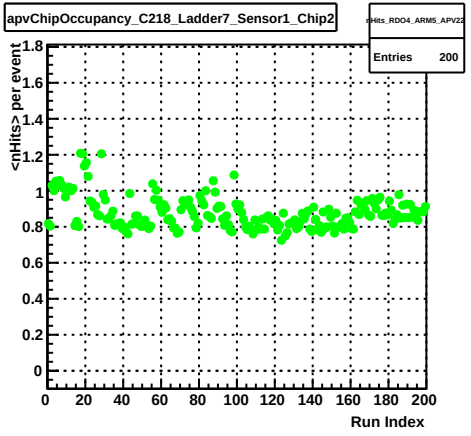
apvChipOccupancy_C218_Ladder7_Sensor1_Chip2
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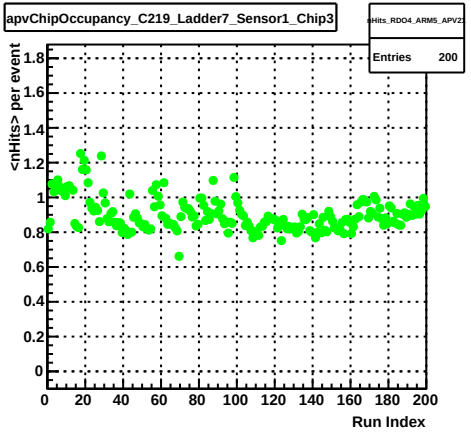
apvChipOccupancy_C219_Ladder7_Sensor1_Chip3
Entries 200
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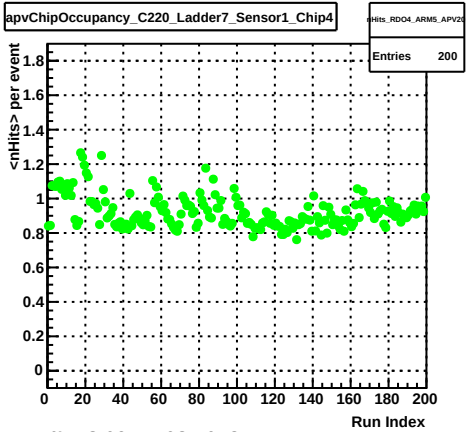
Run Index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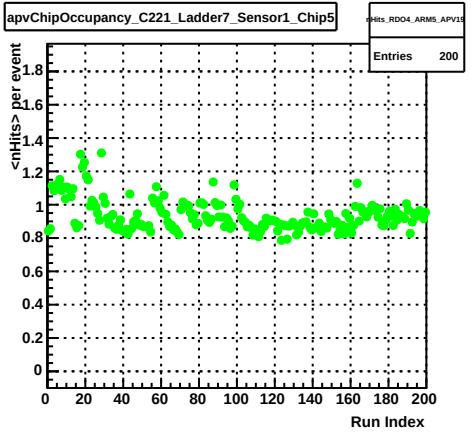
Run Index


apvChipOccupancy_C221_Ladder7_Sensor1_Chip5
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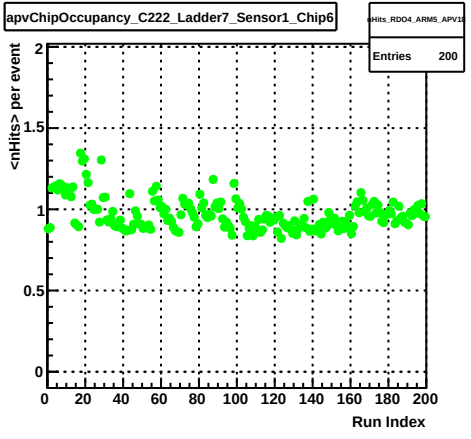
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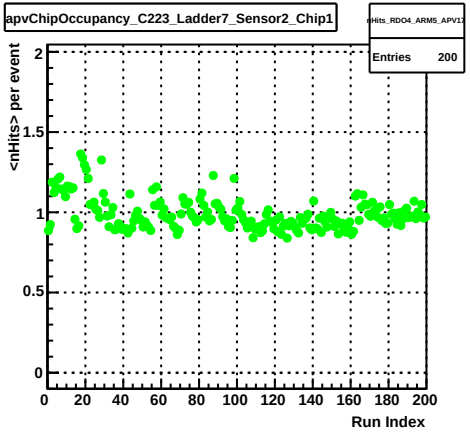
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nHits_RDO4_ARM5_APV13
Entries 200
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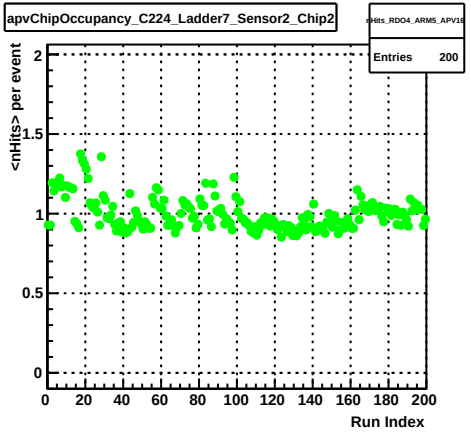
Run Index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nHits_RDO4_ARM5_APV14
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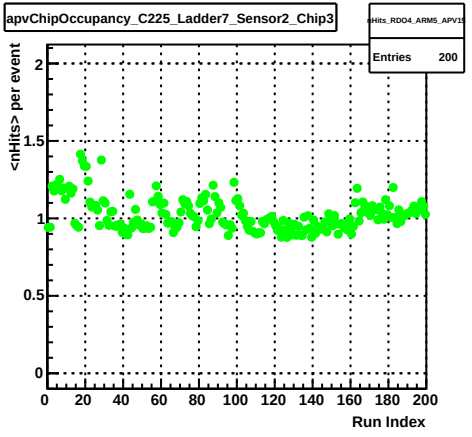
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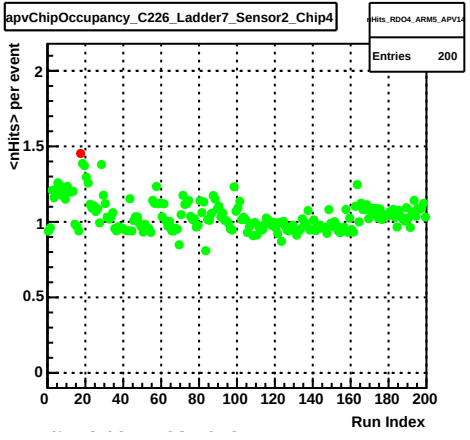
Run Index


apvChipOccupancy_C226_Ladder7_Sensor2_Chip4
nHits_RDO4_ARM5_APV16
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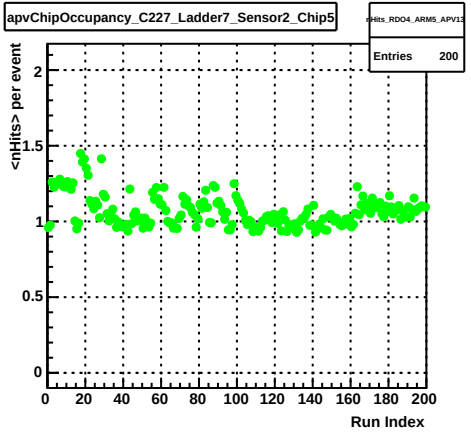
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nHits_RDO4_ARM5_APV17
Entries 200
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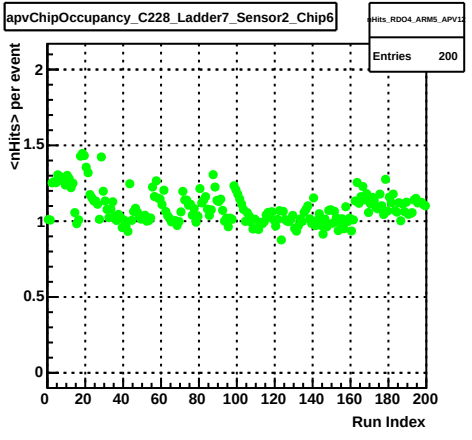
Run Index


apvChipOccupancy_C228_Ladder7_Sensor2_Chip6
nHits_RDO4_ARM5_APV18
Entries 200
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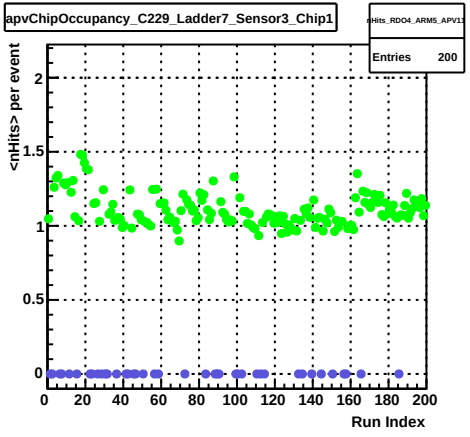
Run Index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Entries 200
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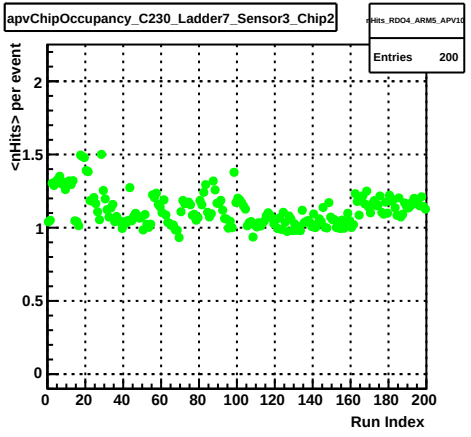
Run Index


apvChipOccupancy_C230_Ladder7_Sensor3_Chip2
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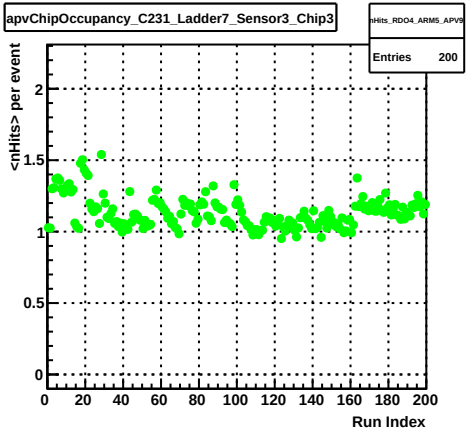
Run Index


apvChipOccupancy_C231_Ladder7_Sensor3_Chip3
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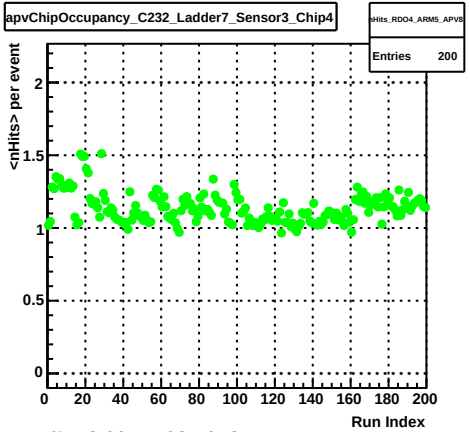
Run Index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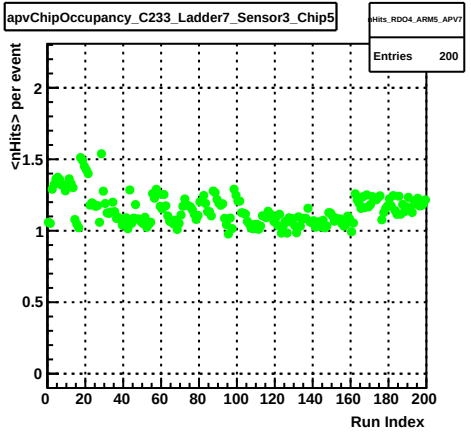
Run Index


apvChipOccupancy_C233_Ladder7_Sensor3_Chip5
Entries 200
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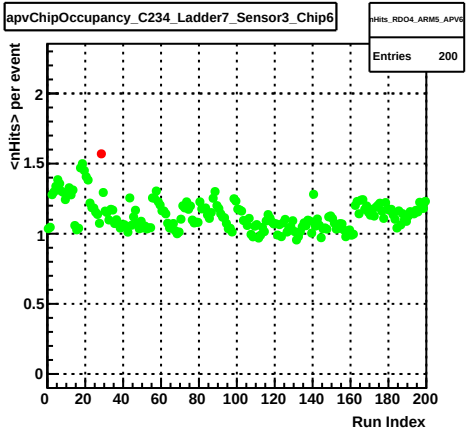
Run Index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Entries 200
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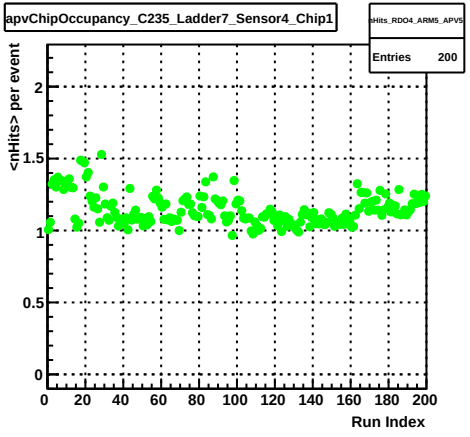
Run Index


apvChipOccupancy_C235_Ladder7_Sensor4_Chip1
nHits_RDO4_ARM5_APV5
Entries 200
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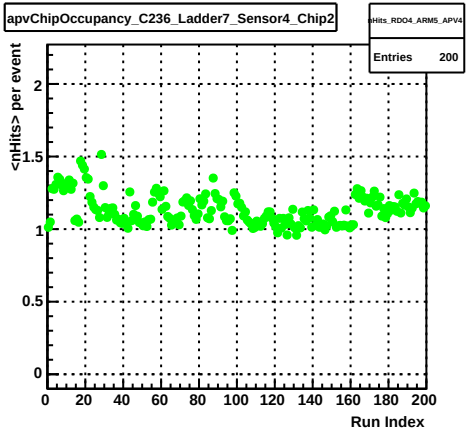
Run Index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Entries 200
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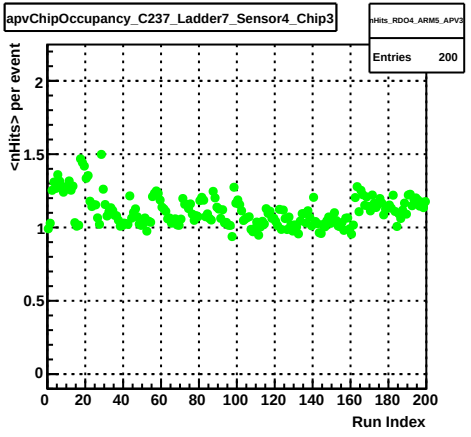
Run Index


apvChipOccupancy_C237_Ladder7_Sensor4_Chip3
nHits_RDO4_ARM5_APV3
Entries 200
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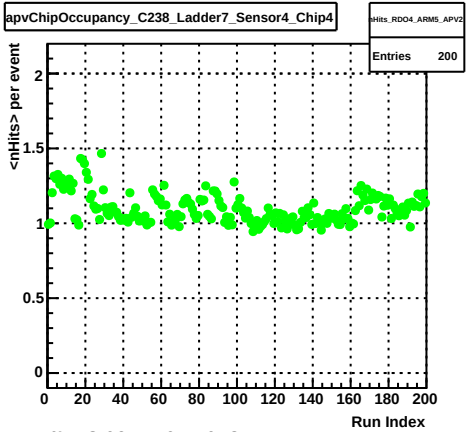
Run Index


apvChipOccupancy_C238_Ladder7_Sensor4_Chip4
nHits_RDO4_ARM5_APV2
Entries 200
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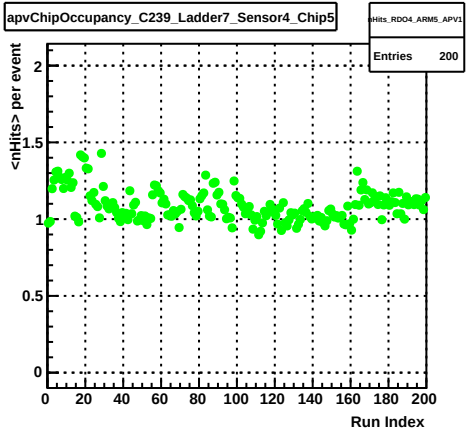
Run Index


apvChipOccupancy_C239_Ladder7_Sensor4_Chip5
nHits_RDO4_ARM5_APV1
Entries 200
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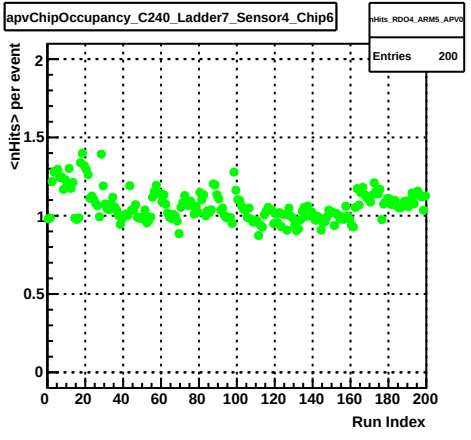
Run Index


apvChipOccupancy_C240_Ladder7_Sensor4_Chip6
nHits_RDO4_ARM5_APV0
Entries 200
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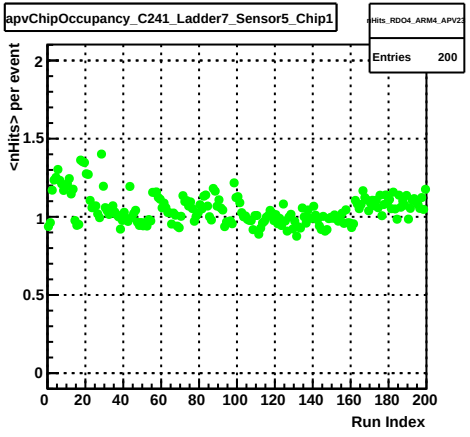
Run Index


apvChipOccupancy_C241_Ladder7_Sensor5_Chip1
Entries 200
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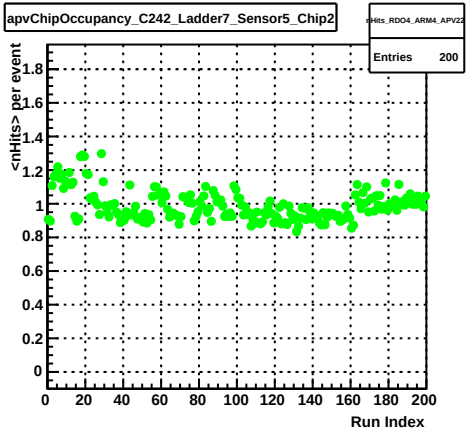
Run Index


apvChipOccupancy_C242_Ladder7_Sensor5_Chip2
Entries 200
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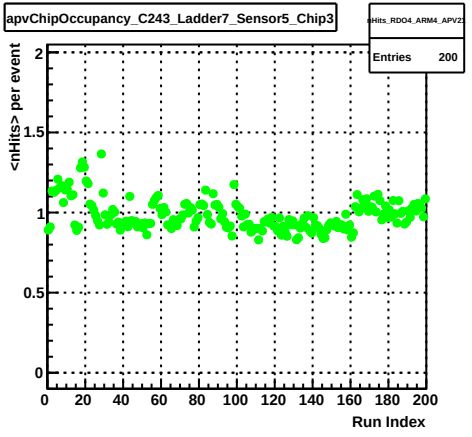
Run Index


apvChipOccupancy_C243_Ladder7_Sensor5_Chip3
Entries 200
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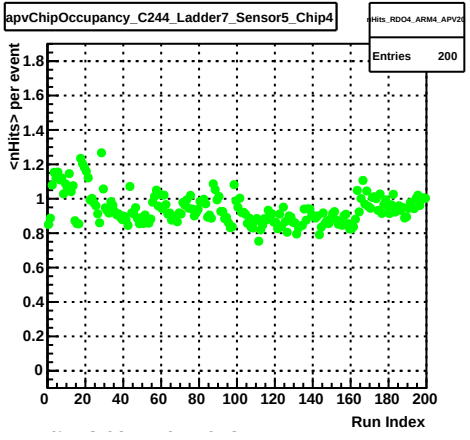
Run Index


apvChipOccupancy_C244_Ladder7_Sensor5_Chip4
Entries 200
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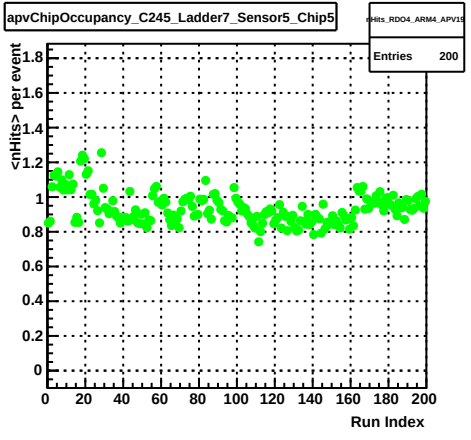
Run Index


apvChipOccupancy_C245_Ladder7_Sensor5_Chip5
Entries 200
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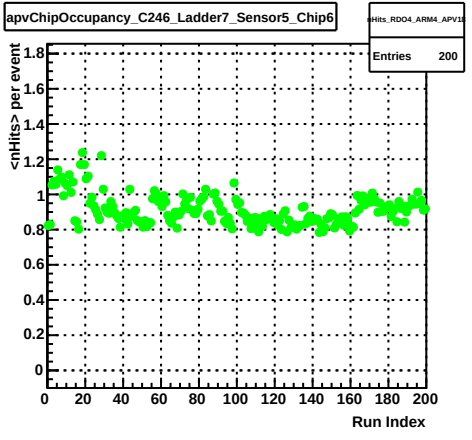
Run Index


apvChipOccupancy_C246_Ladder7_Sensor5_Chip6
Entries 200
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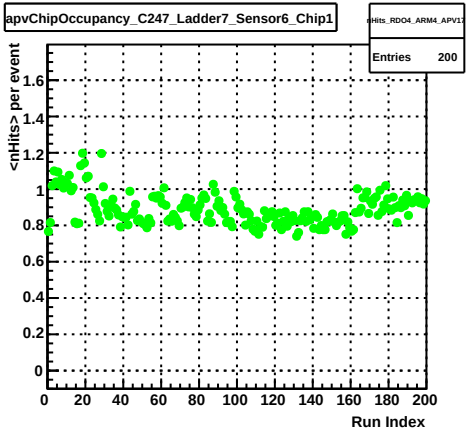
Run Index


apvChipOccupancy_C247_Ladder7_Sensor6_Chip1
Entries 200
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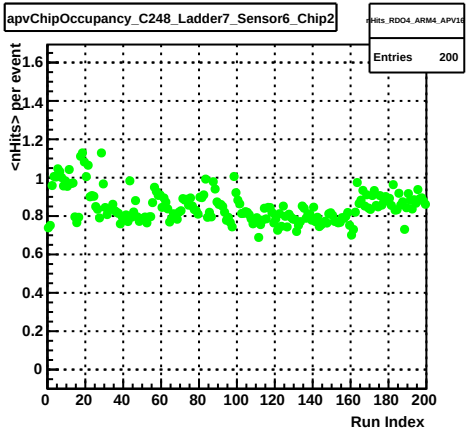
Run Index


apvChipOccupancy_C248_Ladder7_Sensor6_Chip2
Entries 200
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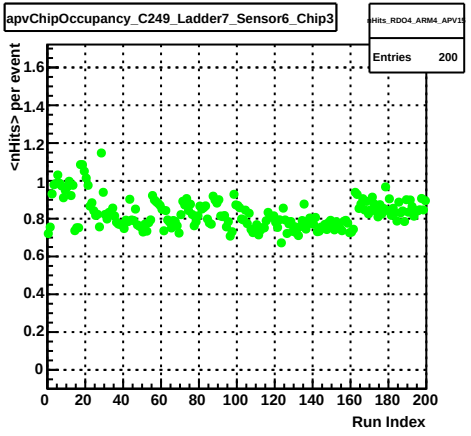
Run Index


apvChipOccupancy_C249_Ladder7_Sensor6_Chip3
Entries 200
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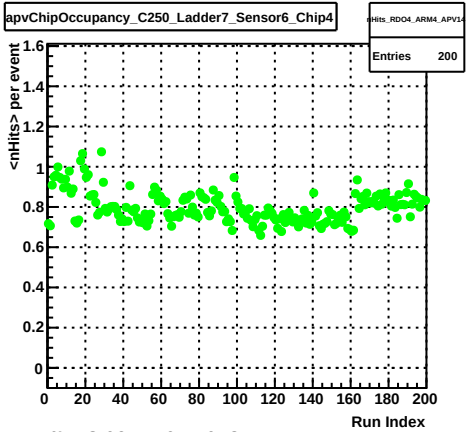
Run Index


apvChipOccupancy_C250_Ladder7_Sensor6_Chip4
Entries 200
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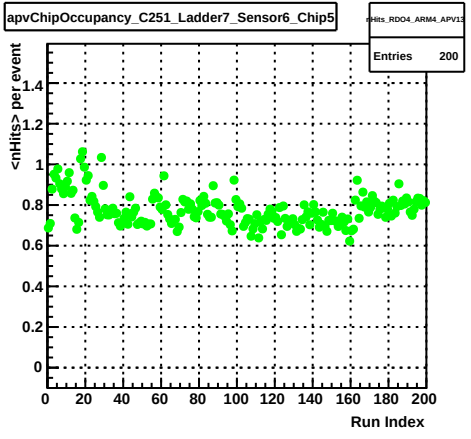
Run Index


apvChipOccupancy_C251_Ladder7_Sensor6_Chip5
Entries 200
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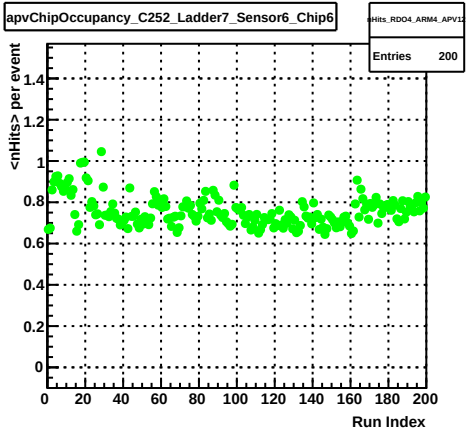
Run Index


apvChipOccupancy_C252_Ladder7_Sensor6_Chip6
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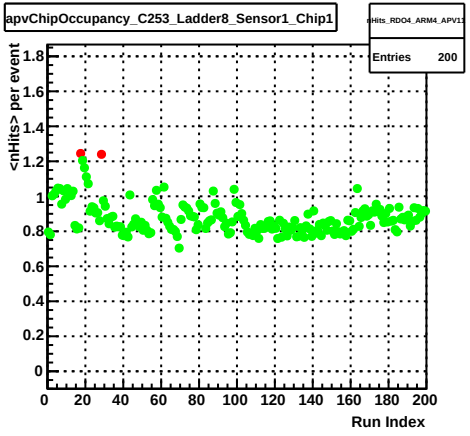
Run Index


apvChipOccupancy_C253_Ladder8_Sensor1_Chip1
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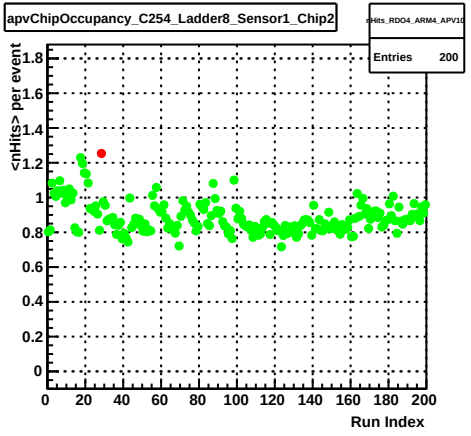
Run Index


apvChipOccupancy_C254_Ladder8_Sensor1_Chip2
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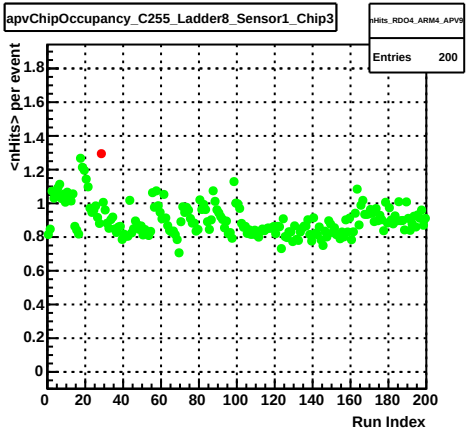
Run Index


apvChipOccupancy_C255_Ladder8_Sensor1_Chip3
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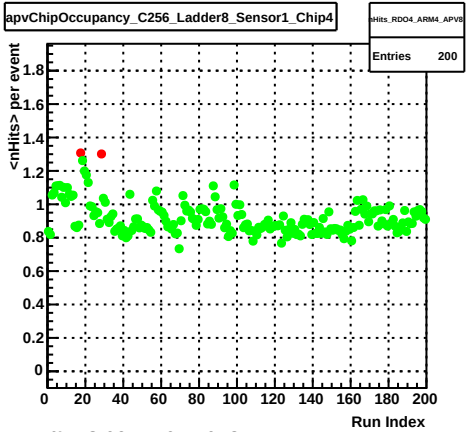
Run Index


apvChipOccupancy_C256_Ladder8_Sensor1_Chip4
Entries 200
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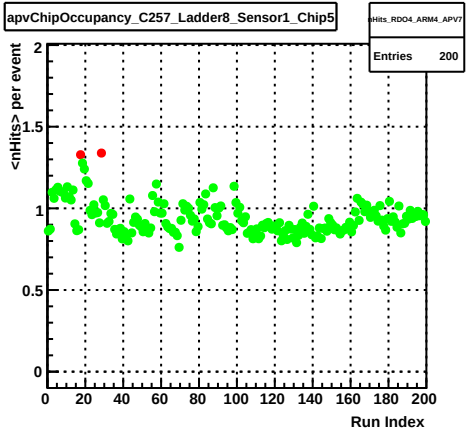
Run Index


apvChipOccupancy_C257_Ladder8_Sensor1_Chip5
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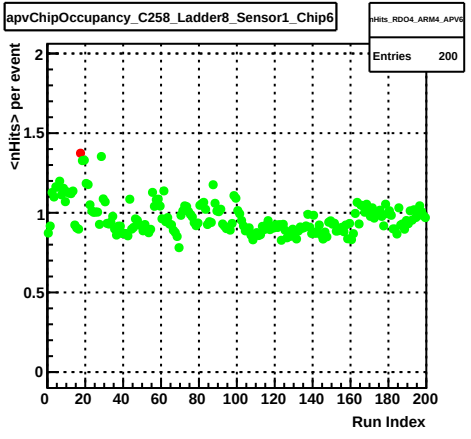
Run Index


apvChipOccupancy_C258_Ladder8_Sensor1_Chip6
Entries 200
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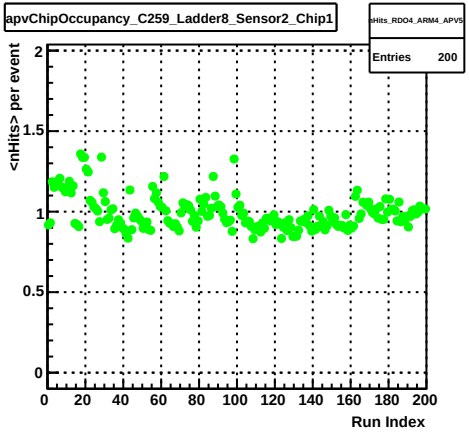
Run Index


apvChipOccupancy_C259_Ladder8_Sensor2_Chip1
nHits_RDO4_ARM4_APV5
Entries 200
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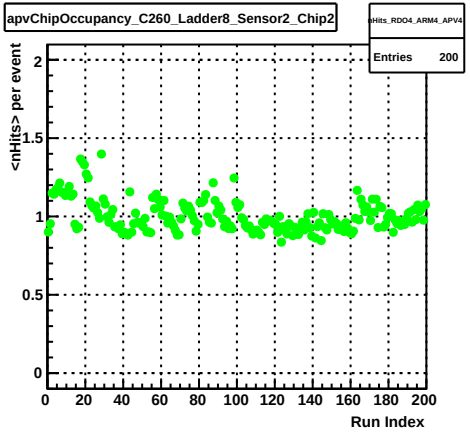
Run Index


apvChipOccupancy_C260_Ladder8_Sensor2_Chip2
nHits_RDO4_ARM4_APV4
Entries 200
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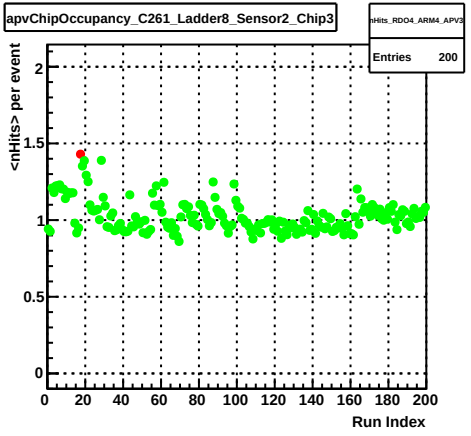
Run Index


apvChipOccupancy_C261_Ladder8_Sensor2_Chip3
nHits_RDO4_ARM4_APV3
Entries 200
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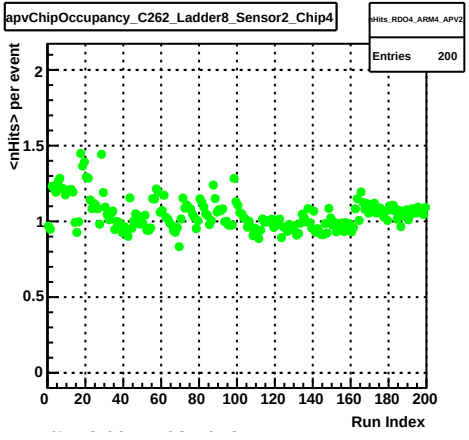
Run Index


apvChipOccupancy_C262_Ladder8_Sensor2_Chip4
nHits_RDO4_ARM4_APV2
Entries 200
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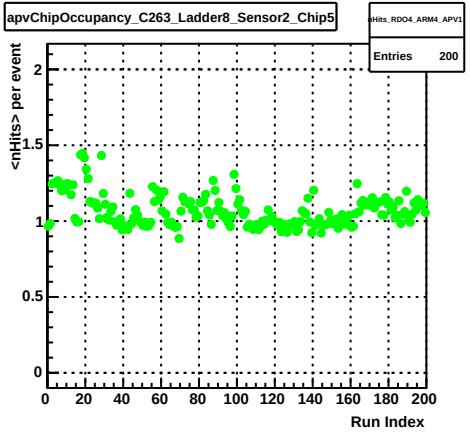
Run Index


apvChipOccupancy_C263_Ladder8_Sensor2_Chip5
nHits_RDO4_ARM4_APV1
Entries 200
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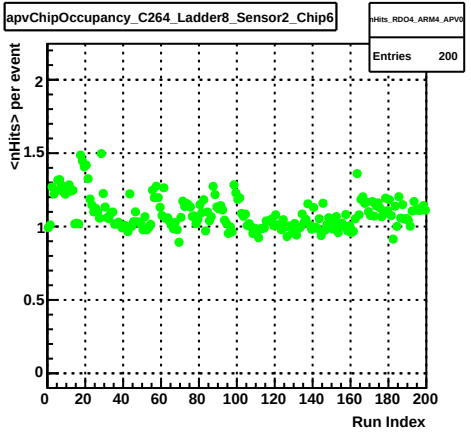
Run Index


apvChipOccupancy_C264_Ladder8_Sensor2_Chip6
nHits_RDO4_ARM4_APV0
Entries 200
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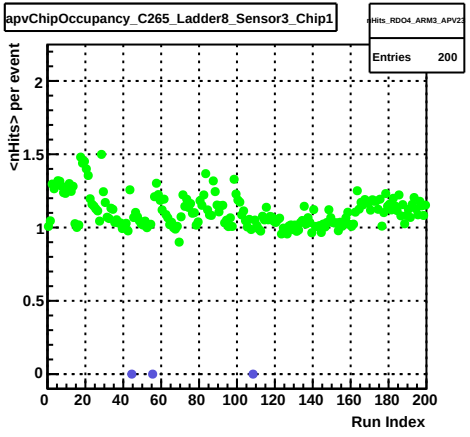
Run Index


apvChipOccupancy_C265_Ladder8_Sensor3_Chip1
Entries 200
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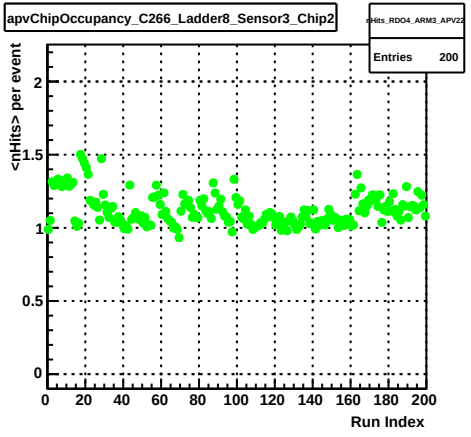
Run Index


apvChipOccupancy_C266_Ladder8_Sensor3_Chip2
Entries 200
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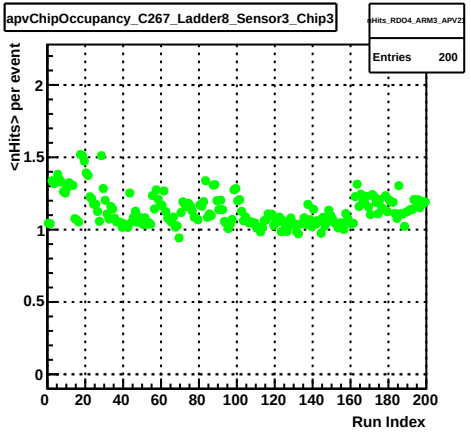
Run Index


apvChipOccupancy_C267_Ladder8_Sensor3_Chip3
Entries 200
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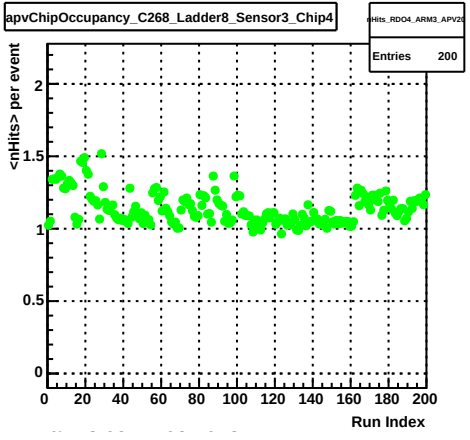
Run Index


apvChipOccupancy_C268_Ladder8_Sensor3_Chip4
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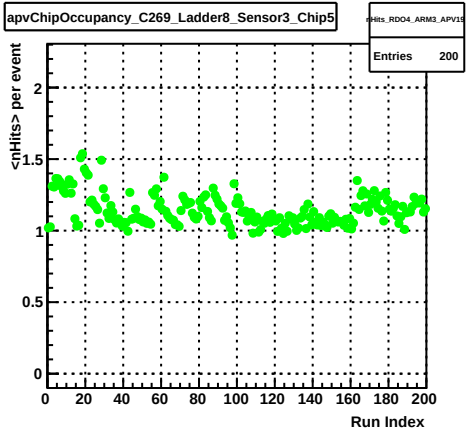
Run Index


apvChipOccupancy_C269_Ladder8_Sensor3_Chip5
Entries 200
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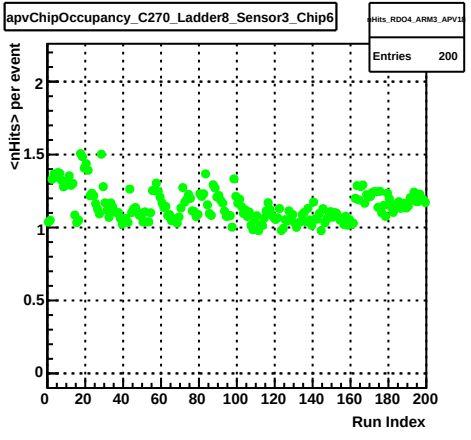
Run Index


apvChipOccupancy_C270_Ladder8_Sensor3_Chip6
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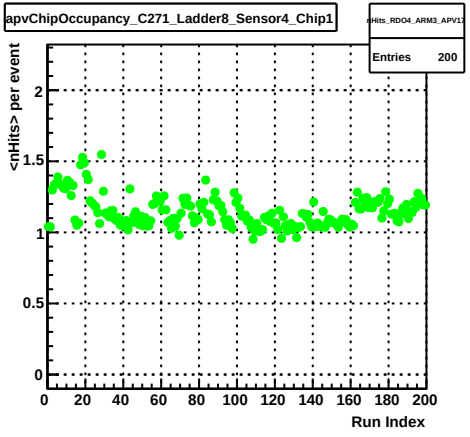
apvChipOccupancy_C271_Ladder8_Sensor4_Chip1
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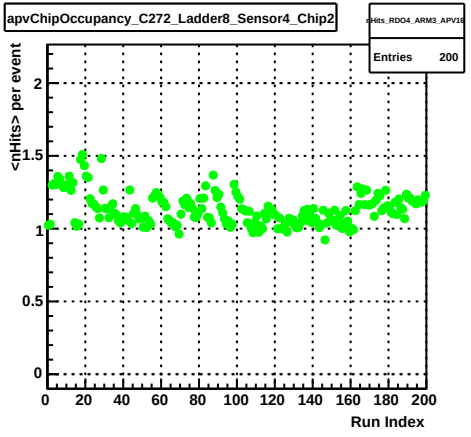
apvChipOccupancy_C272_Ladder8_Sensor4_Chip2
Entries 200
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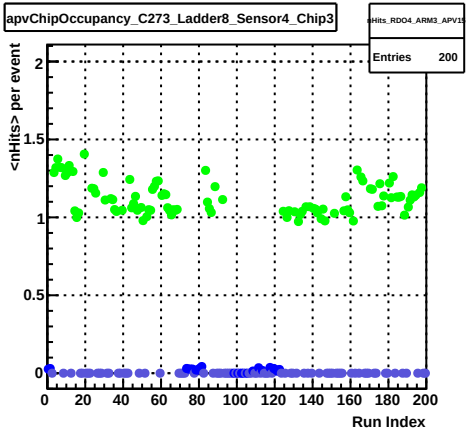
Run Index


apvChipOccupancy_C273_Ladder8_Sensor4_Chip3
Entries 200
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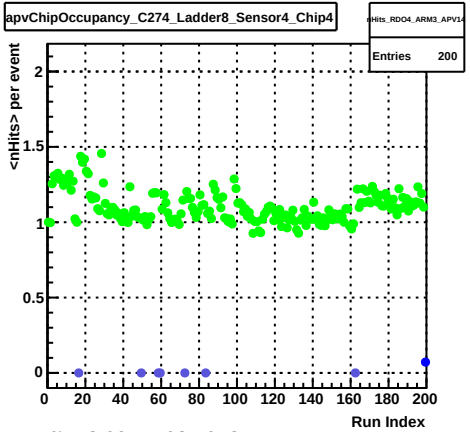
Run Index


apvChipOccupancy_C274_Ladder8_Sensor4_Chip4
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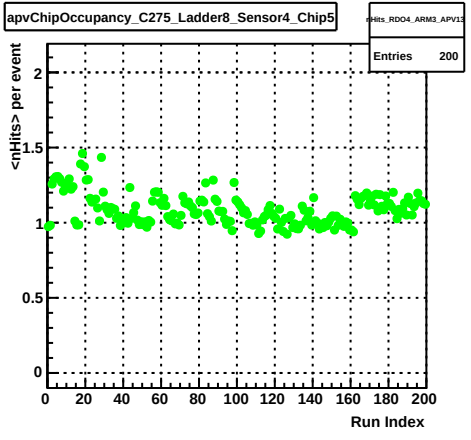
Run Index


apvChipOccupancy_C275_Ladder8_Sensor4_Chip5
Entries 200
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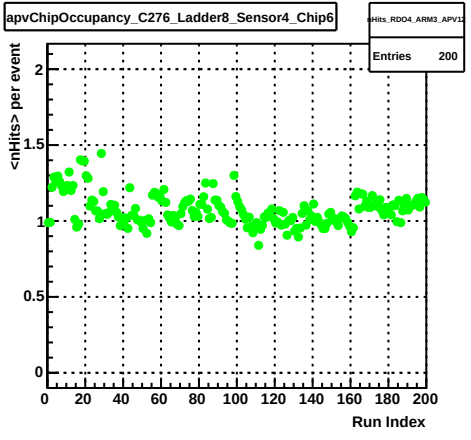
Run Index


apvChipOccupancy_C276_Ladder8_Sensor4_Chip6
Entries 200
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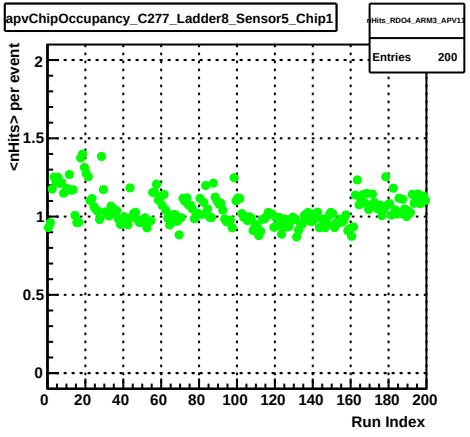
Run Index


apvChipOccupancy_C277_Ladder8_Sensor5_Chip1
Entries 200
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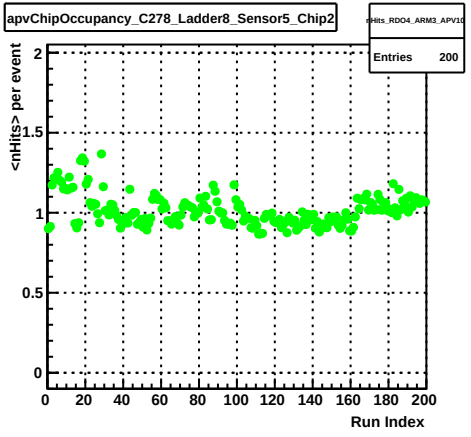
Run Index


apvChipOccupancy_C278_Ladder8_Sensor5_Chip2
Entries 200
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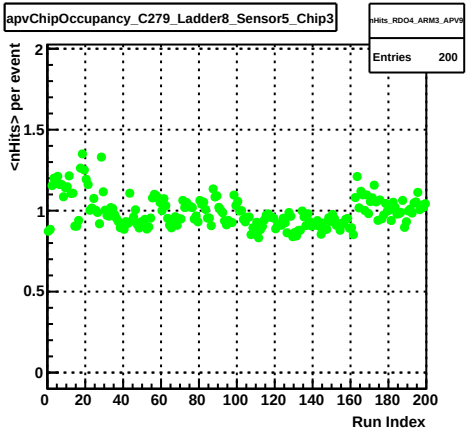
Run Index


apvChipOccupancy_C279_Ladder8_Sensor5_Chip3
Entries 200
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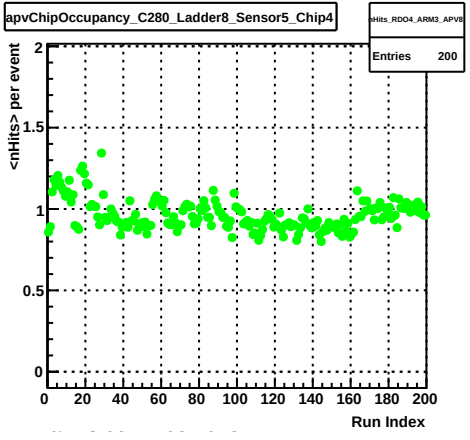
Run Index


apvChipOccupancy_C280_Ladder8_Sensor5_Chip4
Entries 200
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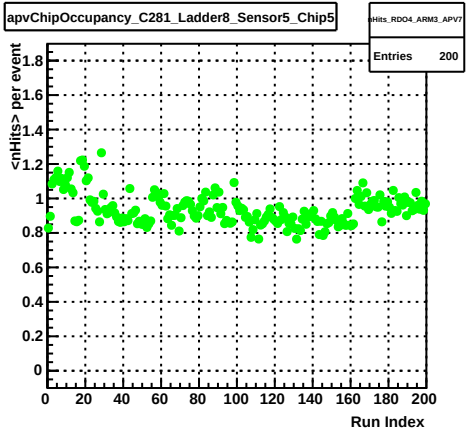
Run Index


apvChipOccupancy_C281_Ladder8_Sensor5_Chip5
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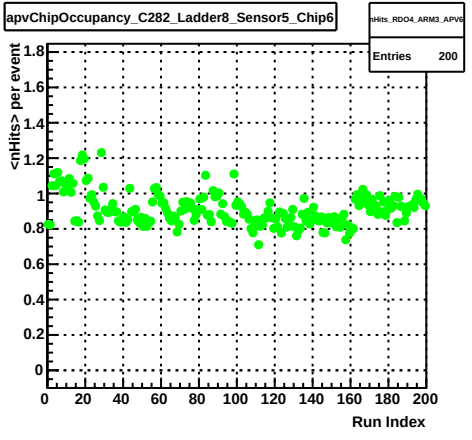
Run Index


apvChipOccupancy_C282_Ladder8_Sensor5_Chip6
Entries 200
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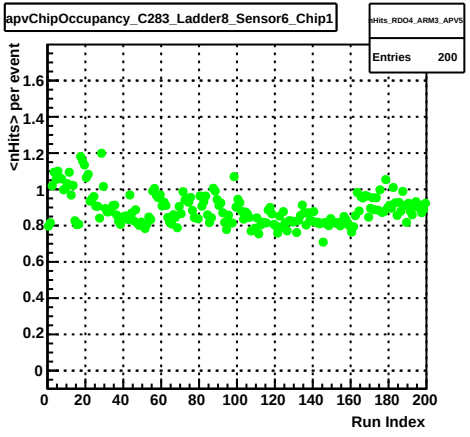
Run Index


apvChipOccupancy_C283_Ladder8_Sensor6_Chip1
nHits_RDO4_ARM3_APV5
Entries 200
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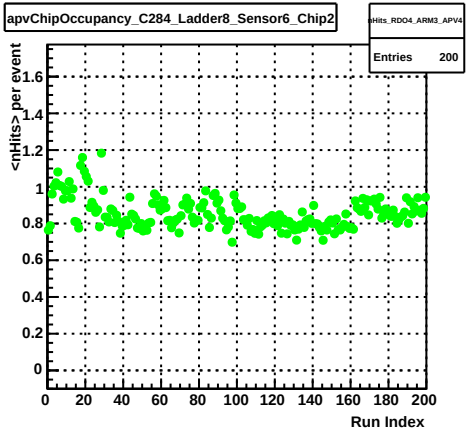
Run Index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nHits_RDO4_ARM3_APV4
Entries 200
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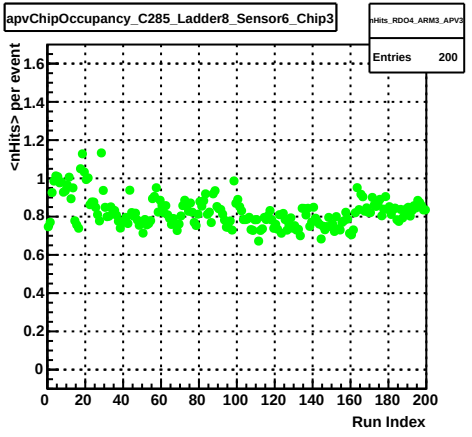
Run Index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Entries 200
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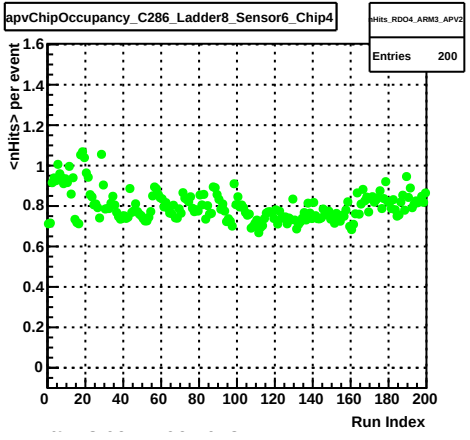
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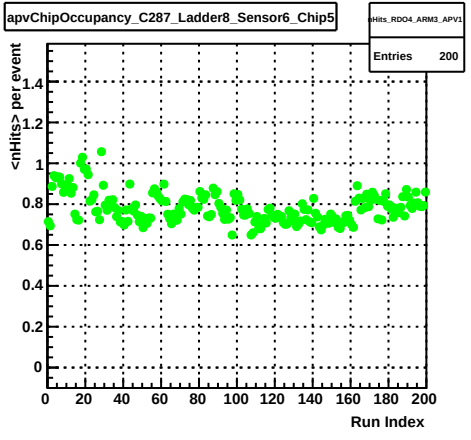
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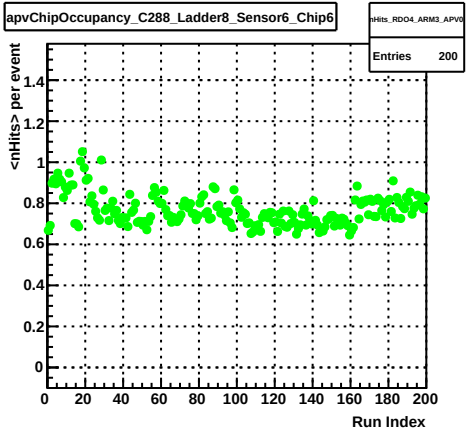
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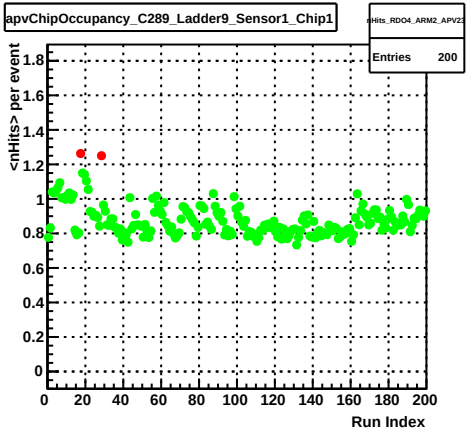
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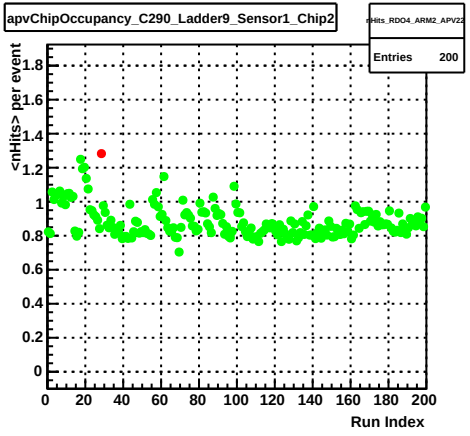
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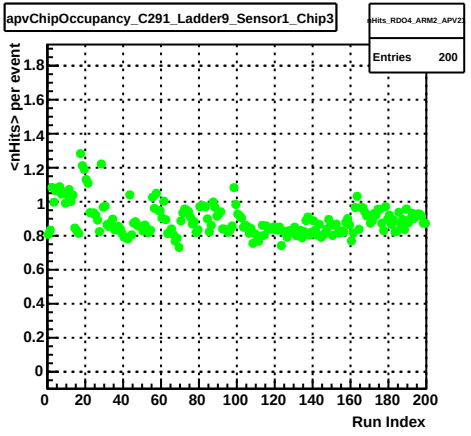
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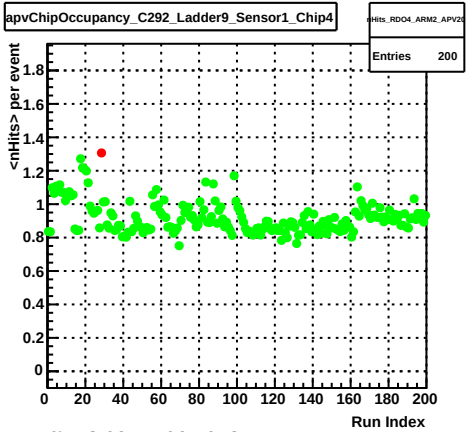
apvChipOccupancy_C292_Ladder9_Sensor1_Chip4
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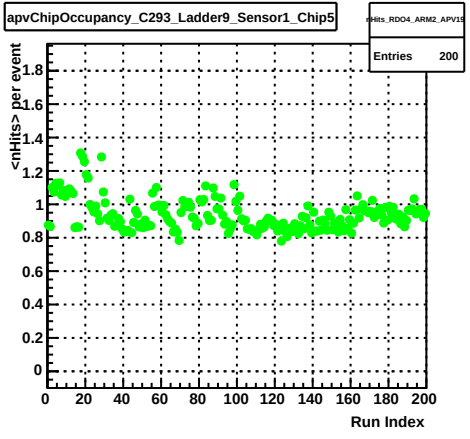
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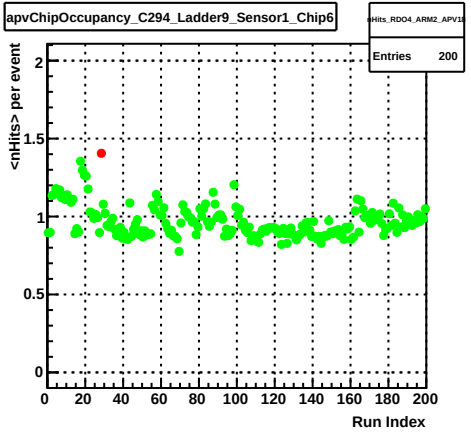
apvChipOccupancy_C294_Ladder9_Sensor1_Chip6
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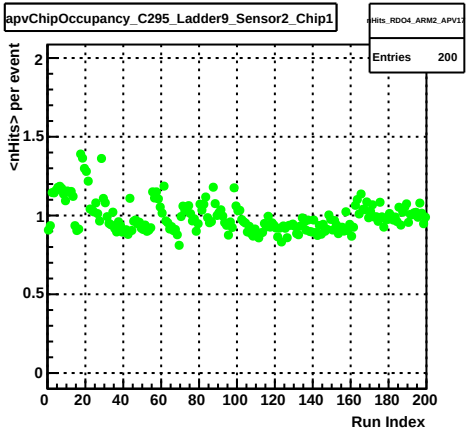
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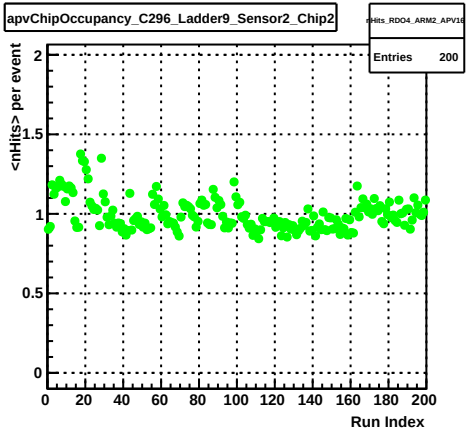
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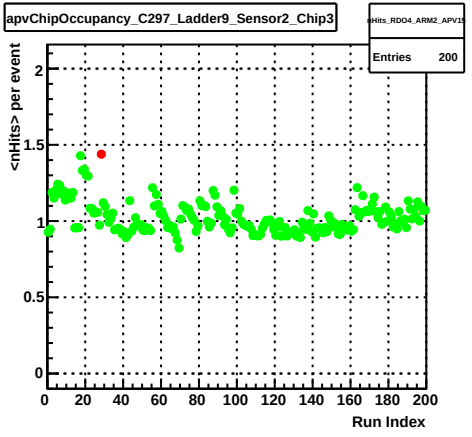
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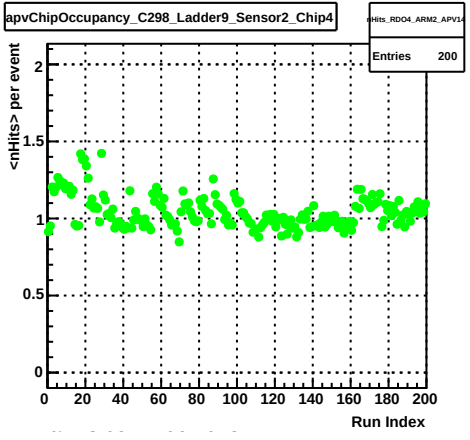
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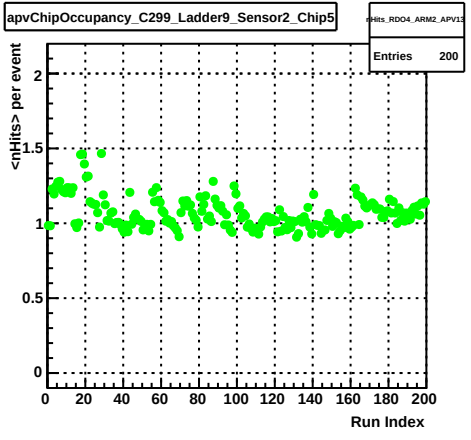
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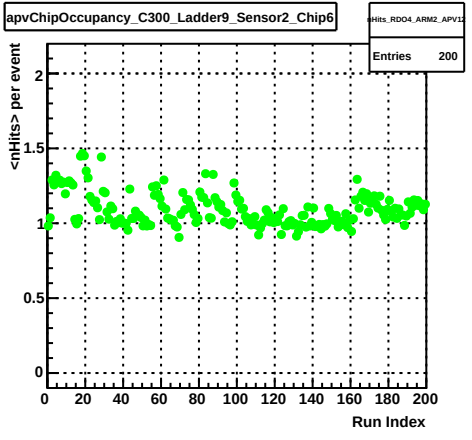
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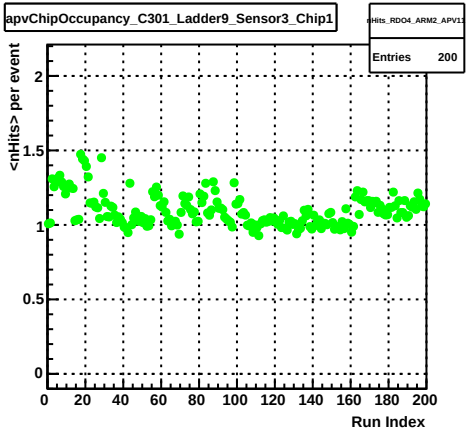
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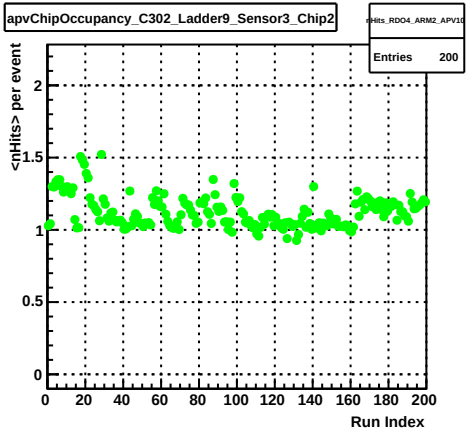
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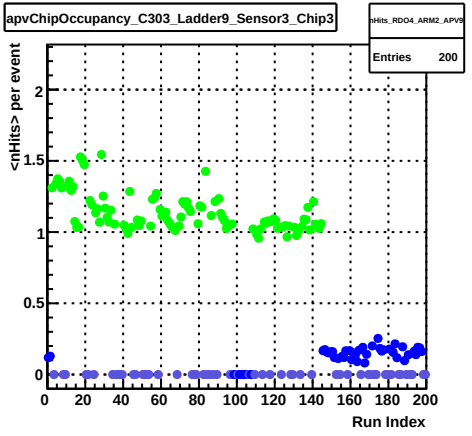
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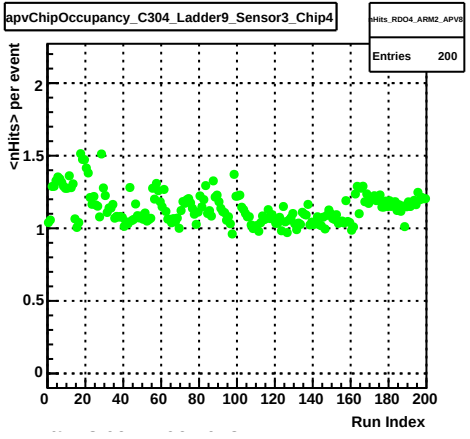
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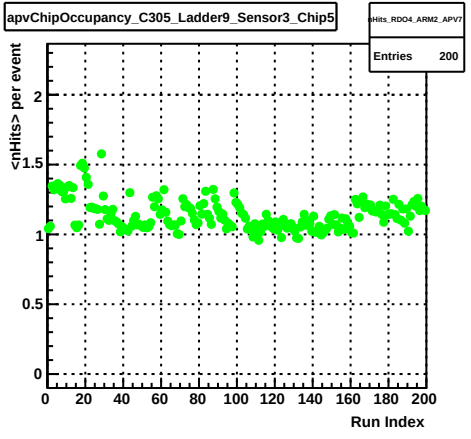
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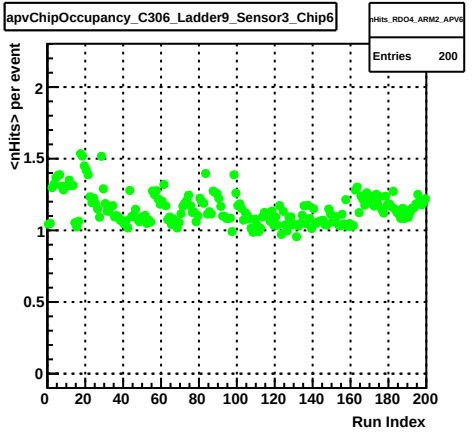
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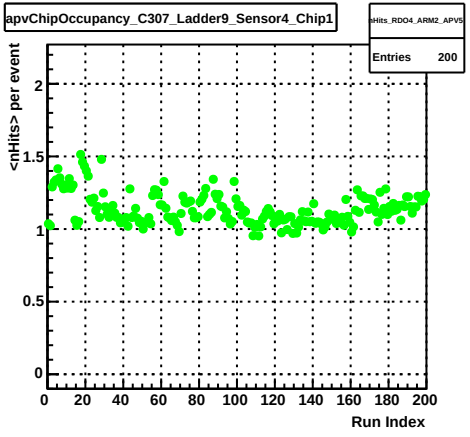
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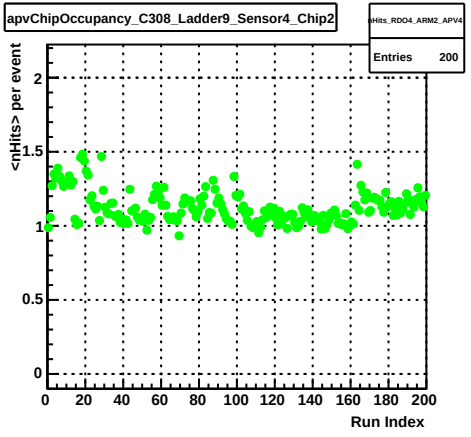
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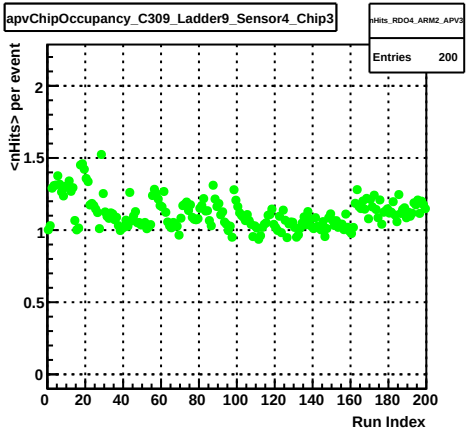
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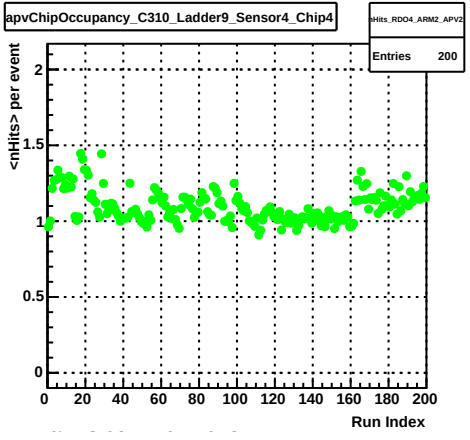
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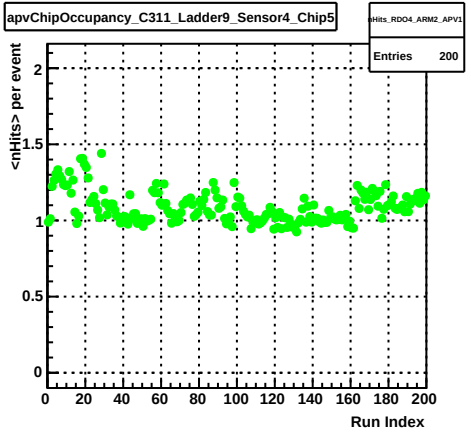
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Entries 200
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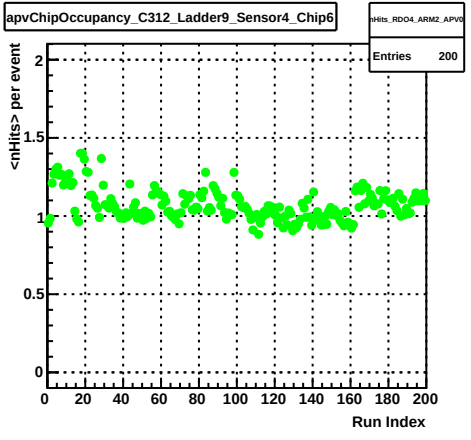
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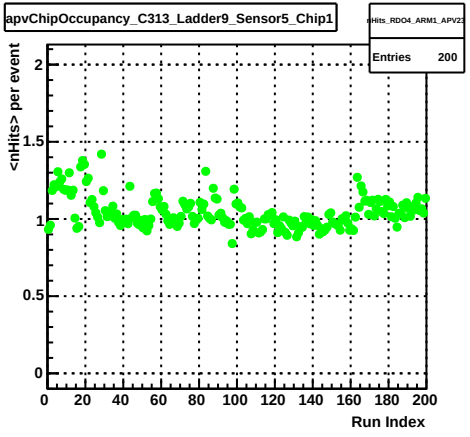
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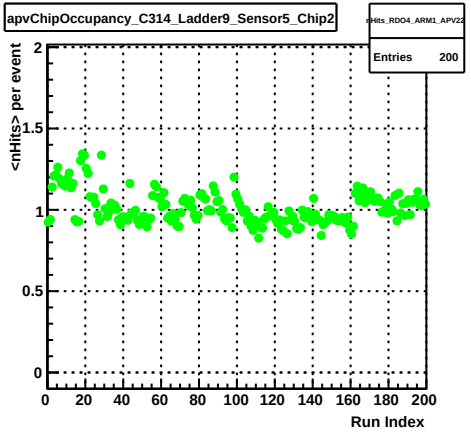
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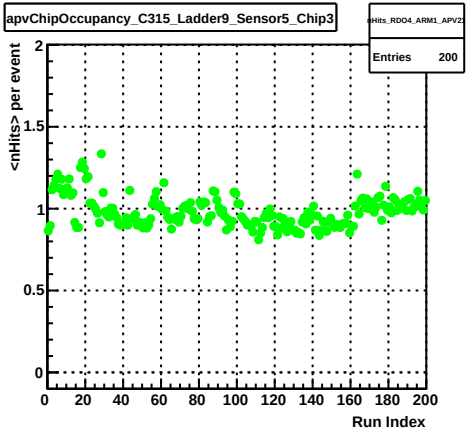
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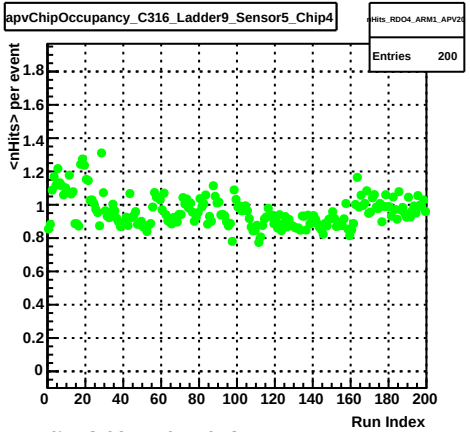
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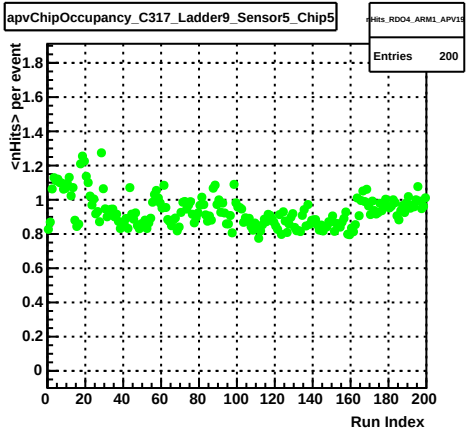
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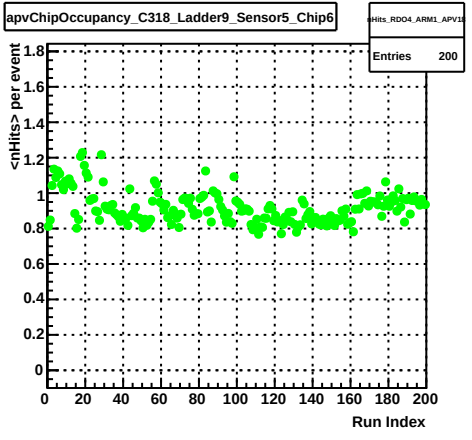
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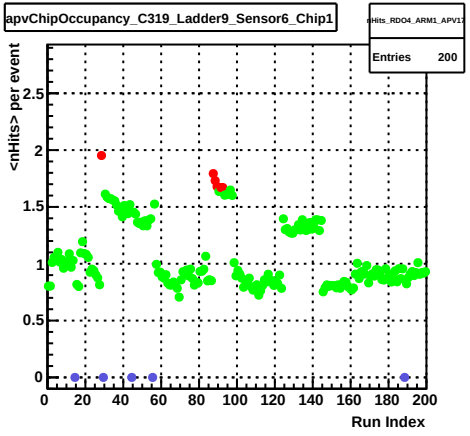
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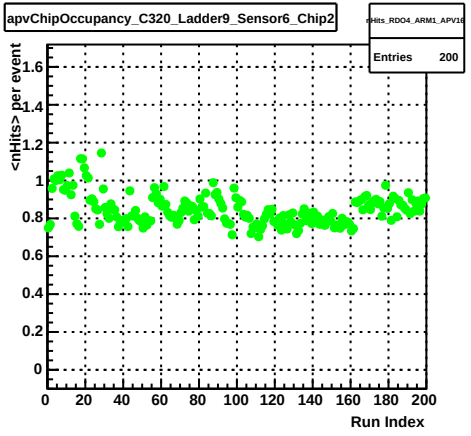
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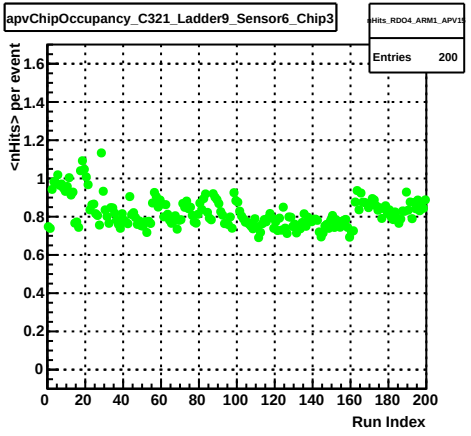
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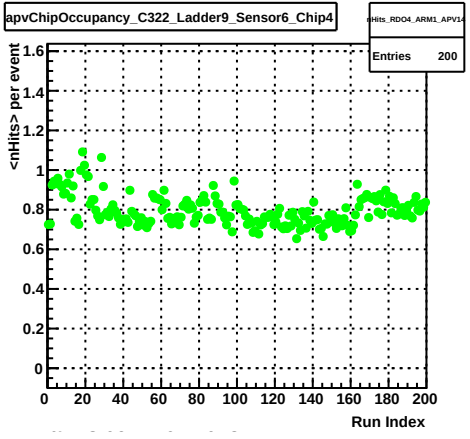
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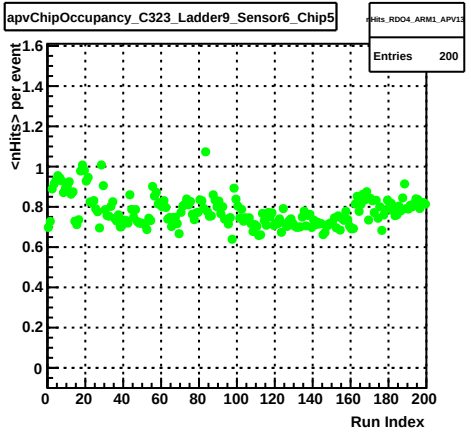
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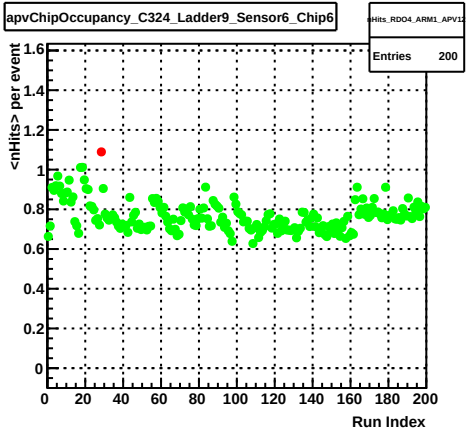
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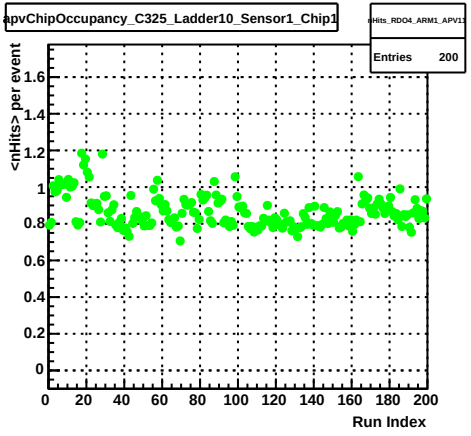
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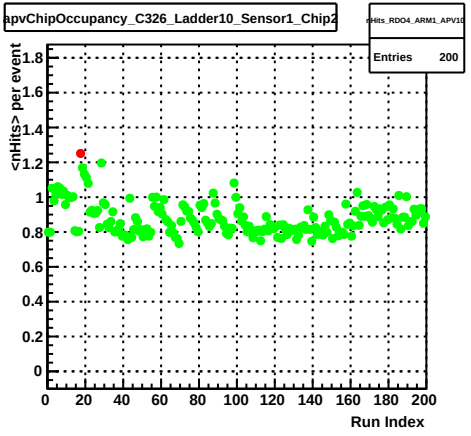
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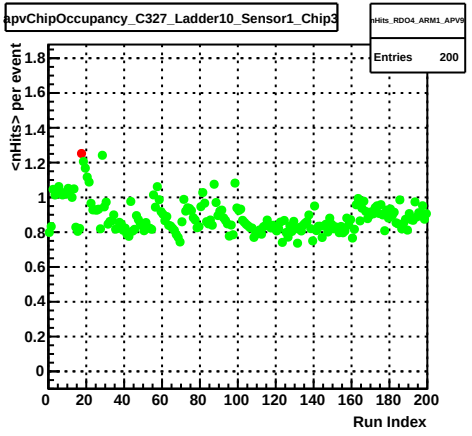
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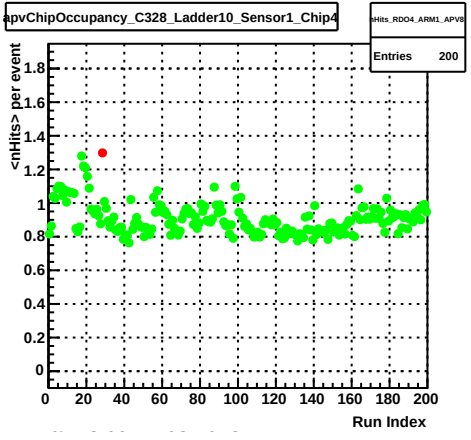
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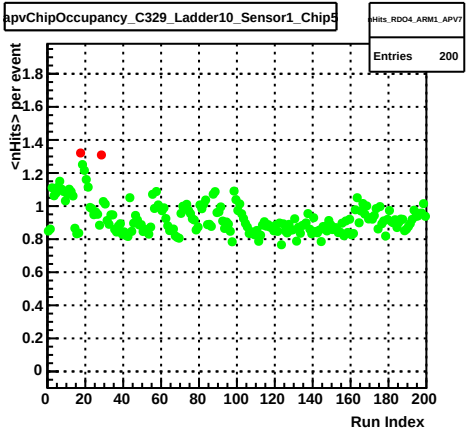
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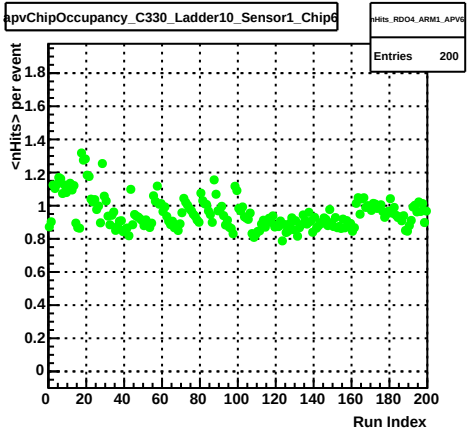
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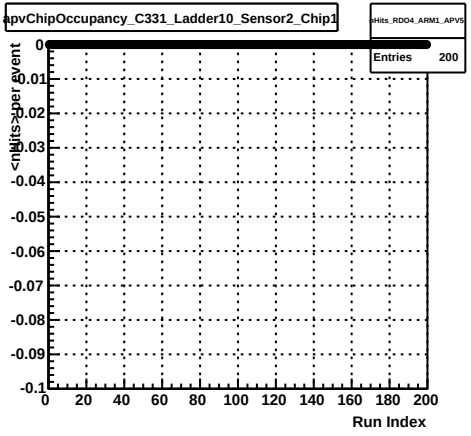
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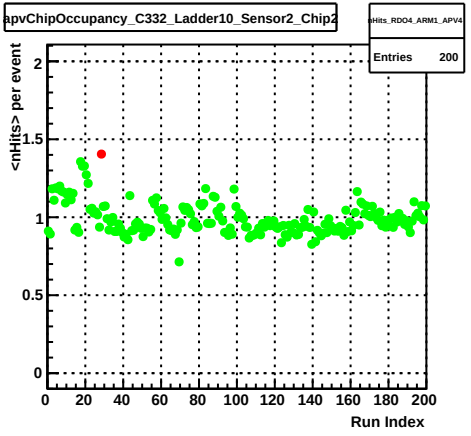
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Entries 200
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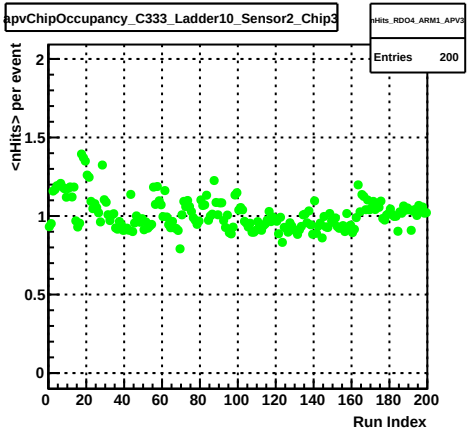
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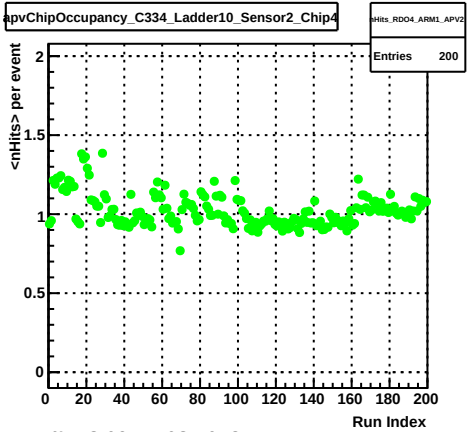
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nHits_RD04_ARM1_APV2
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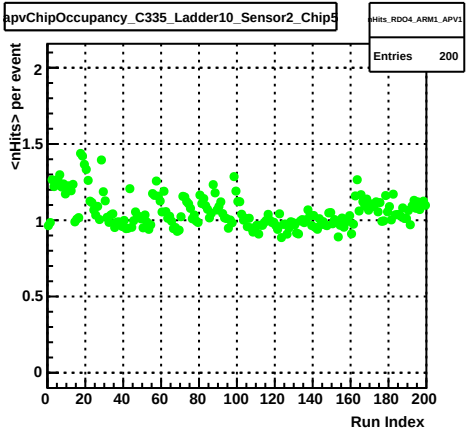
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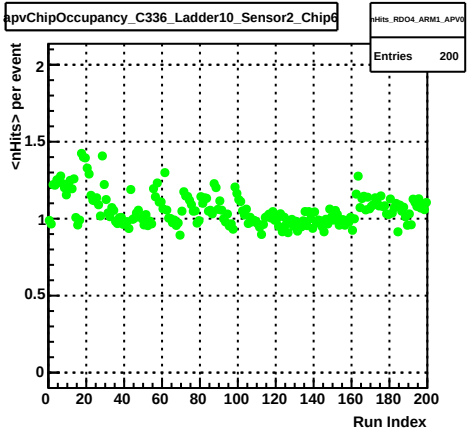
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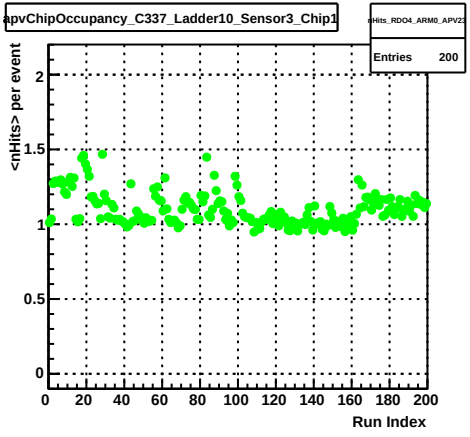
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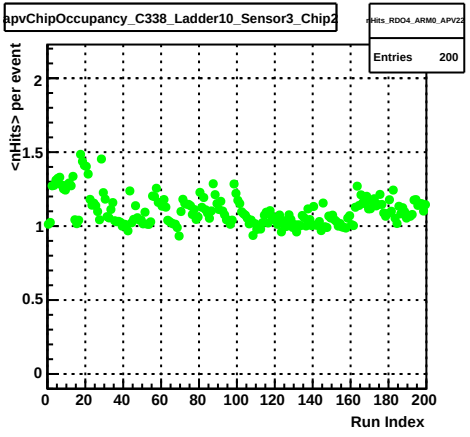
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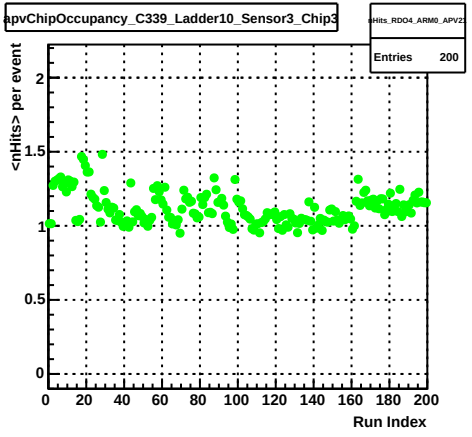
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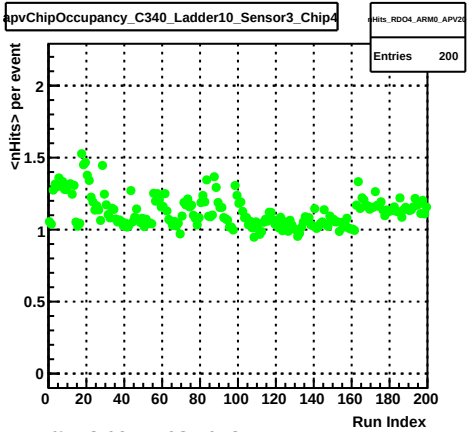
Run Index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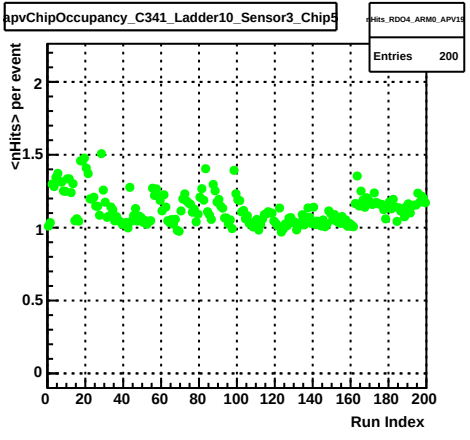
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nHits_RD04_ARM0_APV18
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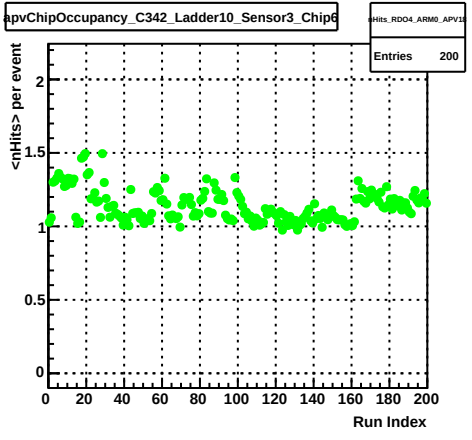
apvChipOccupancy_C342_Ladder10_Sensor3_Chip6
nHits_RD04_ARM0_APV19
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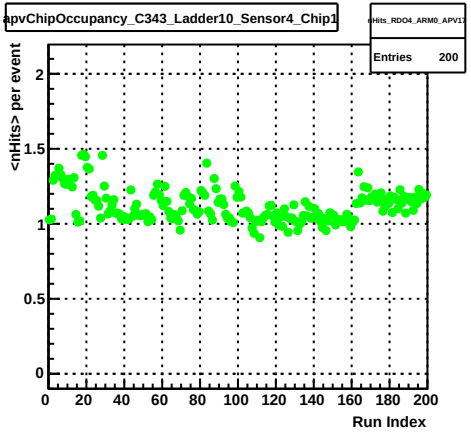
Run Index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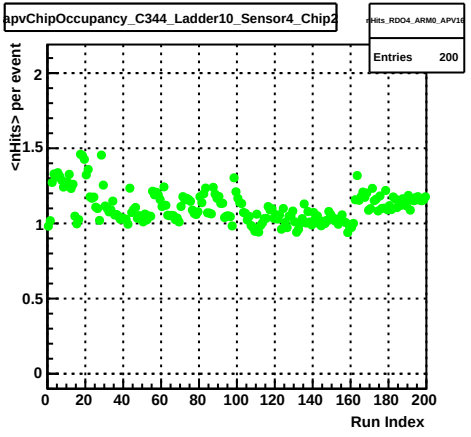
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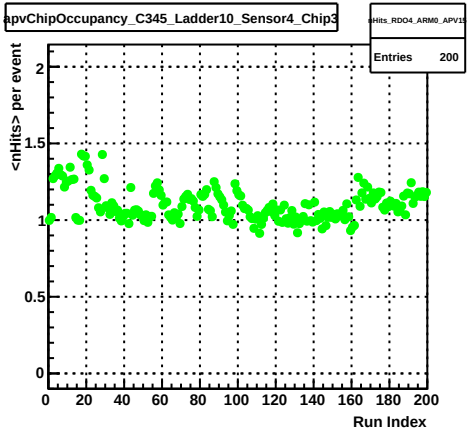
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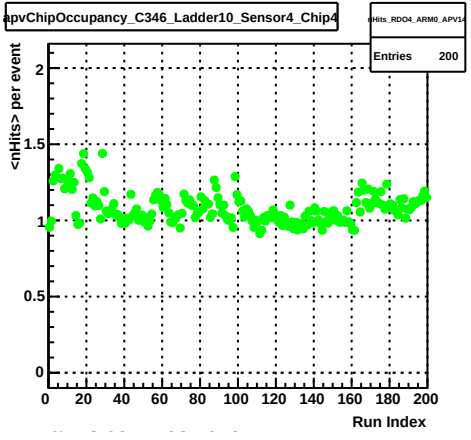
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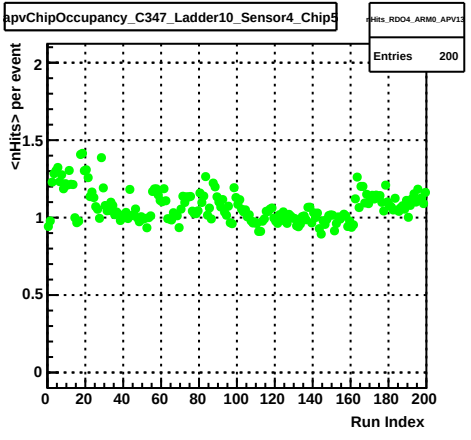
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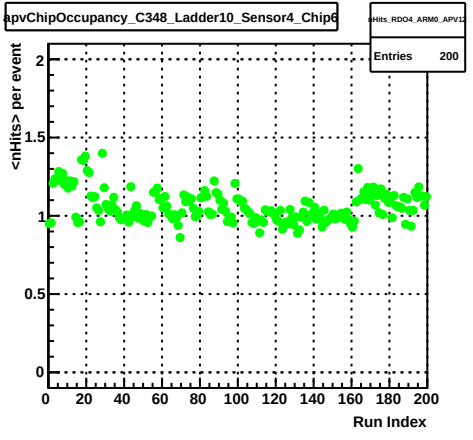
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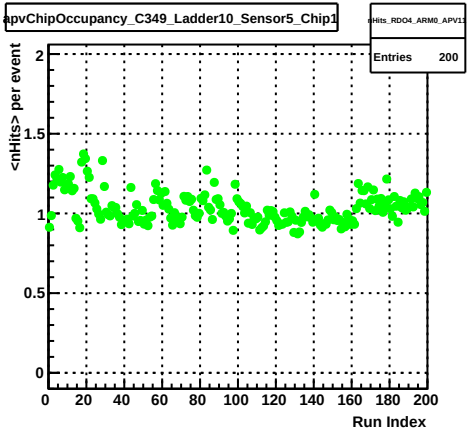
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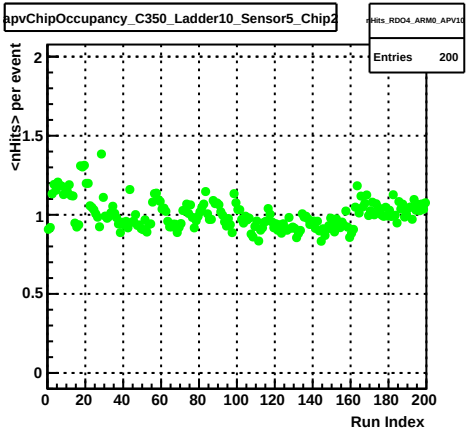
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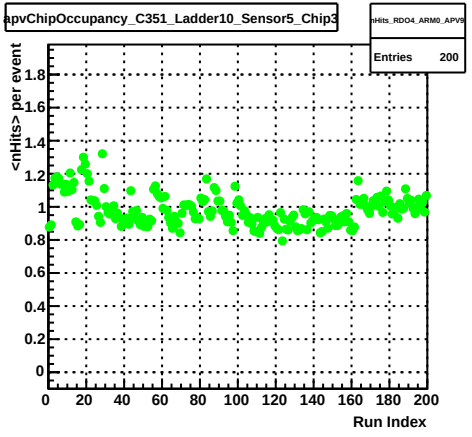
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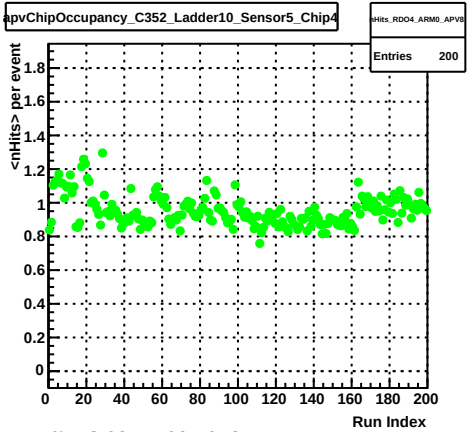
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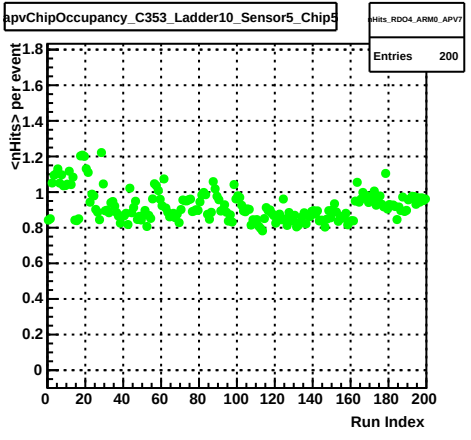
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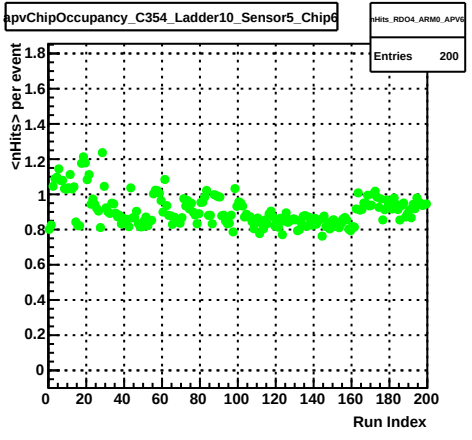
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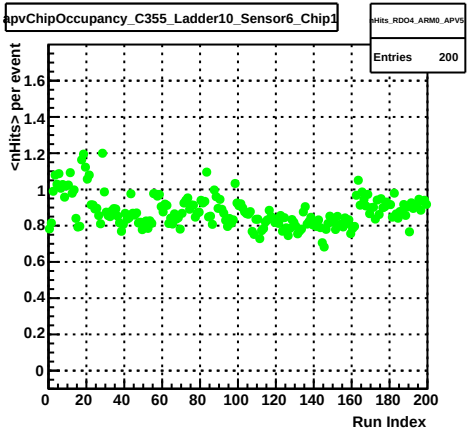
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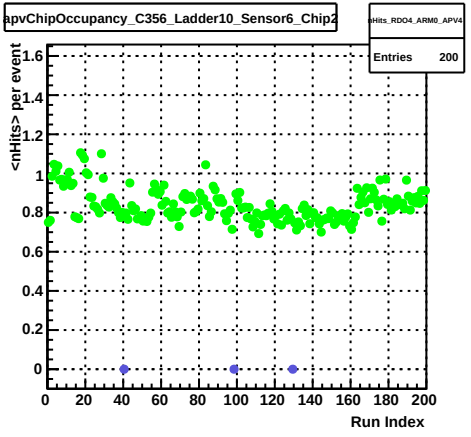
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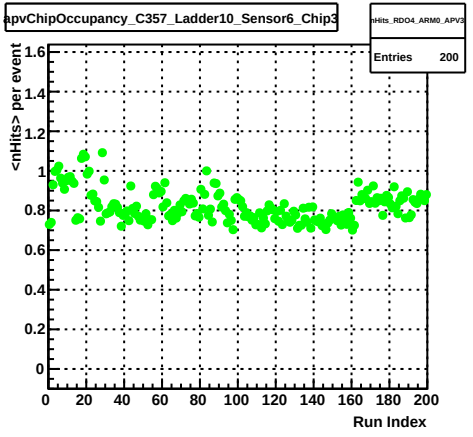
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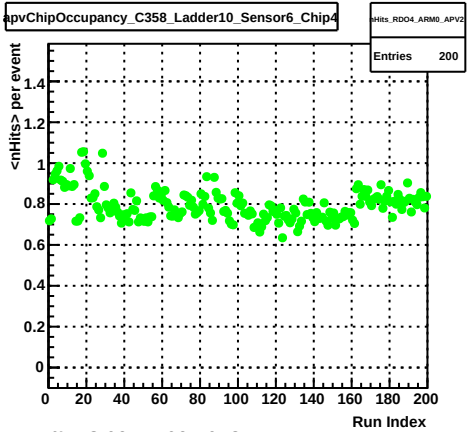
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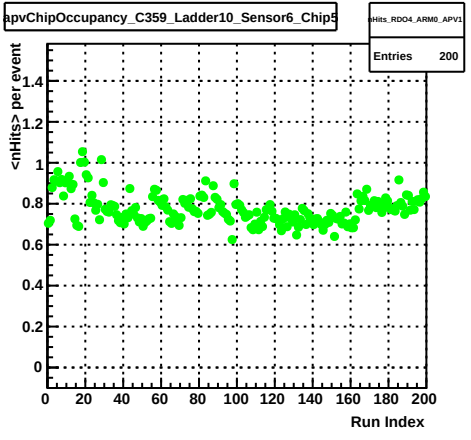
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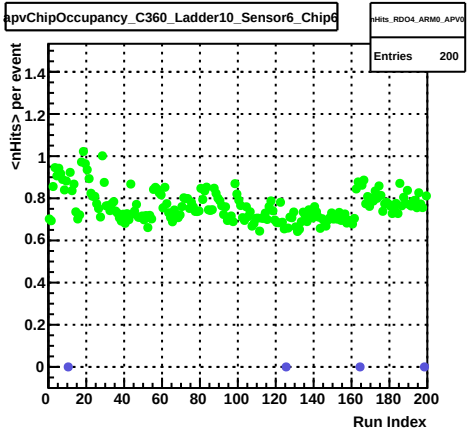
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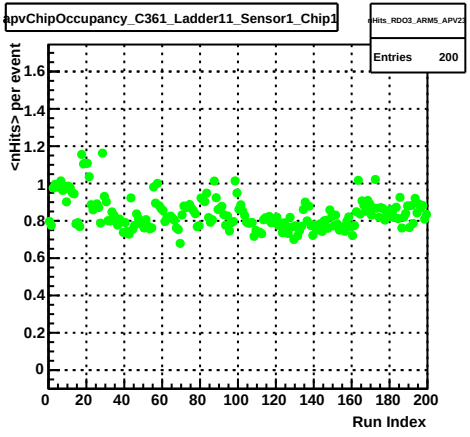
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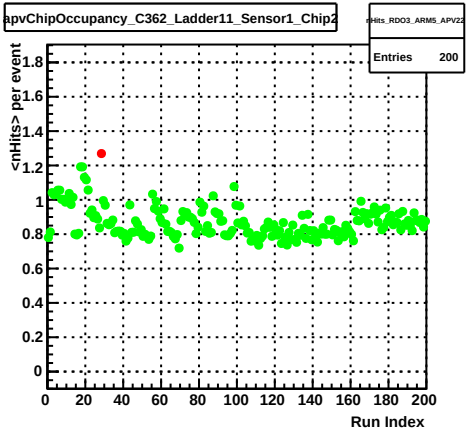
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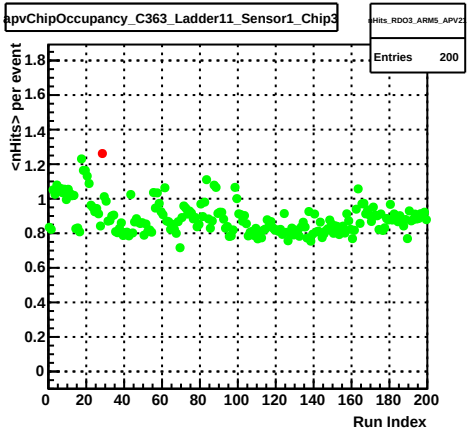
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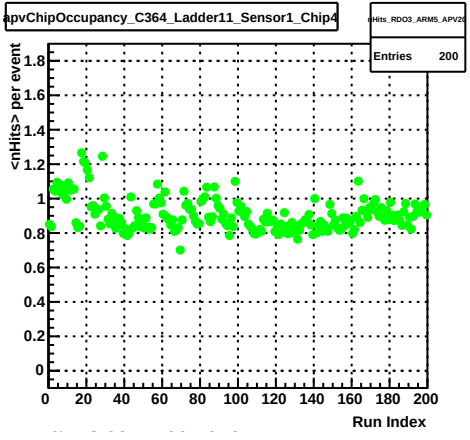
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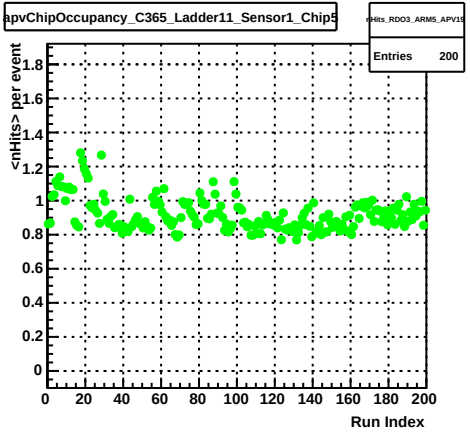
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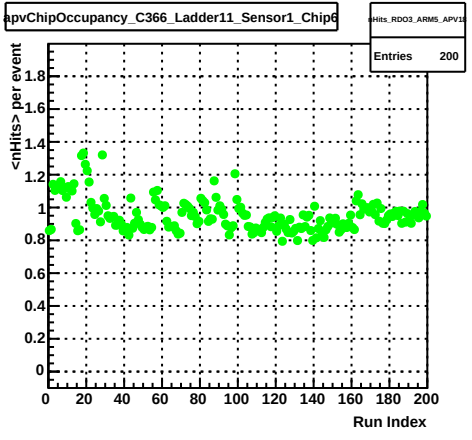
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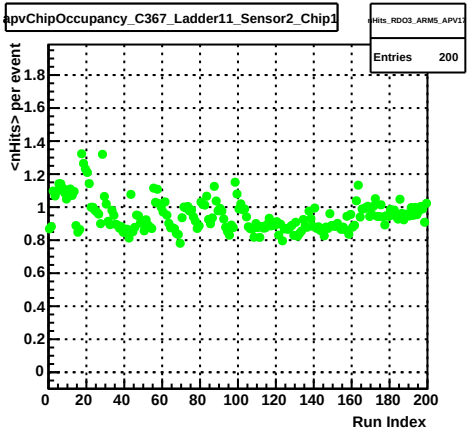
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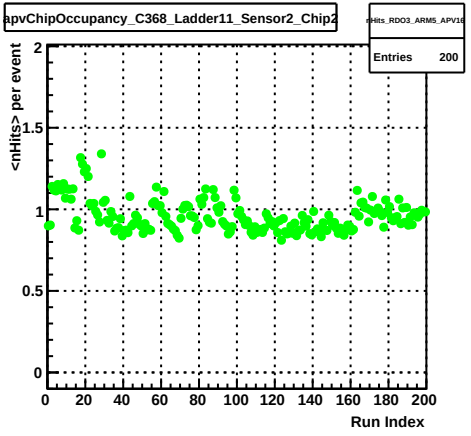
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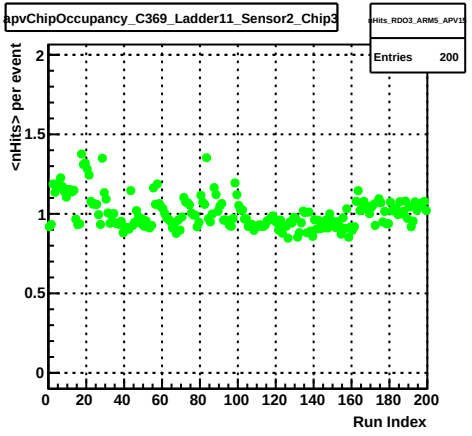
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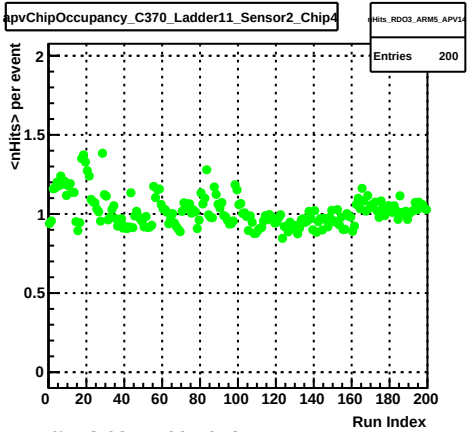
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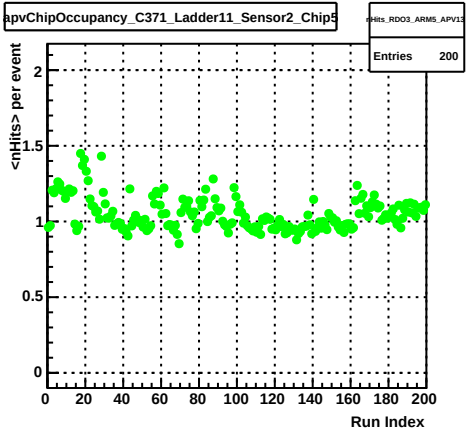
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Entries 200
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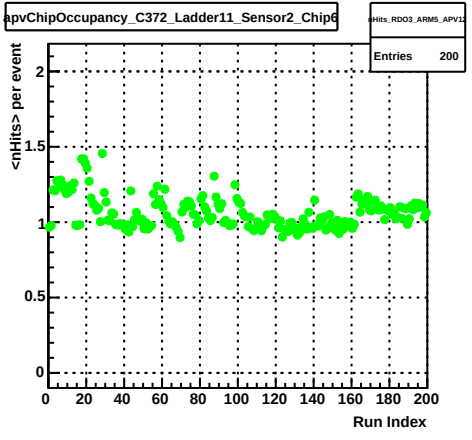
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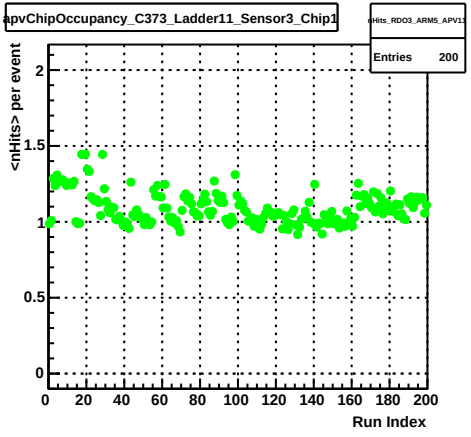
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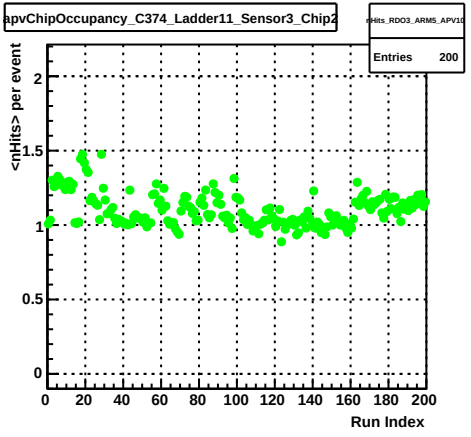
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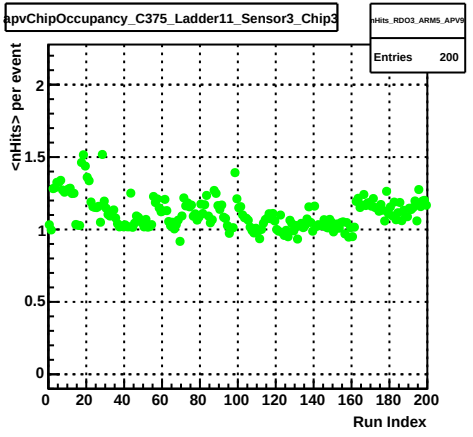
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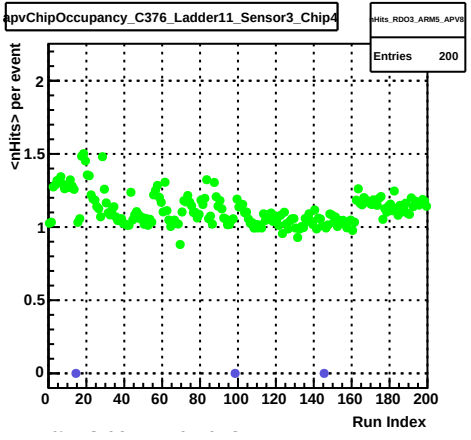
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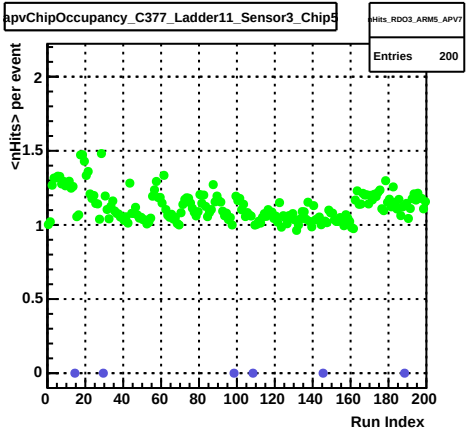
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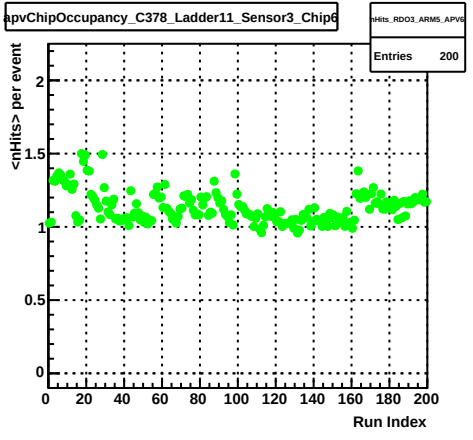
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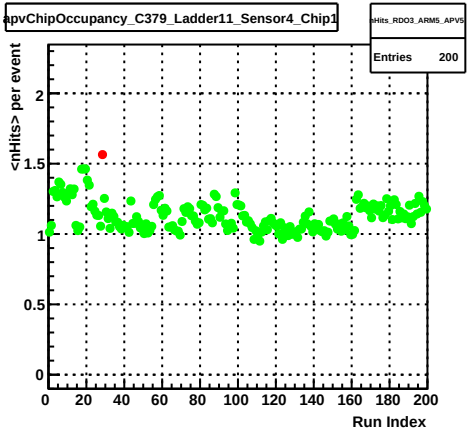
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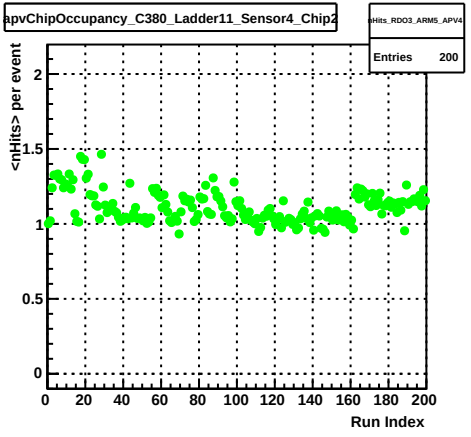
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nHits_RD03_ARM5_APV4
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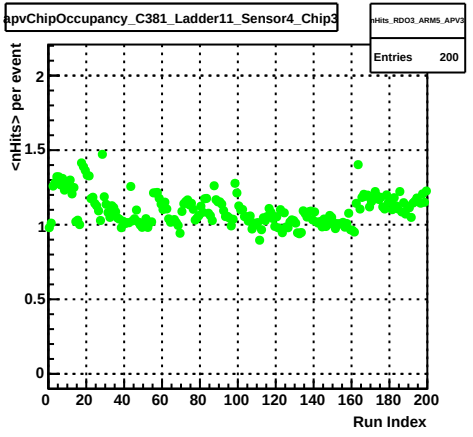
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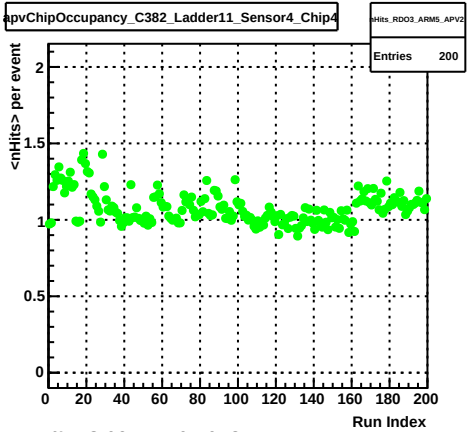
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Entries 200
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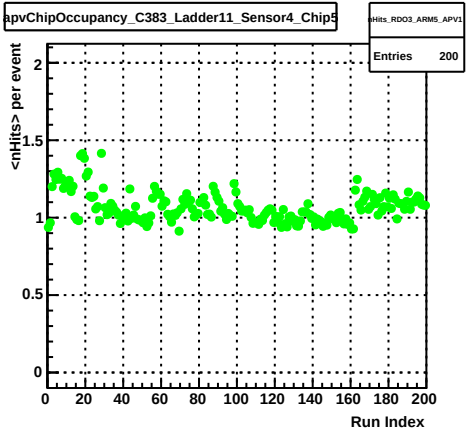
apvChipOccupancy_C383_Ladder11_Sensor4_Chip5
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Entries 200
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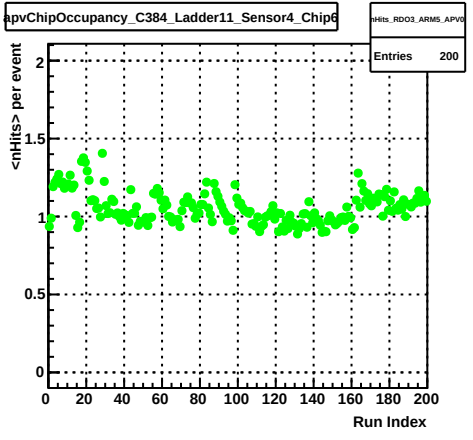
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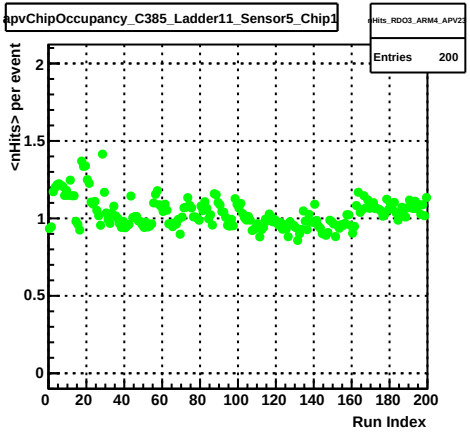
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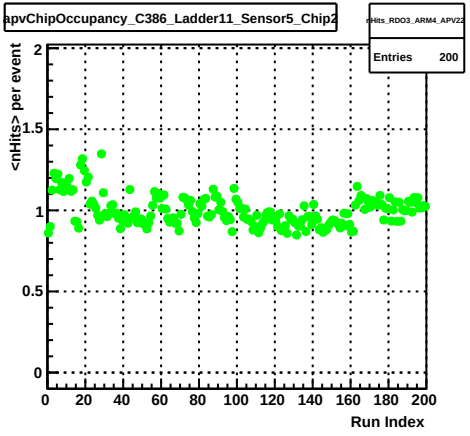
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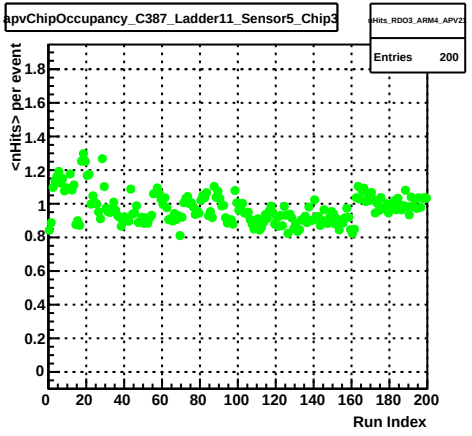
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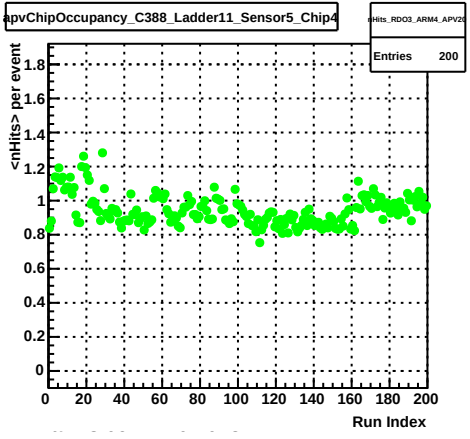
Run Index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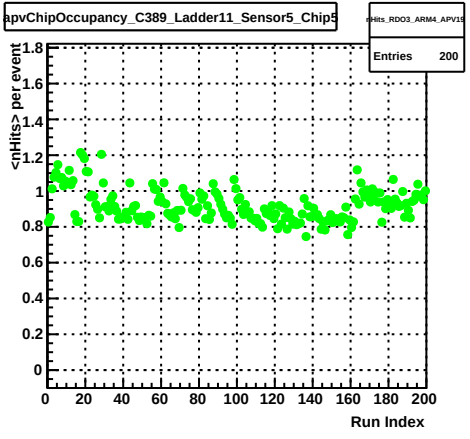
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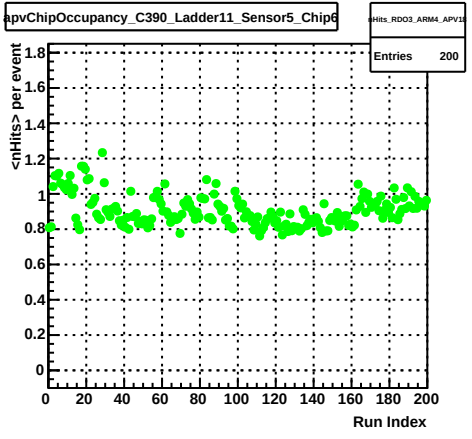
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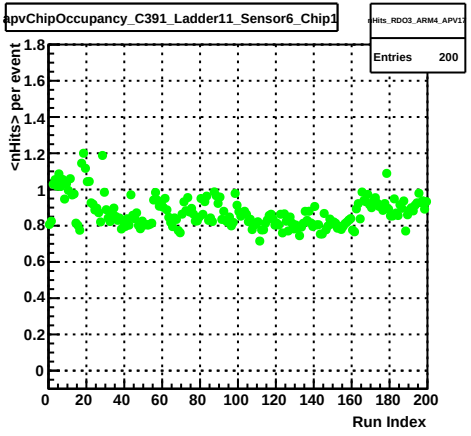
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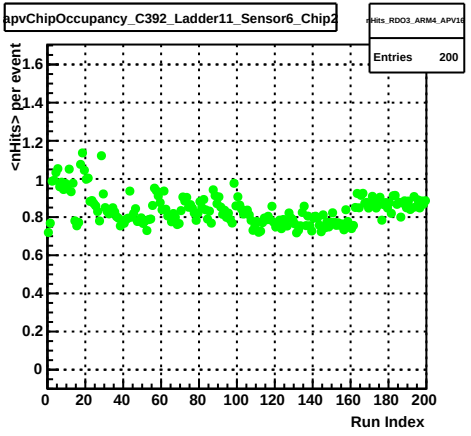
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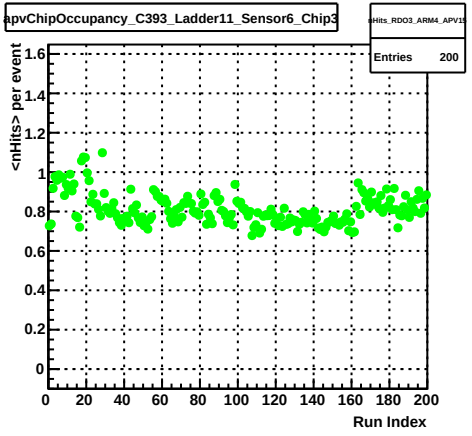
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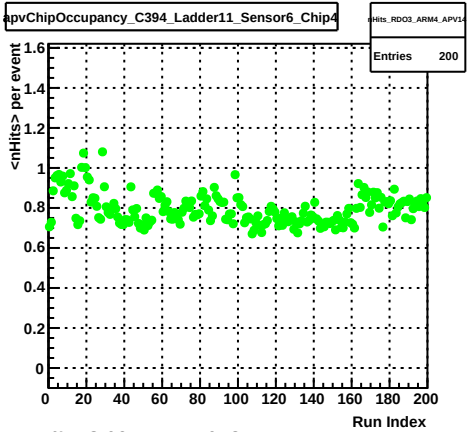
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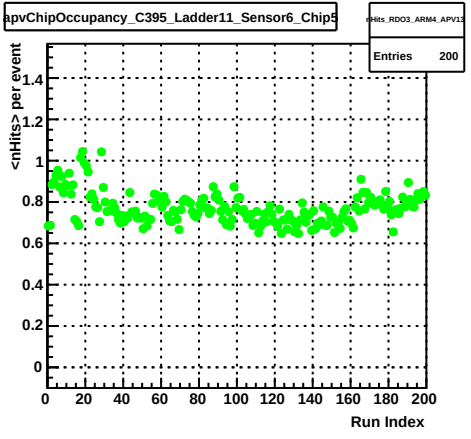
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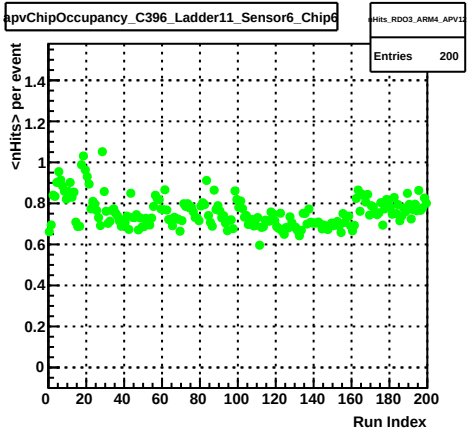
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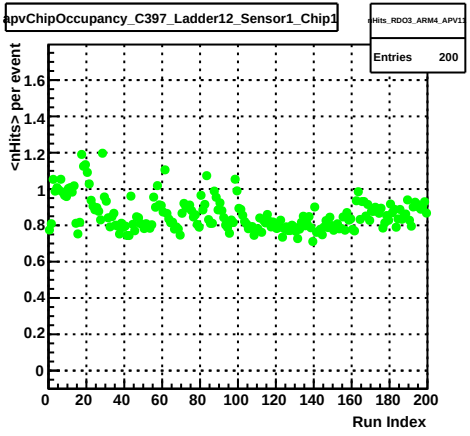
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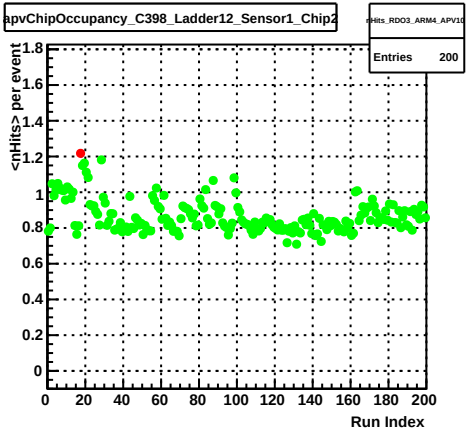
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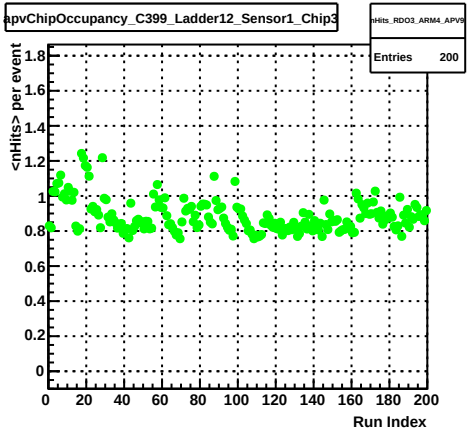
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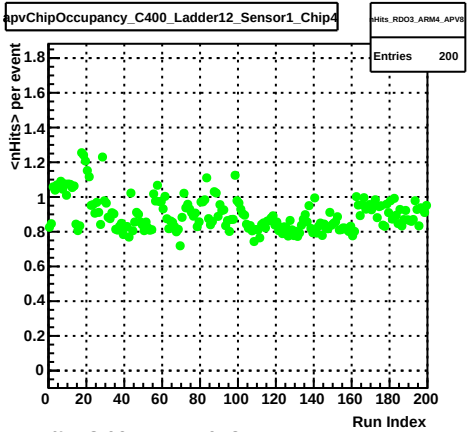
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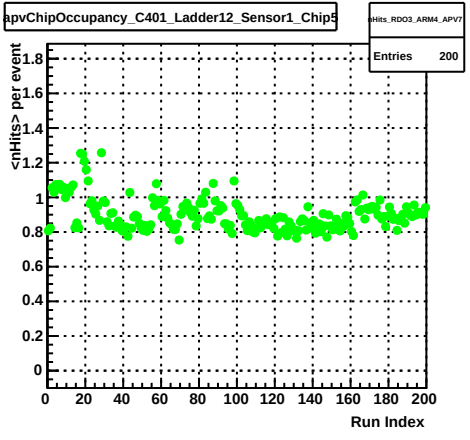
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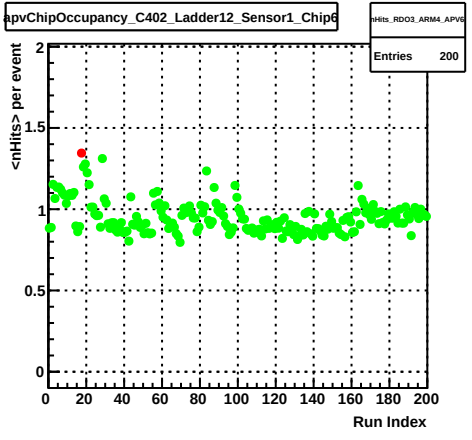
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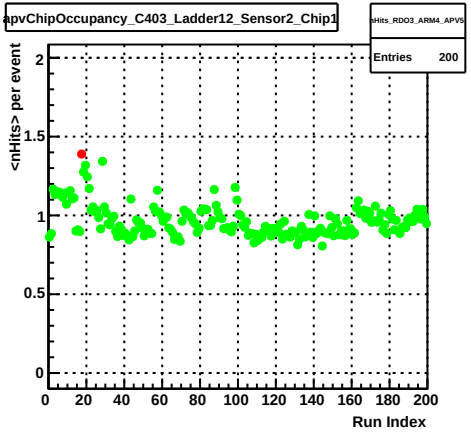
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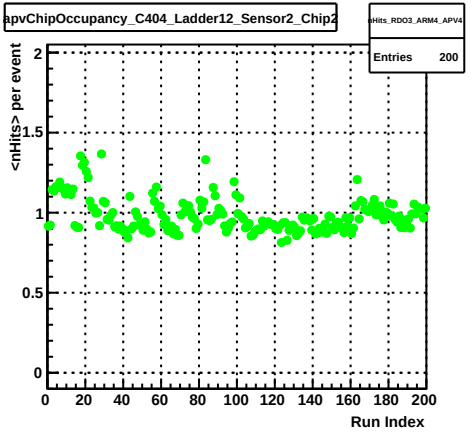
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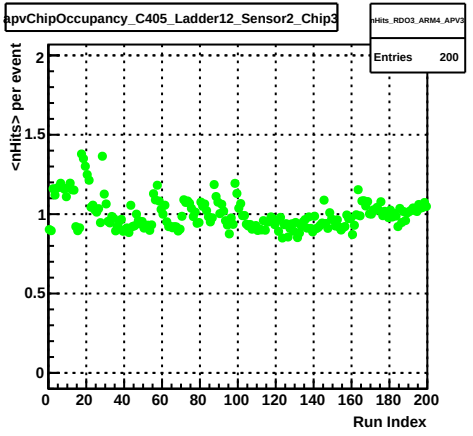
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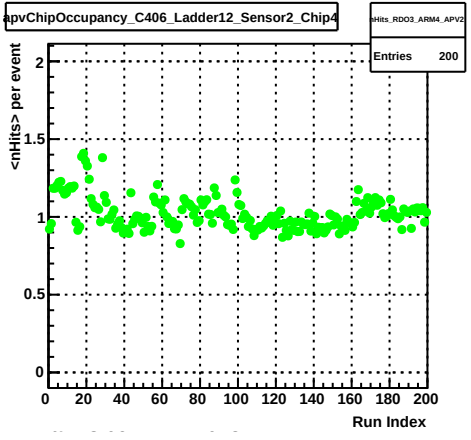
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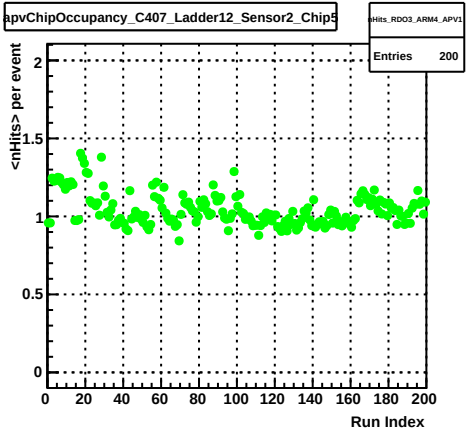
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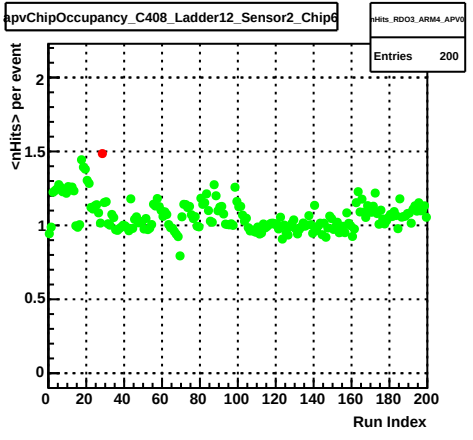
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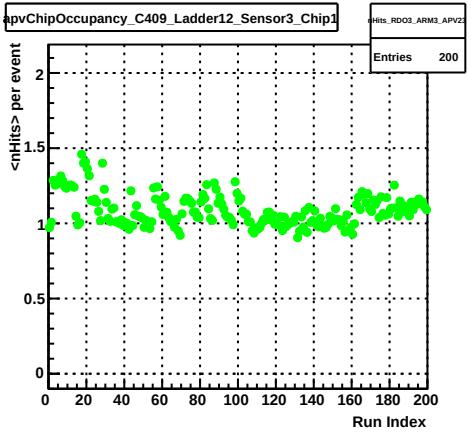
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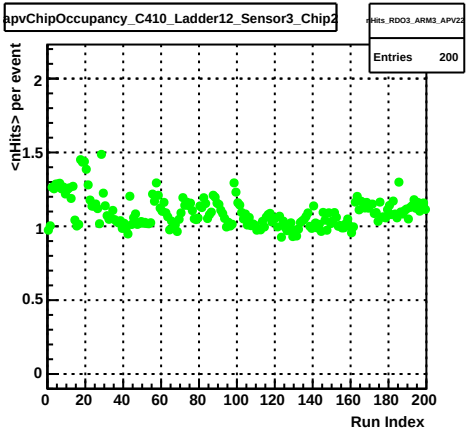
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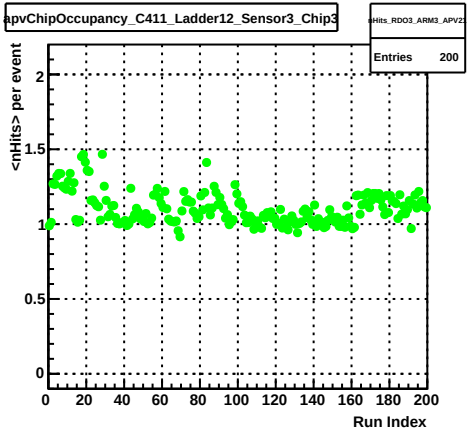
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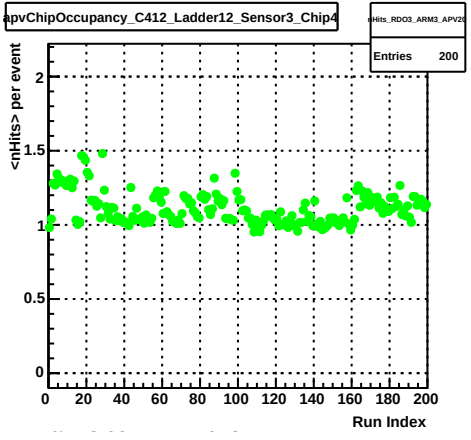
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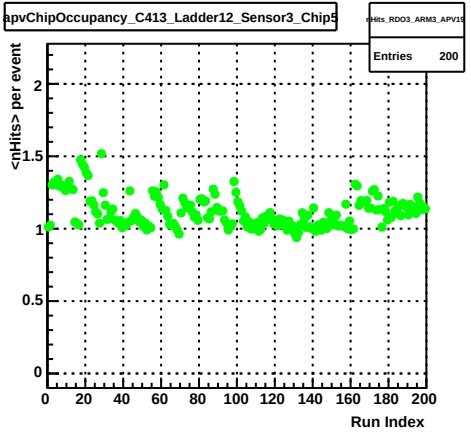
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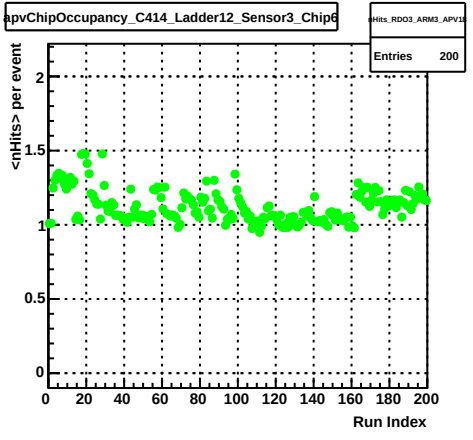
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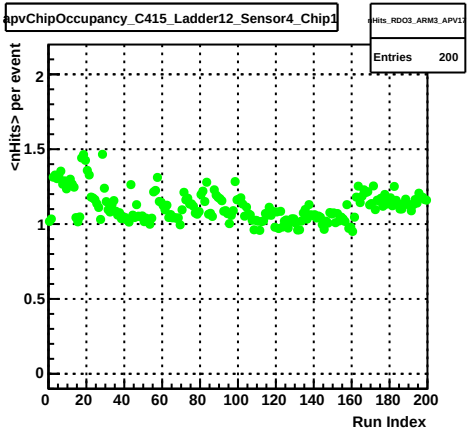
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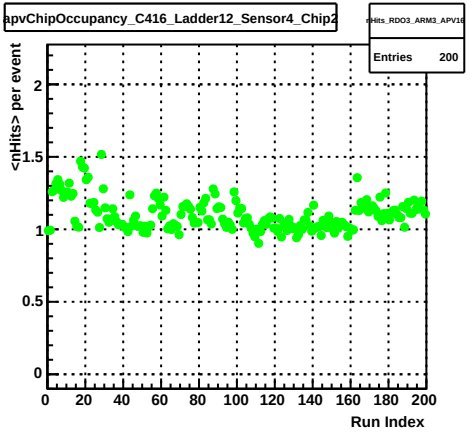
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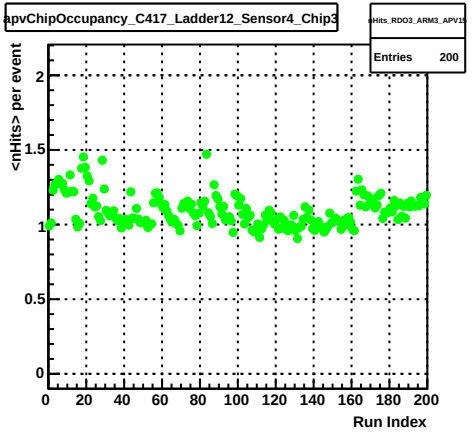
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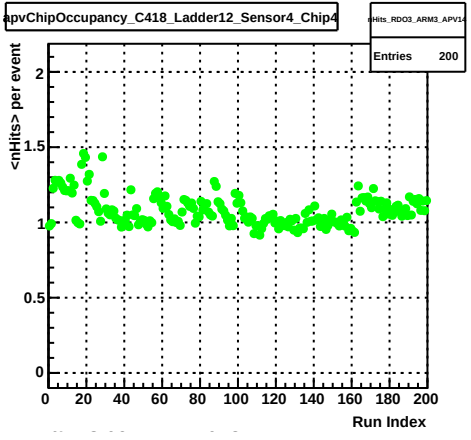
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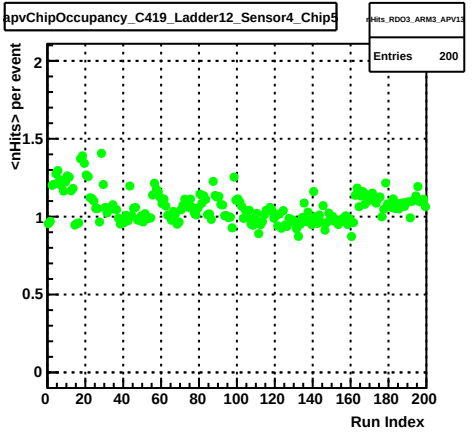
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Entries 200
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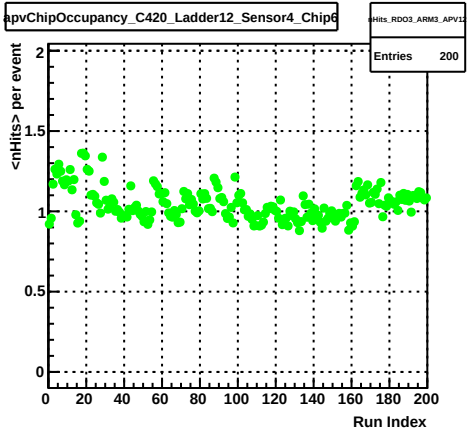
Run Index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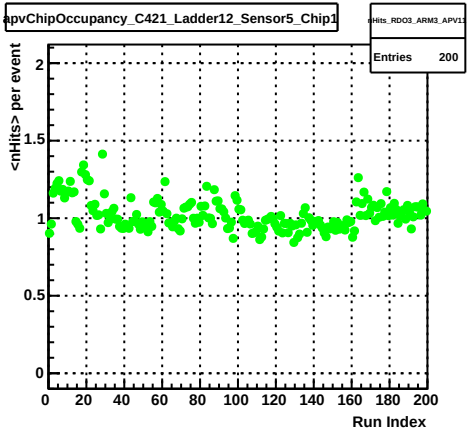
Run Index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Entries 200
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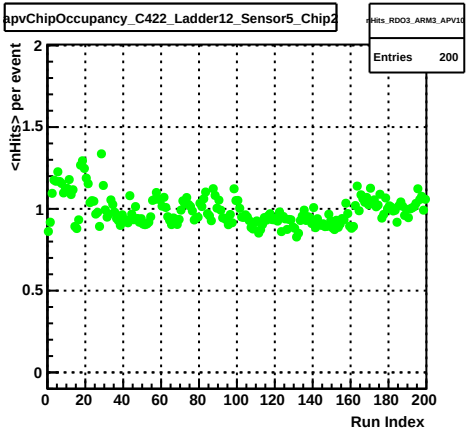
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Entries 200
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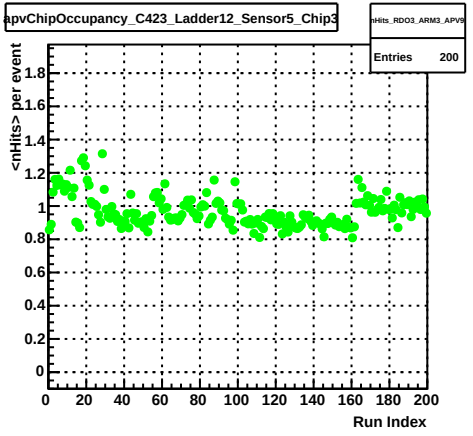
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Entries 200
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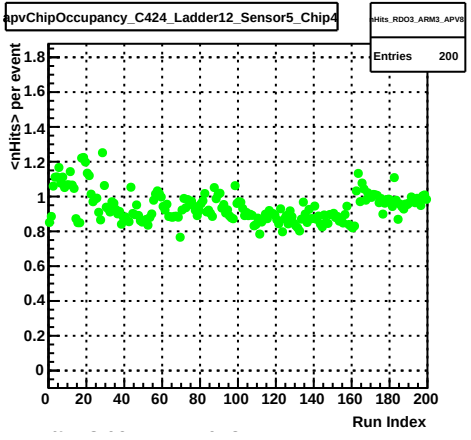
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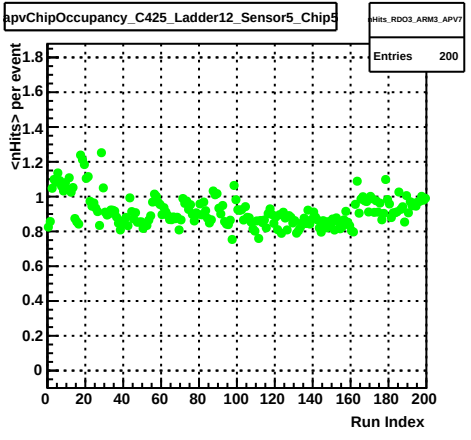
apvChipOccupancy_C425_Ladder12_Sensor5_Chip5
nHits_RDO3_ARM3_APV7
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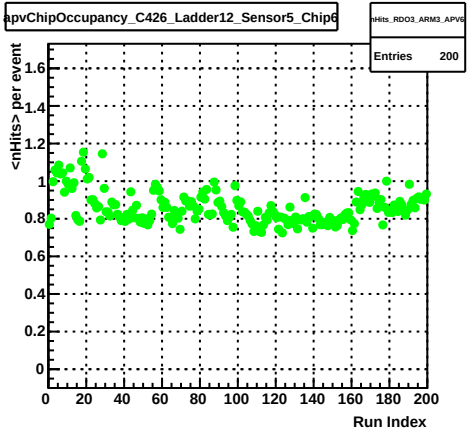
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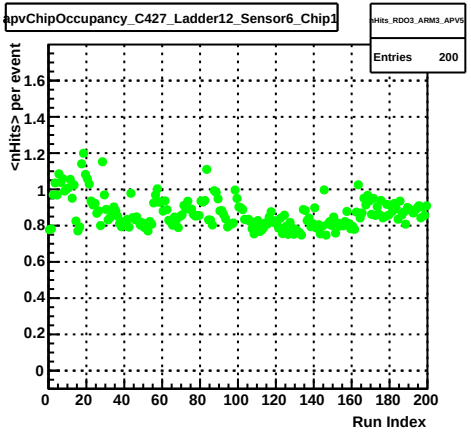
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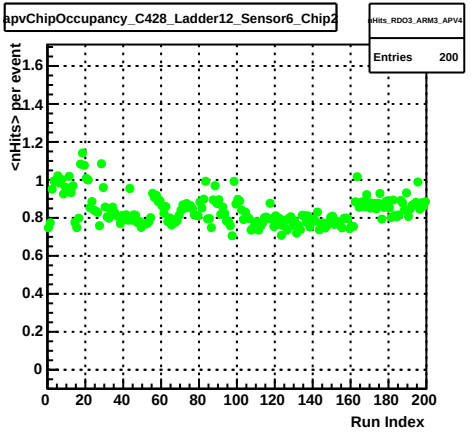
apvChipOccupancy_C428_Ladder12_Sensor6_Chip2
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Entries 200
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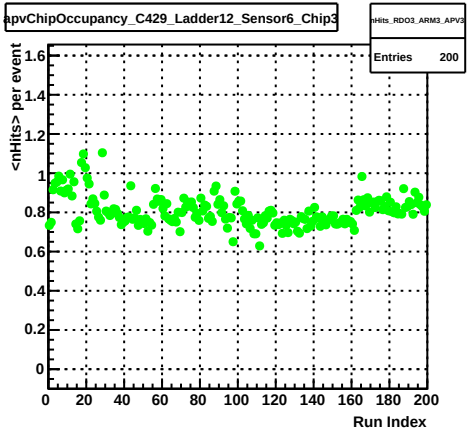
apvChipOccupancy_C429_Ladder12_Sensor6_Chip3
nHits_RDO3_ARM3_APV3
Entries 200
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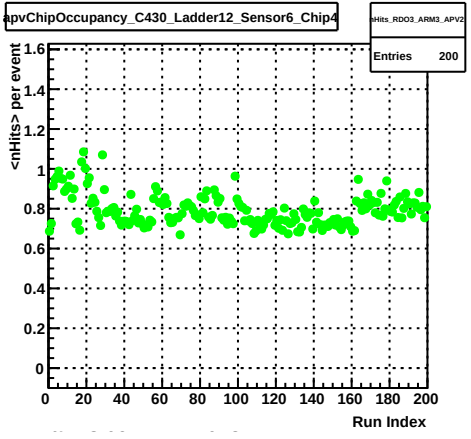
Run Index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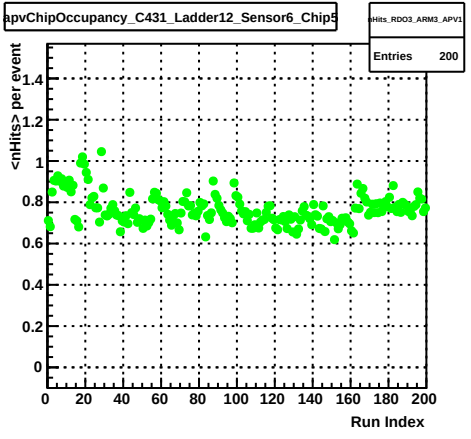
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Entries 200
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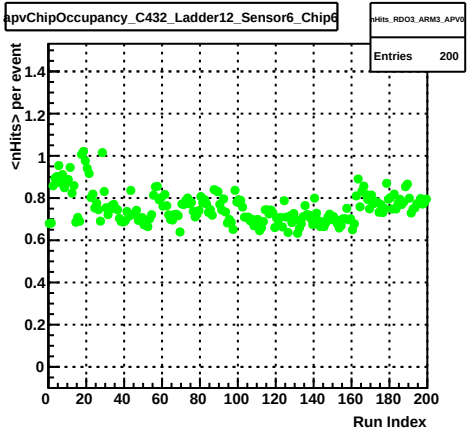
Run Index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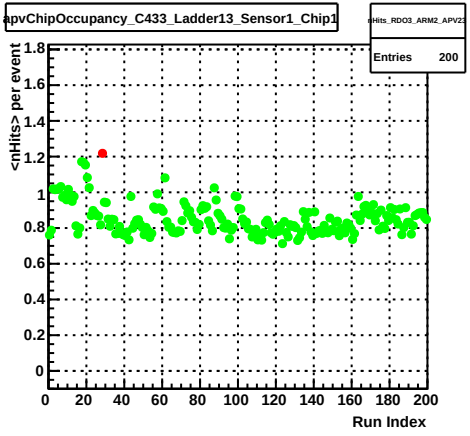
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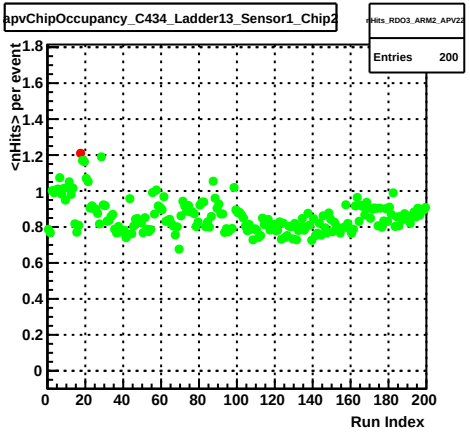
Run Index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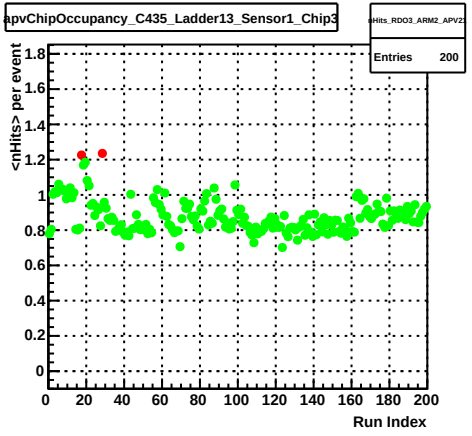
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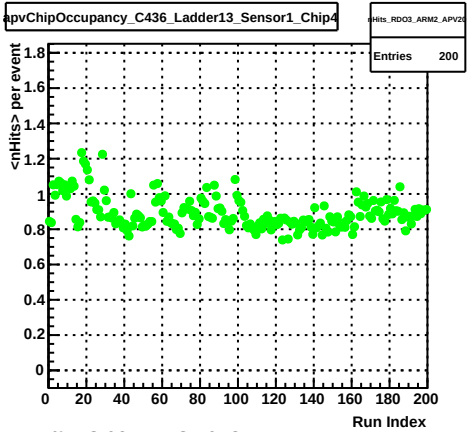
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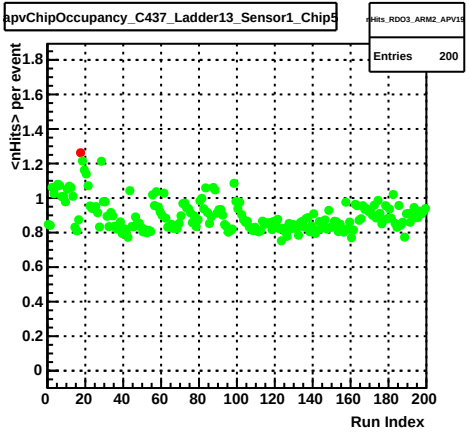
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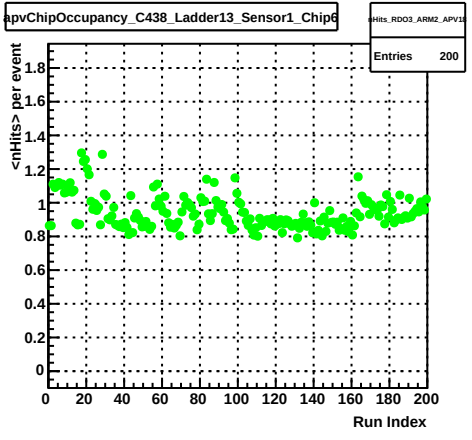
Run Index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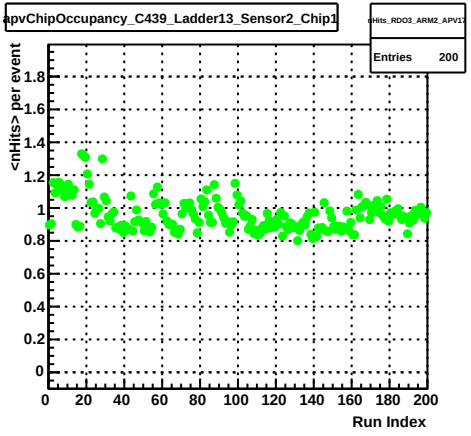
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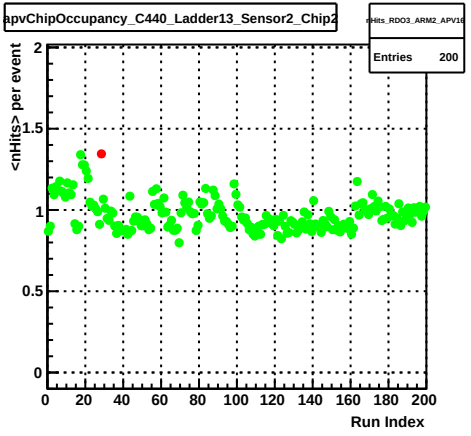
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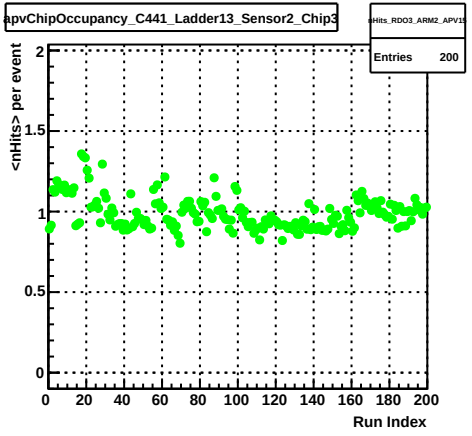
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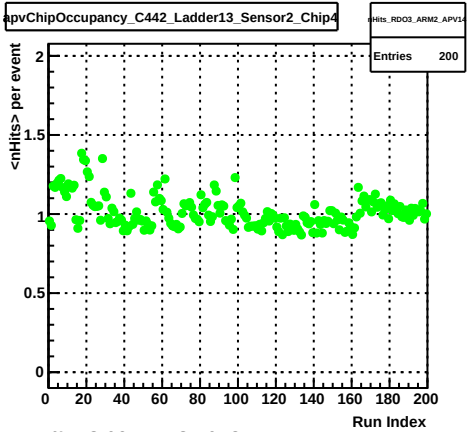
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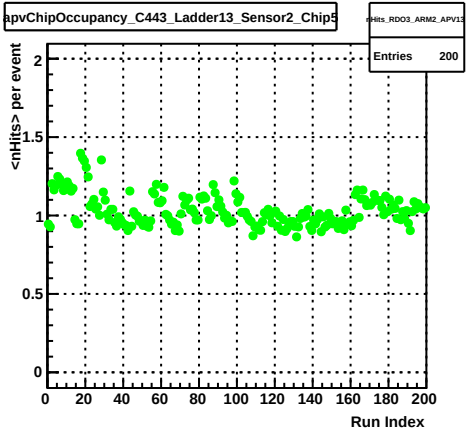
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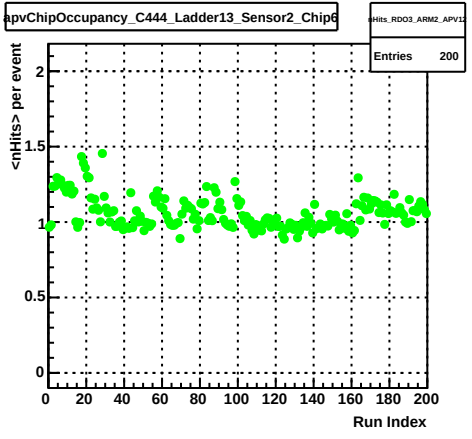
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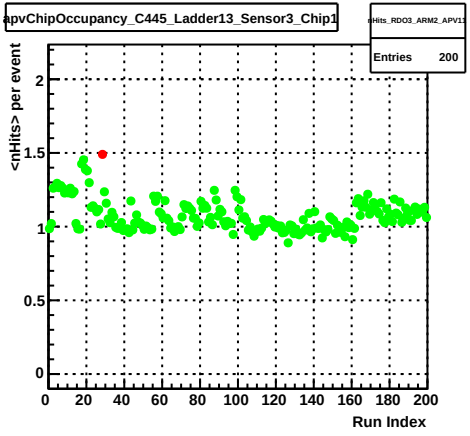
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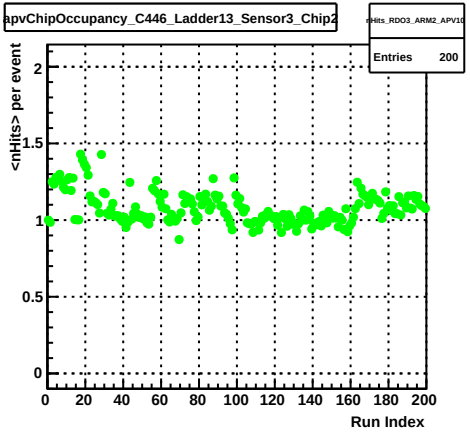
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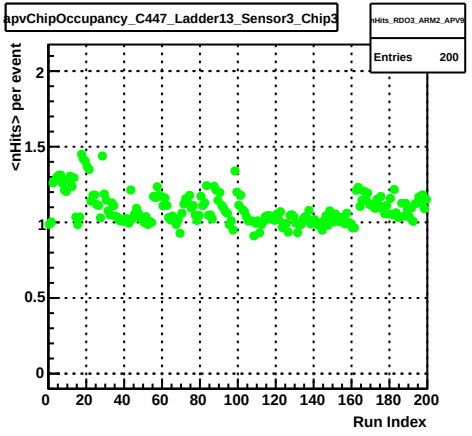
Run Index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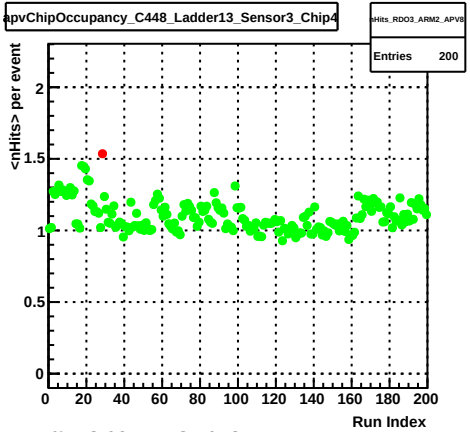
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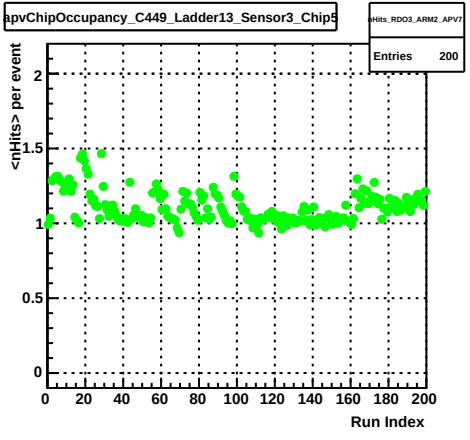
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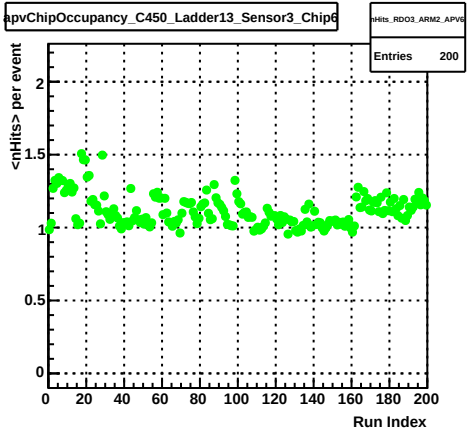
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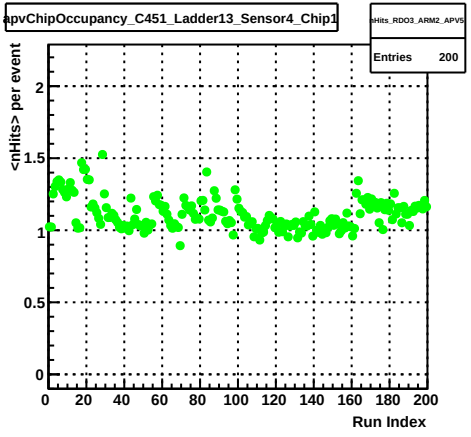
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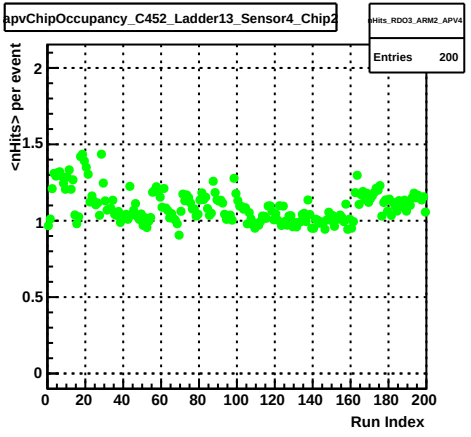
apvChipOccupancy_C452_Ladder13_Sensor4_Chip2
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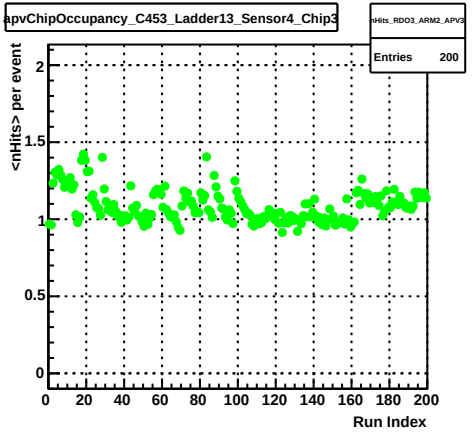
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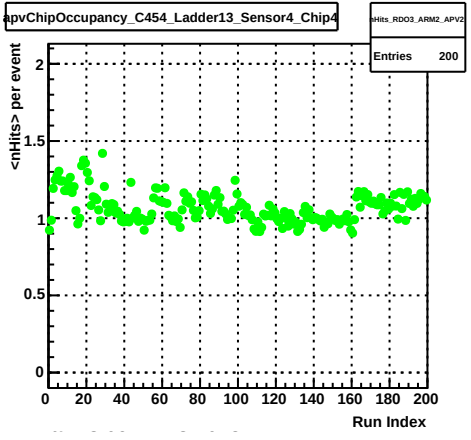
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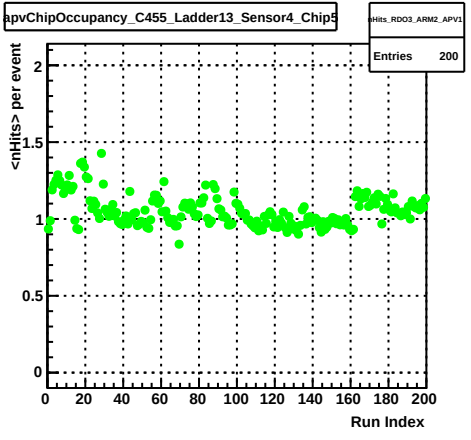
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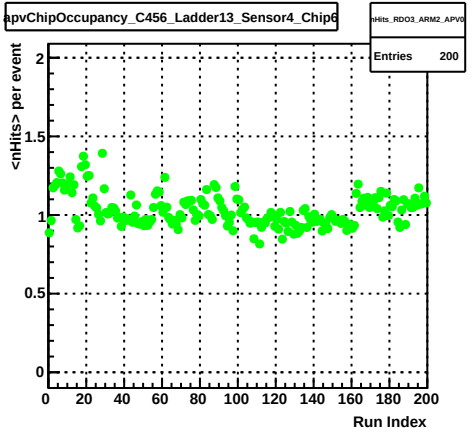
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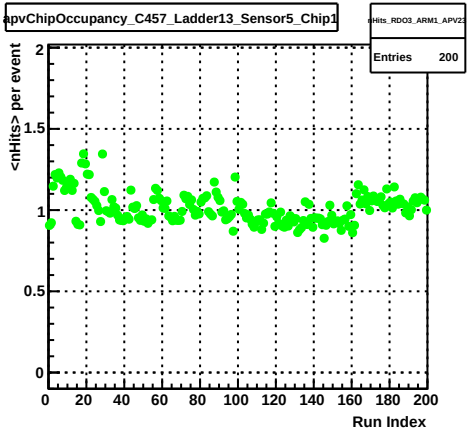
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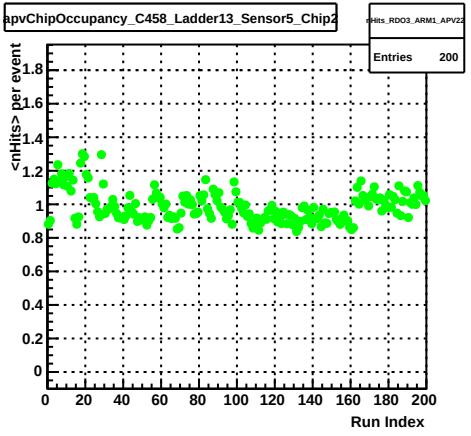
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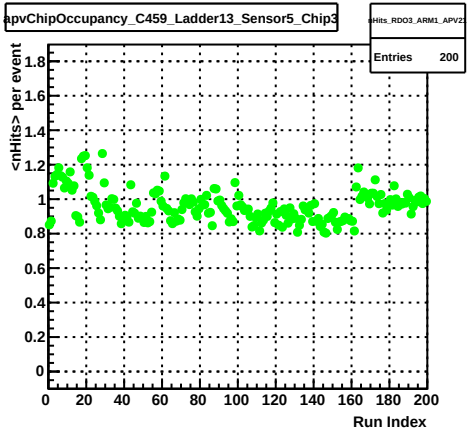
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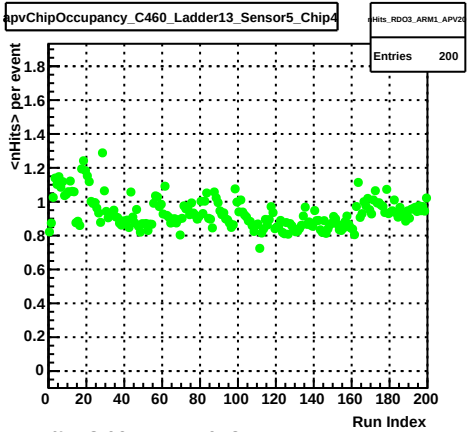
Run Index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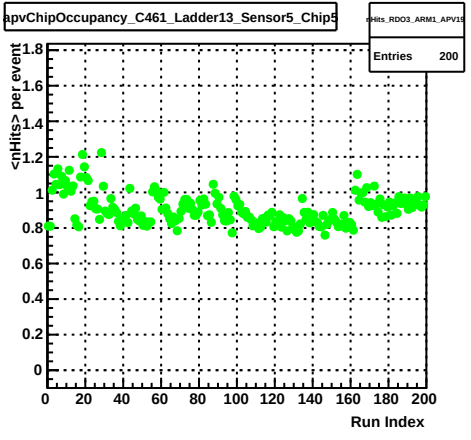
Run Index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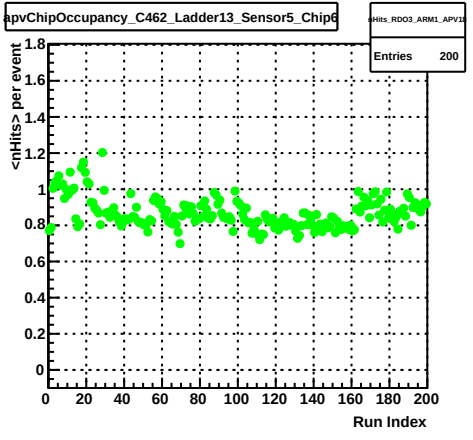
Run Index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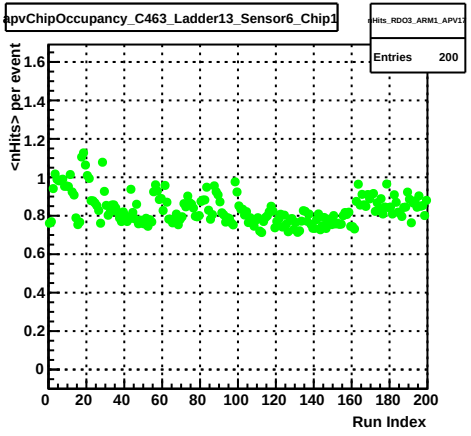
Run Index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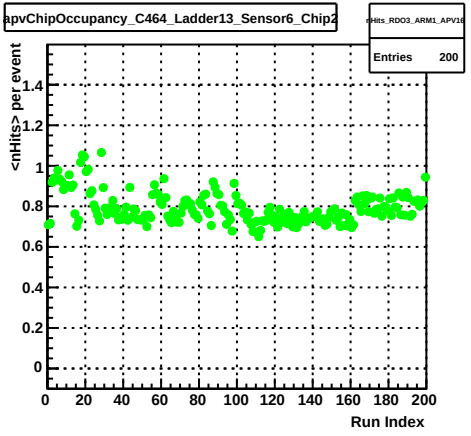
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Entries 200
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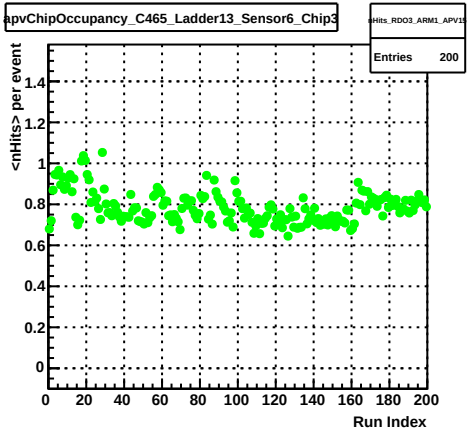
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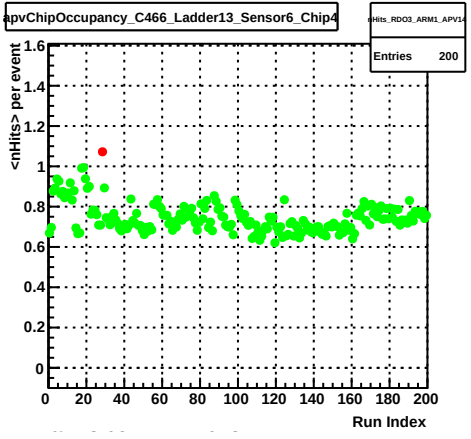
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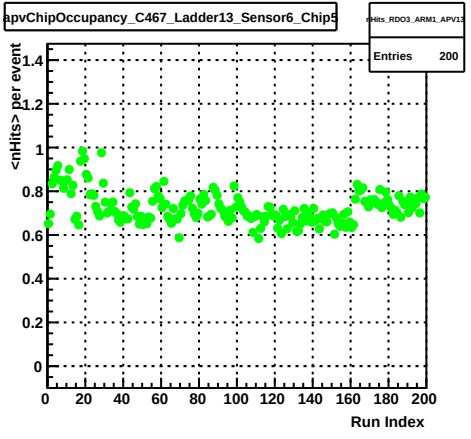
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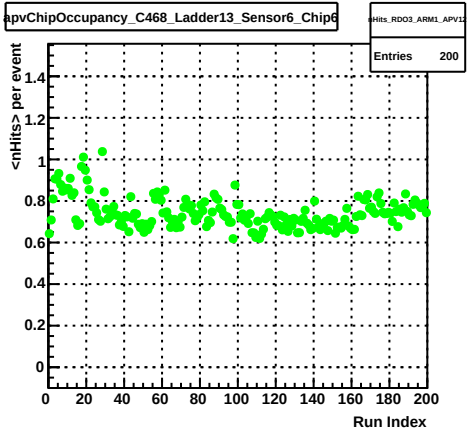
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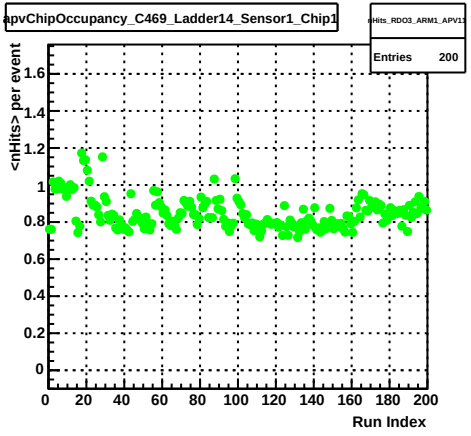
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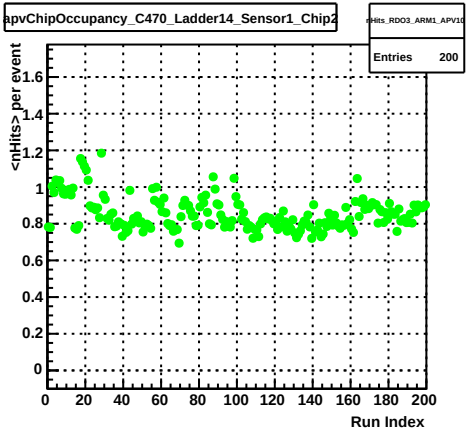
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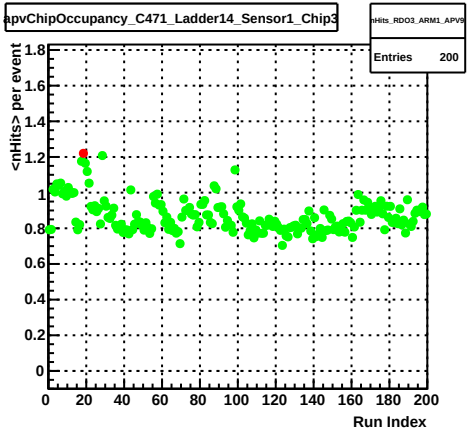
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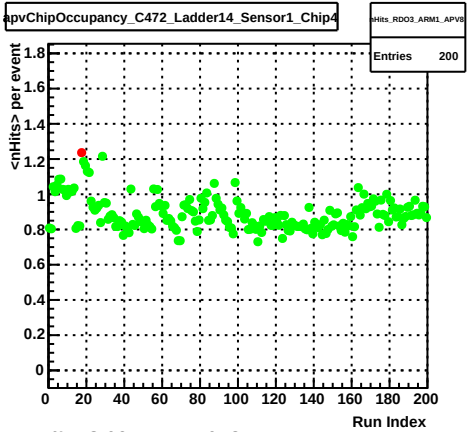
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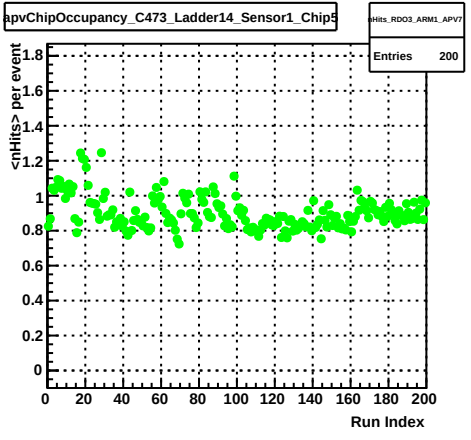
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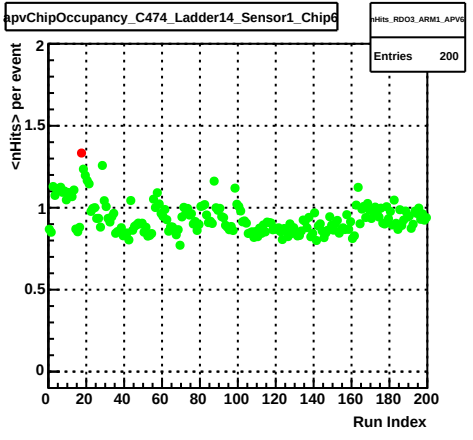
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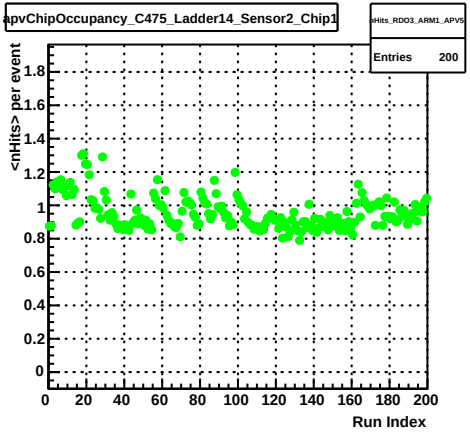
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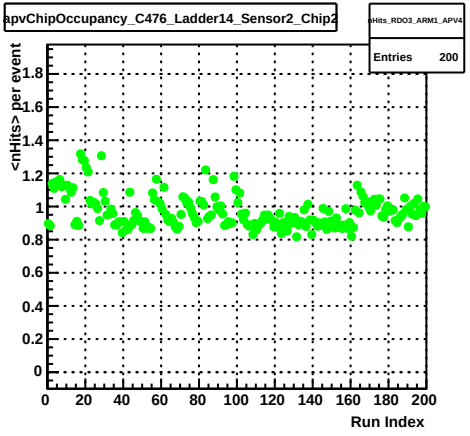
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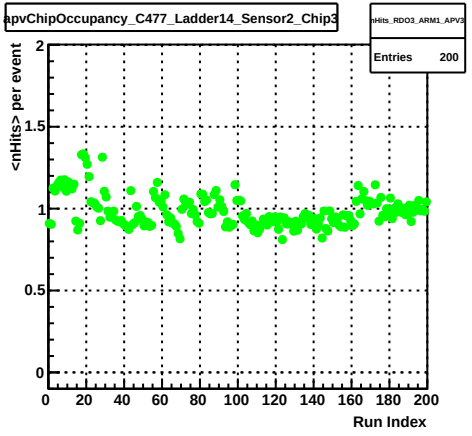
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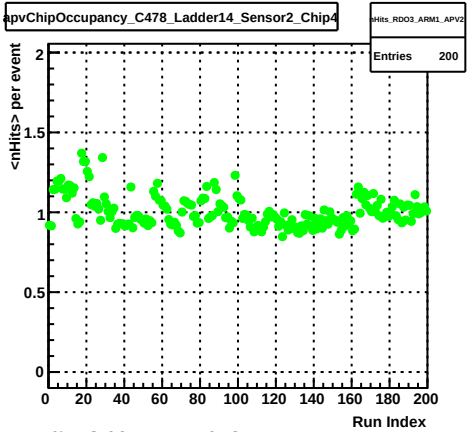
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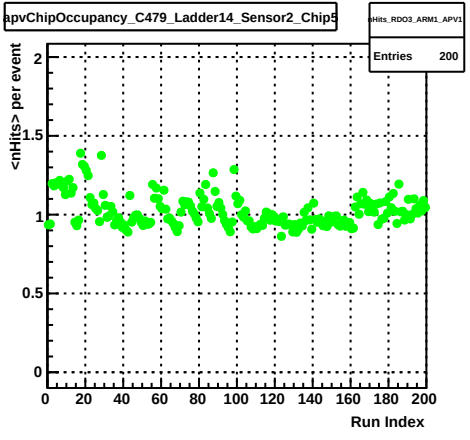
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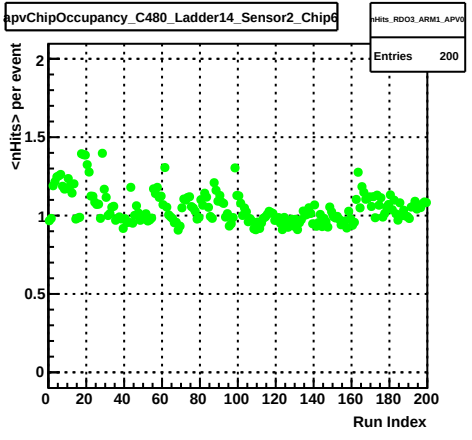
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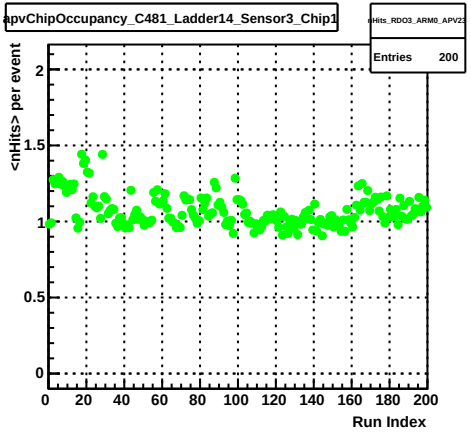
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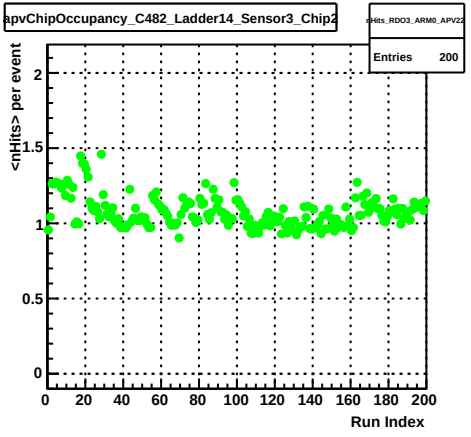
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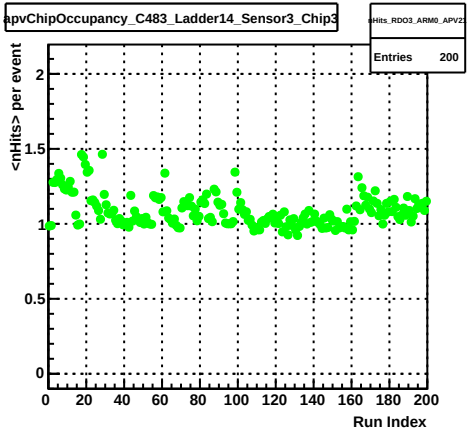
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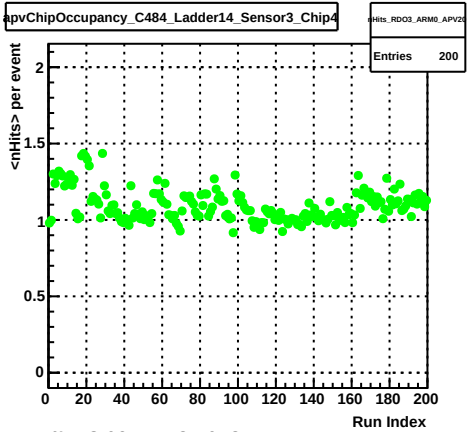
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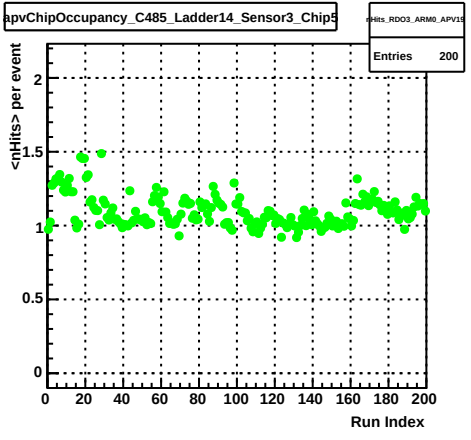
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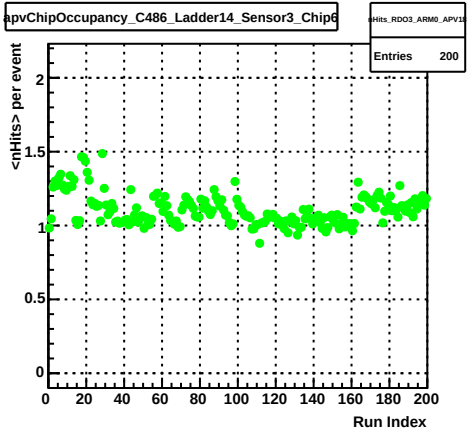
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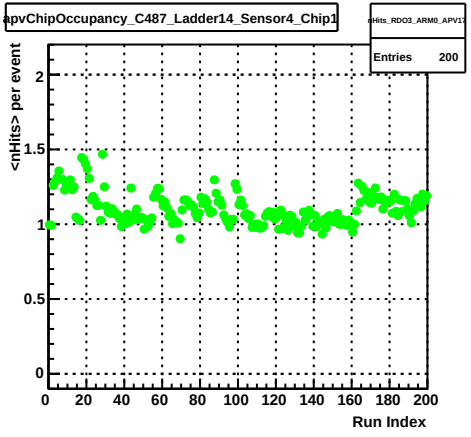
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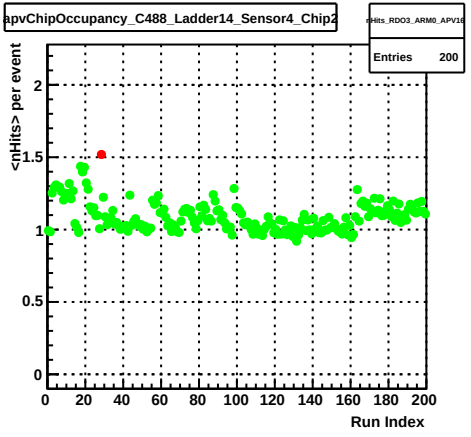
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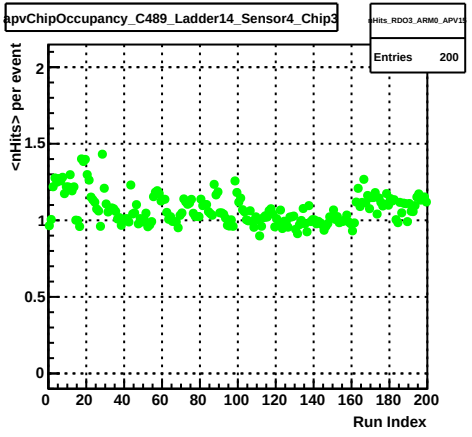
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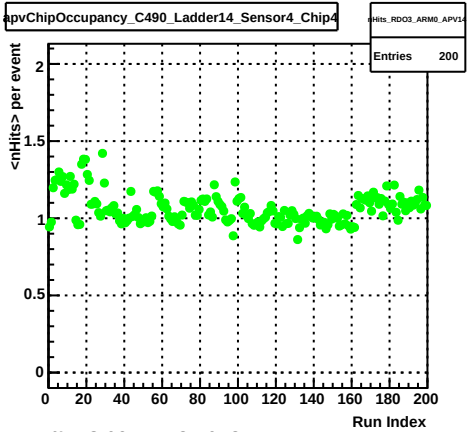
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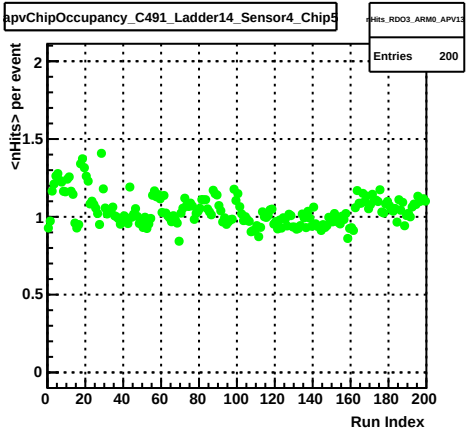
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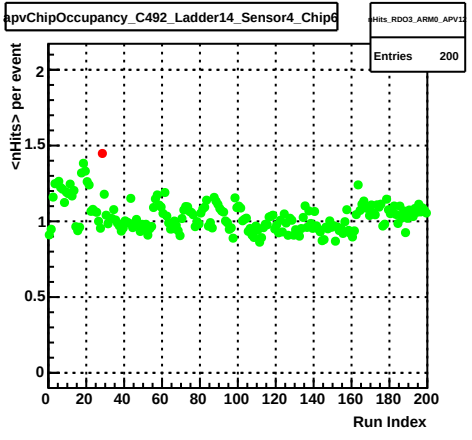
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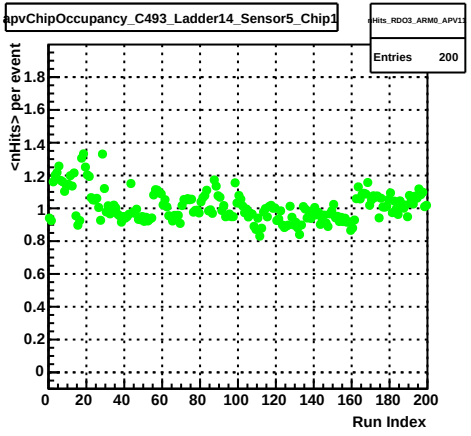
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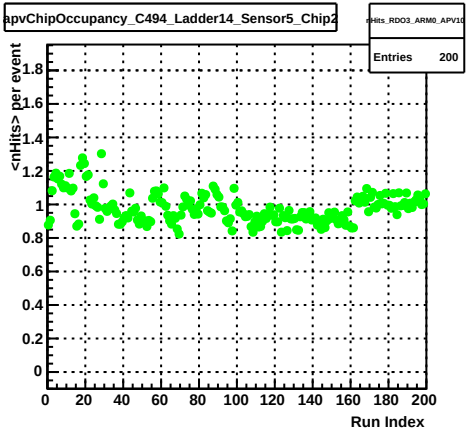
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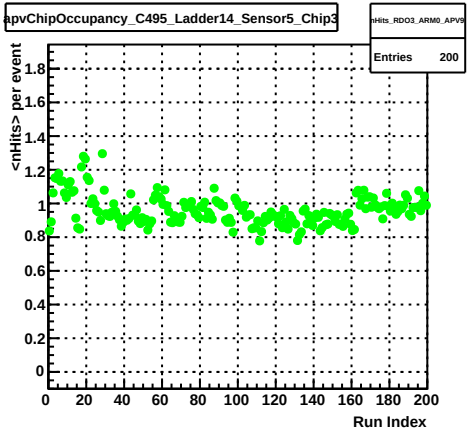
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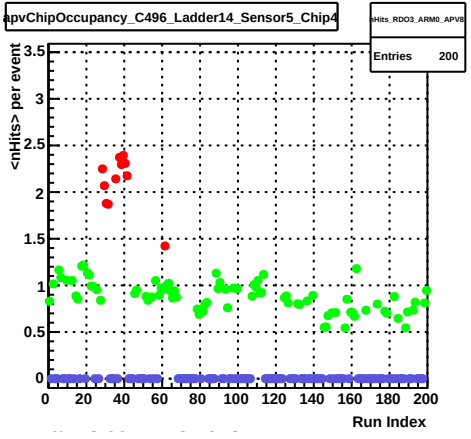
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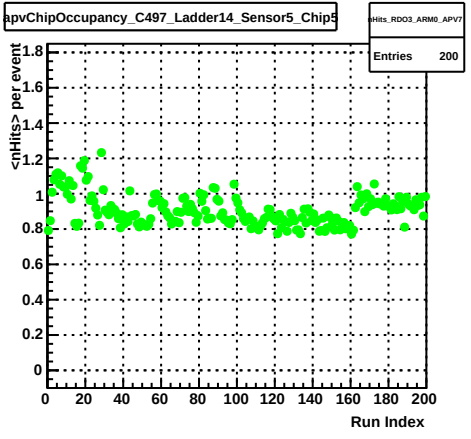
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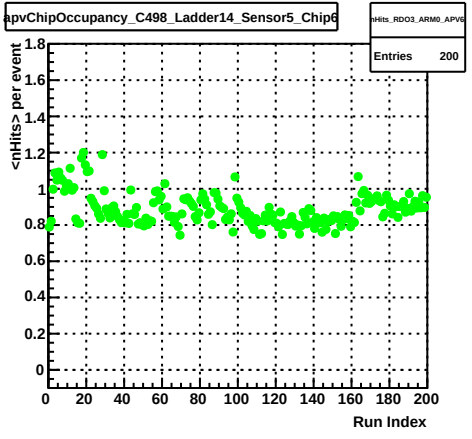
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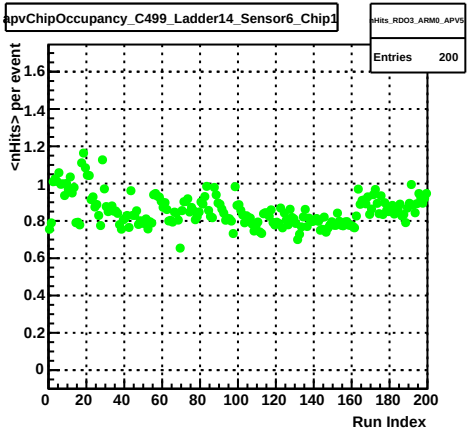
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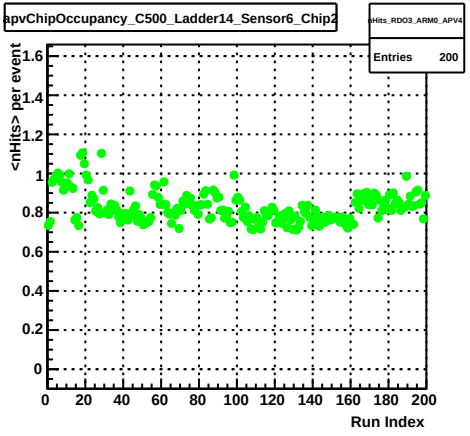
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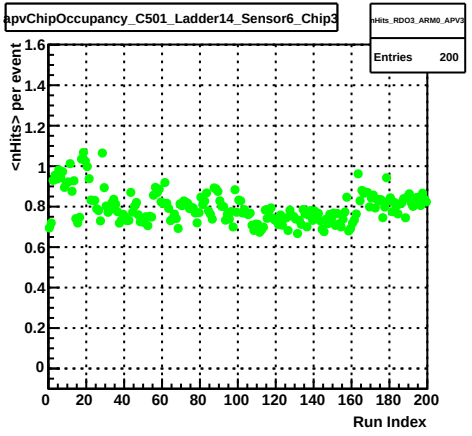
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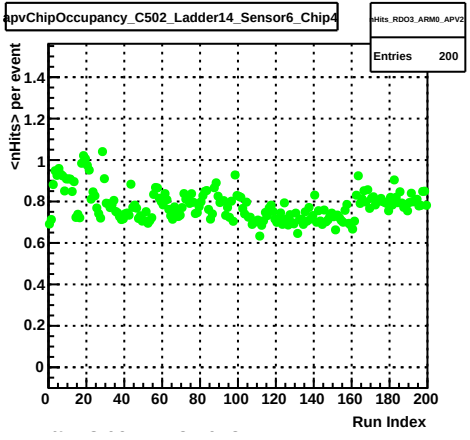
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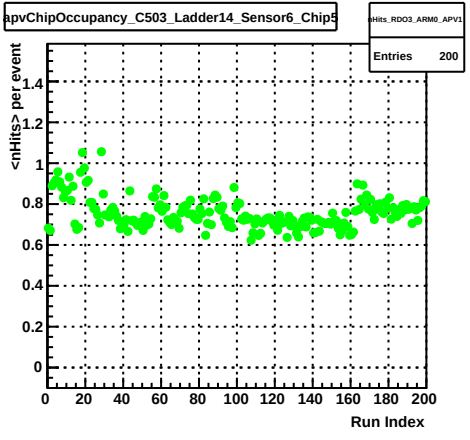
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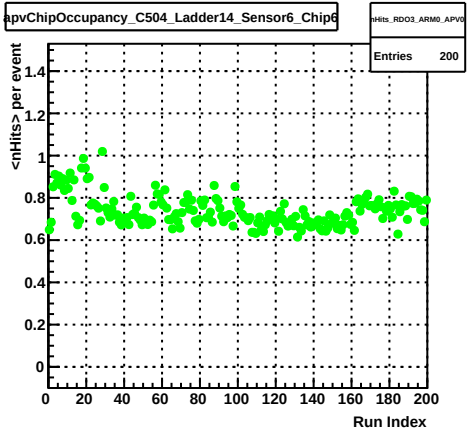
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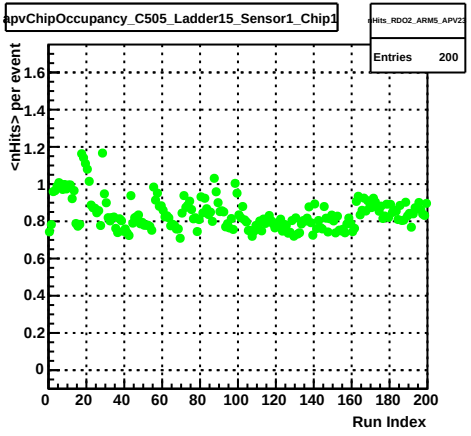
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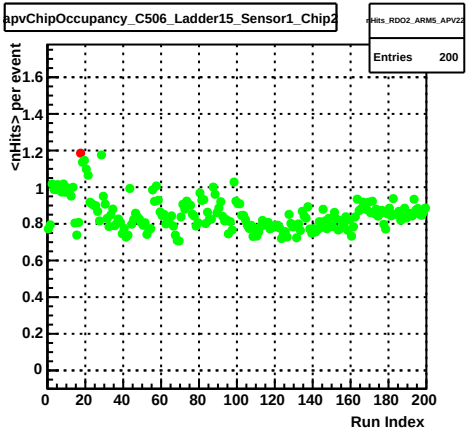
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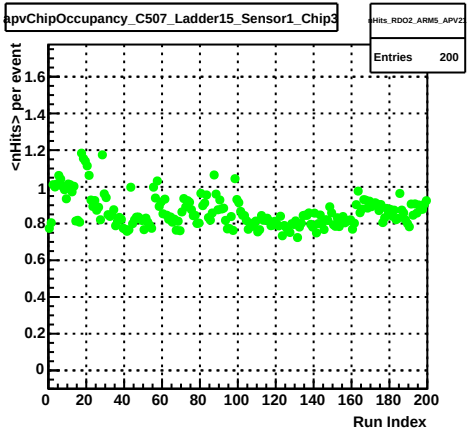
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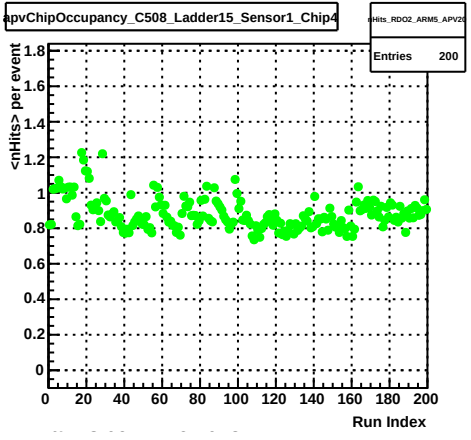
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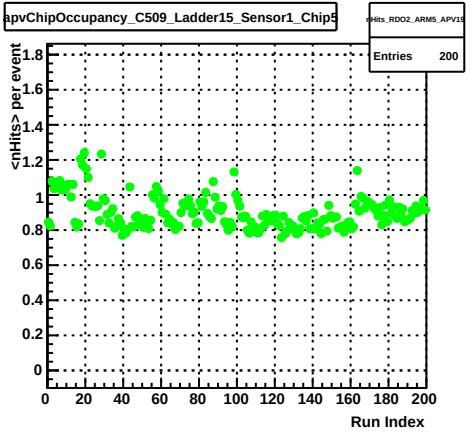
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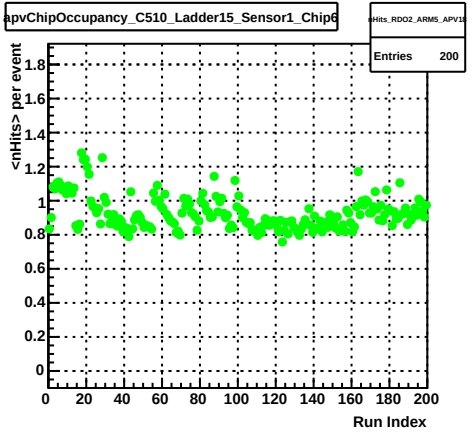
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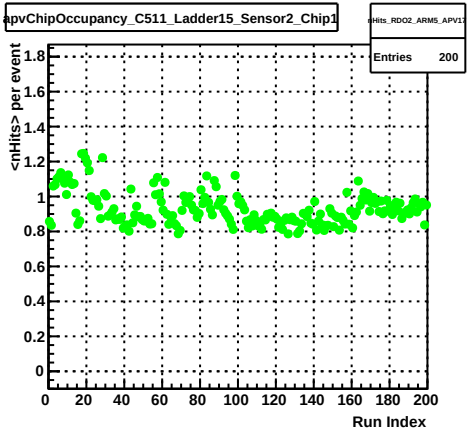
Run Index


apvChipOccupancy_C511_Ladder15_Sensor2_Chip1
nHits_RDO2_ARM5_APV13
Entries 200
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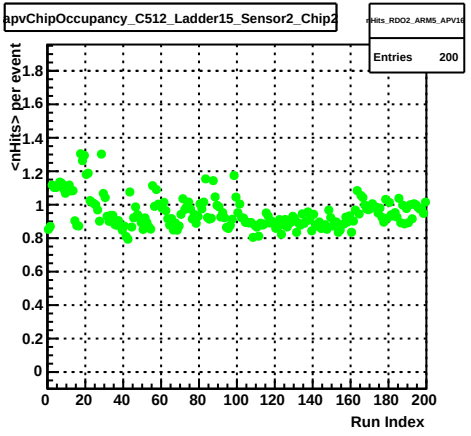
Run Index


apvChipOccupancy_C512_Ladder15_Sensor2_Chip2
nHits_RDO2_ARM5_APV14
Entries 200
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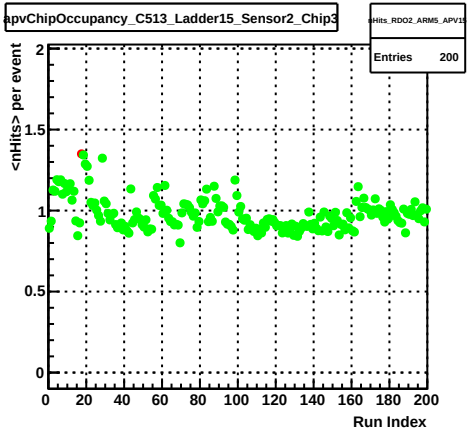
Run Index


apvChipOccupancy_C513_Ladder15_Sensor2_Chip3
nHits_RDO2_ARM5_APV15
Entries 200
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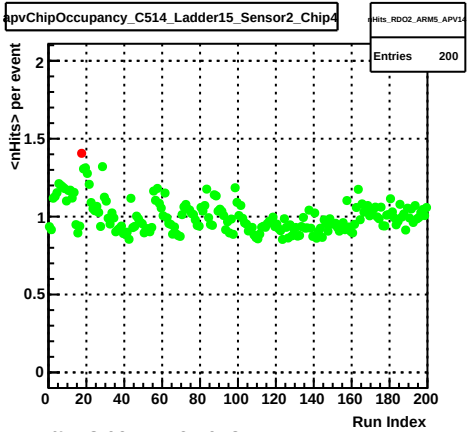
Run Index


apvChipOccupancy_C514_Ladder15_Sensor2_Chip4
nHits_RDO2_ARM5_APV16
Entries 200
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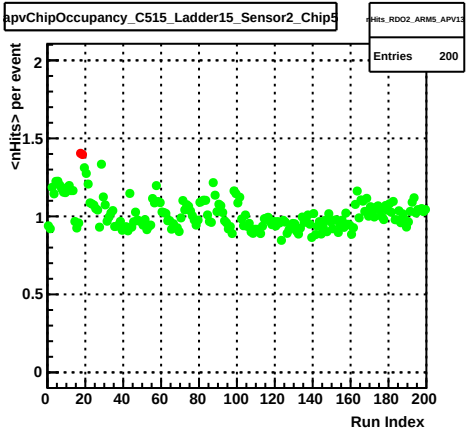
Run Index


apvChipOccupancy_C515_Ladder15_Sensor2_Chip5
nHits_RDO2_ARM5_APV17
Entries 200
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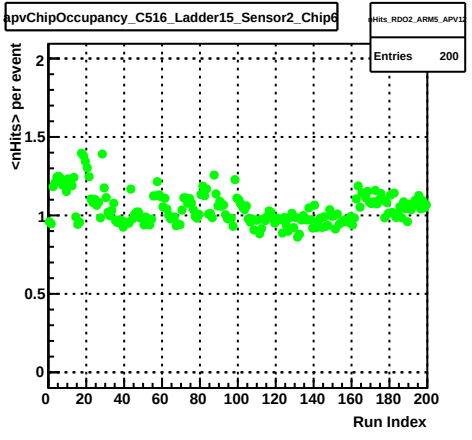
apvChipOccupancy_C516_Ladder15_Sensor2_Chip6
nHits_RDO2_ARM5_APV18
Entries 200
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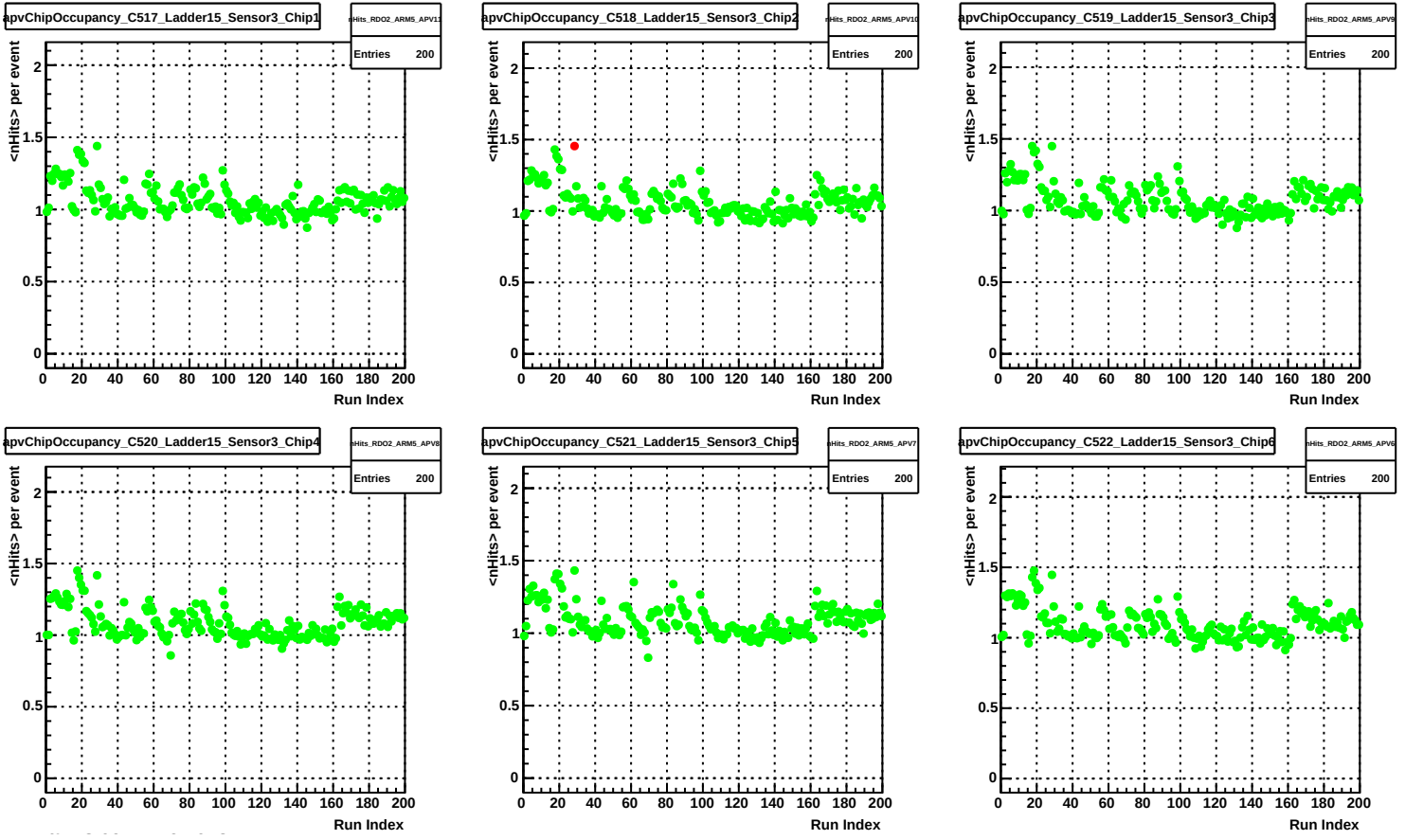
Run Index


apvChipOccupancy_C517_Ladder15_Sensor3_Chip1
nHits_RDO2_ARM5_APV13
Entries 200
apvChipOccupancy_C518_Ladder15_Sensor3_Chip2
nHits_RDO2_ARM5_APV10
Entries 200
apvChipOccupancy_C519_Ladder15_Sensor3_Chip3
nHits_RDO2_ARM5_APV9
Entries 200
apvChipOccupancy_C520_Ladder15_Sensor3_Chip4
nHits_RDO2_ARM5_APV8
Entries 200
apvChipOccupancy_C521_Ladder15_Sensor3_Chip5
nHits_RDO2_ARM5_APV7
Entries 200
apvChipOccupancy_C522_Ladder15_Sensor3_Chip6
nHits_RDO2_ARM5_APV6
Entries 200
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Run Index
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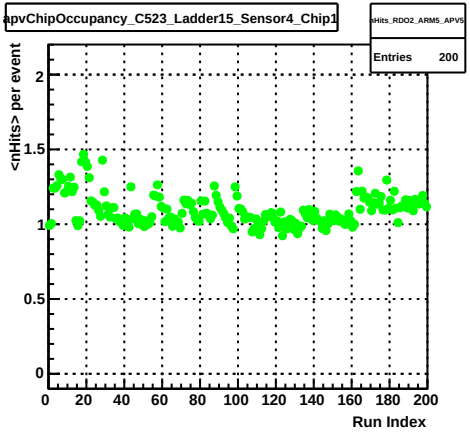
0.5
1
1.5
2
0
20
40
60
80
100
120
140
160
180
200


apvChipOccupancy_C523_Ladder15_Sensor4_Chip1
nHits_RDO2_ARM5_APV5
Entries 200
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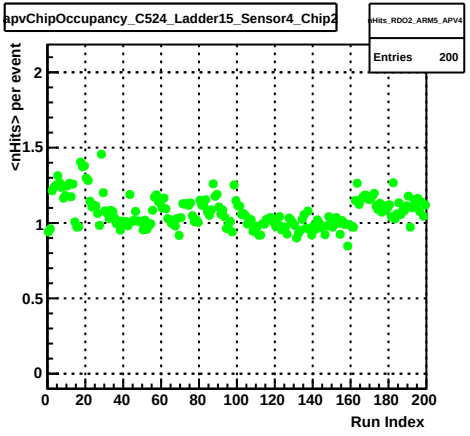
Run Index


apvChipOccupancy_C524_Ladder15_Sensor4_Chip2
nHits_RDO2_ARM5_APV4
Entries 200
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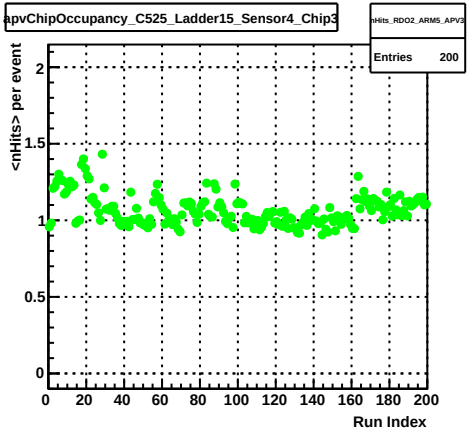
Run Index


apvChipOccupancy_C525_Ladder15_Sensor4_Chip3
nHits_RDO2_ARM5_APV3
Entries 200
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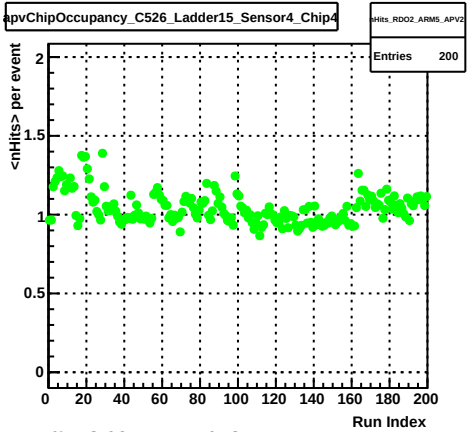
Run Index


apvChipOccupancy_C526_Ladder15_Sensor4_Chip4
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Entries 200
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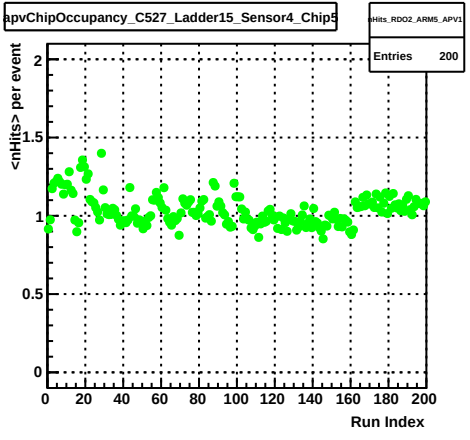
Run Index


apvChipOccupancy_C527_Ladder15_Sensor4_Chip5
nHits_RDO2_ARM5_APV1
Entries 200
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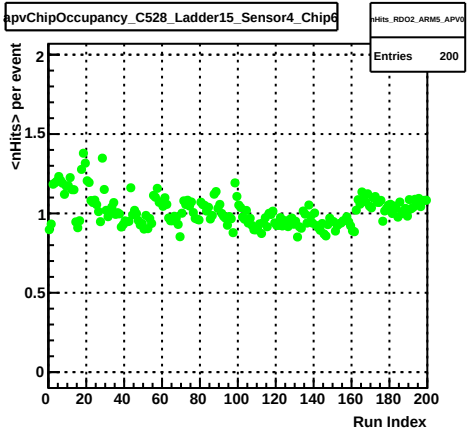
Run Index


apvChipOccupancy_C528_Ladder15_Sensor4_Chip6
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Entries 200
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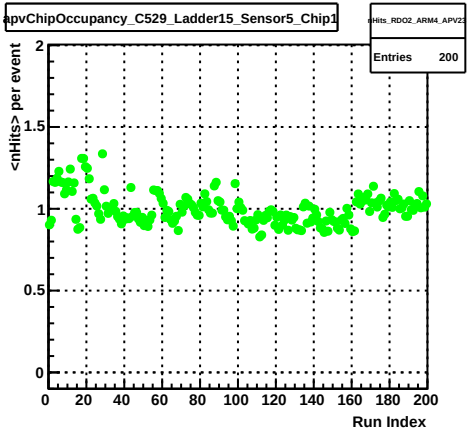
Run Index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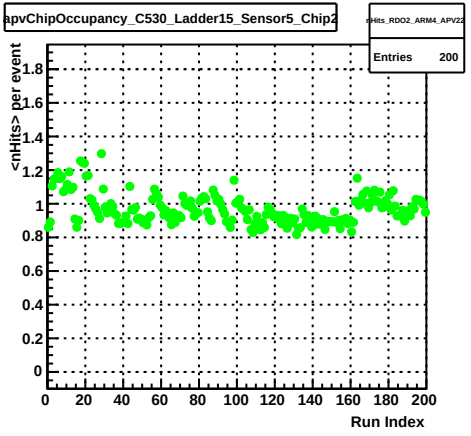
Run Index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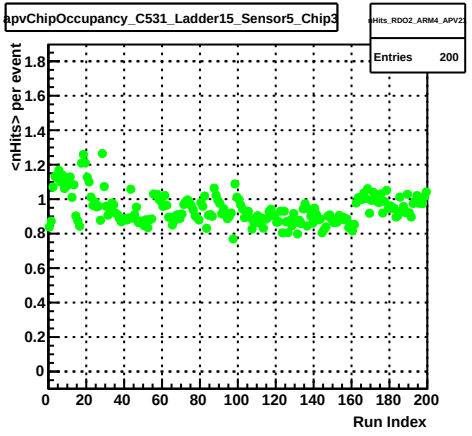
Run Index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Entries 200
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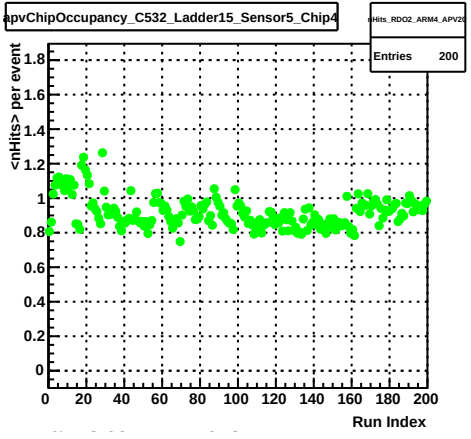
Run Index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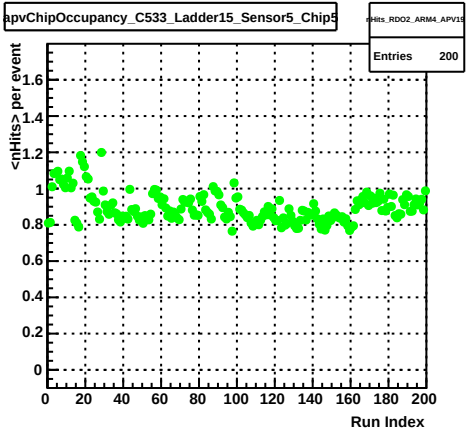
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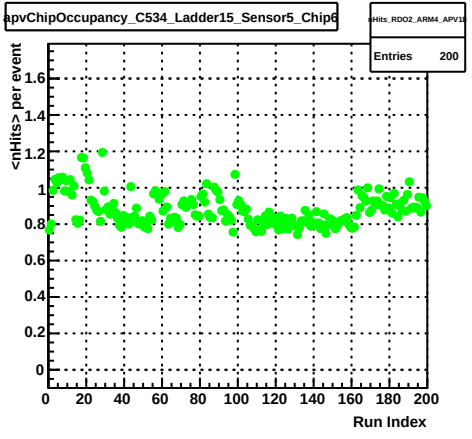
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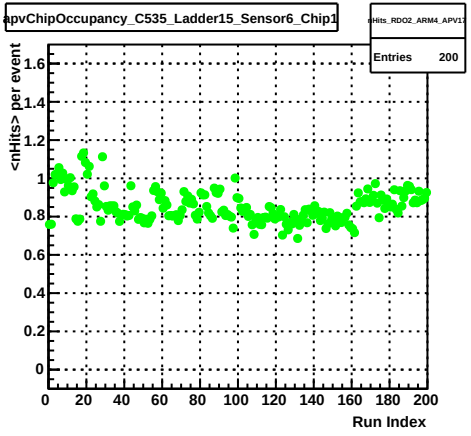
Run Index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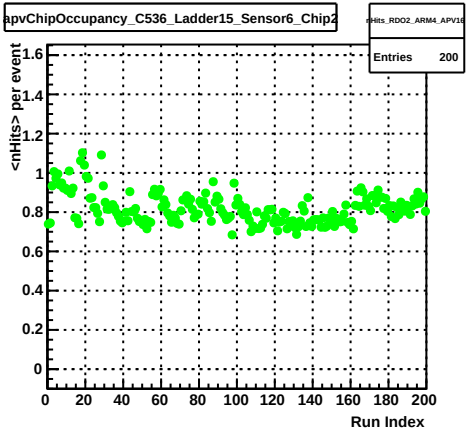
Run Index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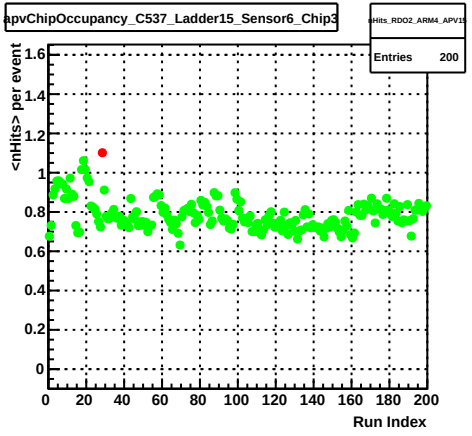
Run Index


apvChipOccupancy_C537_Ladder15_Sensor6_Chip3
Entries 200
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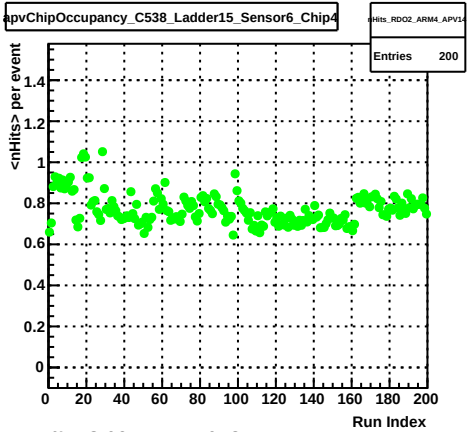
Run Index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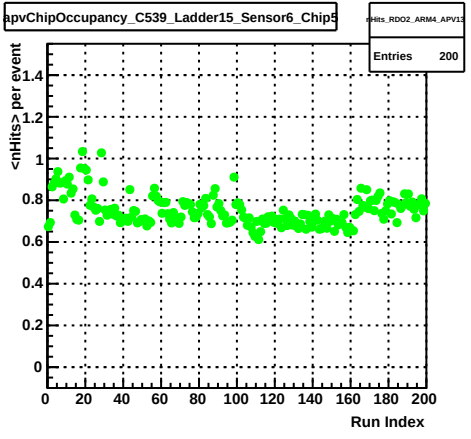
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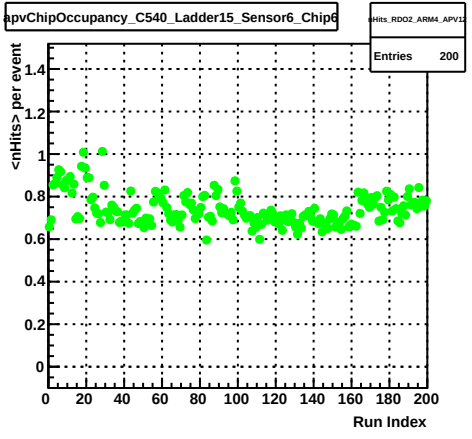
Run Index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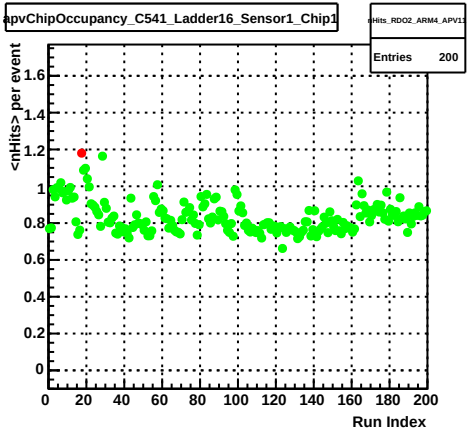
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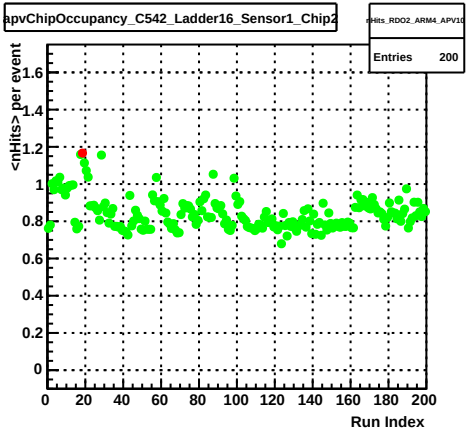
Run Index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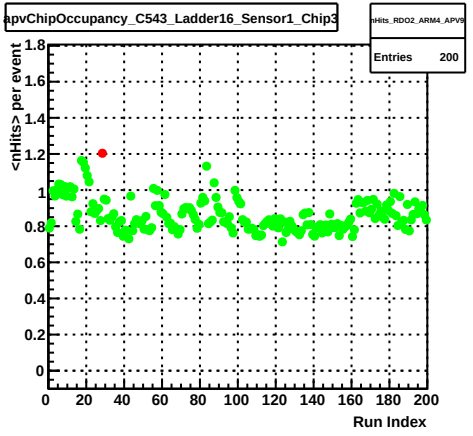
Run Index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Entries 200
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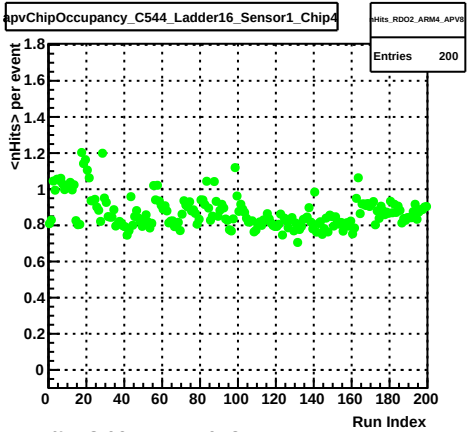
Run Index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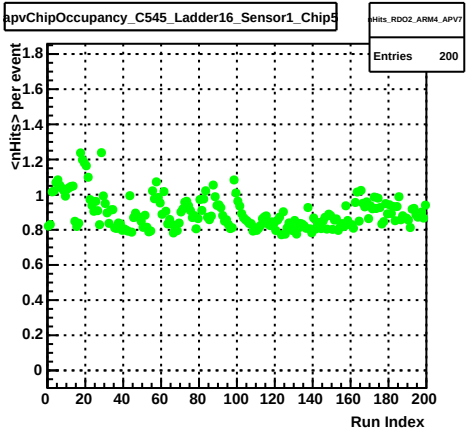
Run Index


apvChipOccupancy_C545_Ladder16_Sensor1_Chip5
nHits_RDO2_ARM4_APV7
Entries 200
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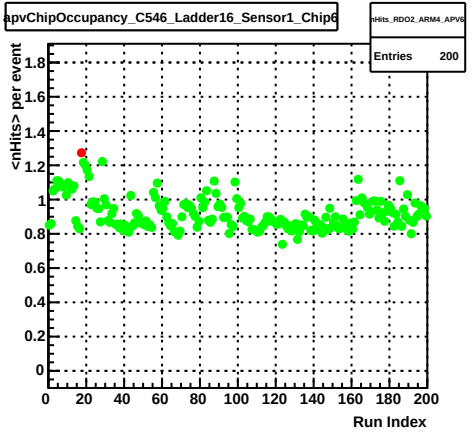
Run Index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nHits_RDO2_ARM4_APV6
Entries 200
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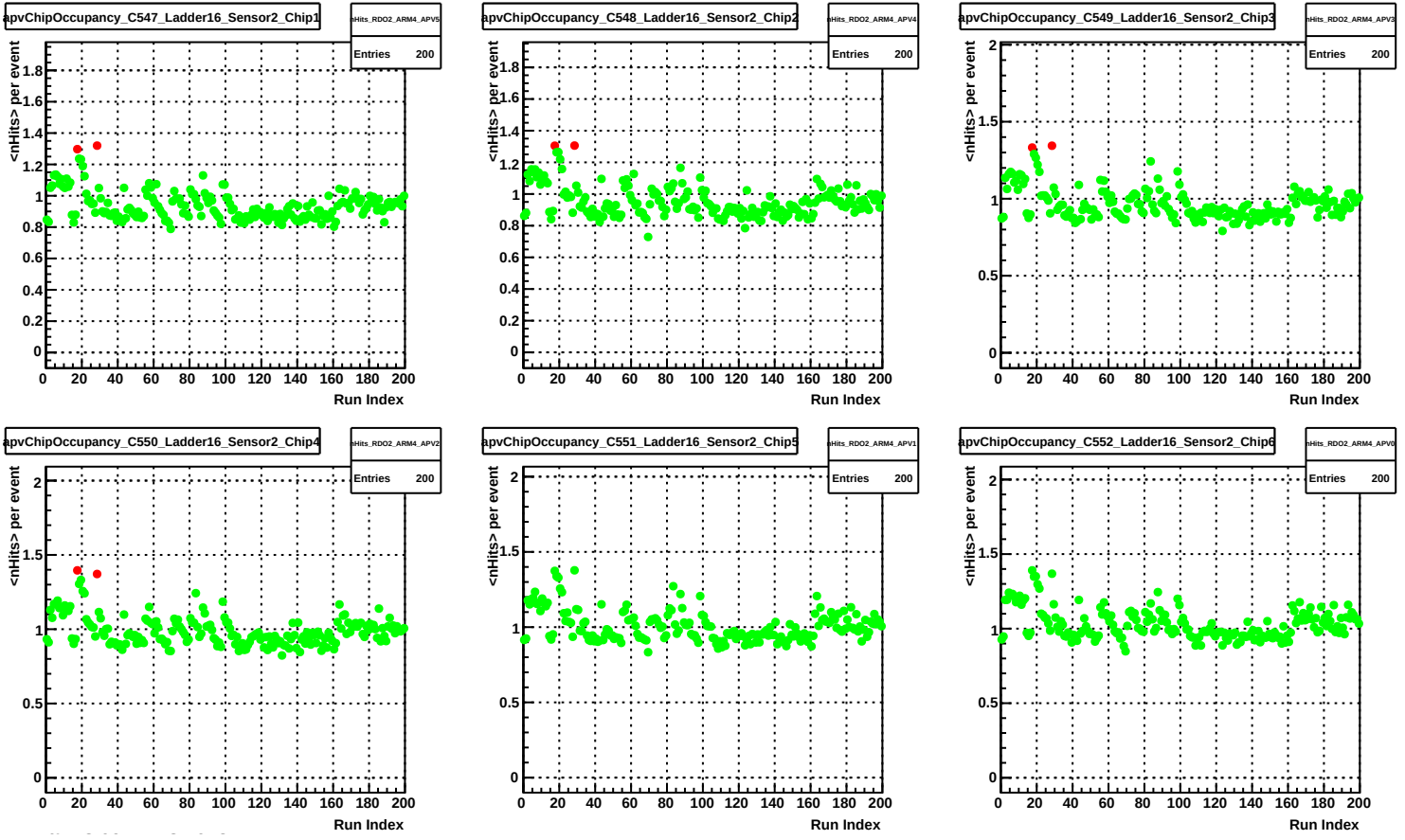
Run Index


apvChipOccupancy_C547_Ladder16_Sensor2_Chip1
nHits_RDO2_ARM4_APV5
Entries 200
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nHits_RDO2_ARM4_APV4
Entries 200
apvChipOccupancy_C549_Ladder16_Sensor2_Chip3
nHits_RDO2_ARM4_APV3
Entries 200
apvChipOccupancy_C550_Ladder16_Sensor2_Chip4
nHits_RDO2_ARM4_APV2
Entries 200
apvChipOccupancy_C551_Ladder16_Sensor2_Chip5
nHits_RDO2_ARM4_APV1
Entries 200
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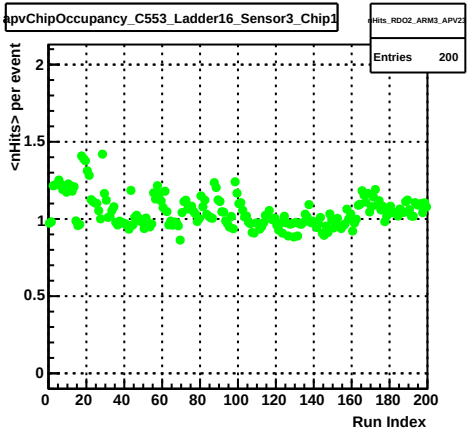
Run Index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Entries 200
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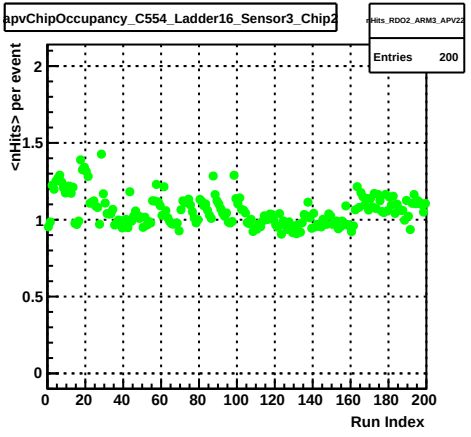
Run Index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Entries 200
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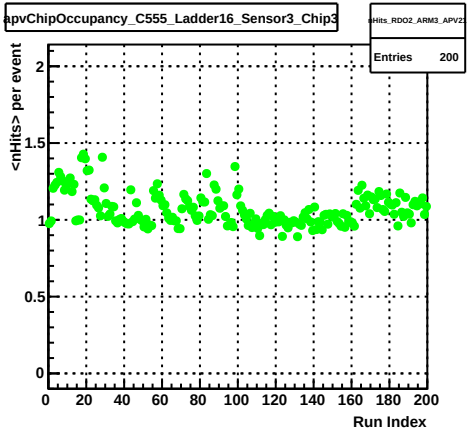
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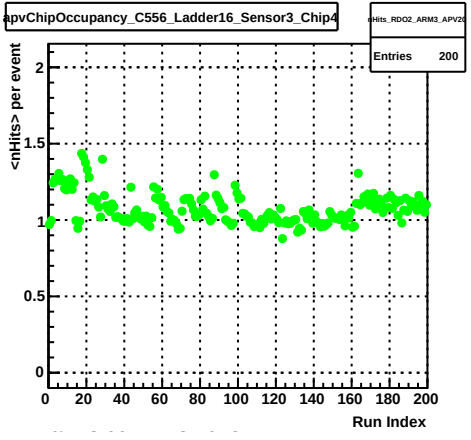
Run Index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Entries 200
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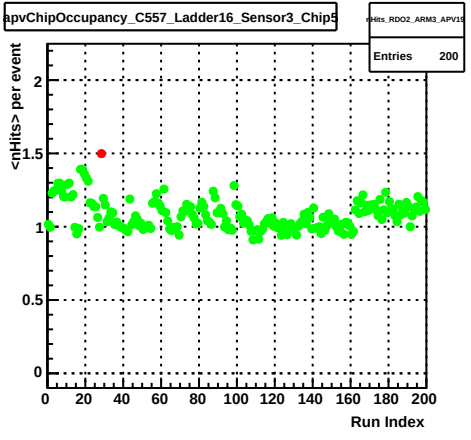
Run Index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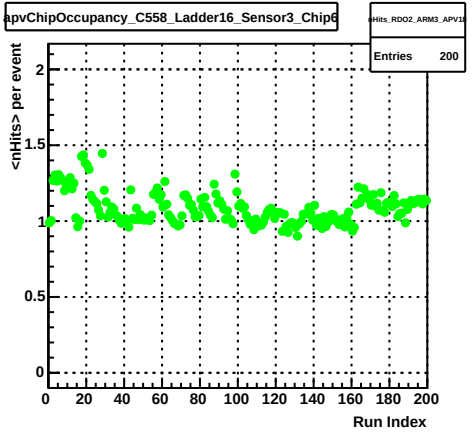
Run Index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Entries 200
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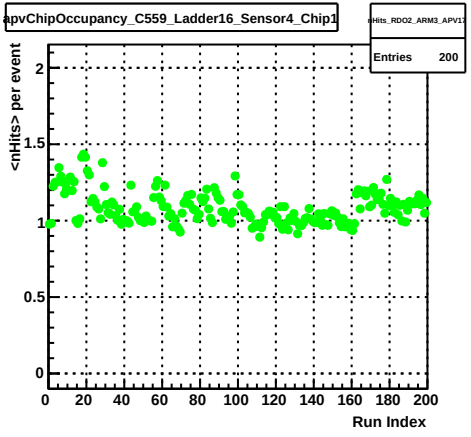
Run Index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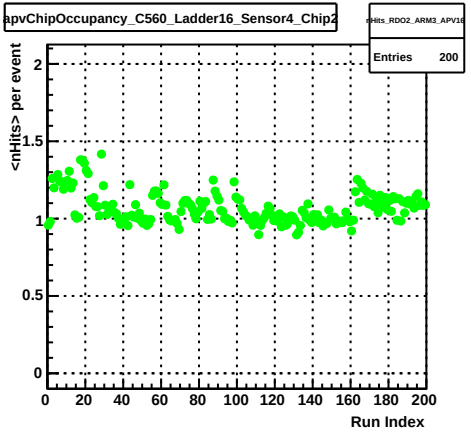
Run Index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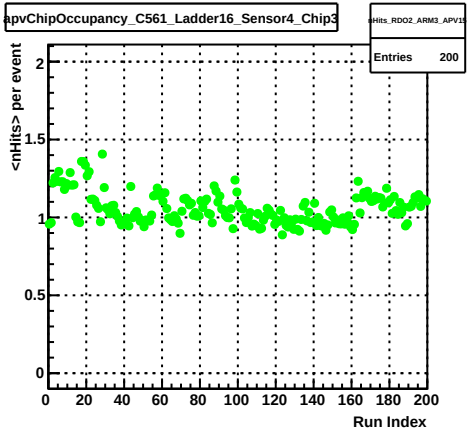
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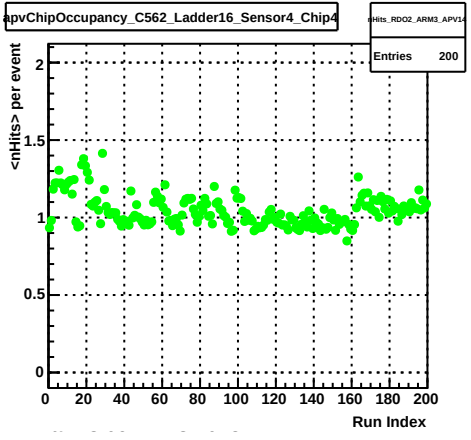
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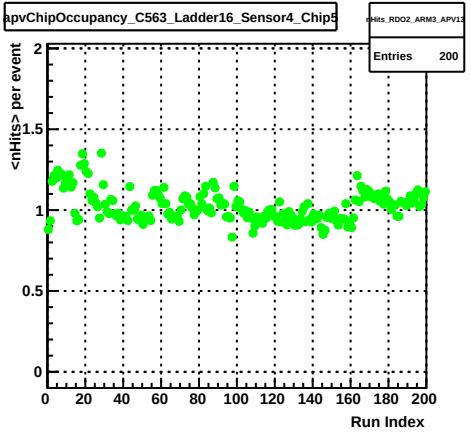
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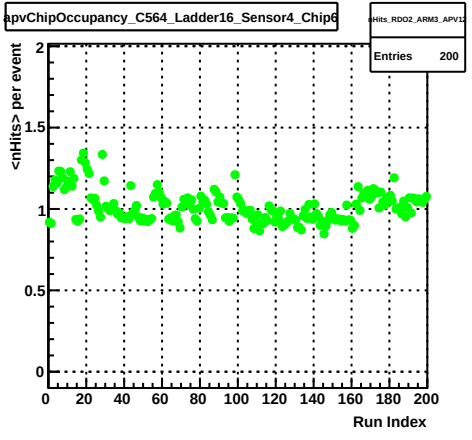
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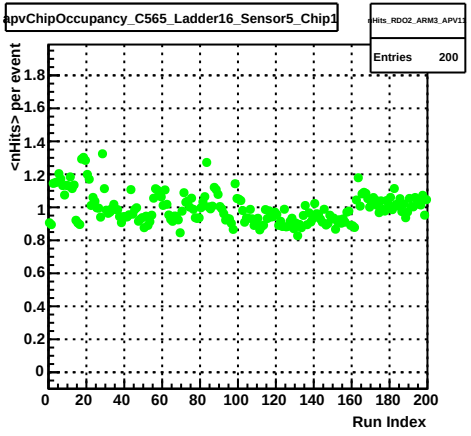
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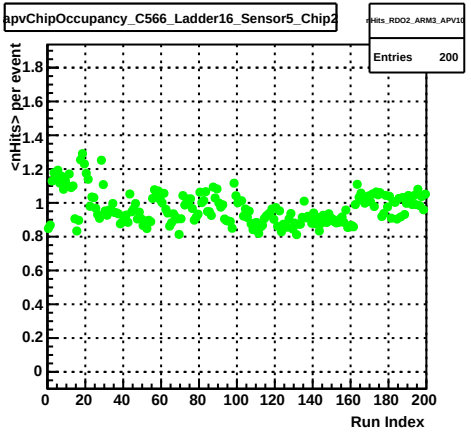
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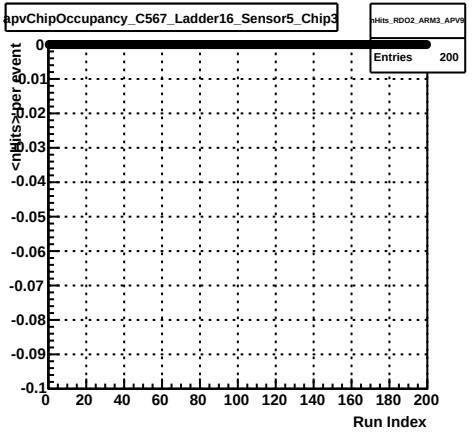
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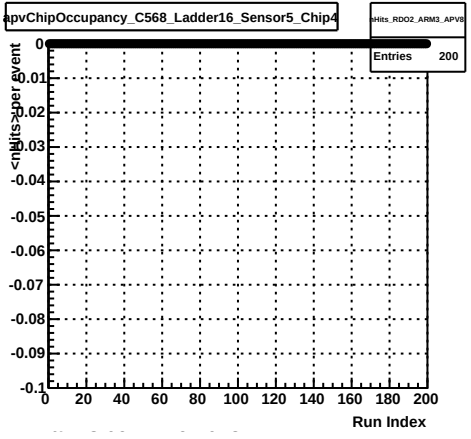
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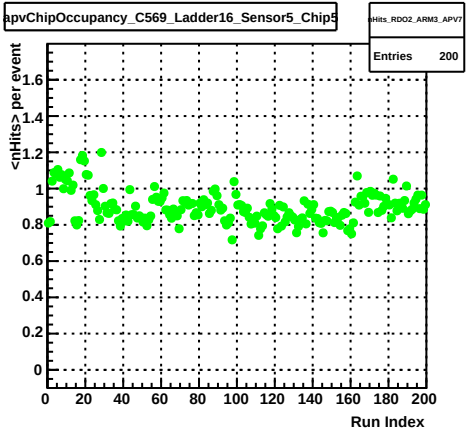
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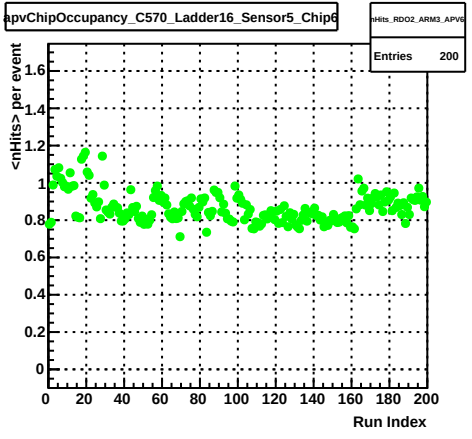
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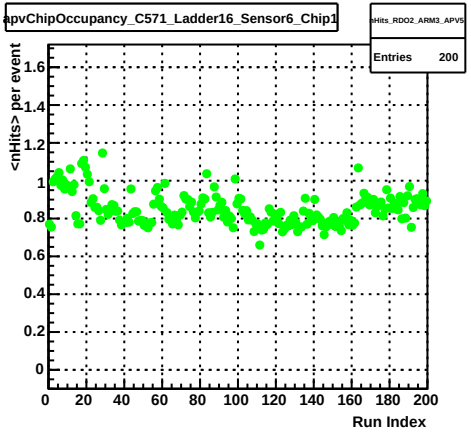
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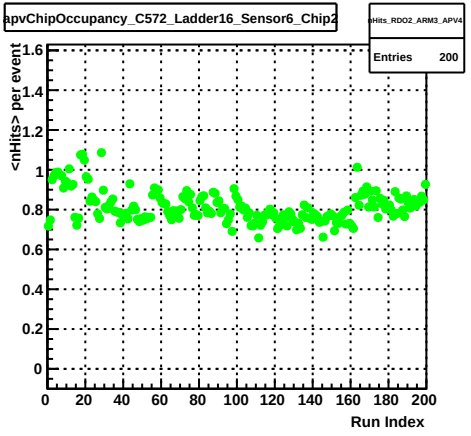
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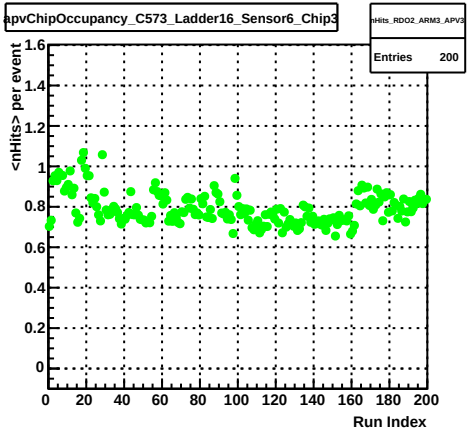
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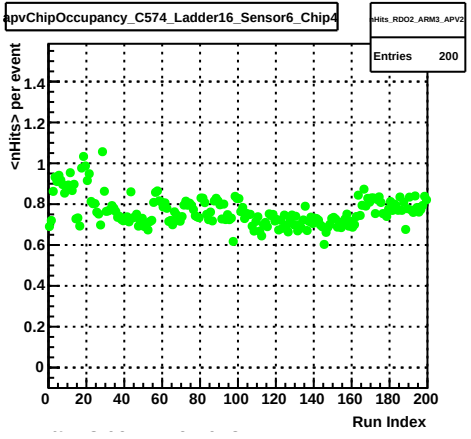
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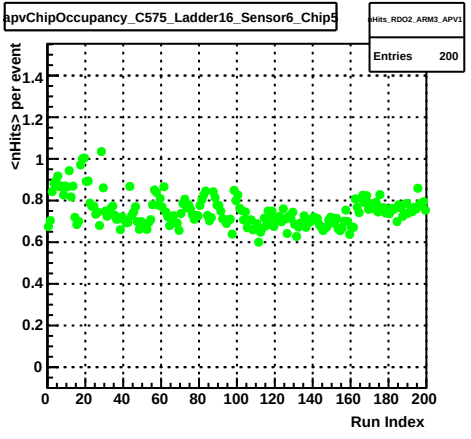
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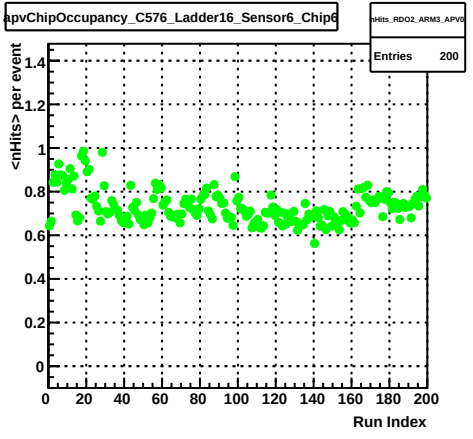
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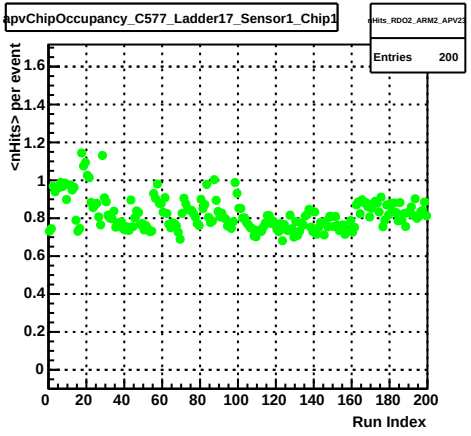
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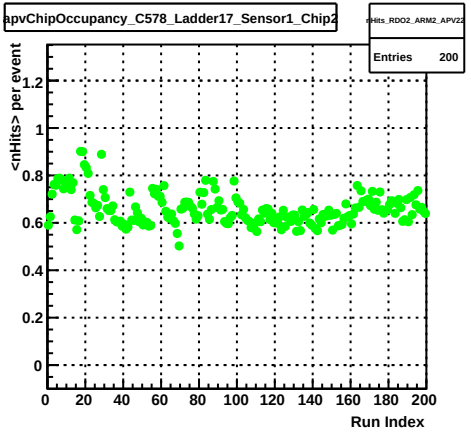
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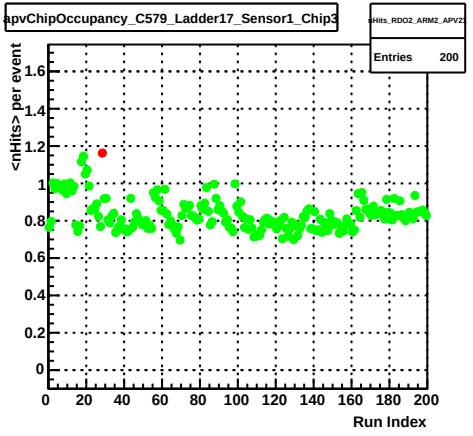
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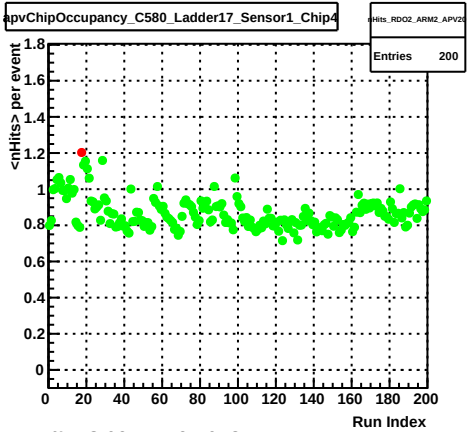
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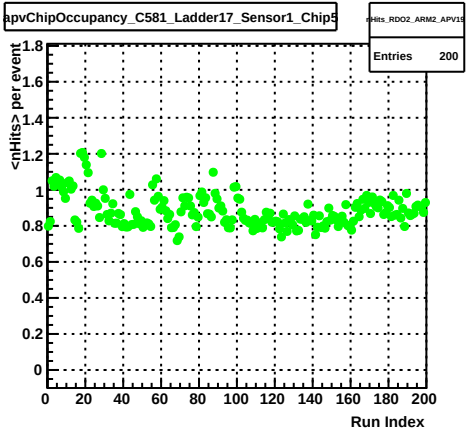
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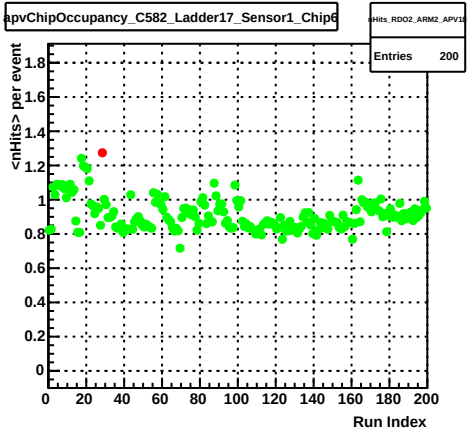
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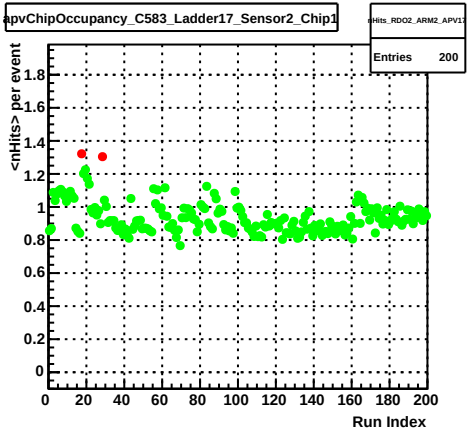
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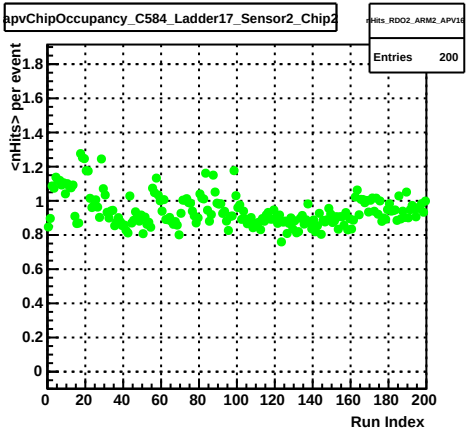
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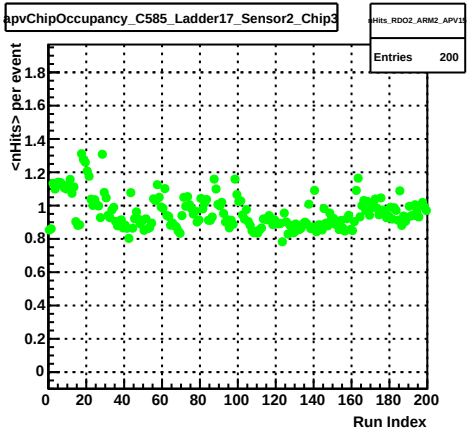
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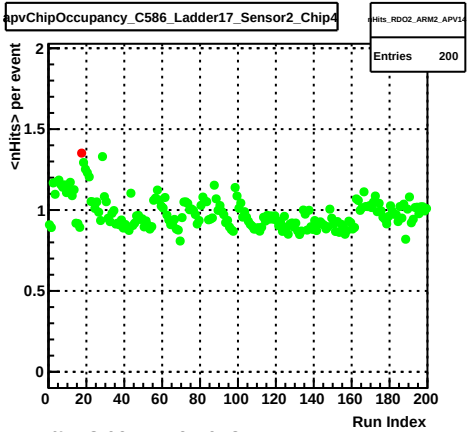
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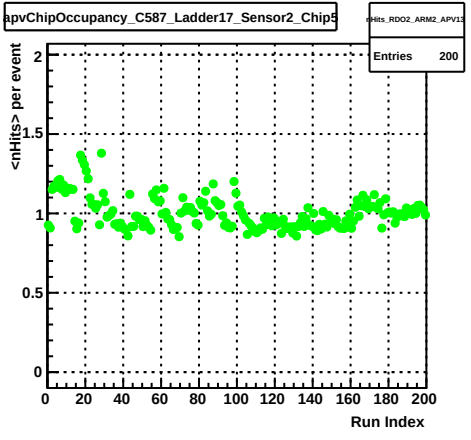
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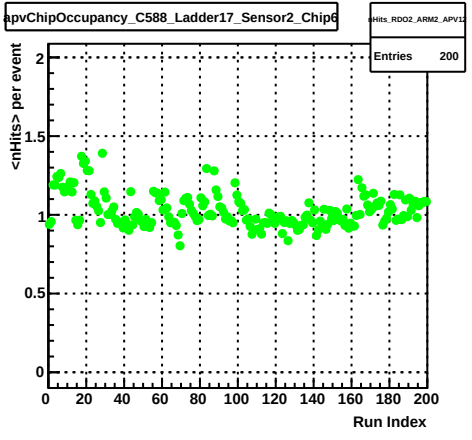
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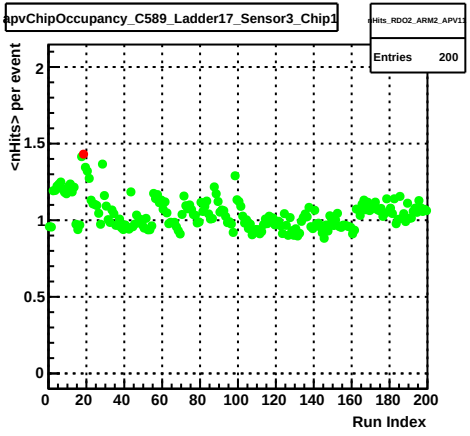
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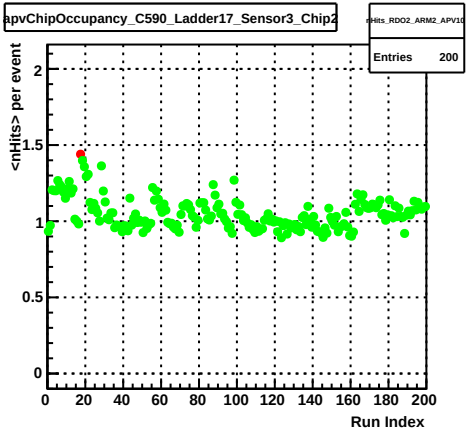
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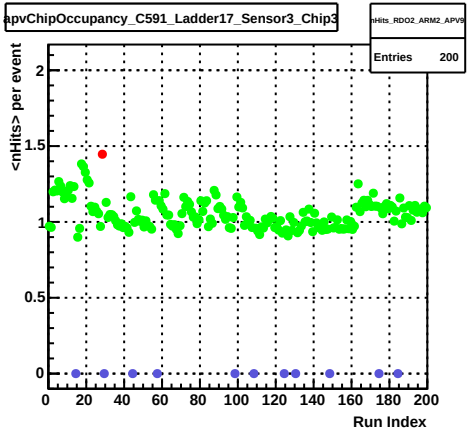
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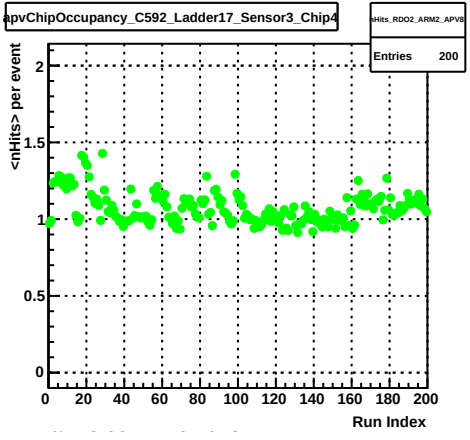
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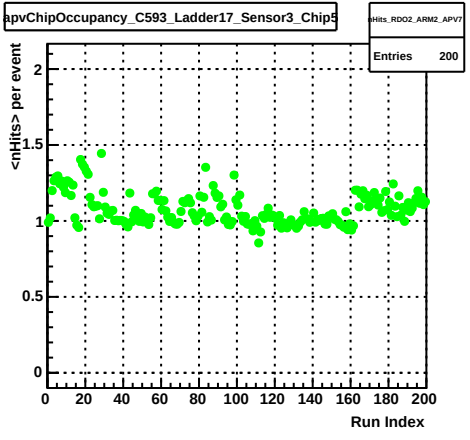
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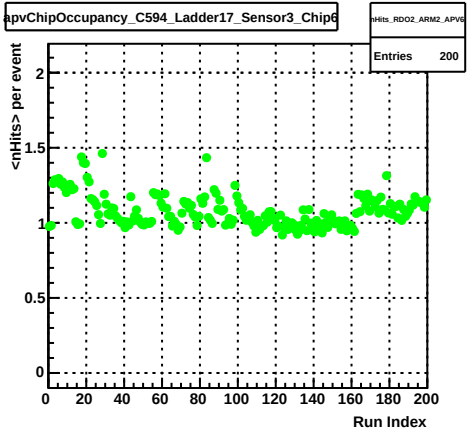
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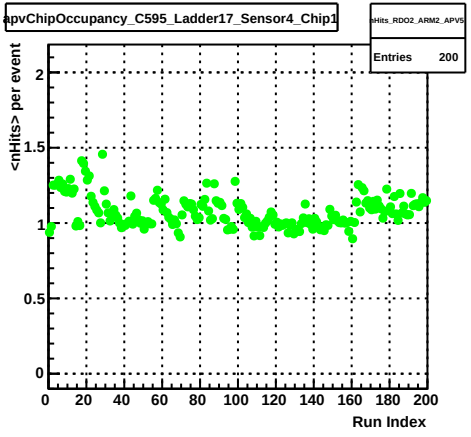
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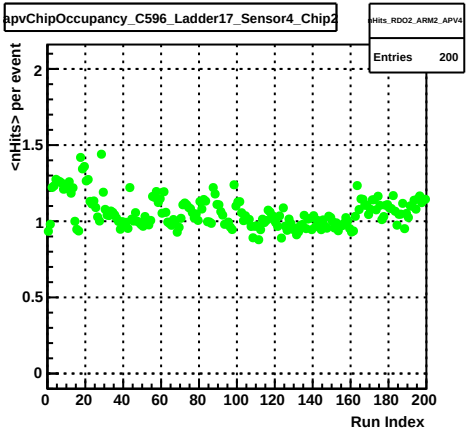
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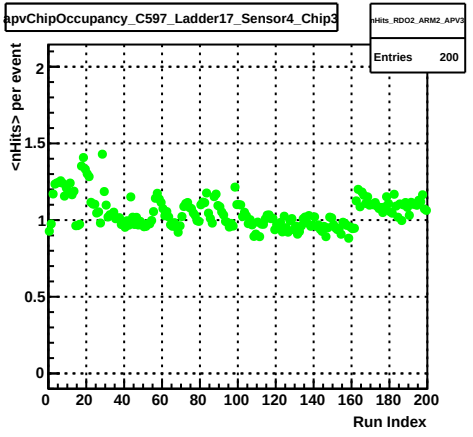
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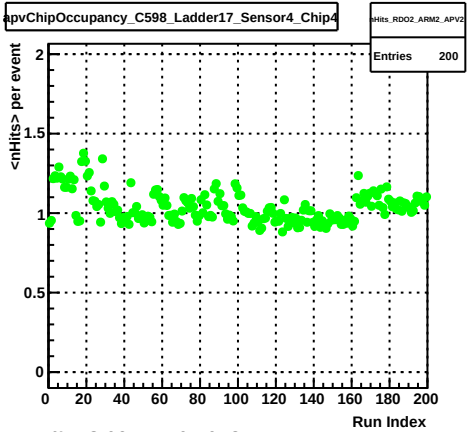
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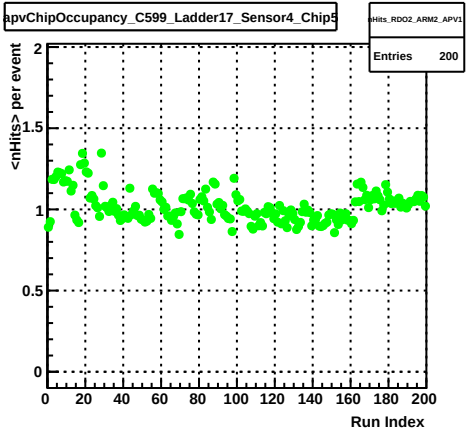
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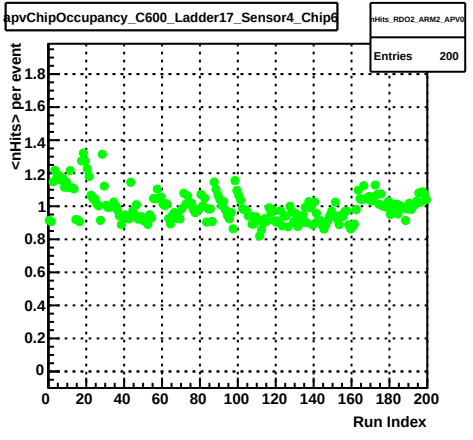
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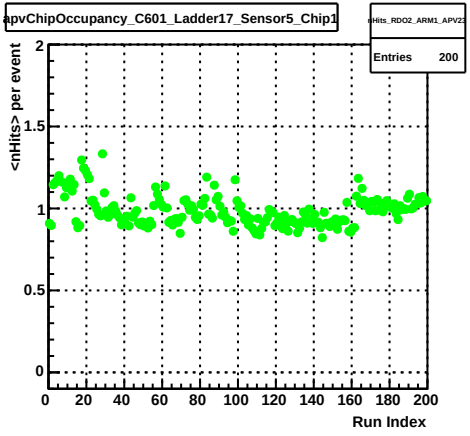
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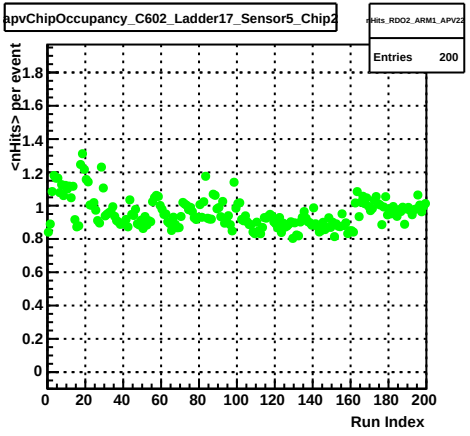
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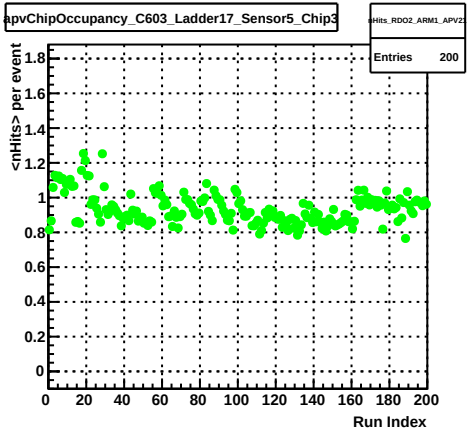
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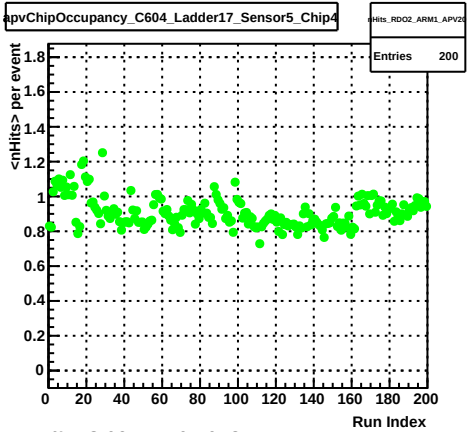
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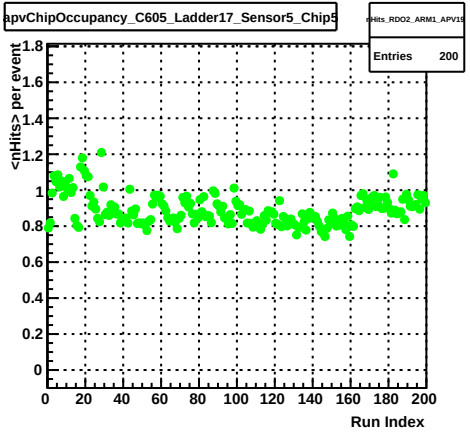
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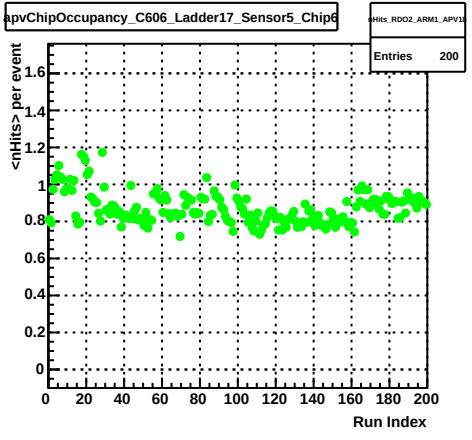
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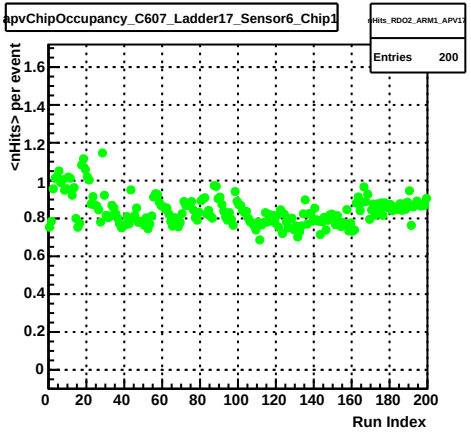
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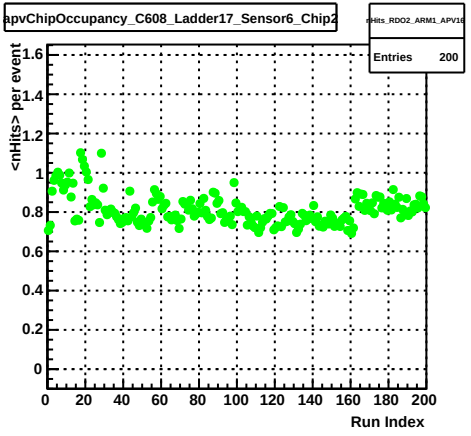
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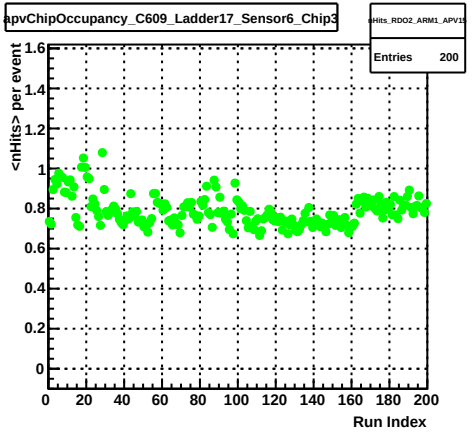
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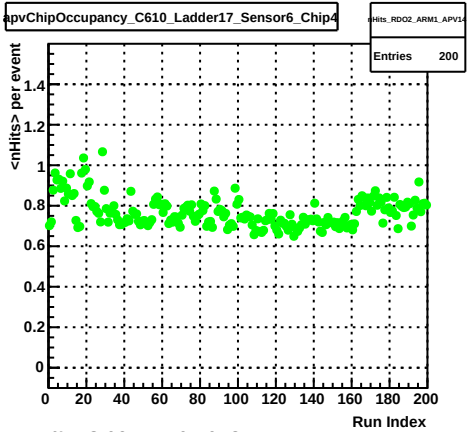
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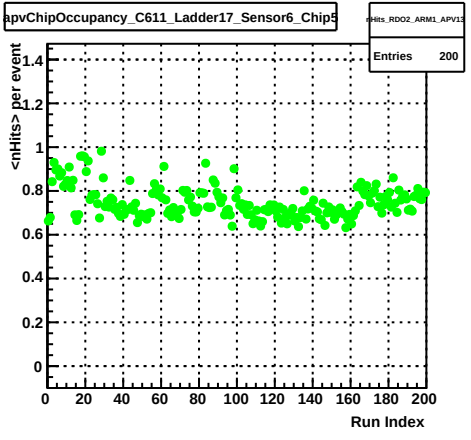
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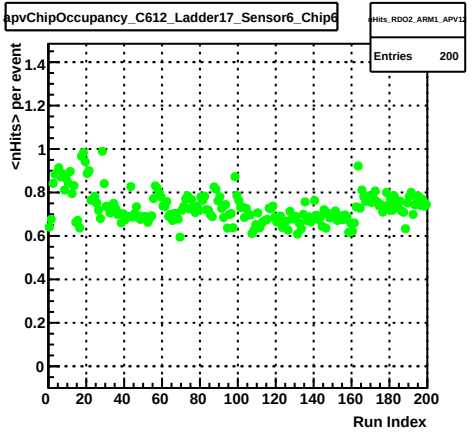
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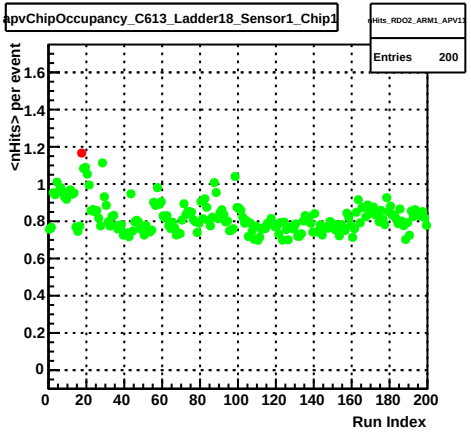
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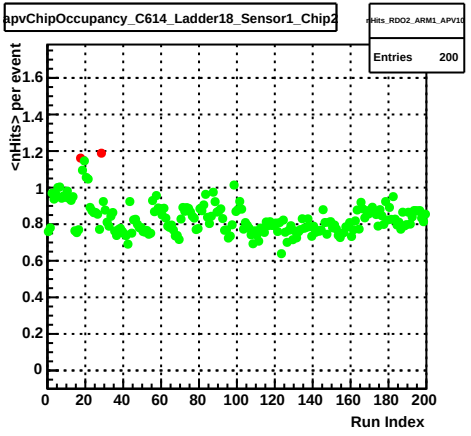
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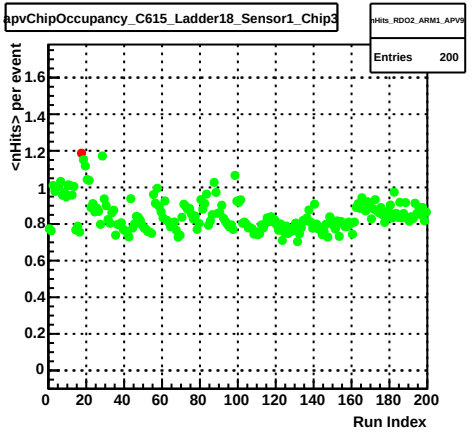
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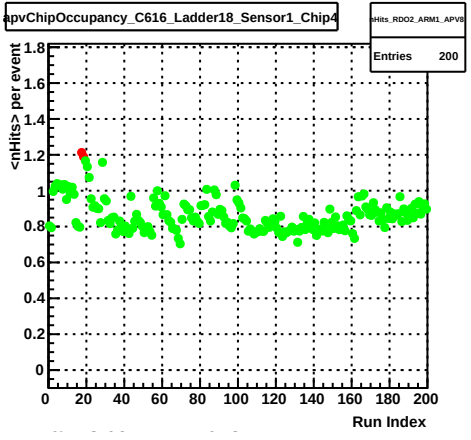
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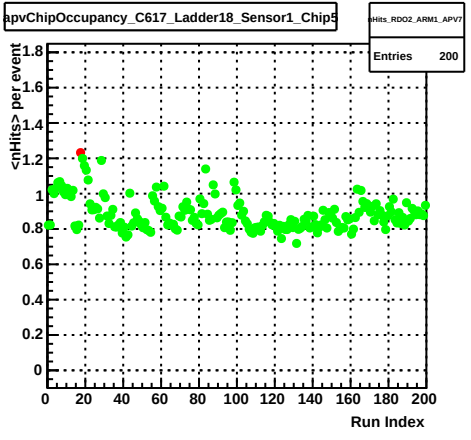
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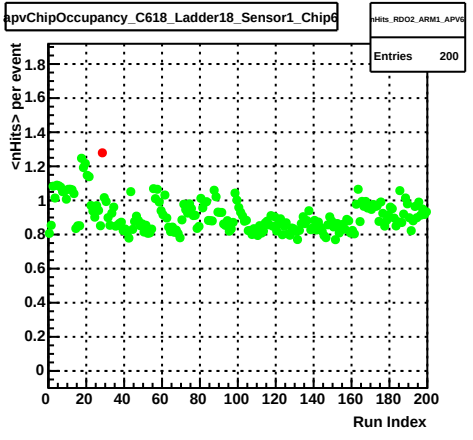
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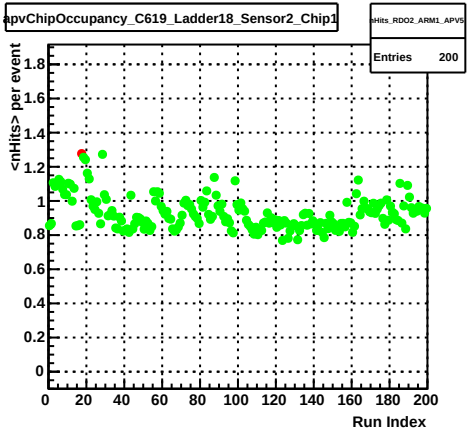
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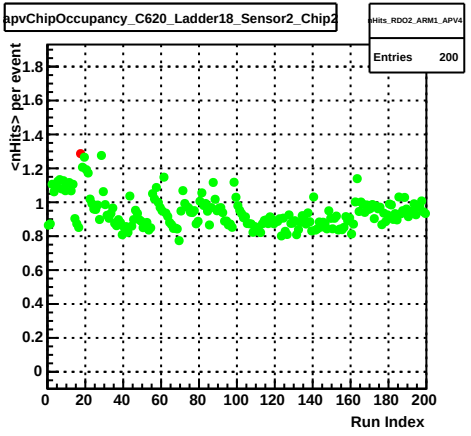
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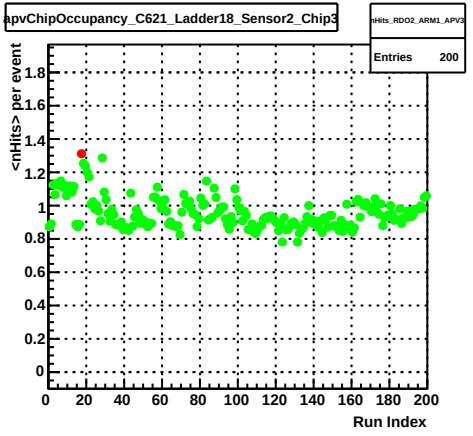
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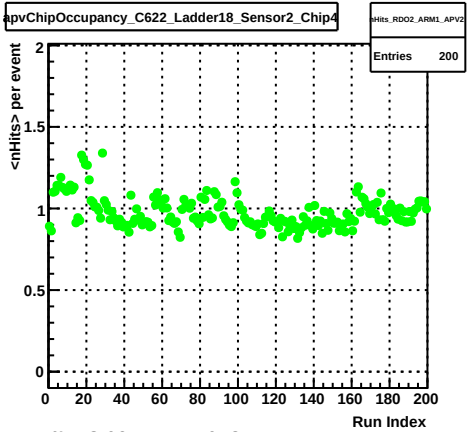
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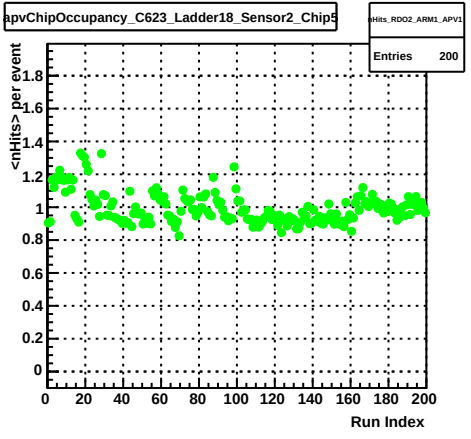
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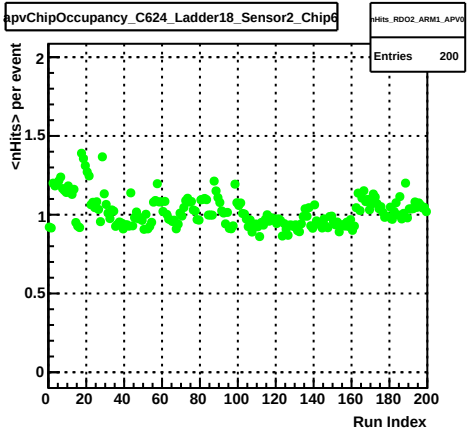
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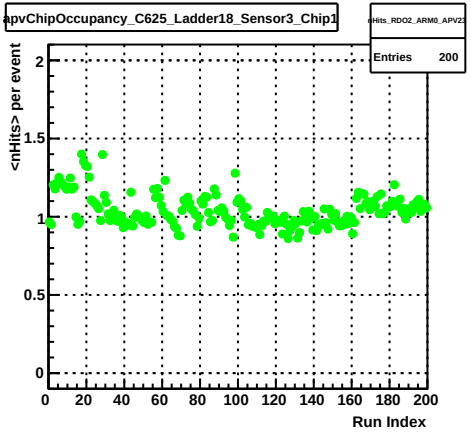
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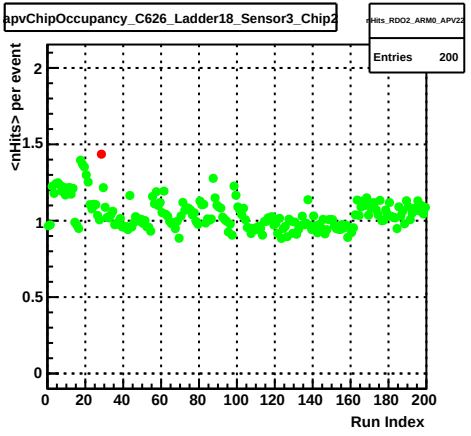
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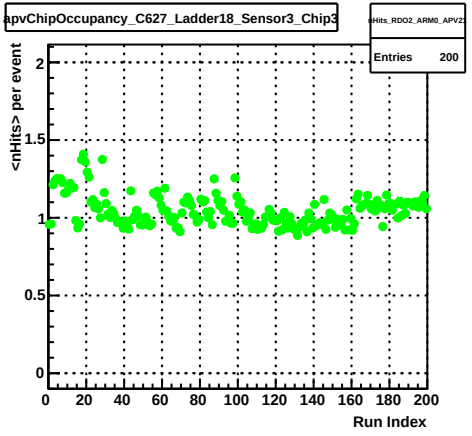
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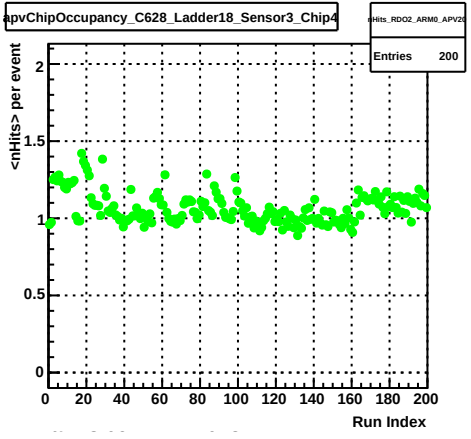
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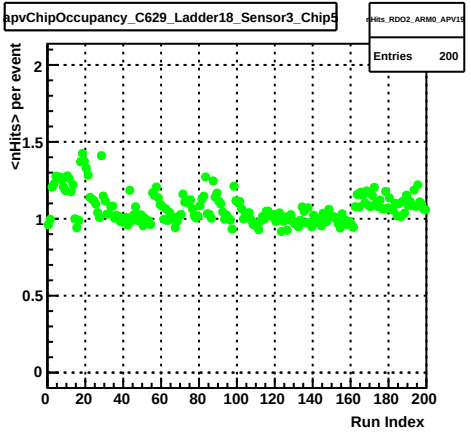
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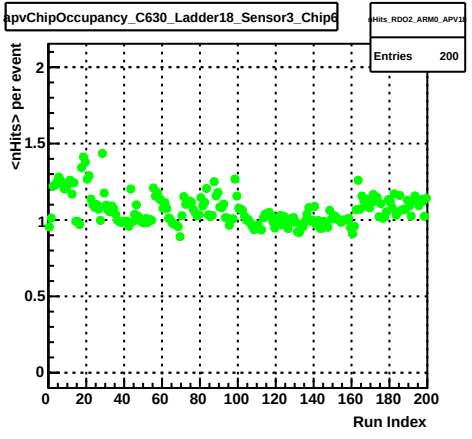
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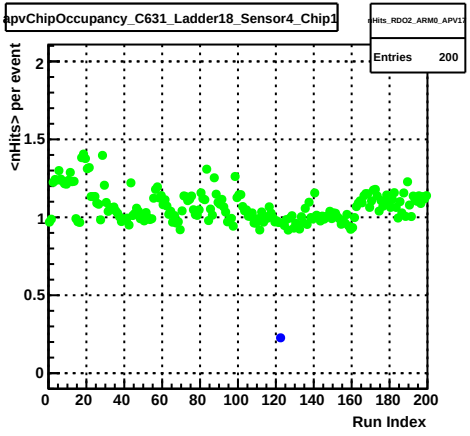
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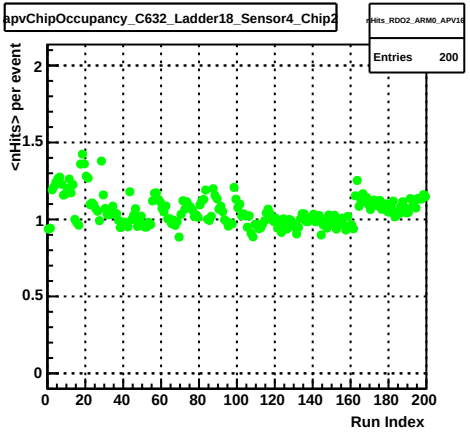
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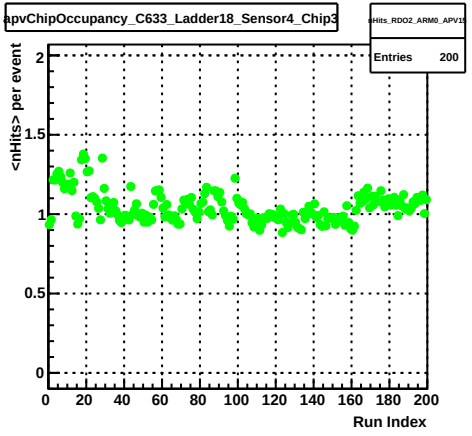
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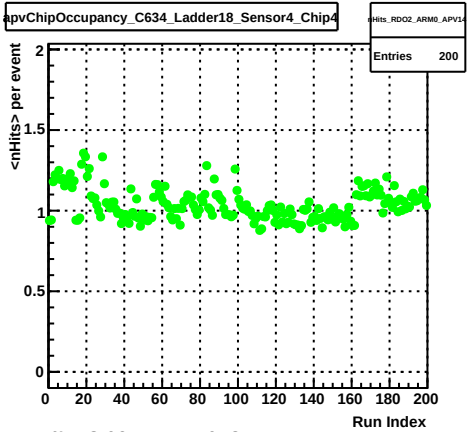
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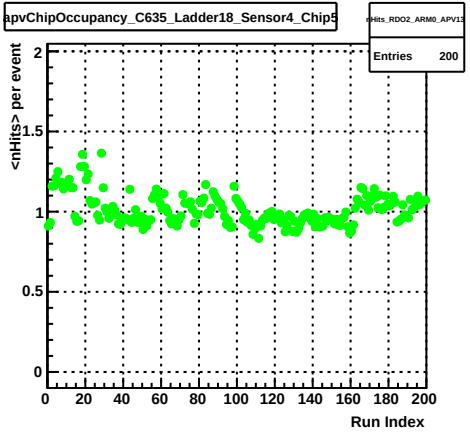
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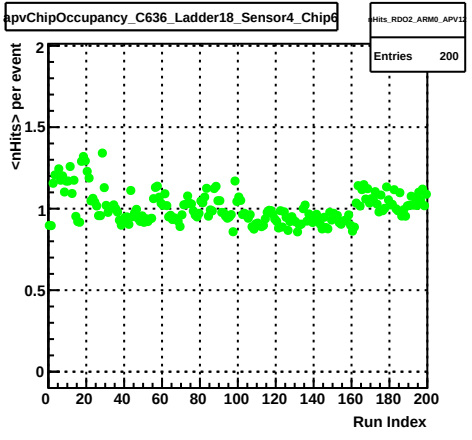
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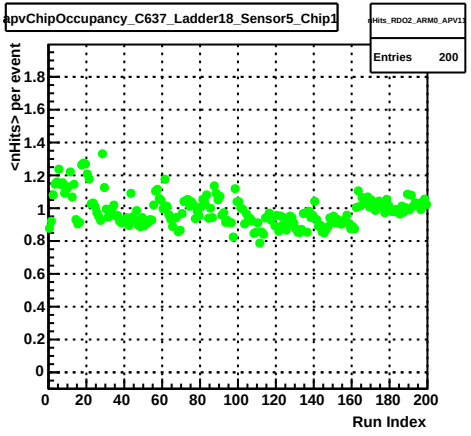
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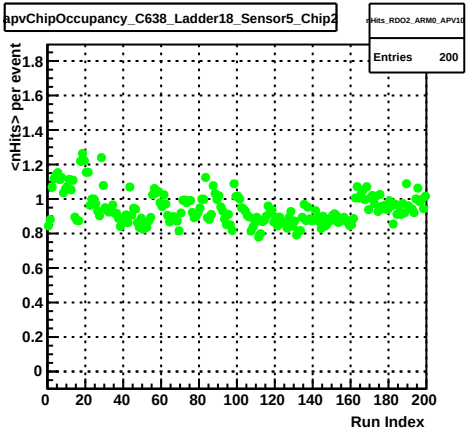
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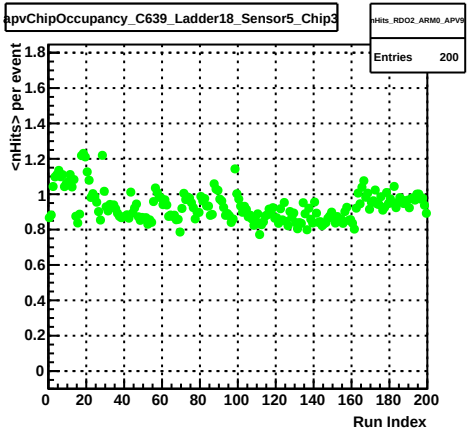
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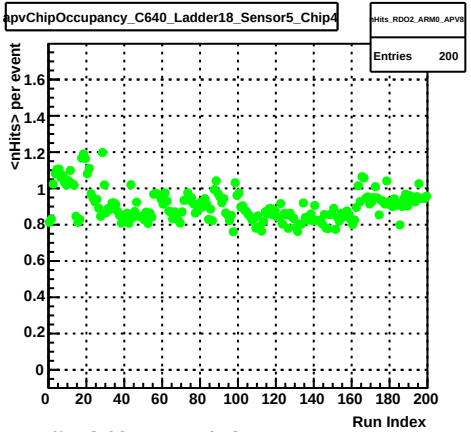
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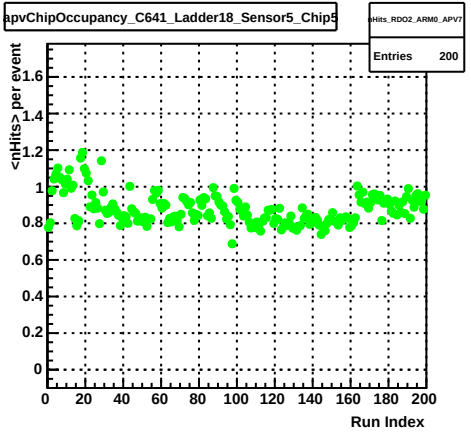
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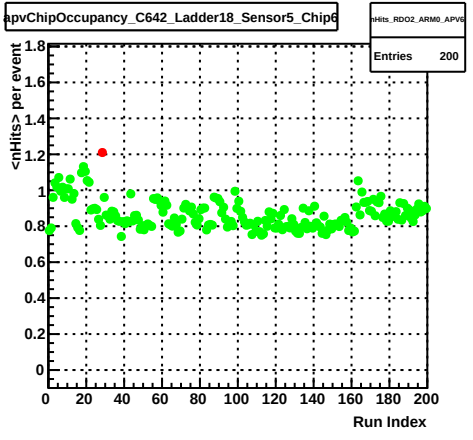
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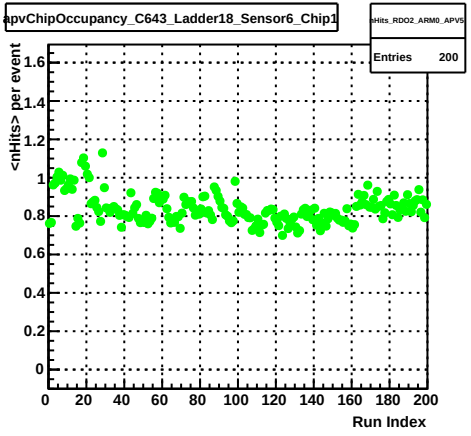
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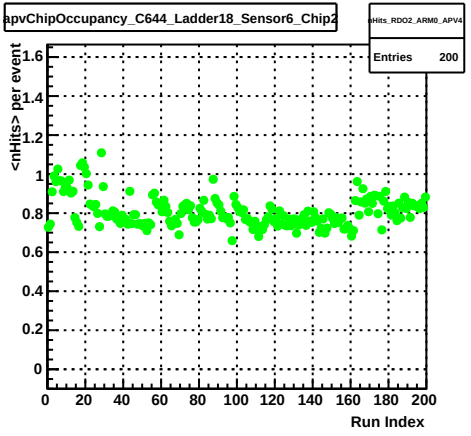
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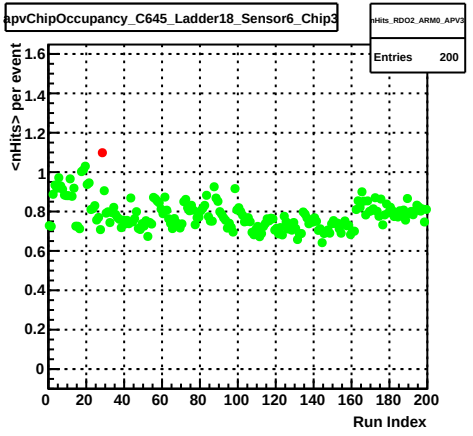
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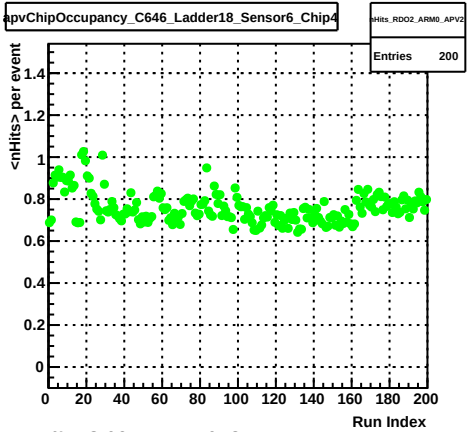
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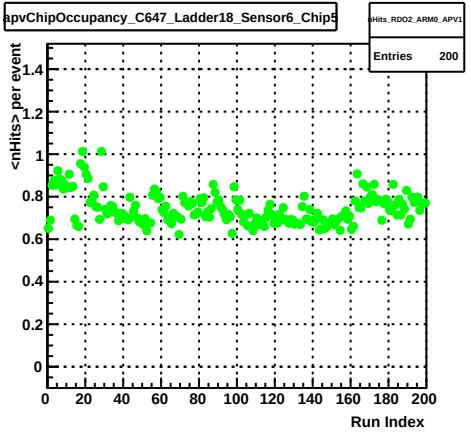
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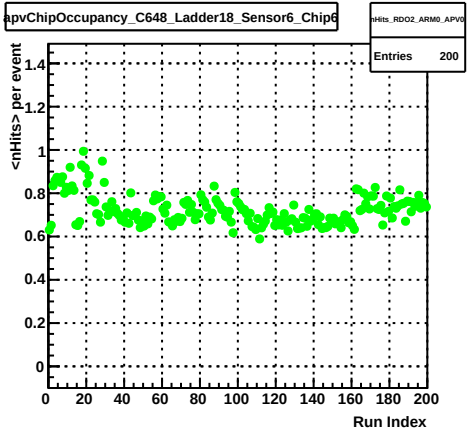
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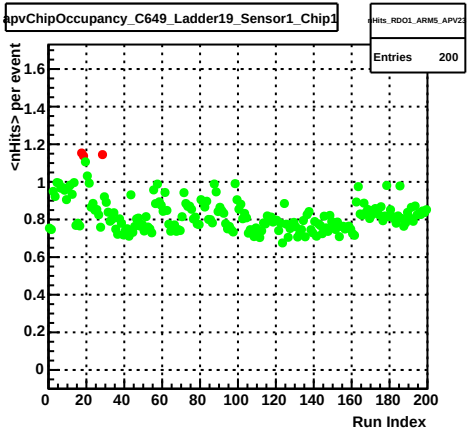
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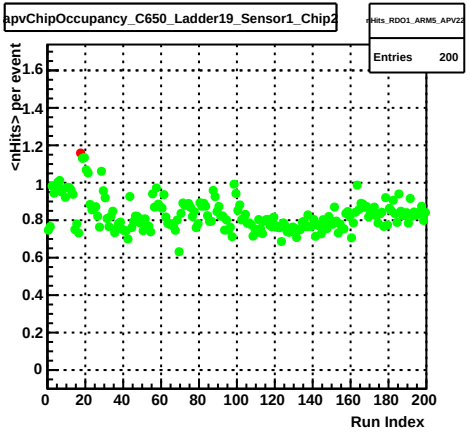
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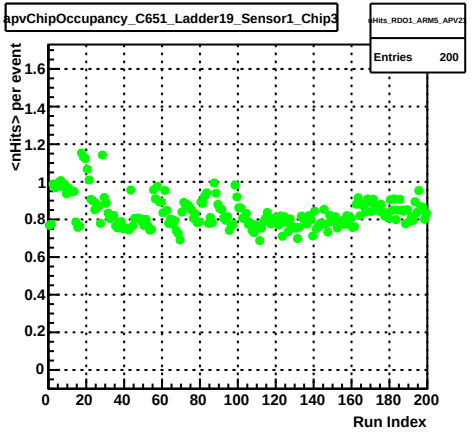
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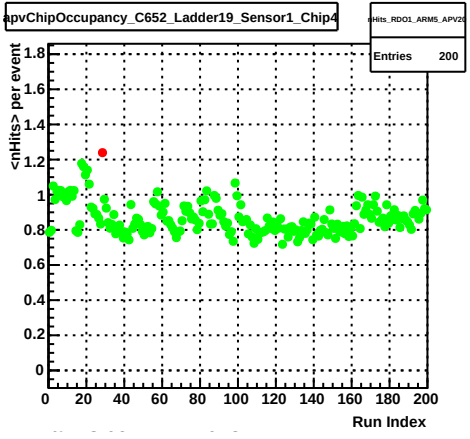
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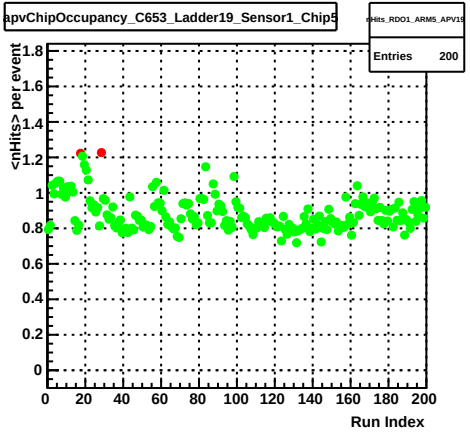
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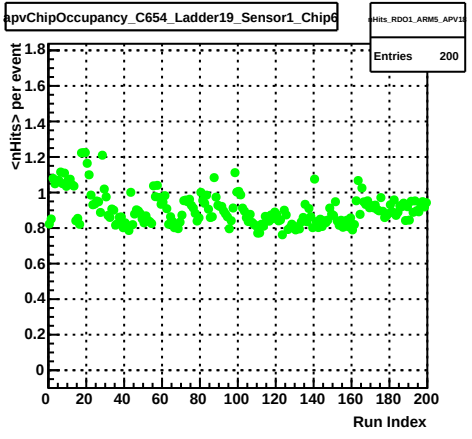
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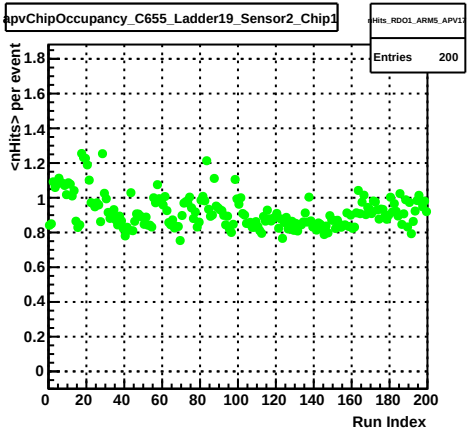
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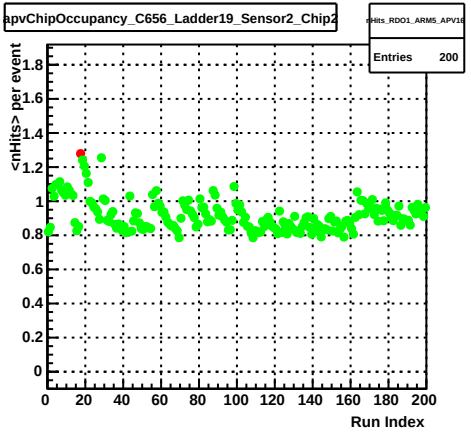
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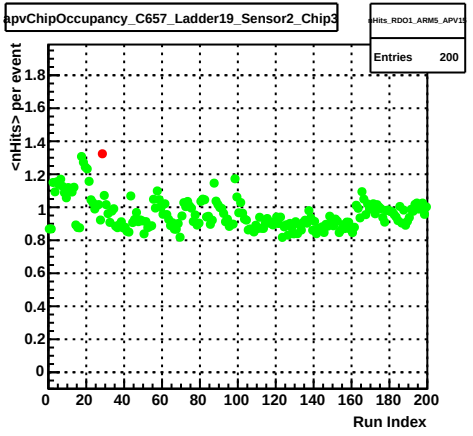
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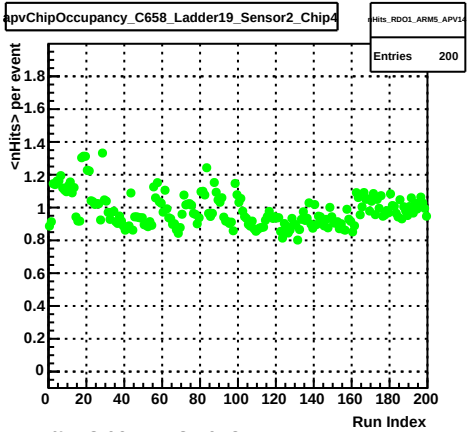
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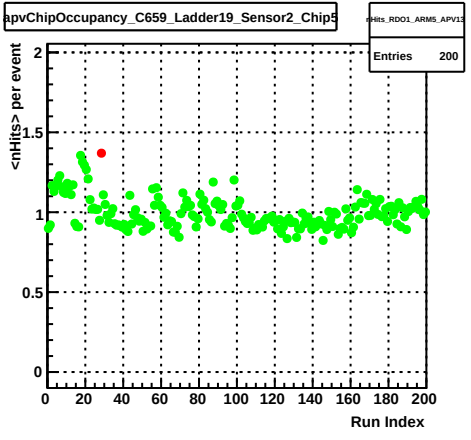
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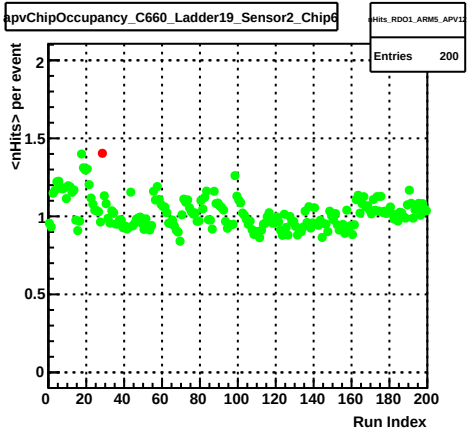
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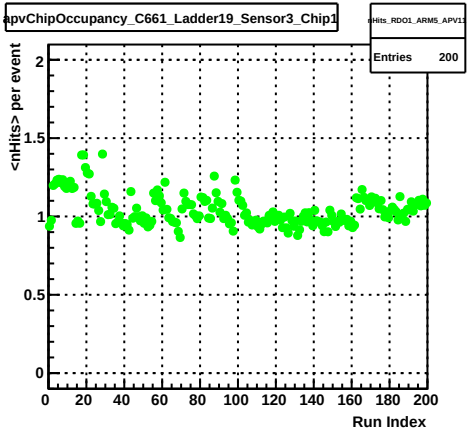
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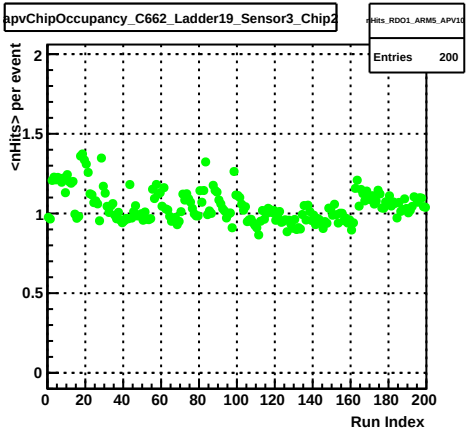
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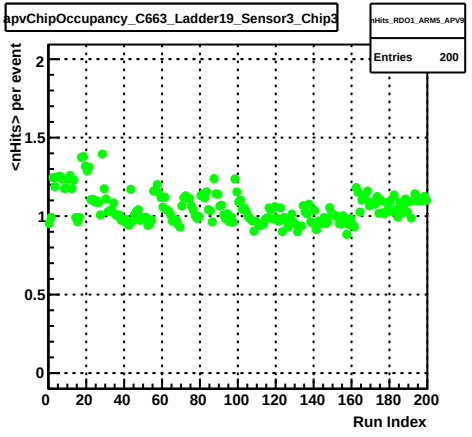
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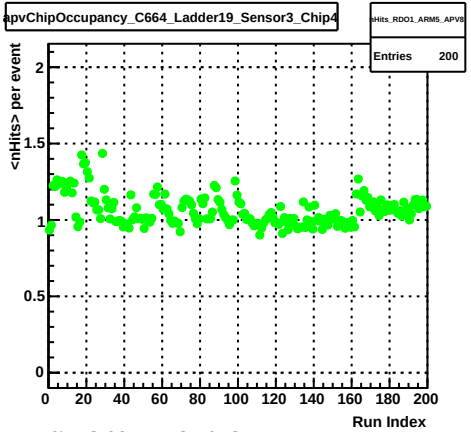
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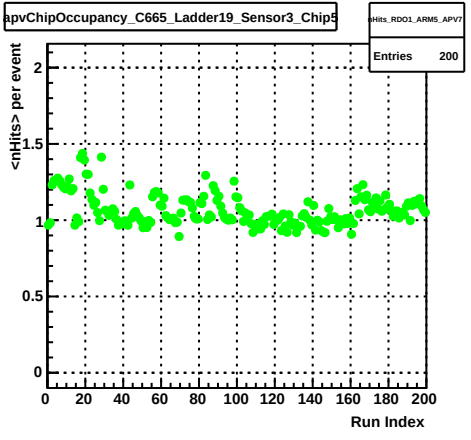
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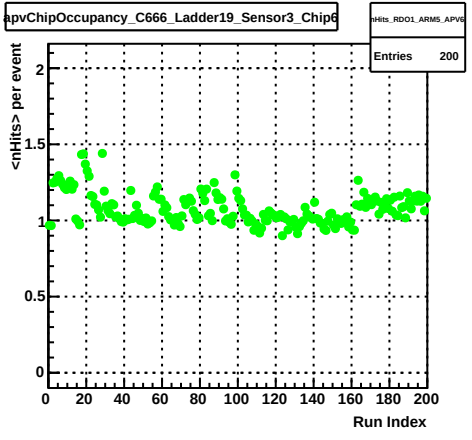
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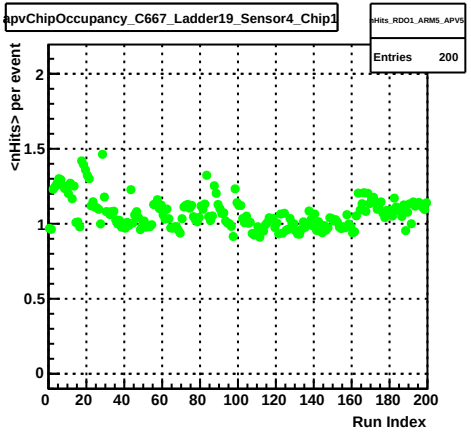
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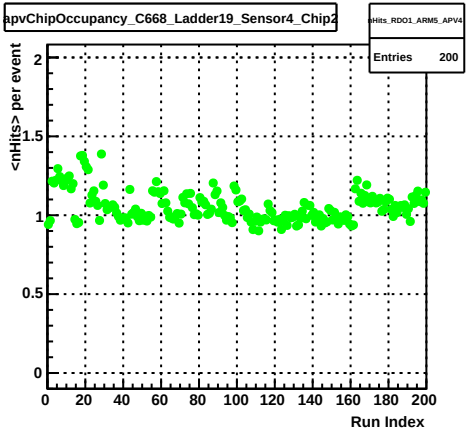
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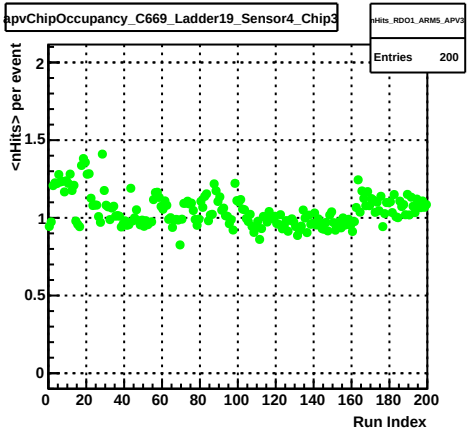
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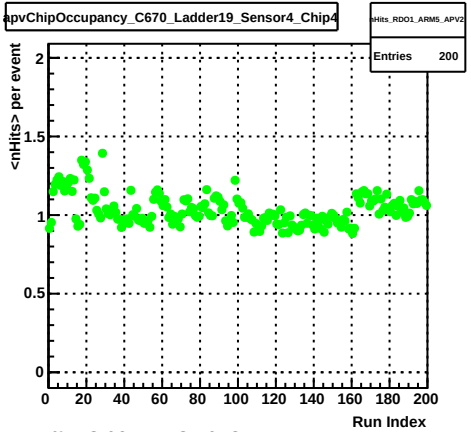
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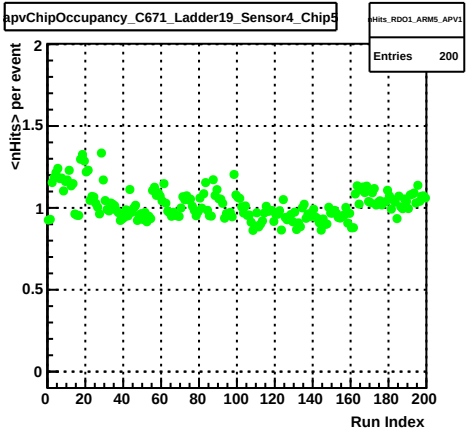
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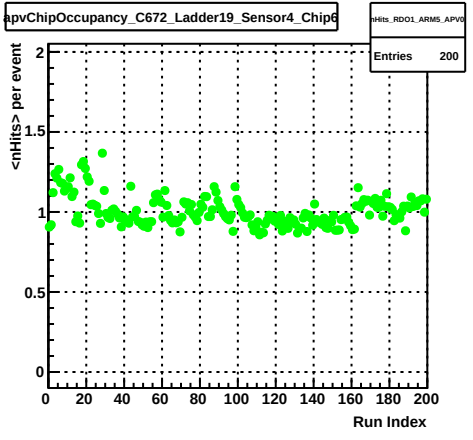
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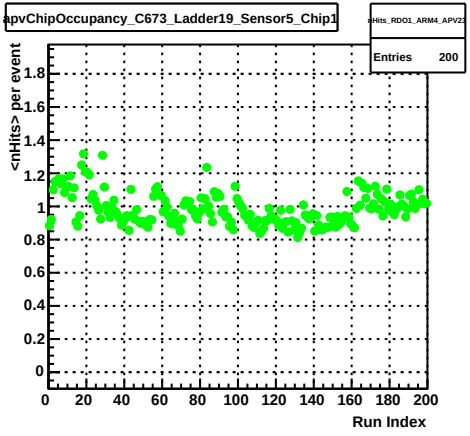
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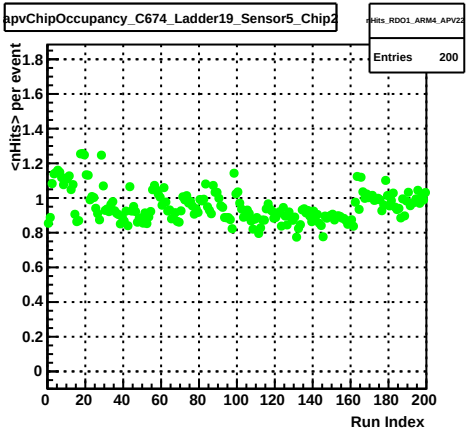
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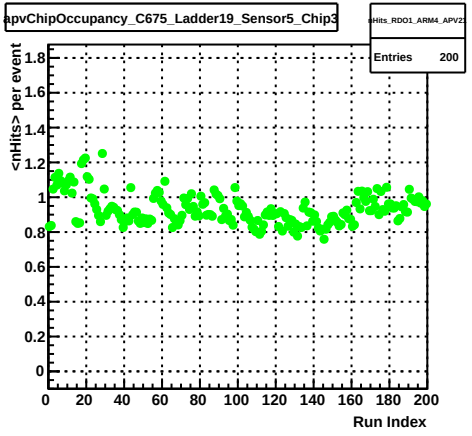
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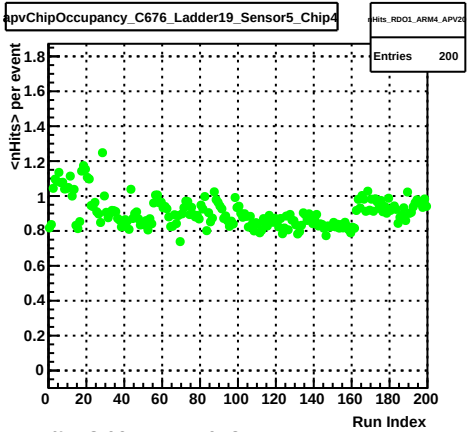
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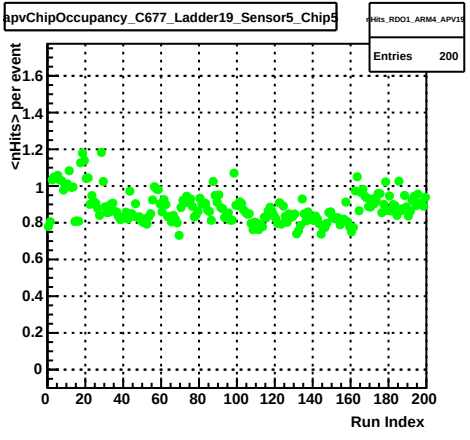
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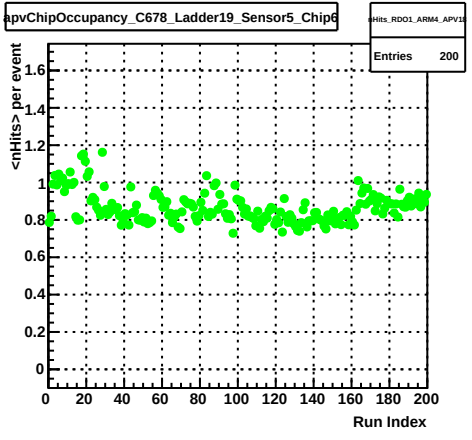
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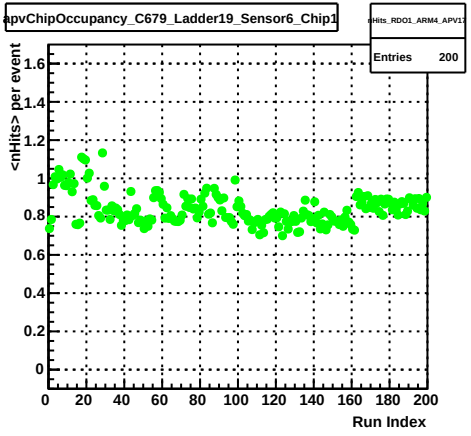
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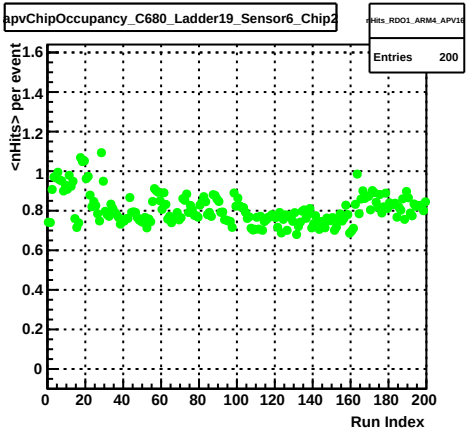
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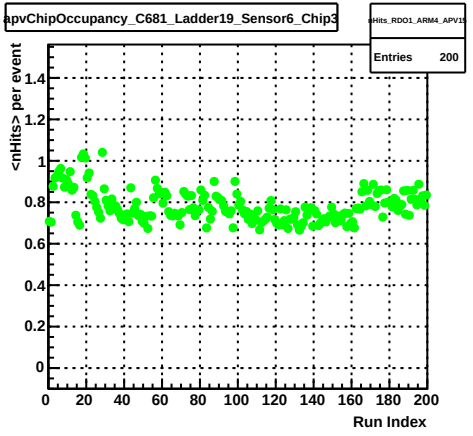
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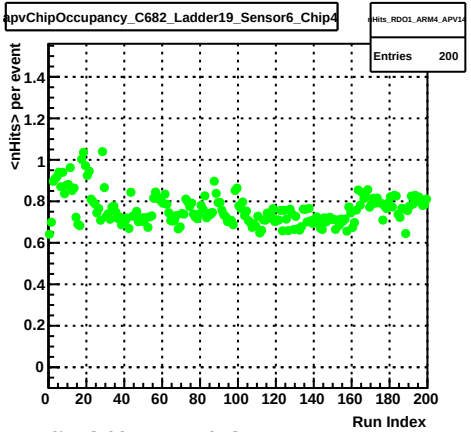
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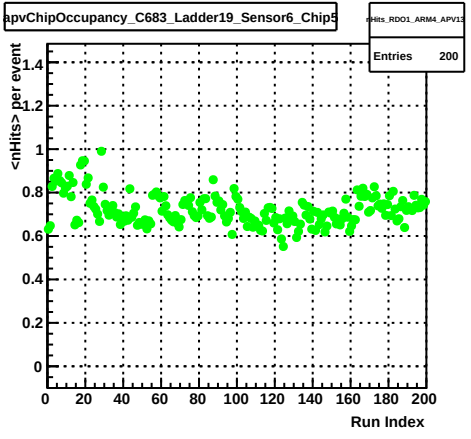
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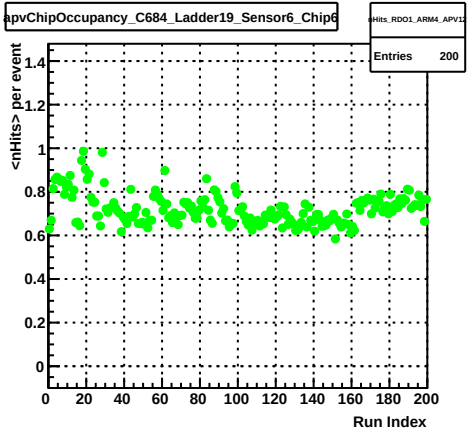
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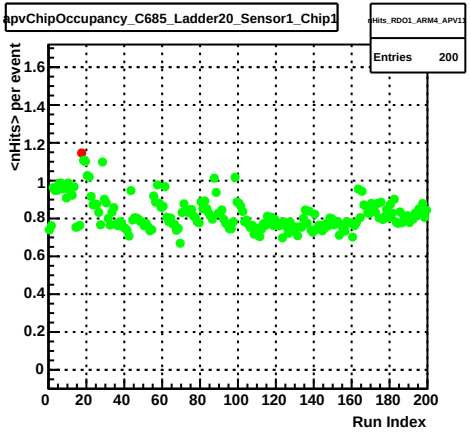
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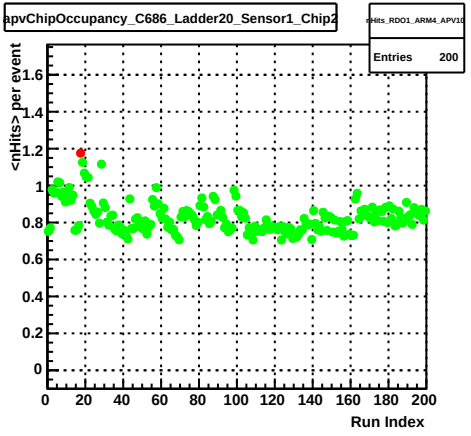
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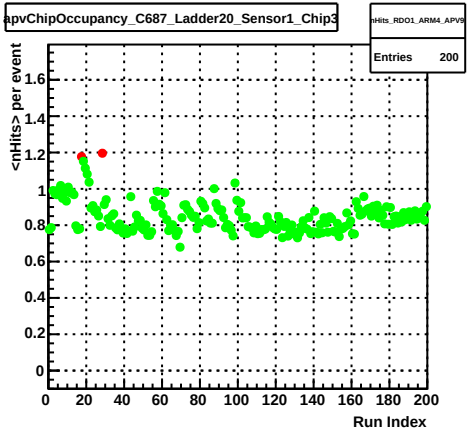
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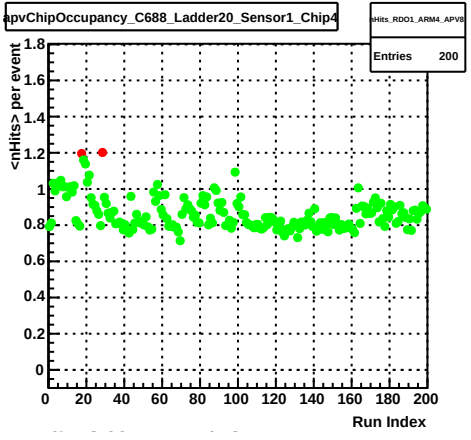
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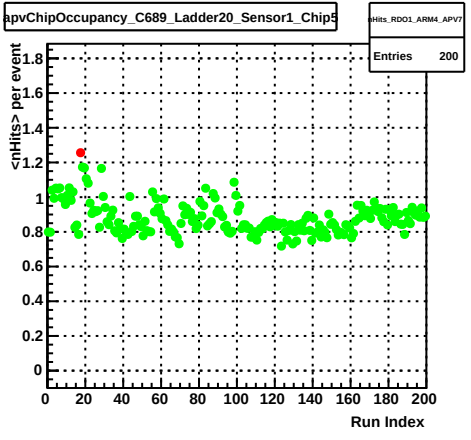
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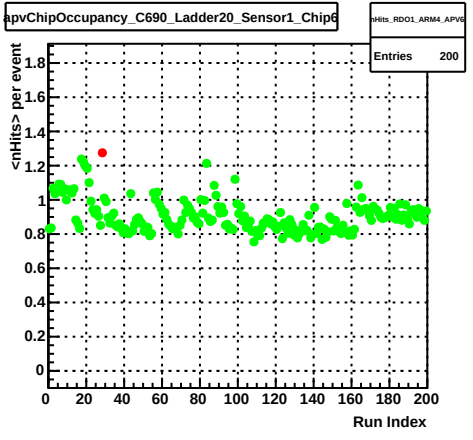
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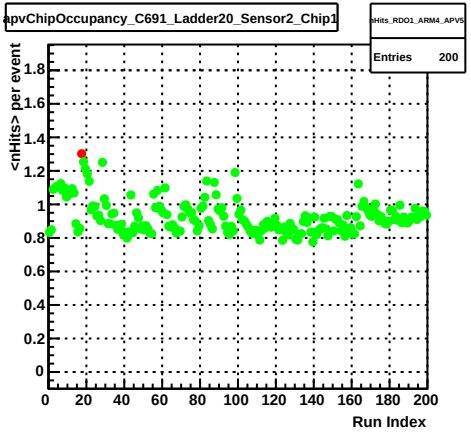
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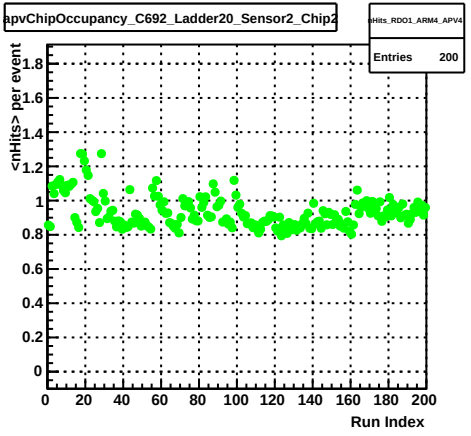
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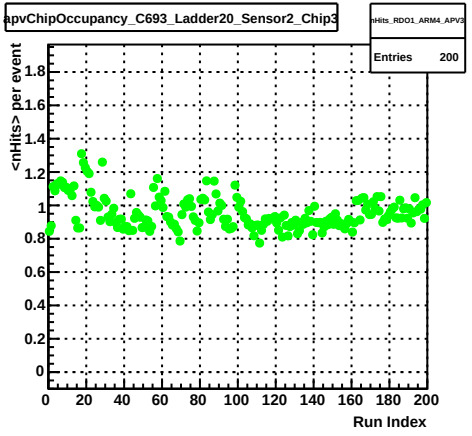
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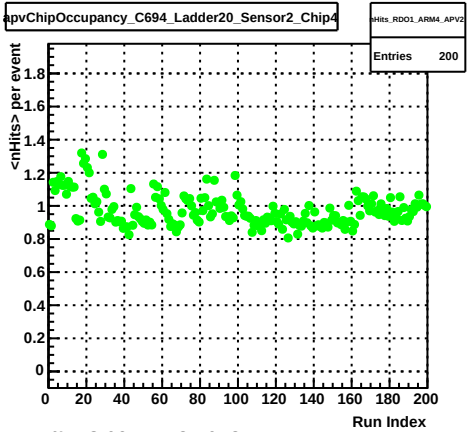
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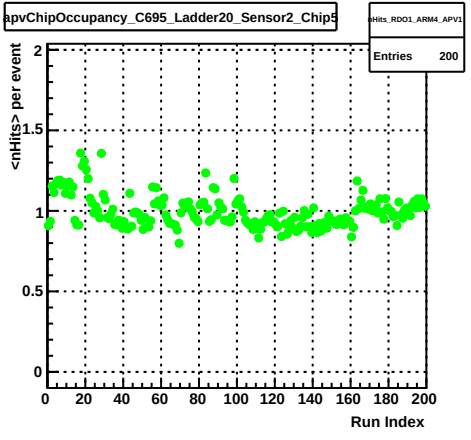
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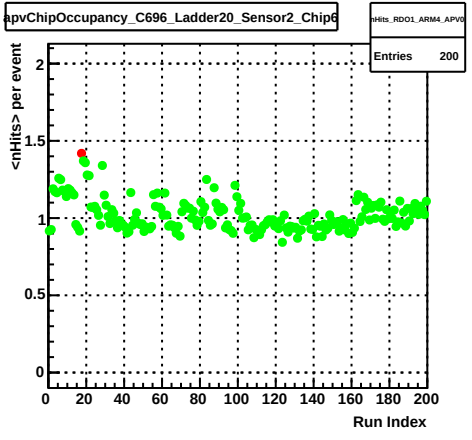
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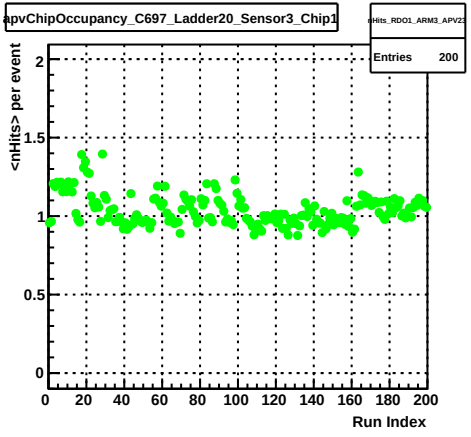
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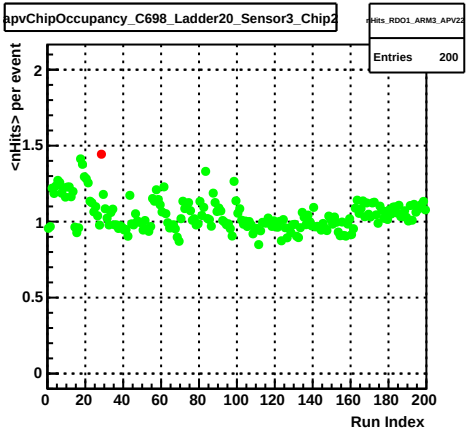
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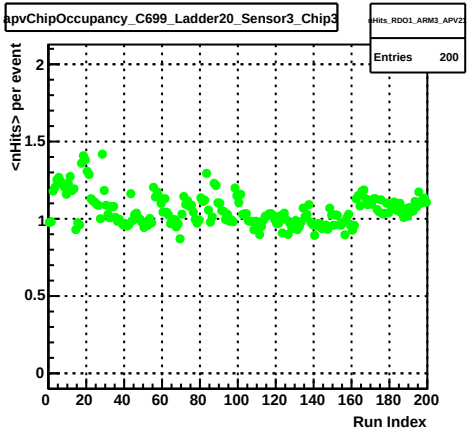
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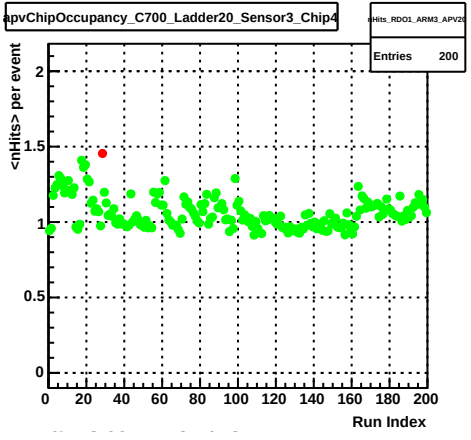
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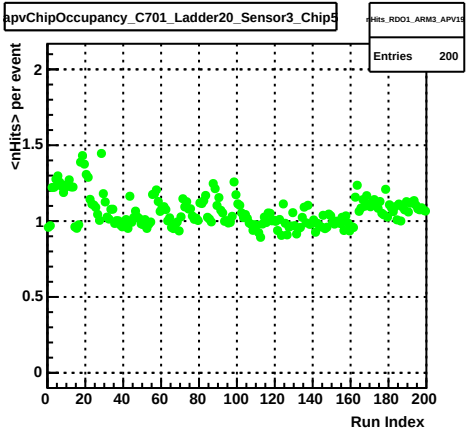
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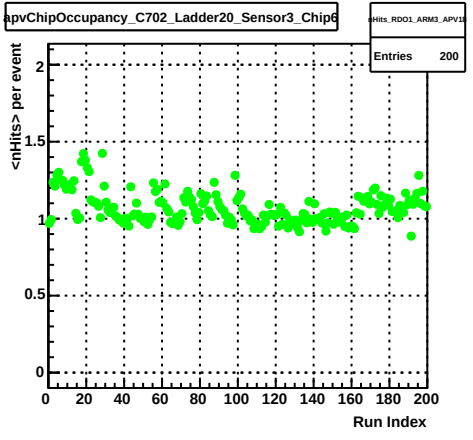
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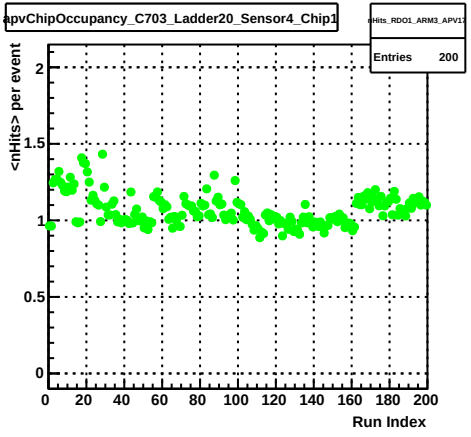
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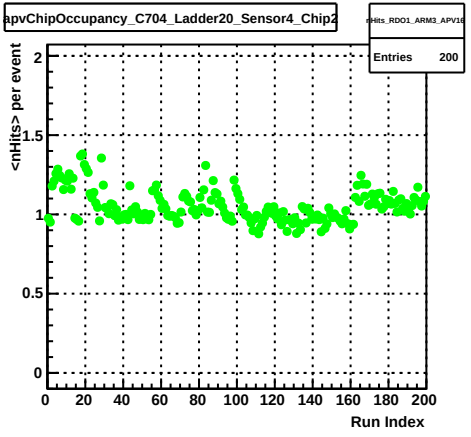
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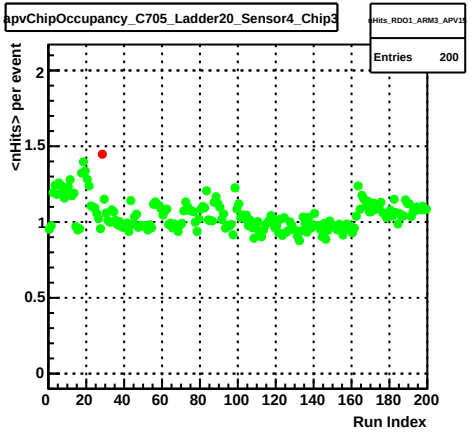
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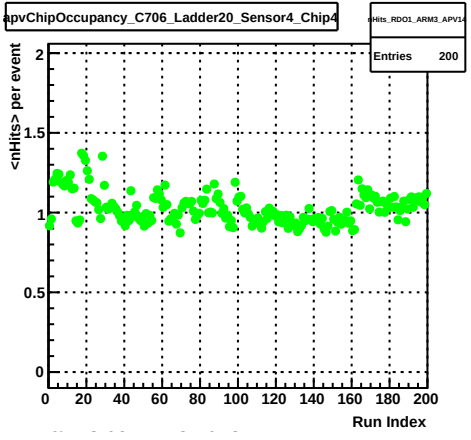
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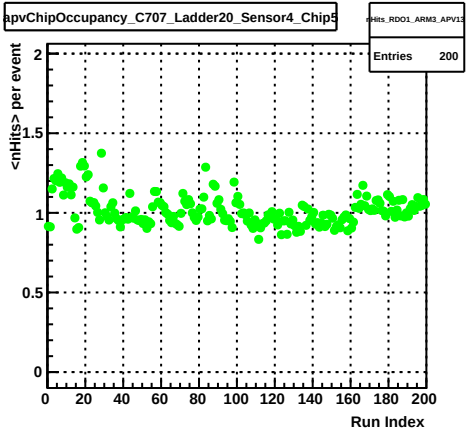
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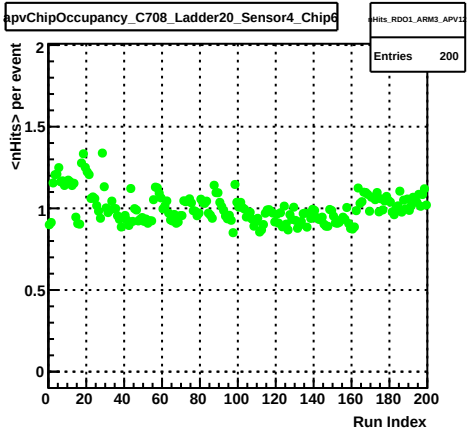
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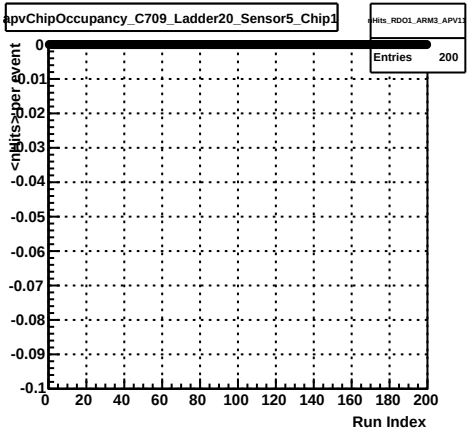
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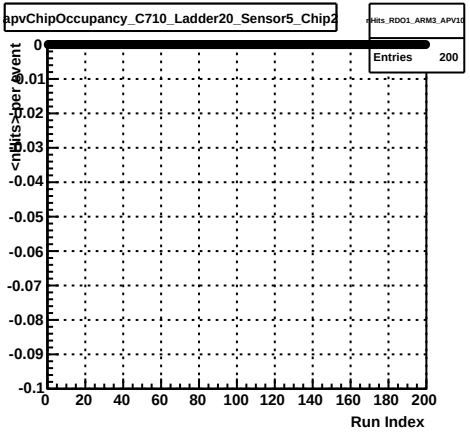
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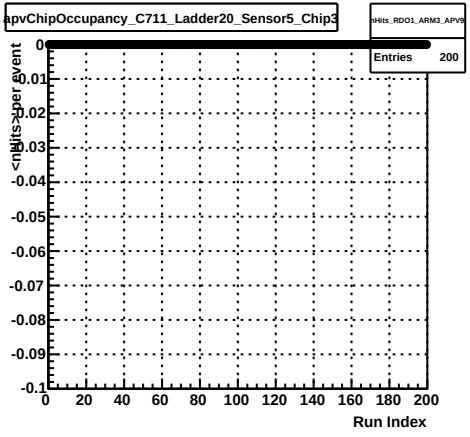
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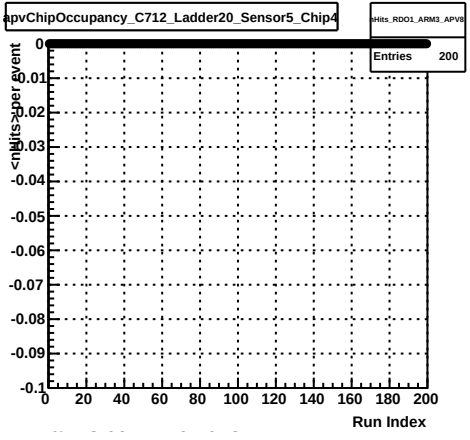
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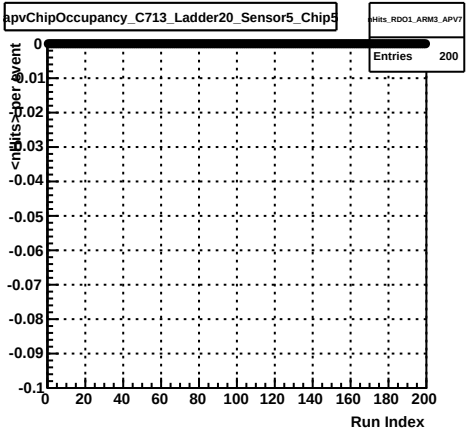
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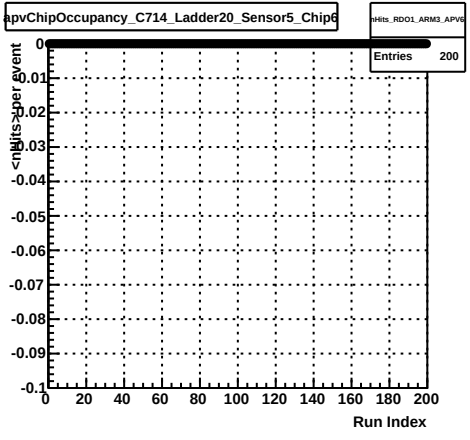
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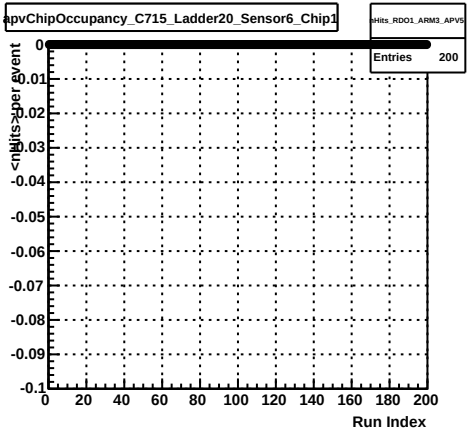
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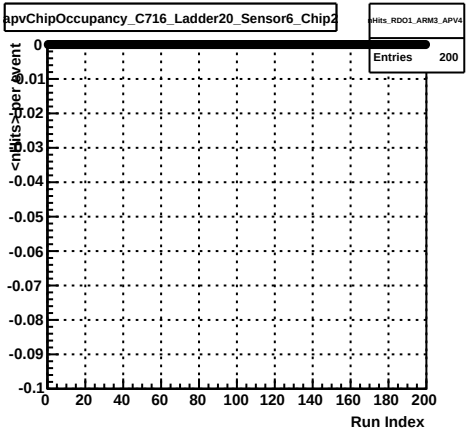
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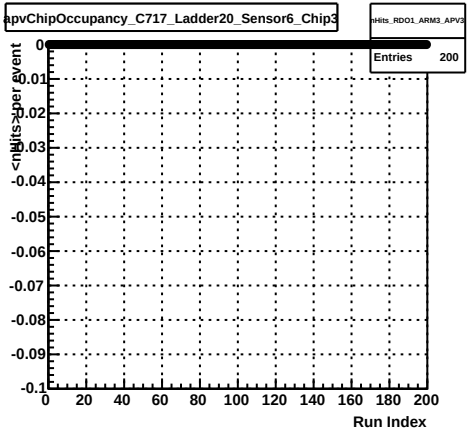
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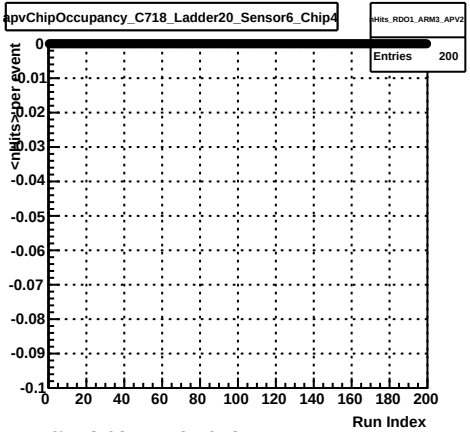
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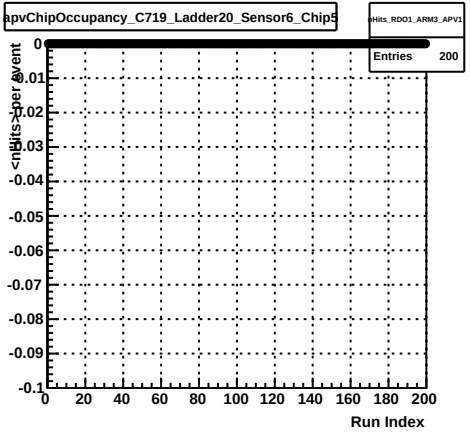
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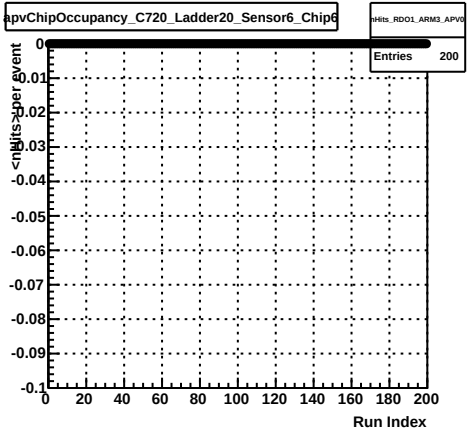
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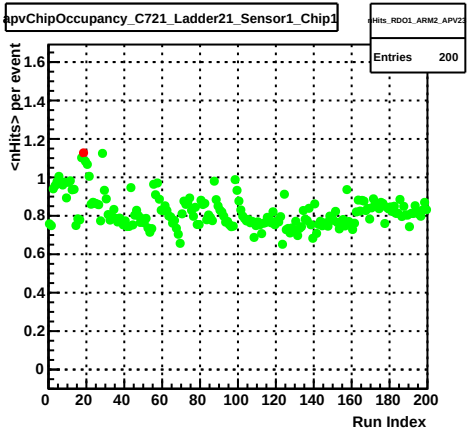
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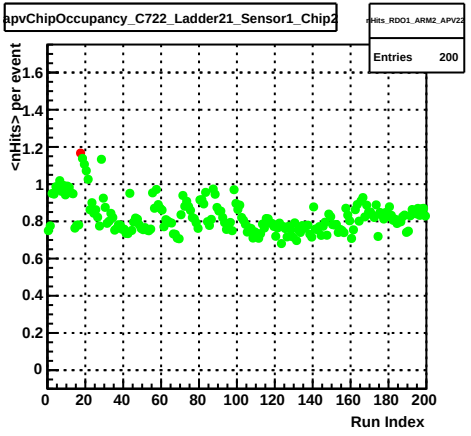
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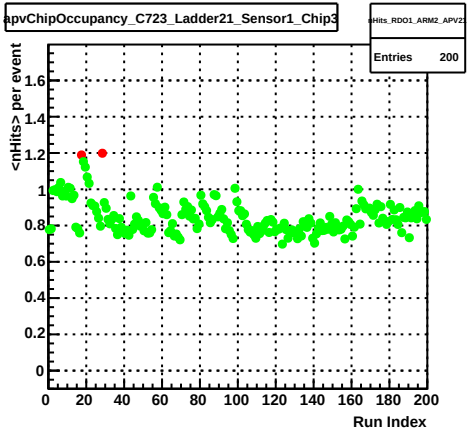
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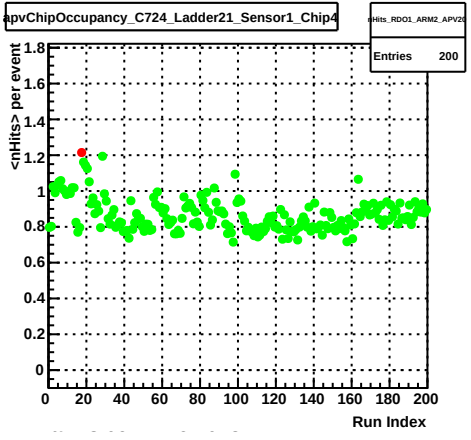
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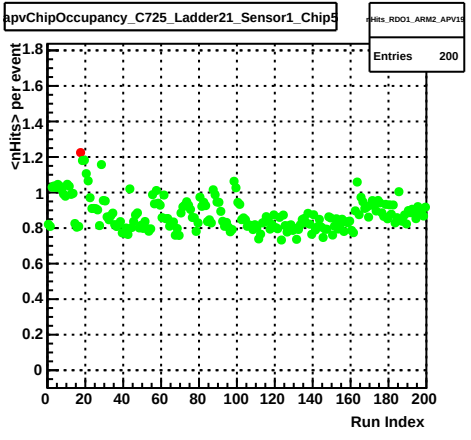
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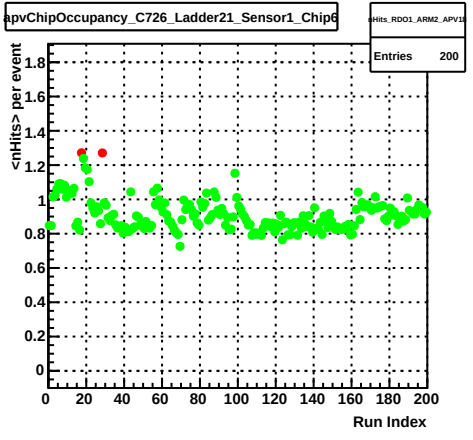
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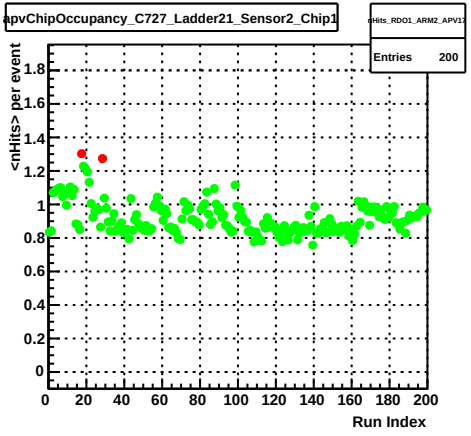
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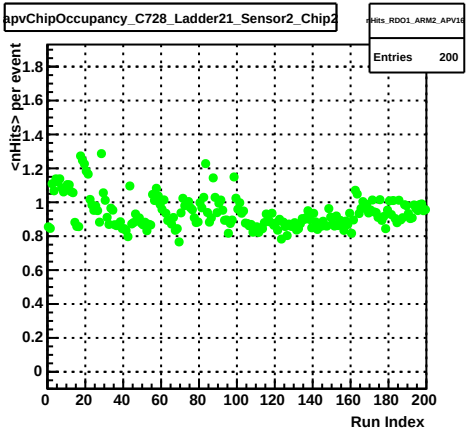
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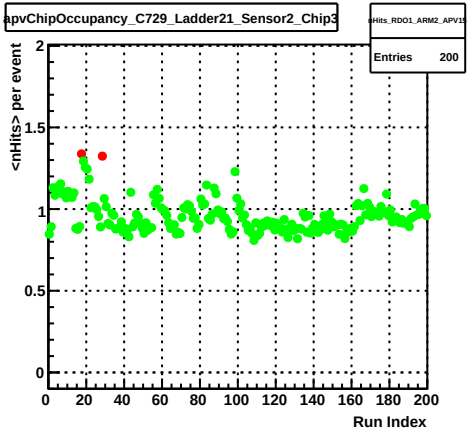
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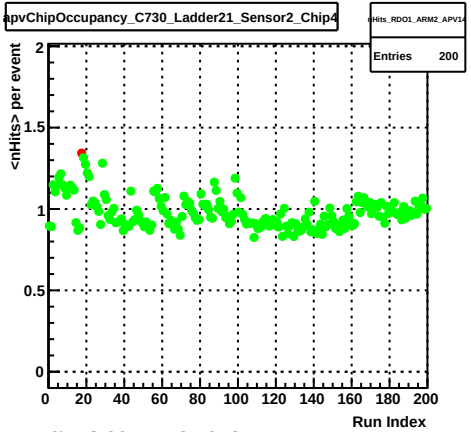
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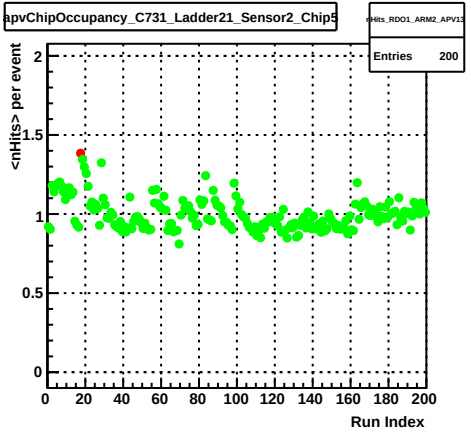
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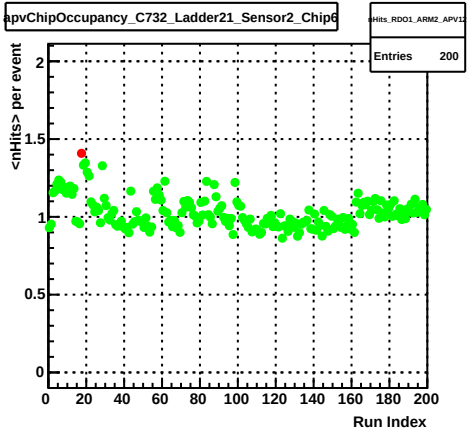
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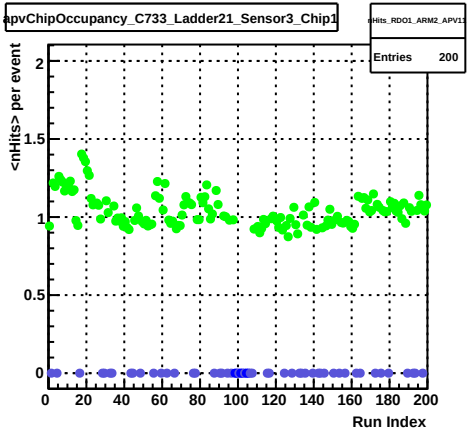
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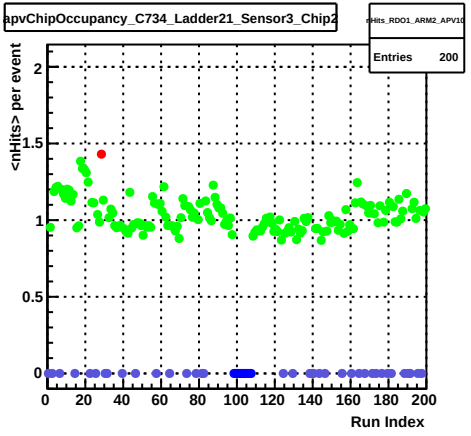
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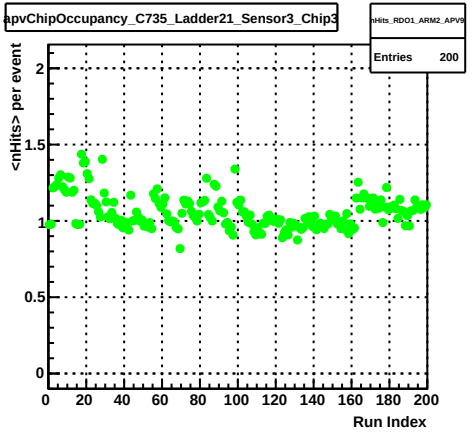
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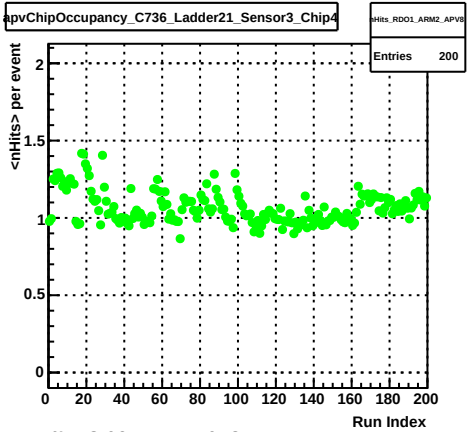
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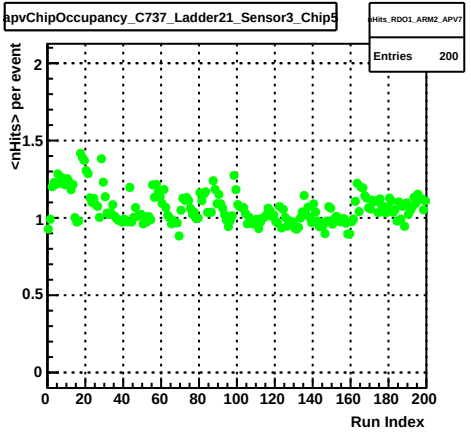
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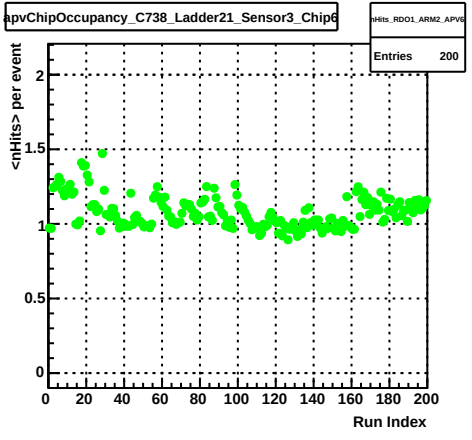
Run Index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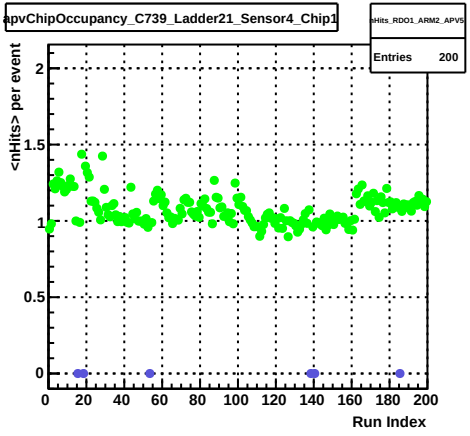
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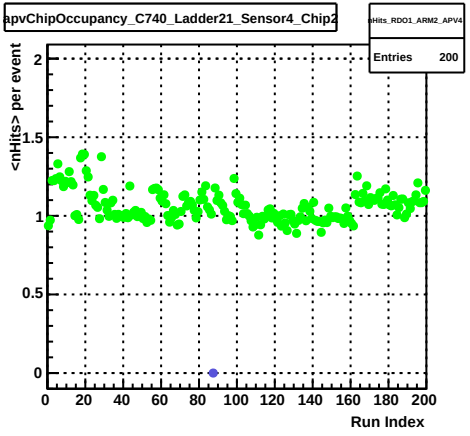
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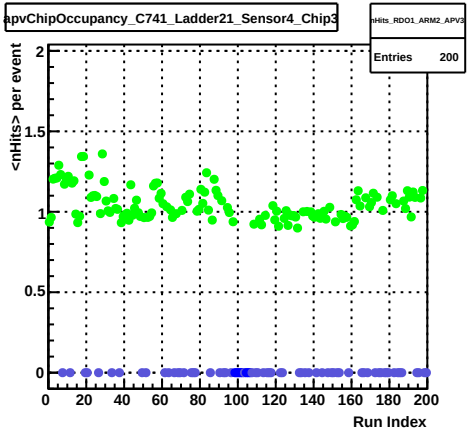
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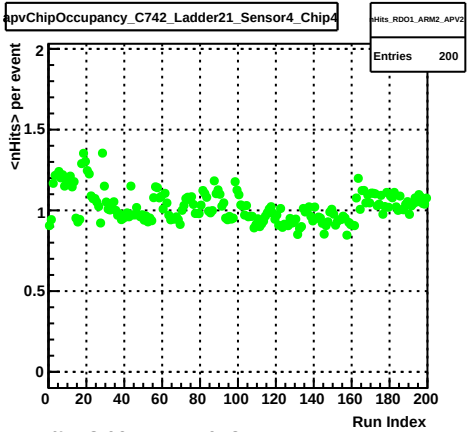
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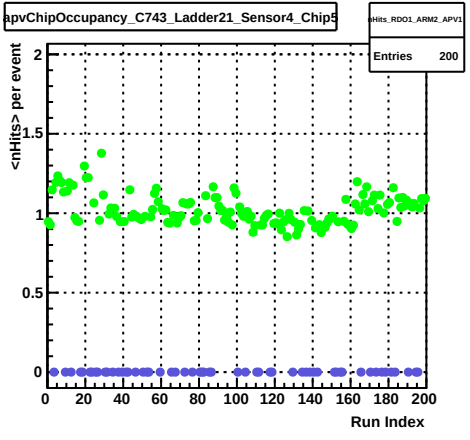
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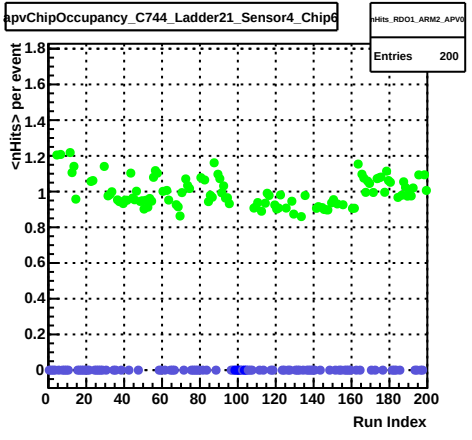
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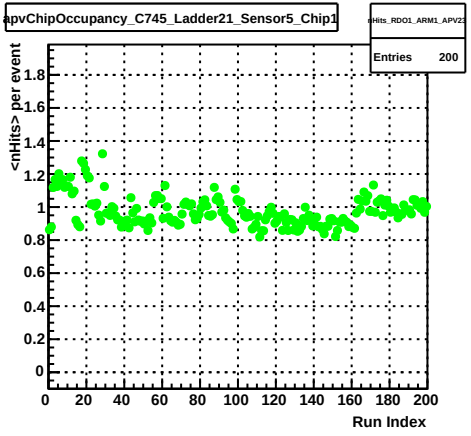
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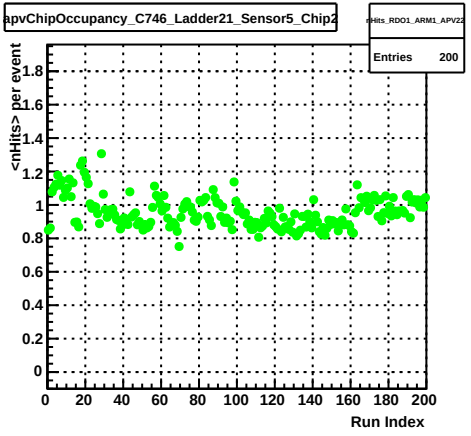
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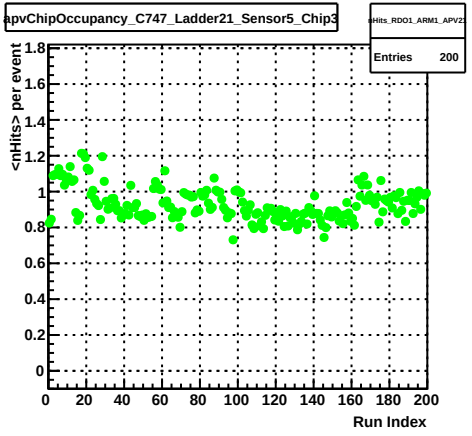
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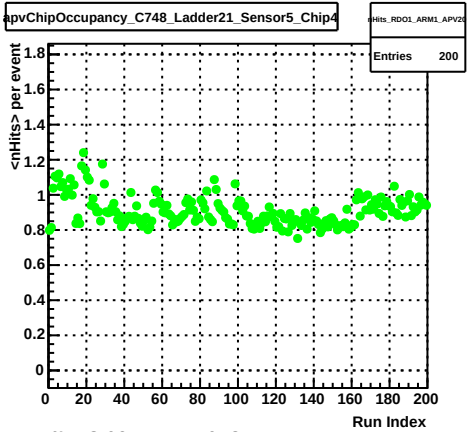
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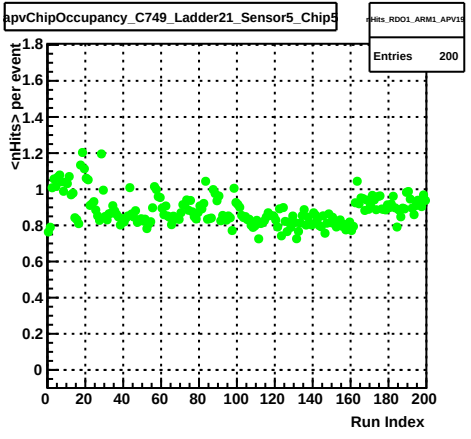
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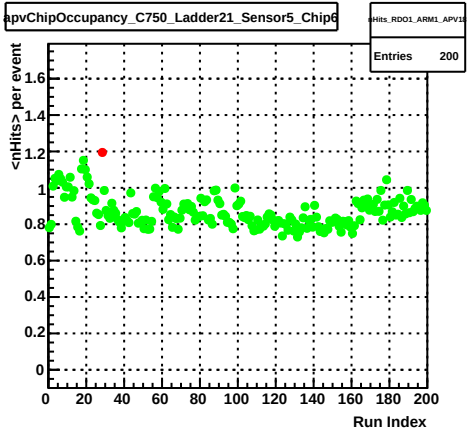
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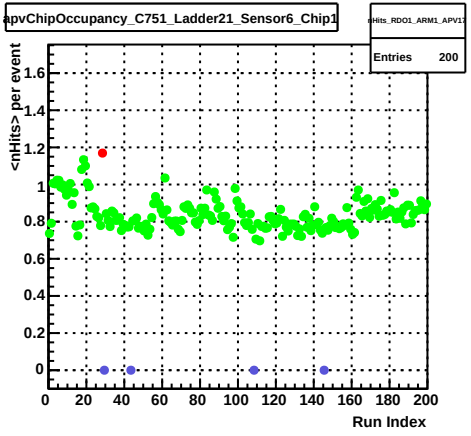
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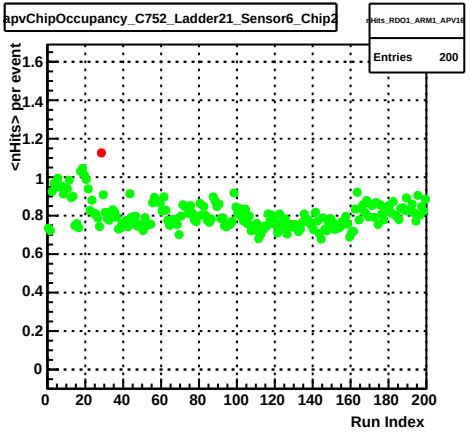
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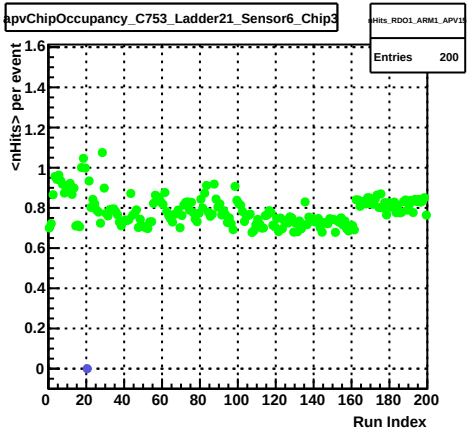
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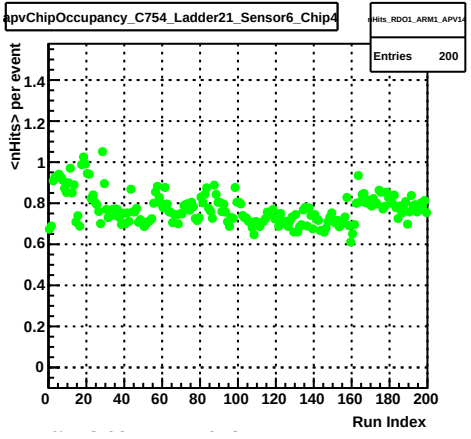
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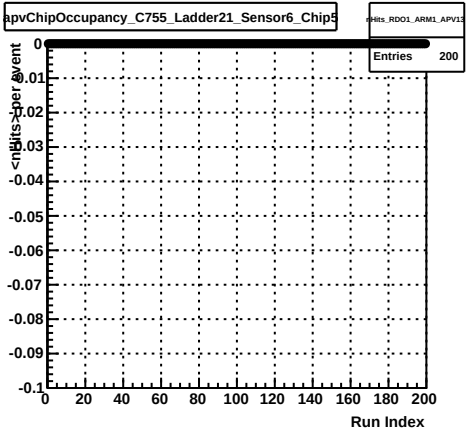
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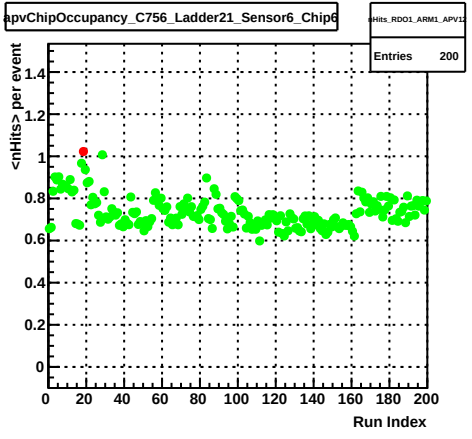
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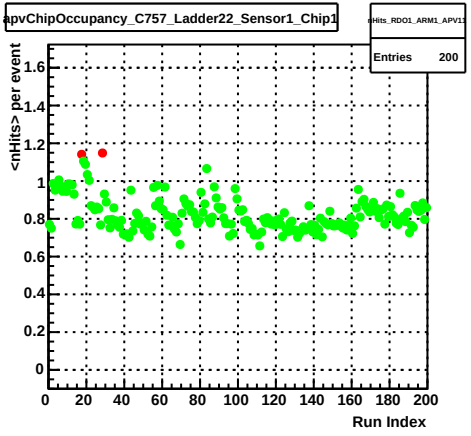
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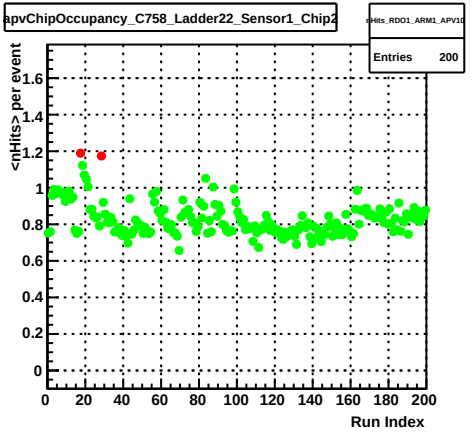
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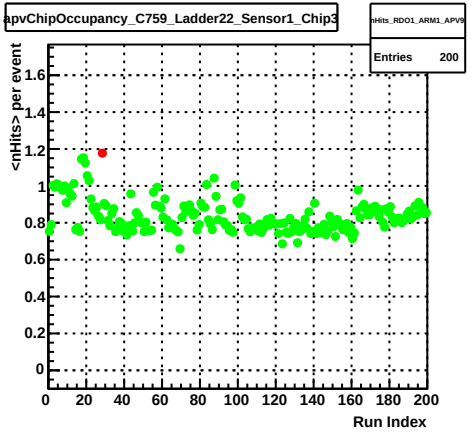
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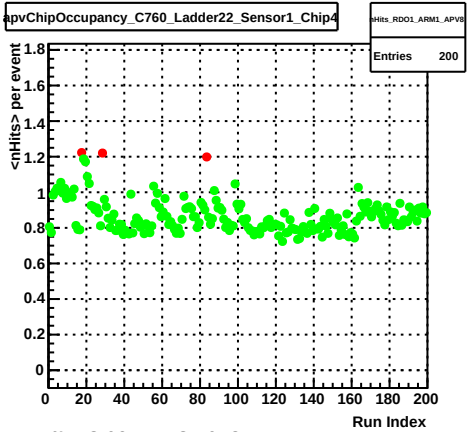
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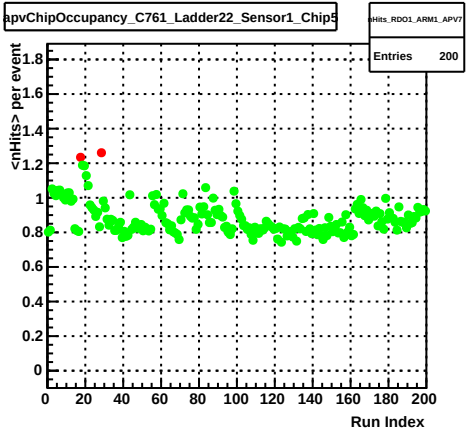
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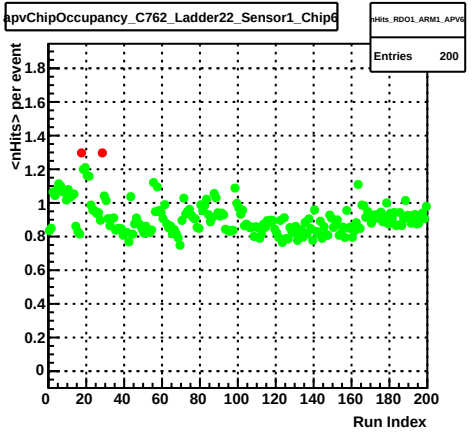
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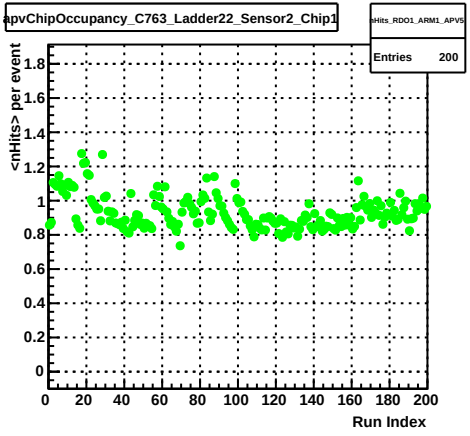
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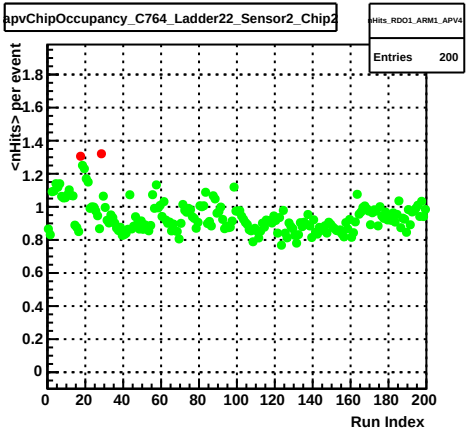
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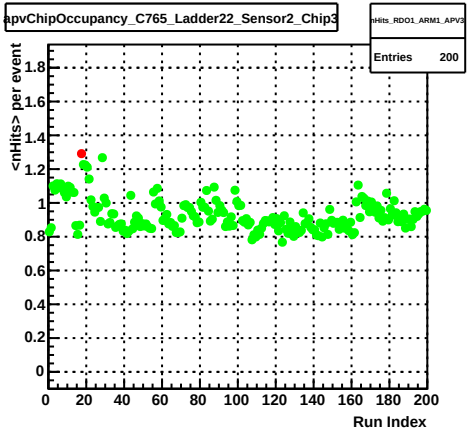
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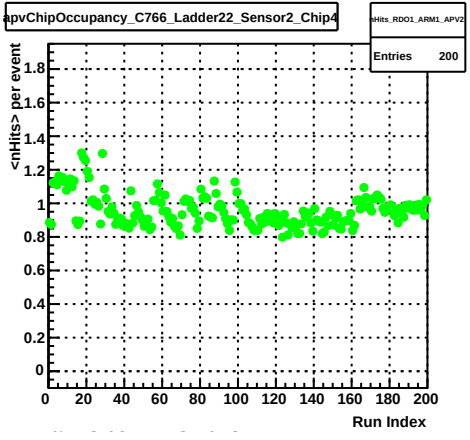
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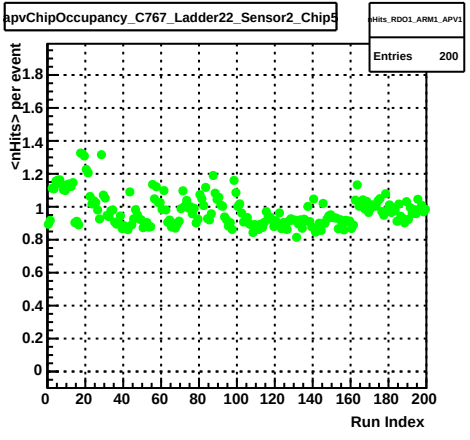
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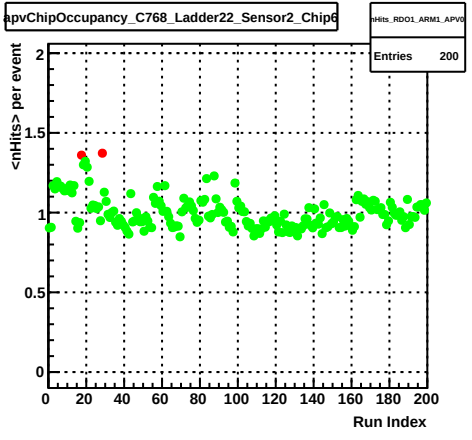
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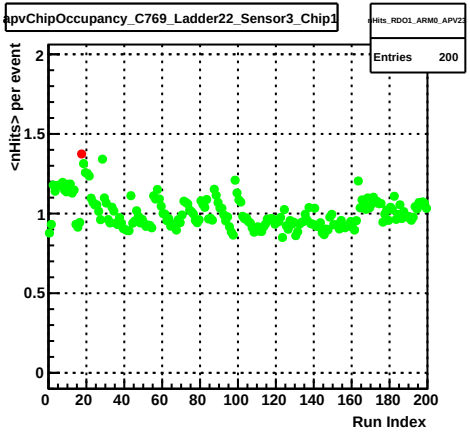
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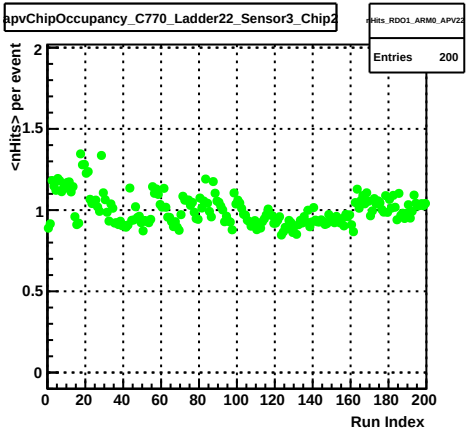
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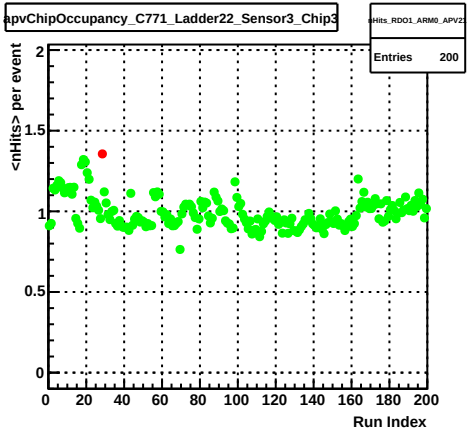
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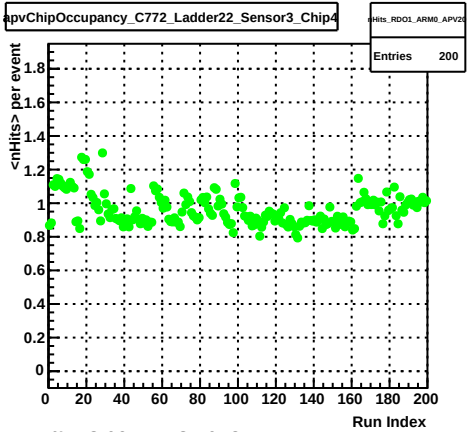
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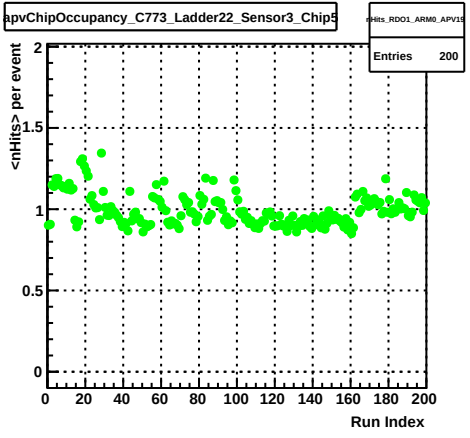
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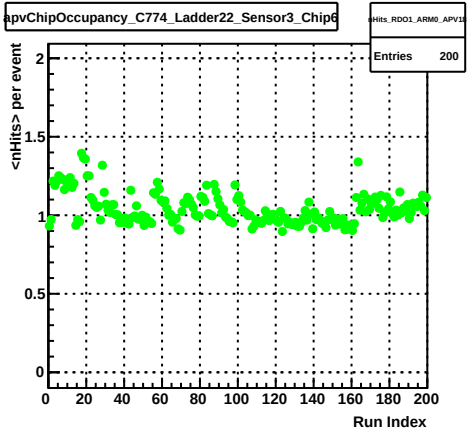
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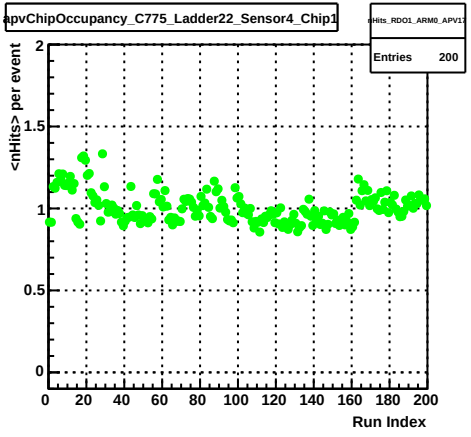
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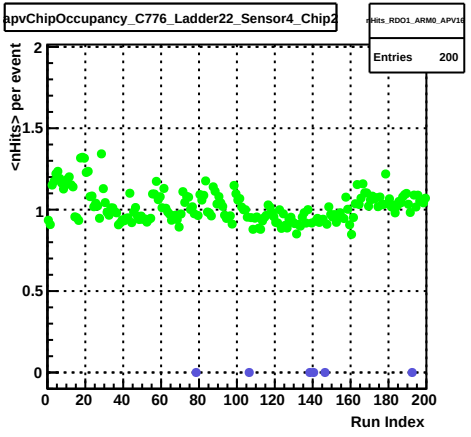
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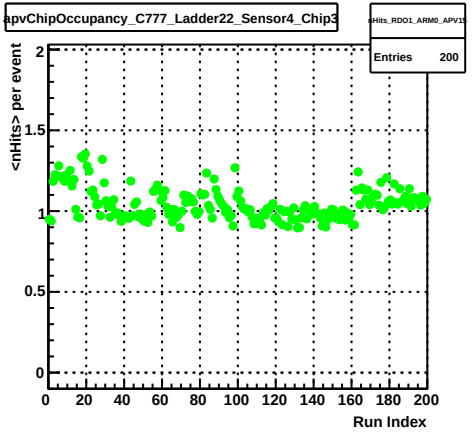
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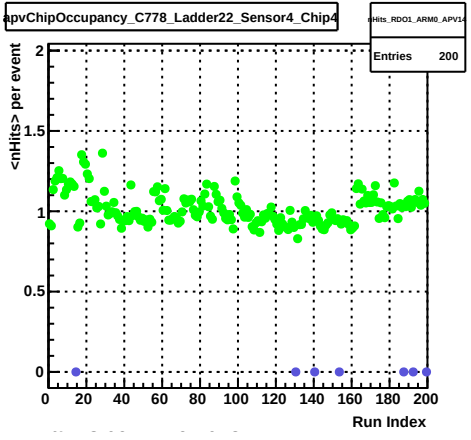
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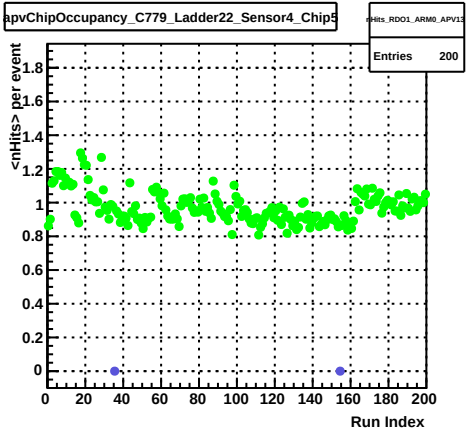
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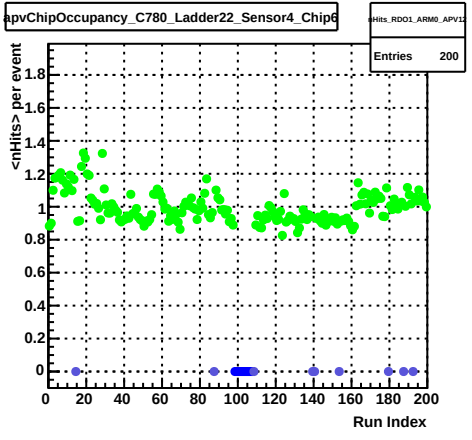
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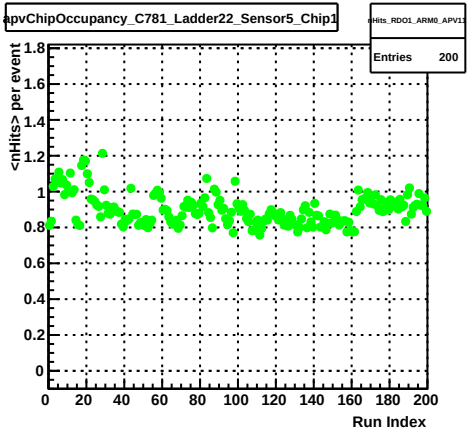
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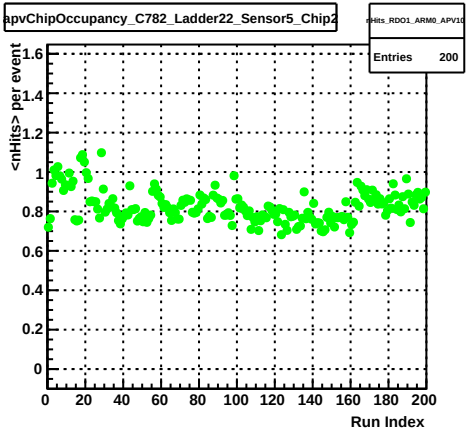
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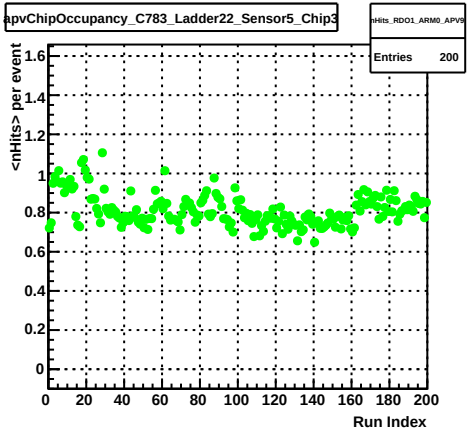
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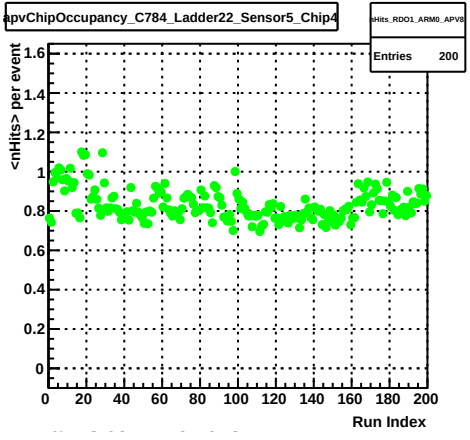
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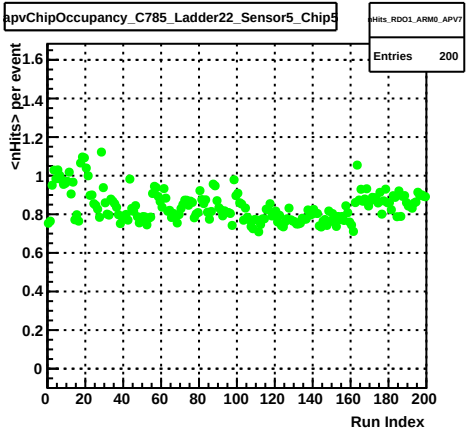
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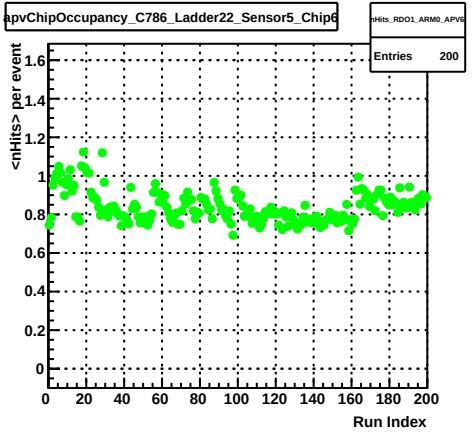
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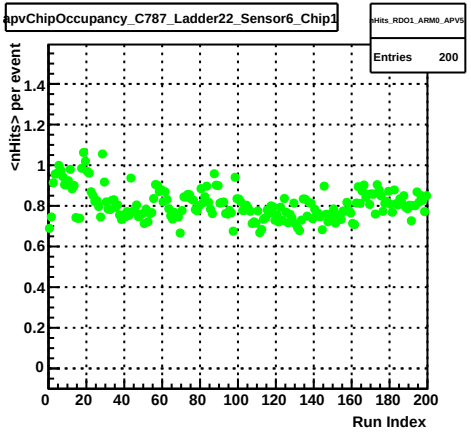
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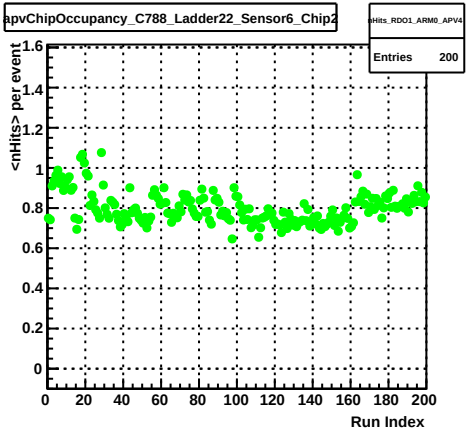
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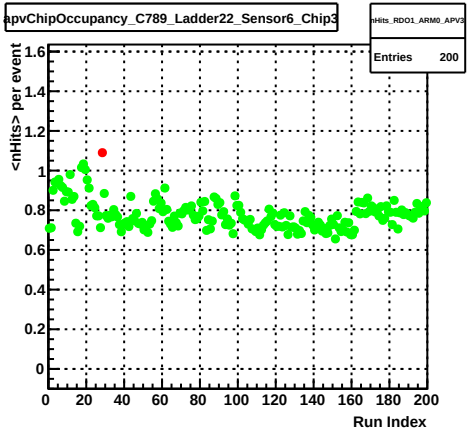
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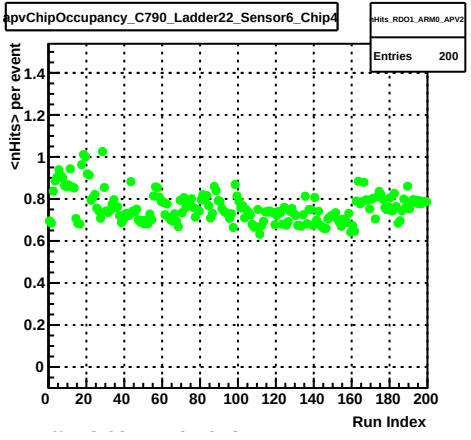
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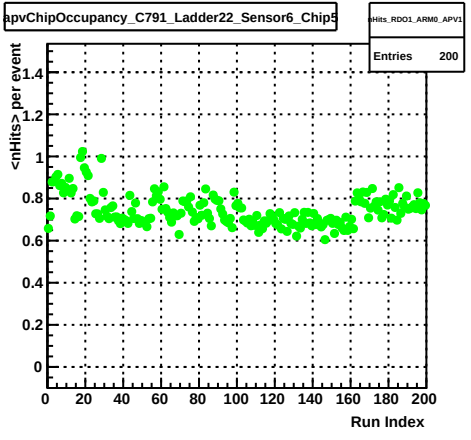
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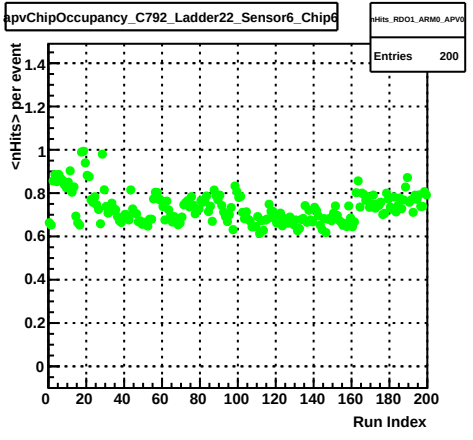
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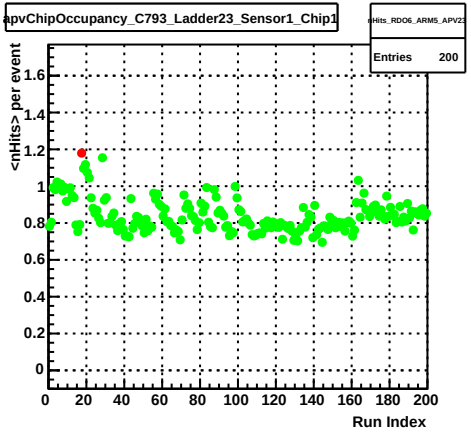
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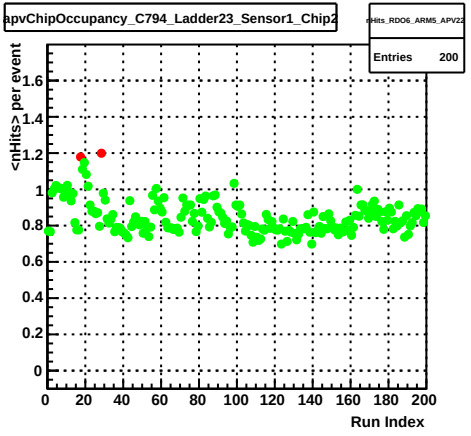
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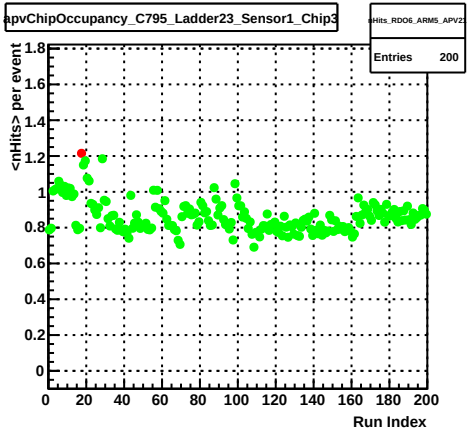
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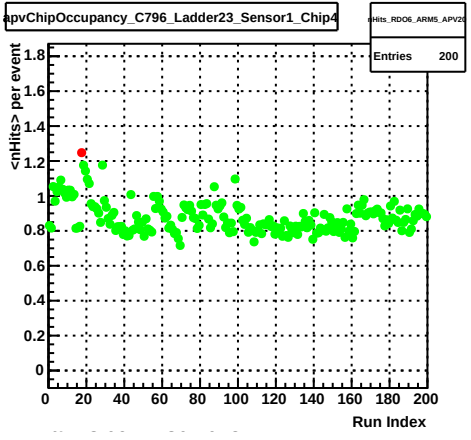
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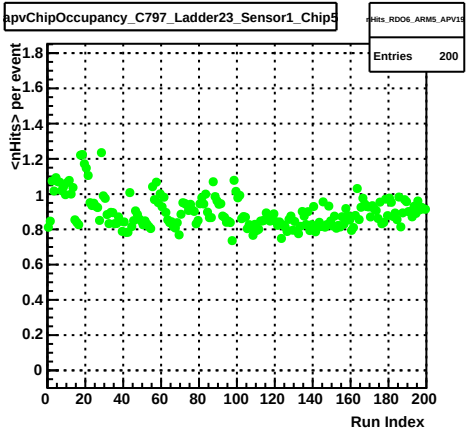
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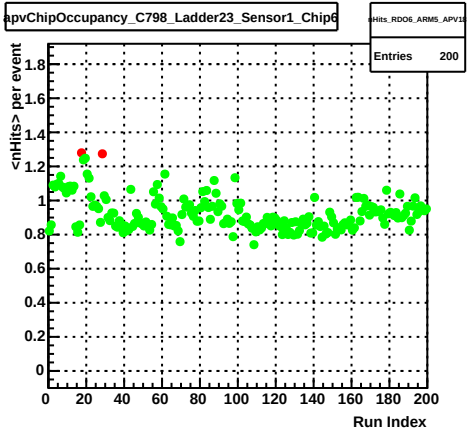
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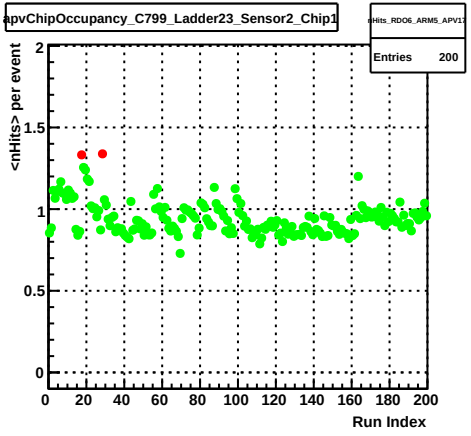
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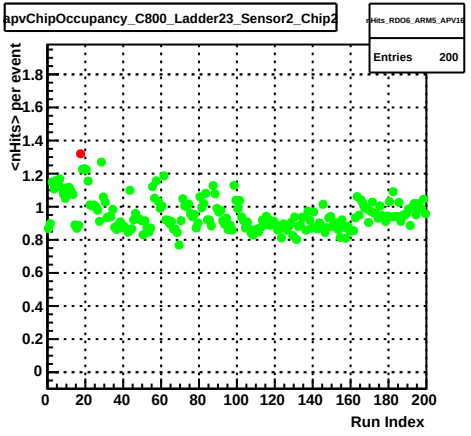
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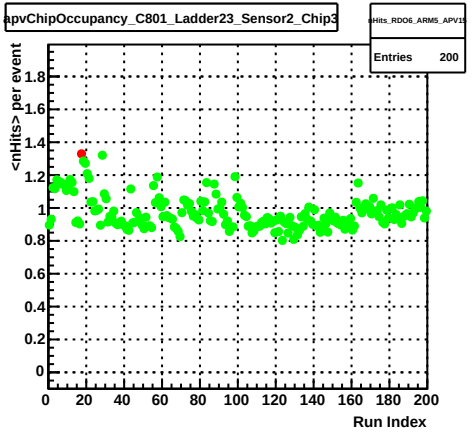
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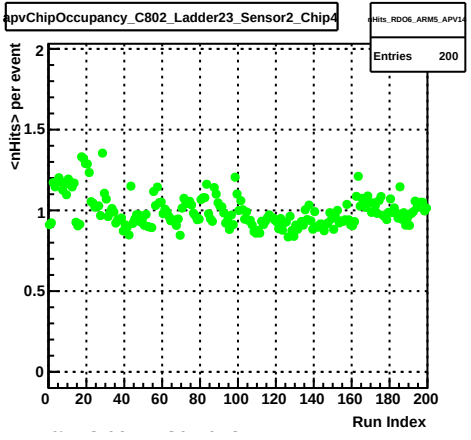
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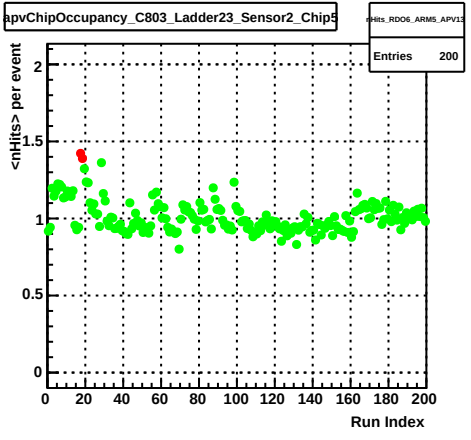
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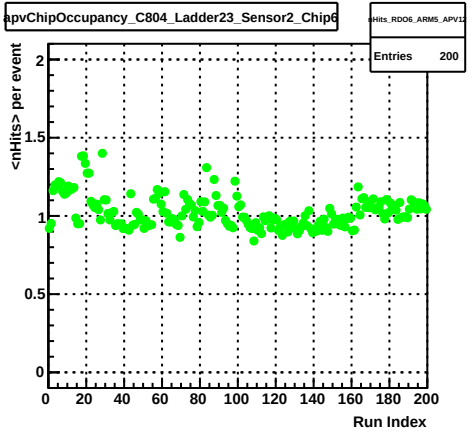
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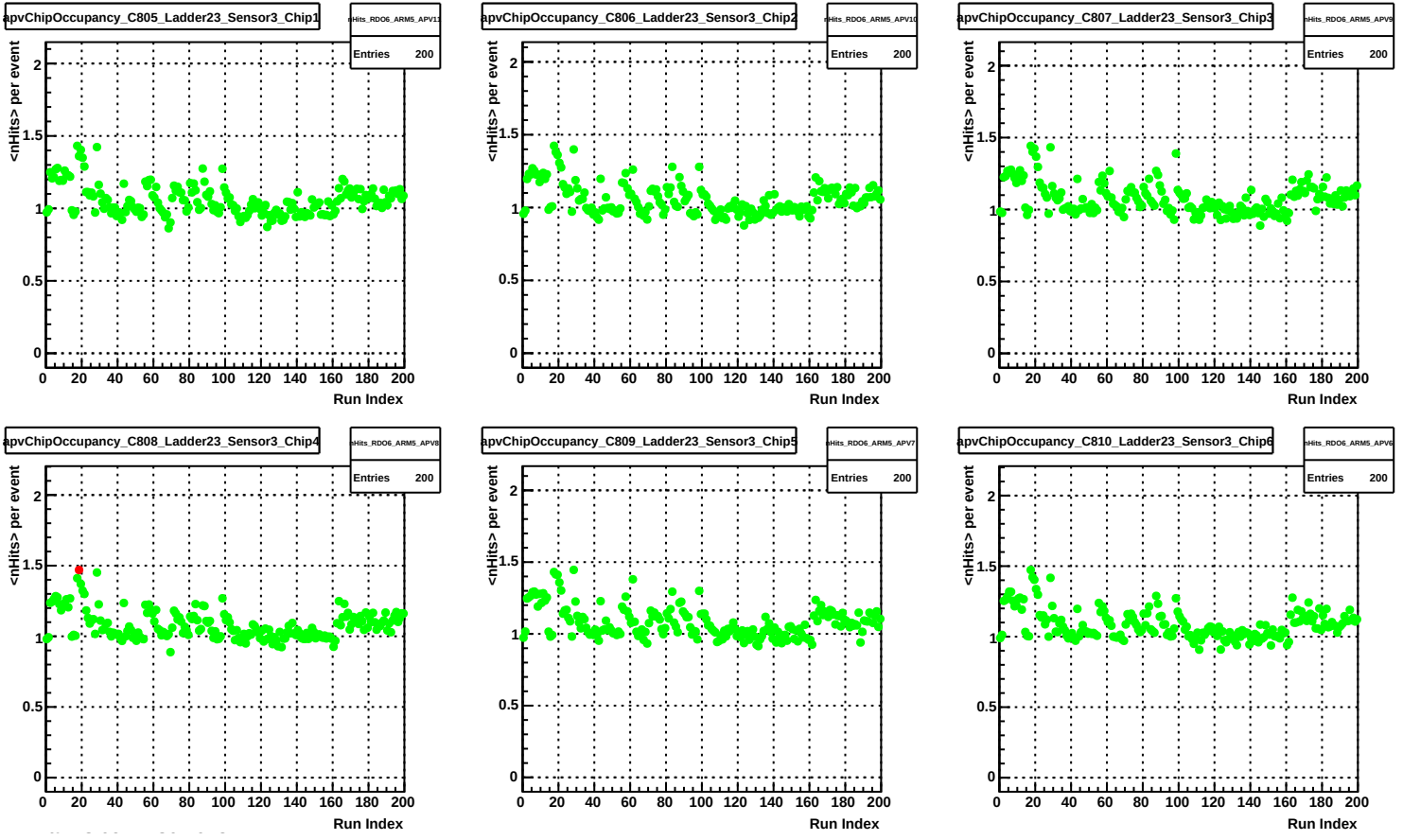
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nHits_RDO6_ARM5_APV10
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apvChipOccupancy_C807_Ladder23_Sensor3_Chip3
nHits_RDO6_ARM5_APV9
Entries 200
apvChipOccupancy_C808_Ladder23_Sensor3_Chip4
nHits_RDO6_ARM5_APV8
Entries 200
apvChipOccupancy_C809_Ladder23_Sensor3_Chip5
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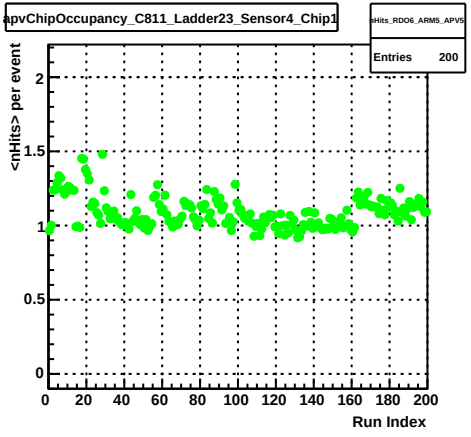
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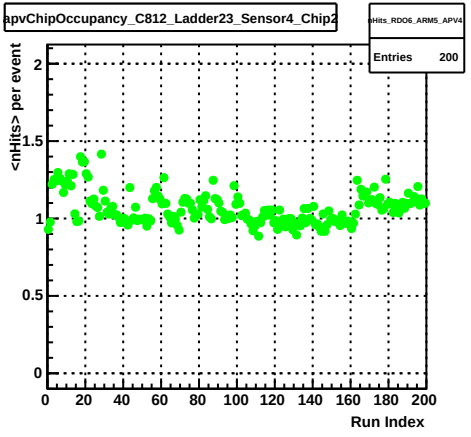
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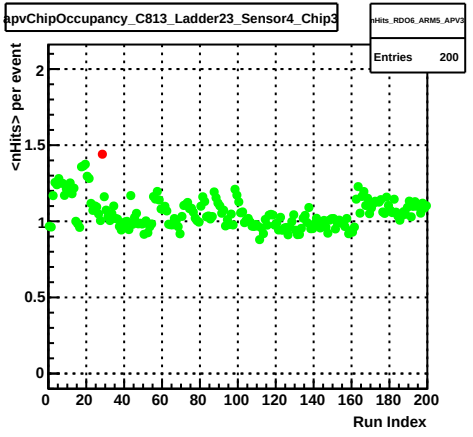
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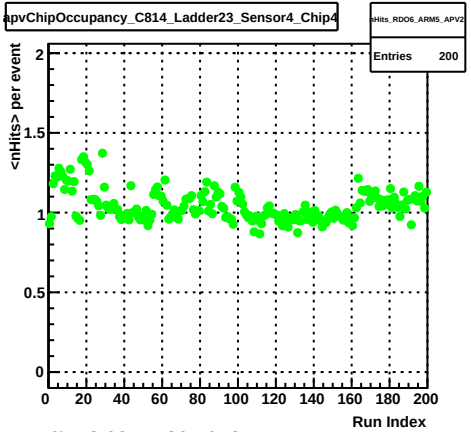
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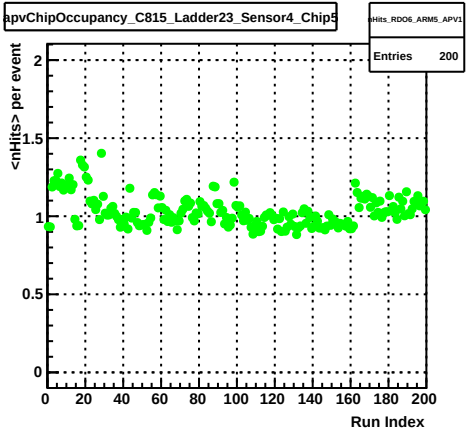
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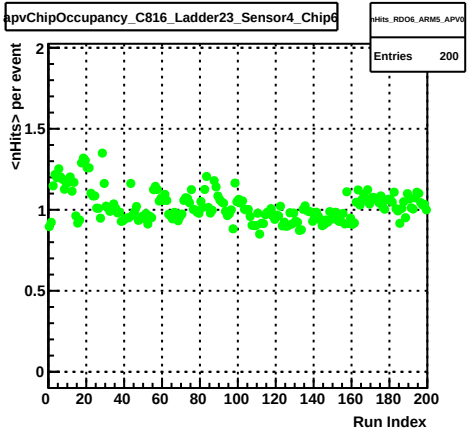
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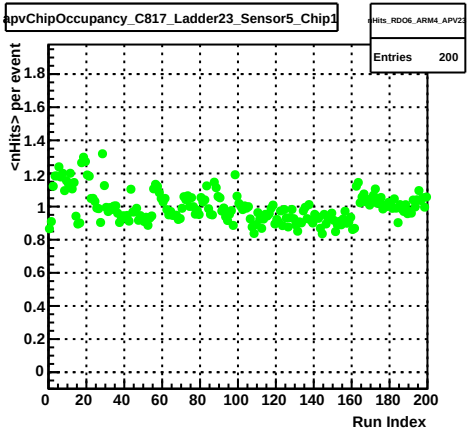
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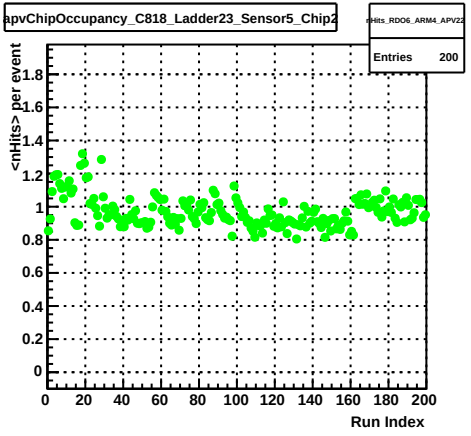
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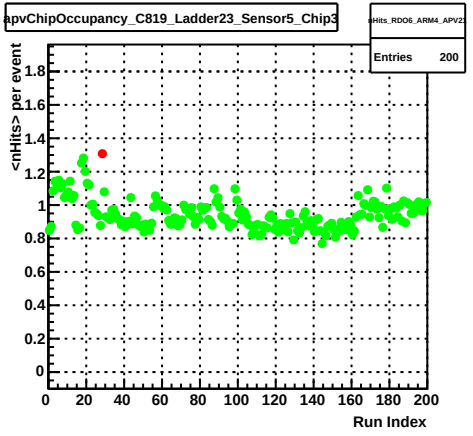
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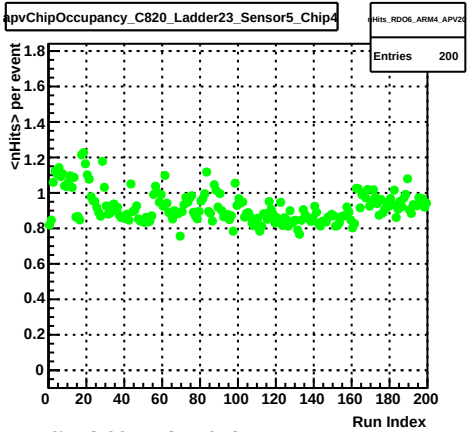
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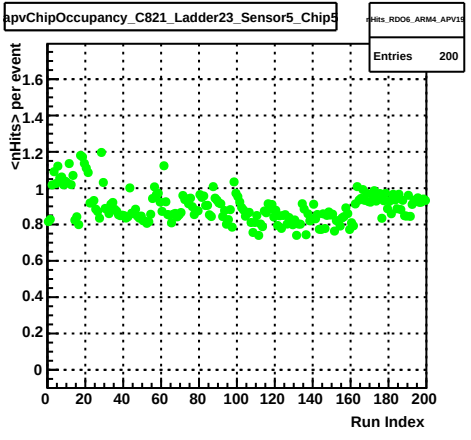
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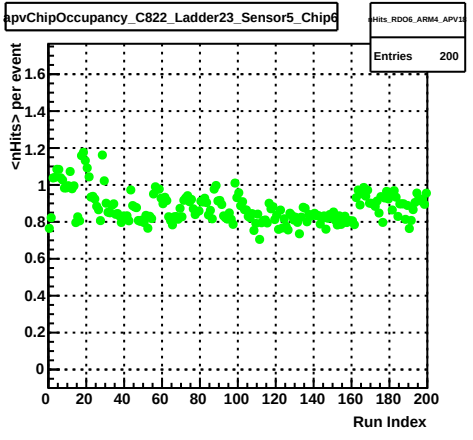
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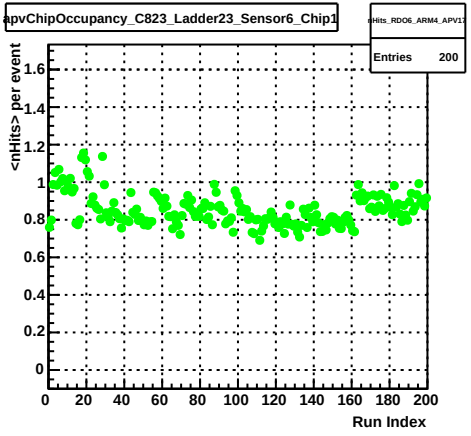
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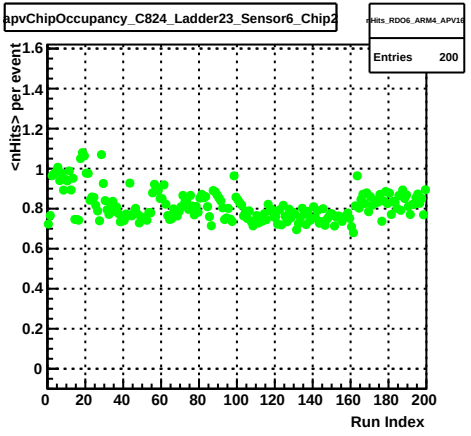
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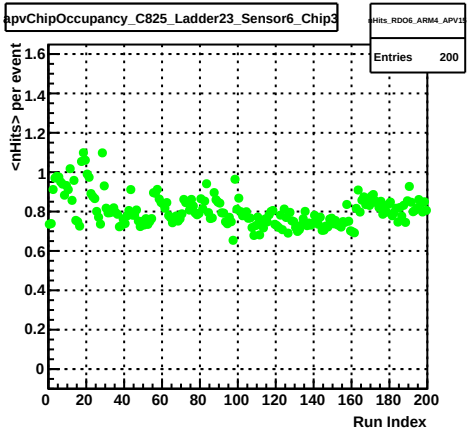
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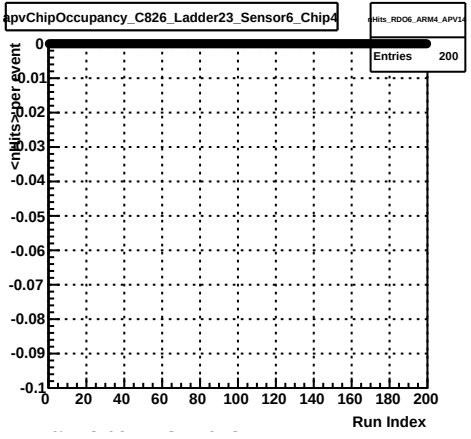
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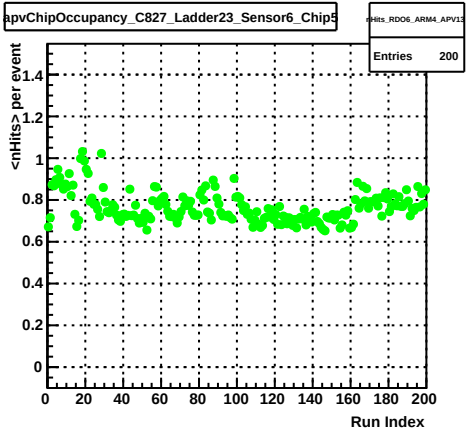
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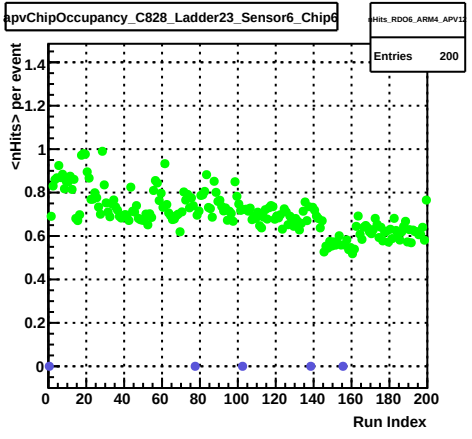
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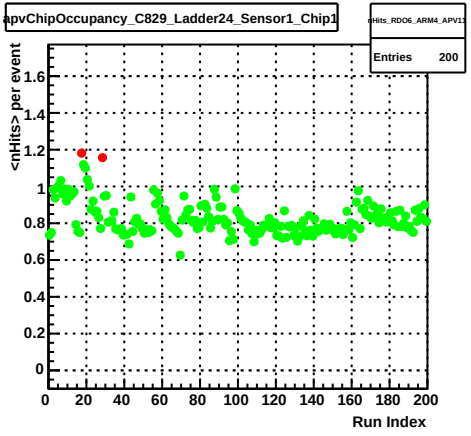
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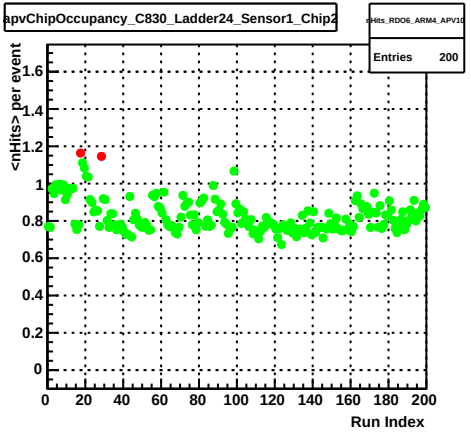
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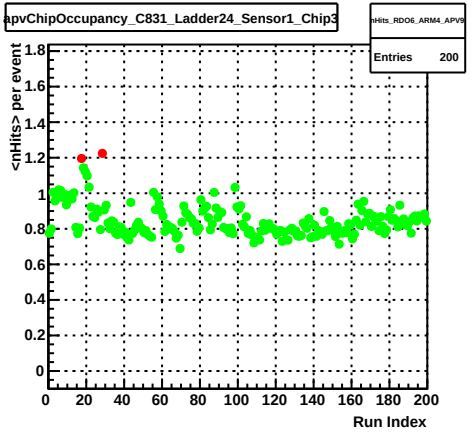
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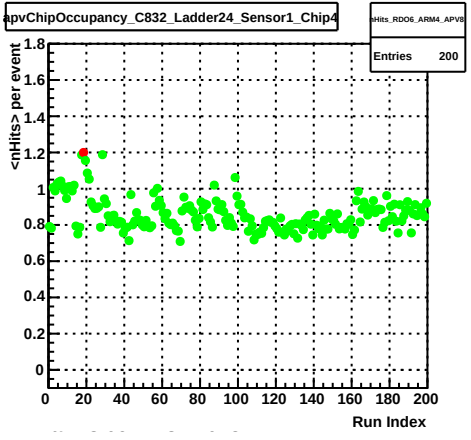
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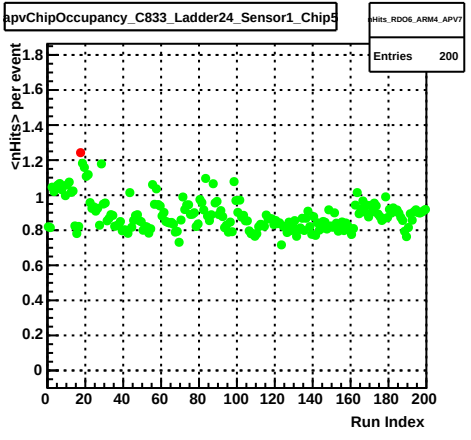
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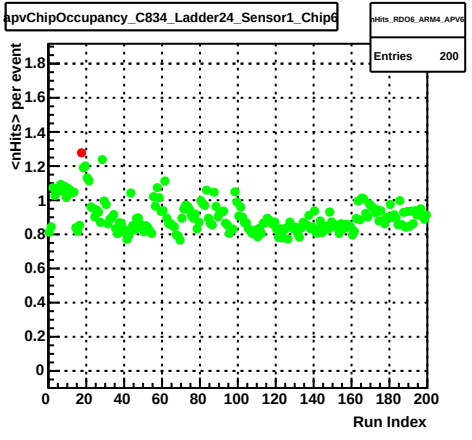
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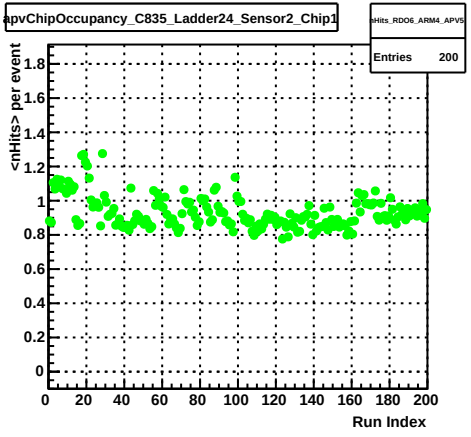
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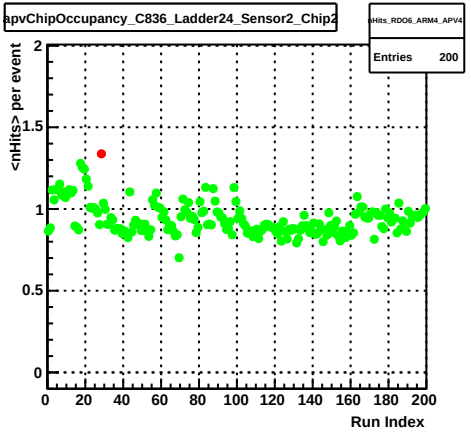
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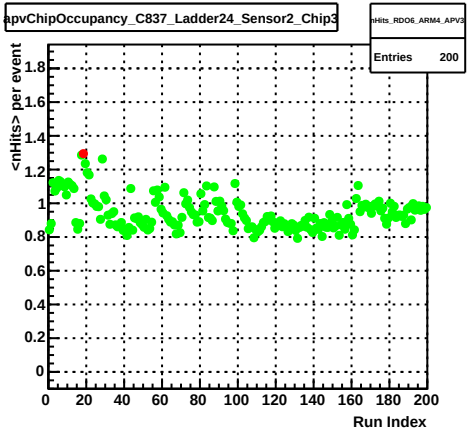
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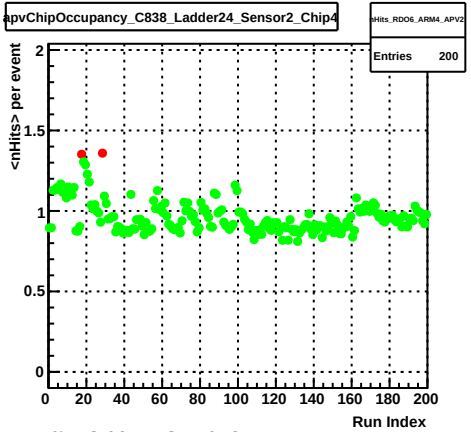
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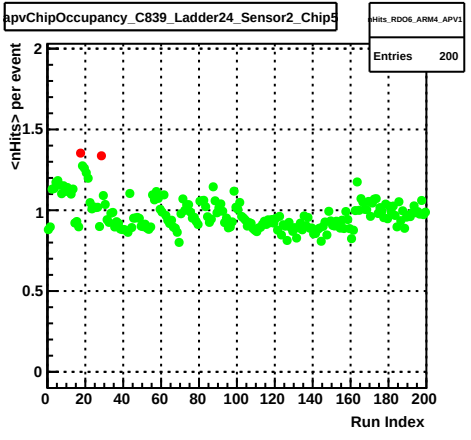
Run Index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Entries 200
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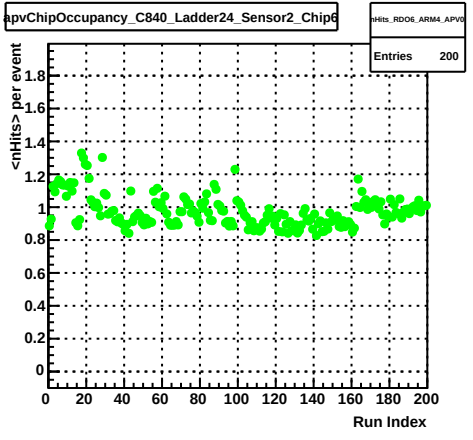
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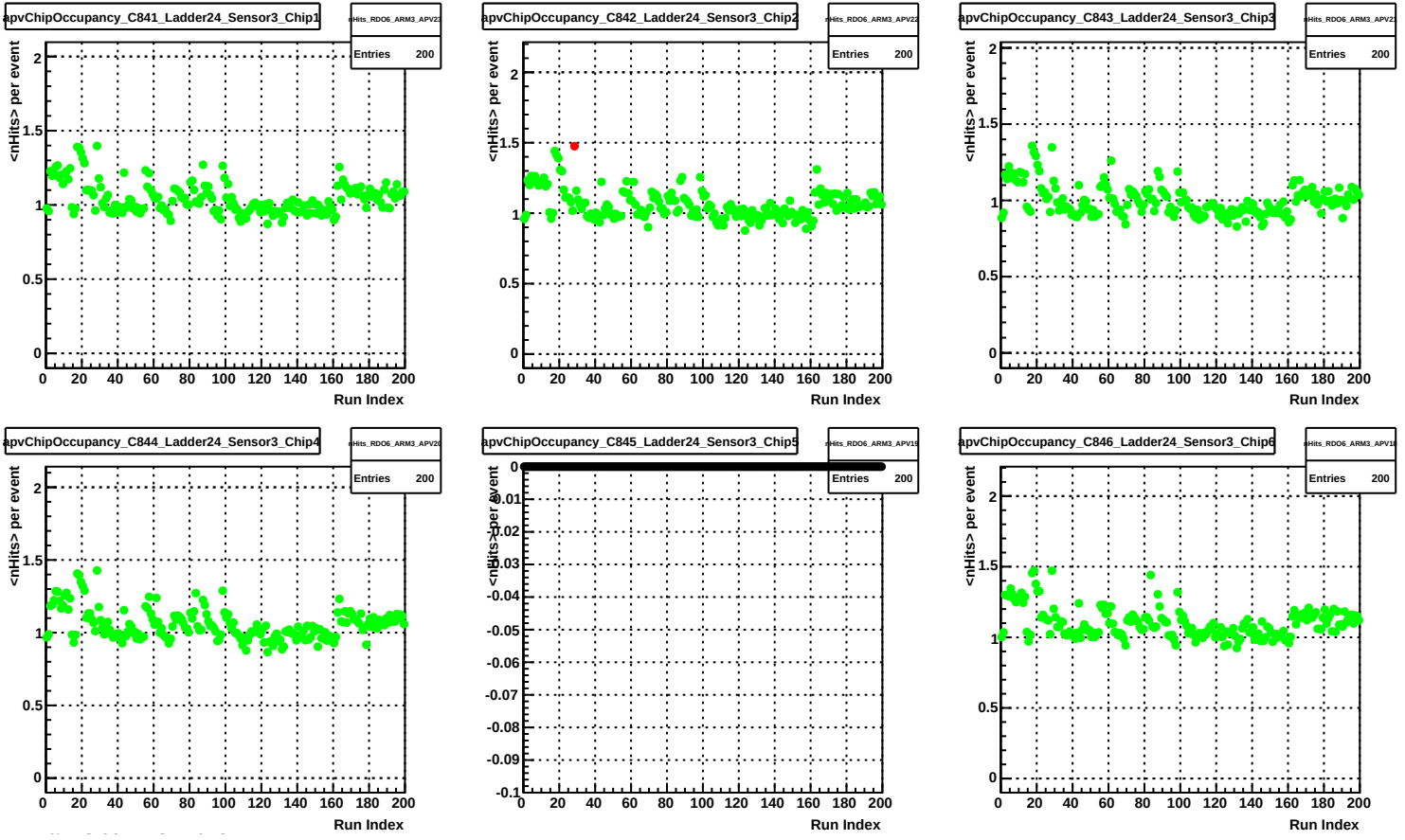
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apvChipOccupancy_C843_Ladder24_Sensor3_Chip3
Entries 200
apvChipOccupancy_C844_Ladder24_Sensor3_Chip4
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apvChipOccupancy_C845_Ladder24_Sensor3_Chip5
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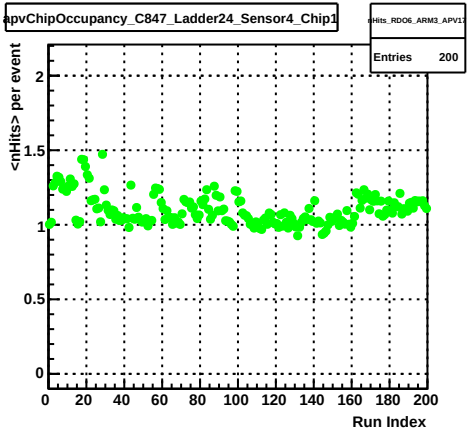
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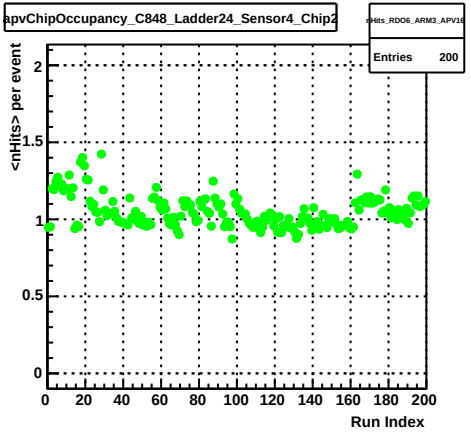
Run Index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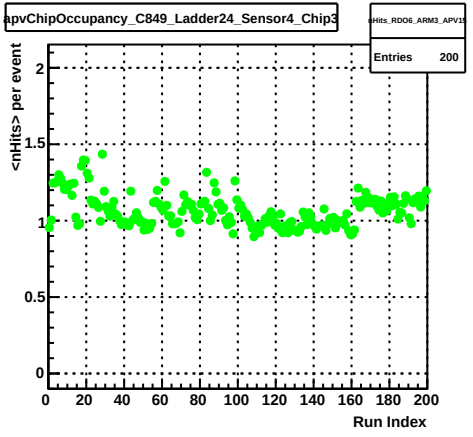
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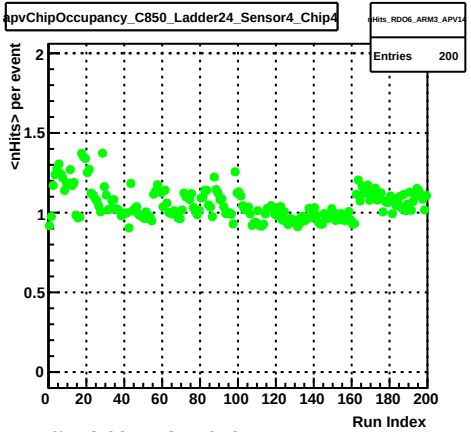
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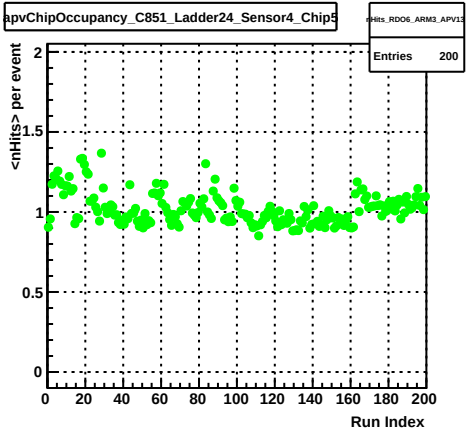
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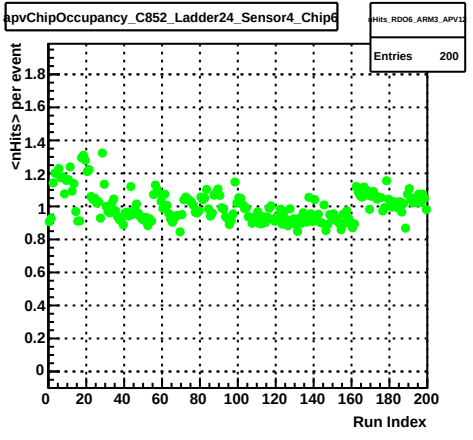
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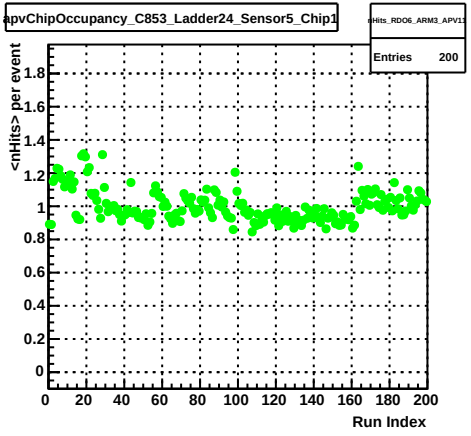
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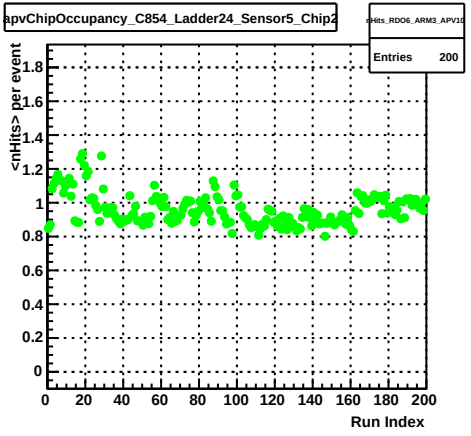
Run Index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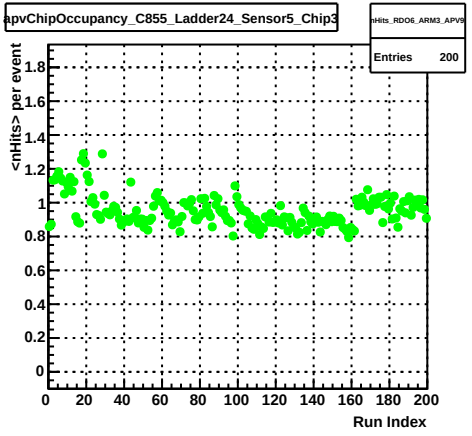
Run Index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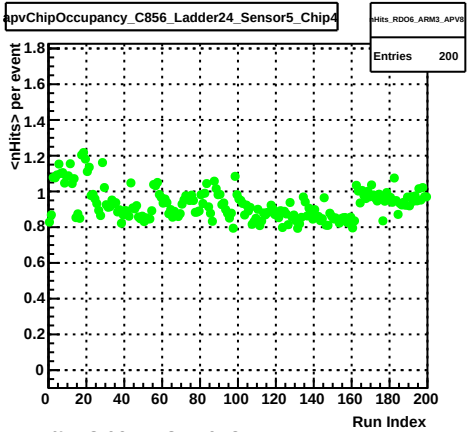
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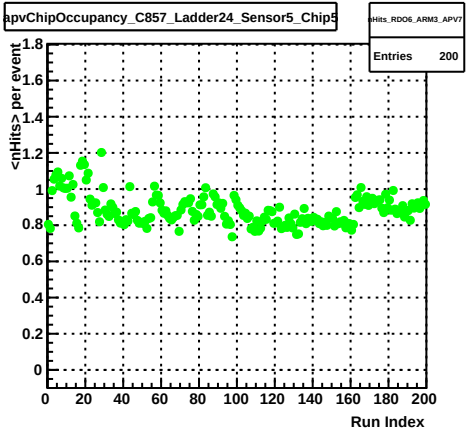
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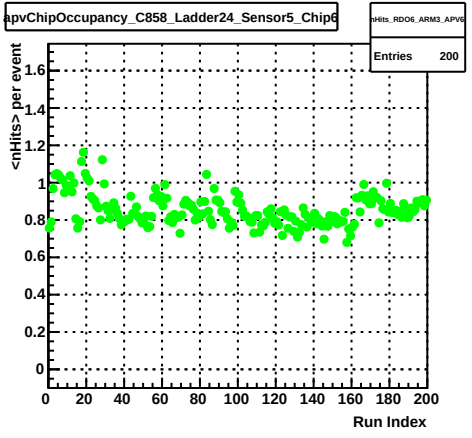
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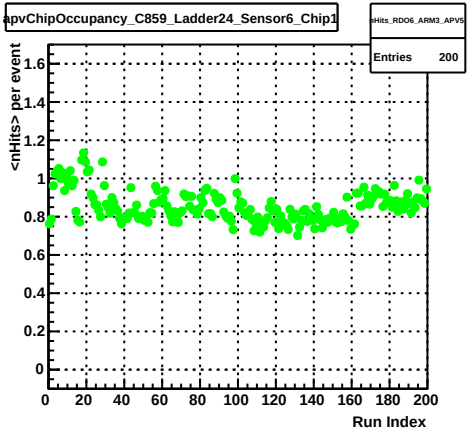
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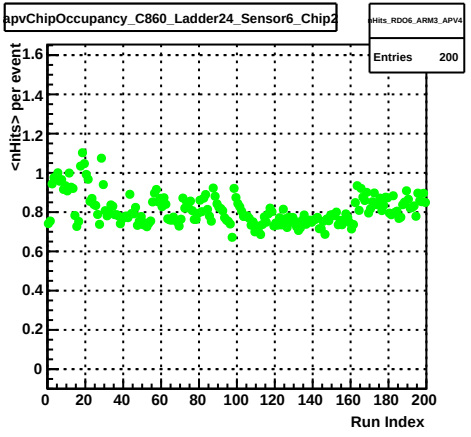
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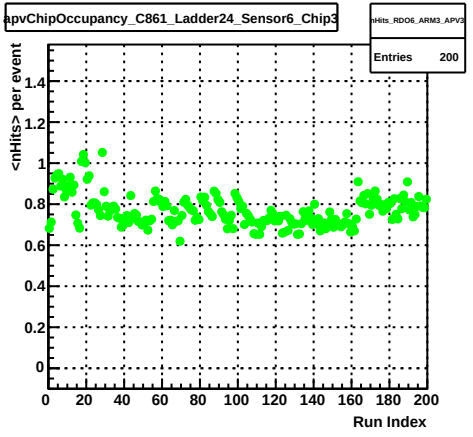
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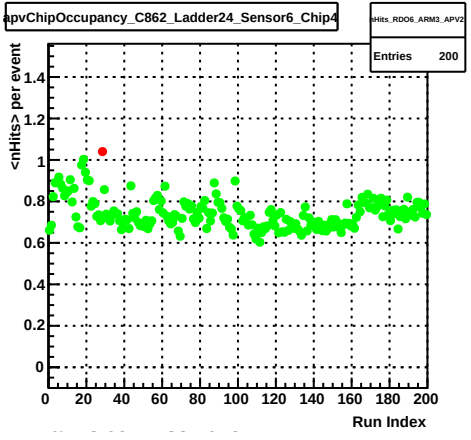
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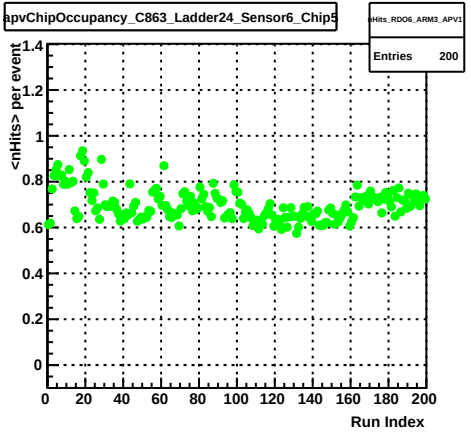
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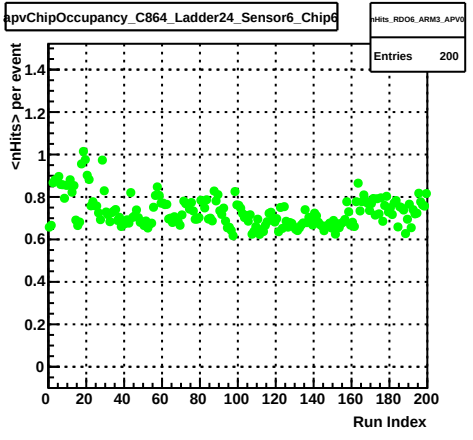
Run Index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Entries 200
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