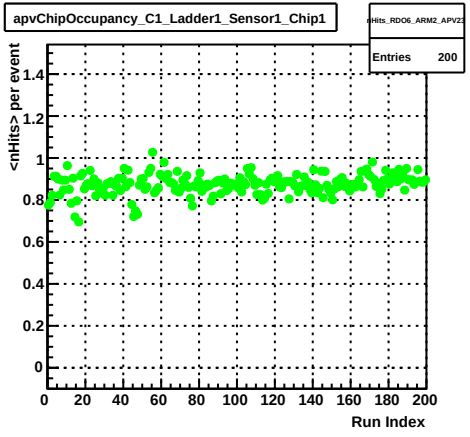
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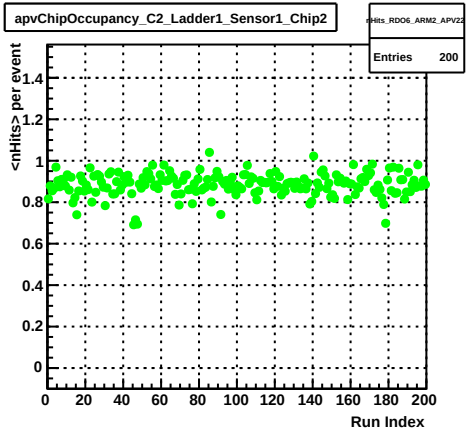
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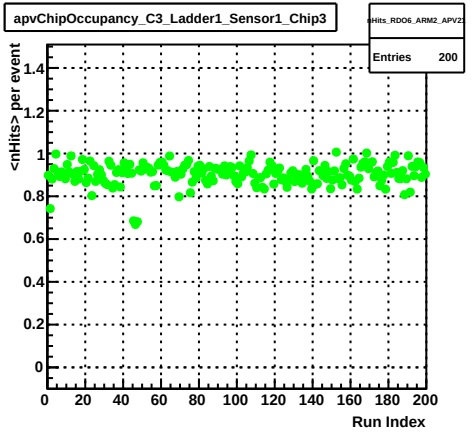
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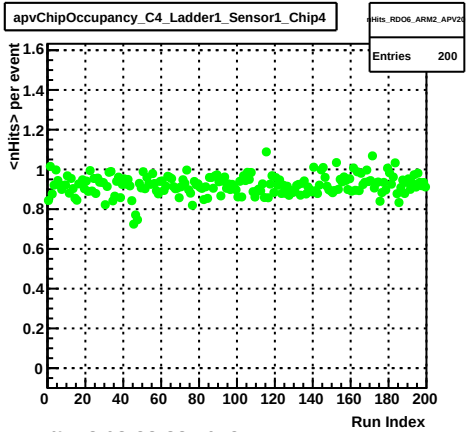
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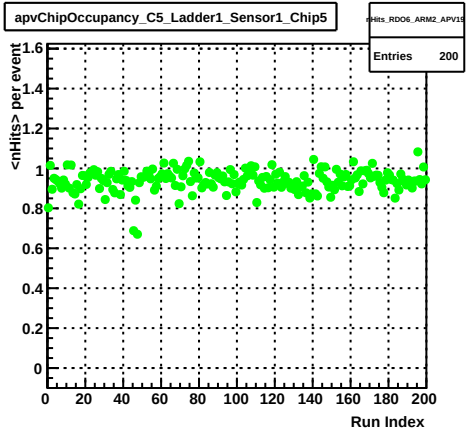
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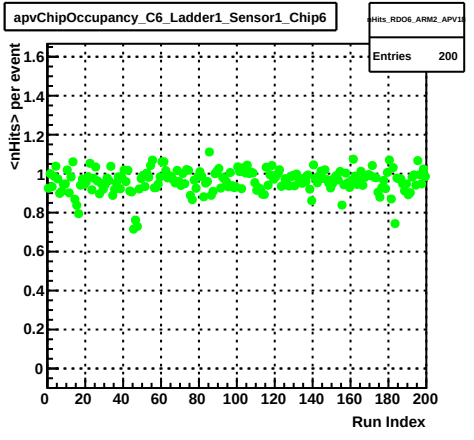
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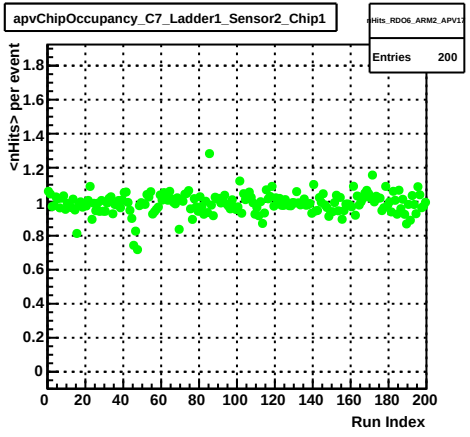
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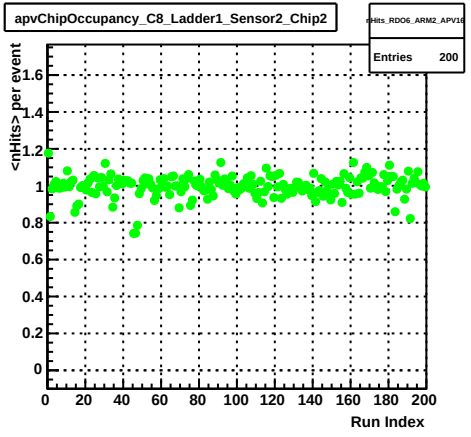
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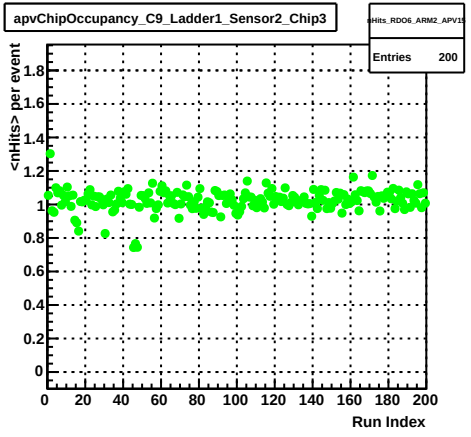
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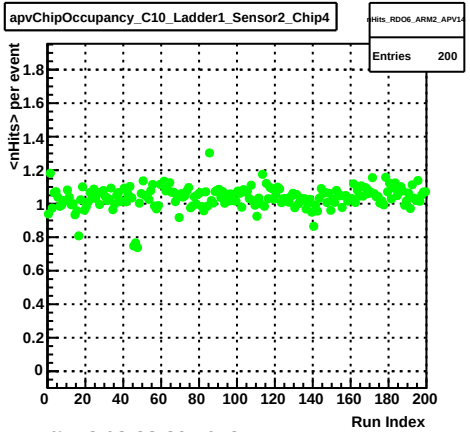
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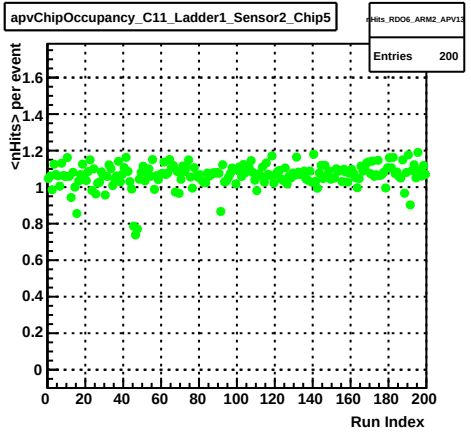
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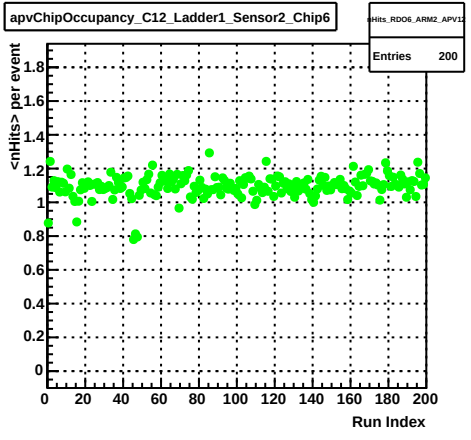
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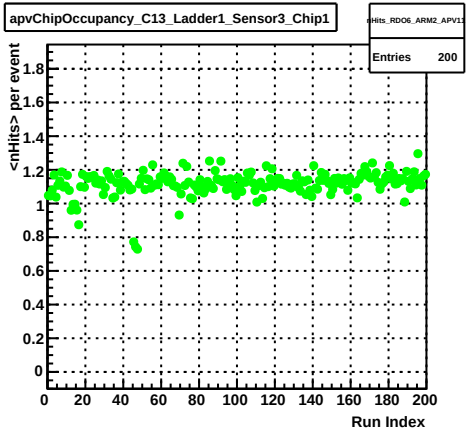
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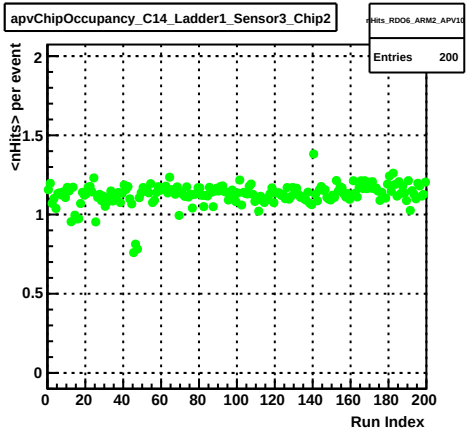
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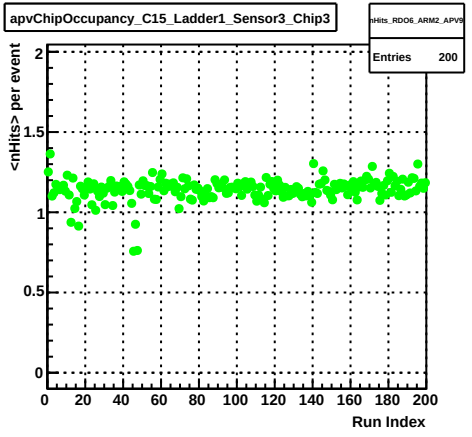
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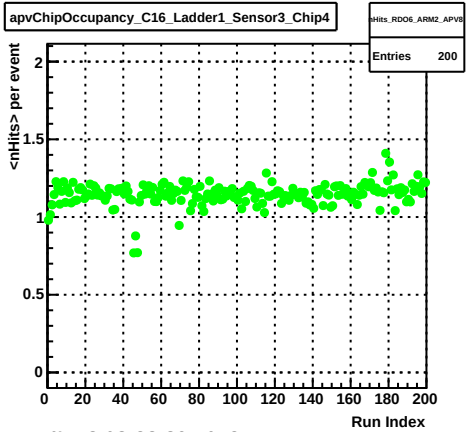
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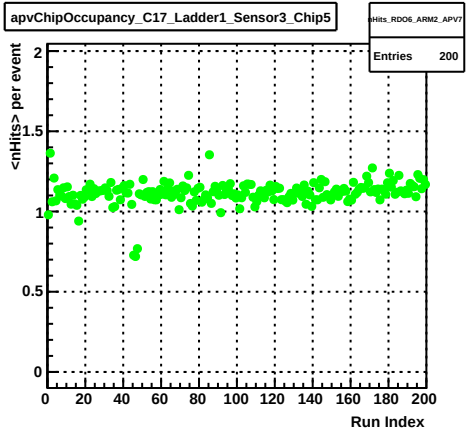
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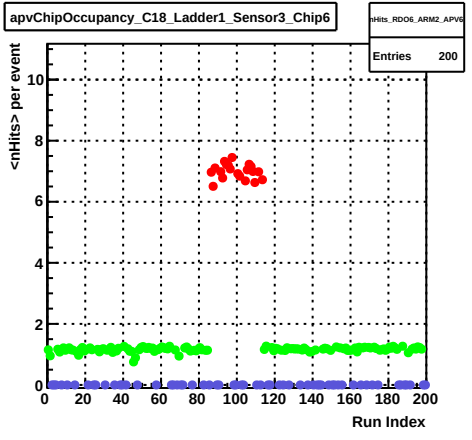
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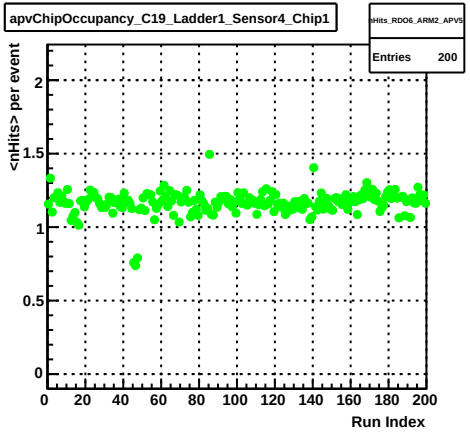
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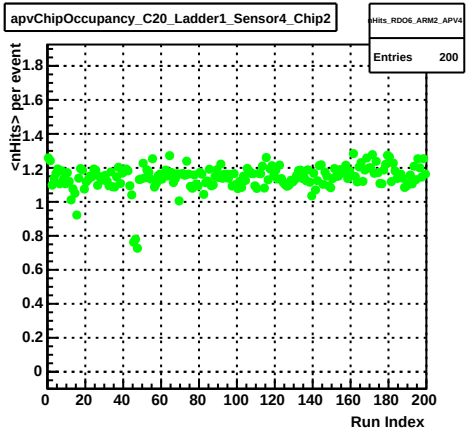
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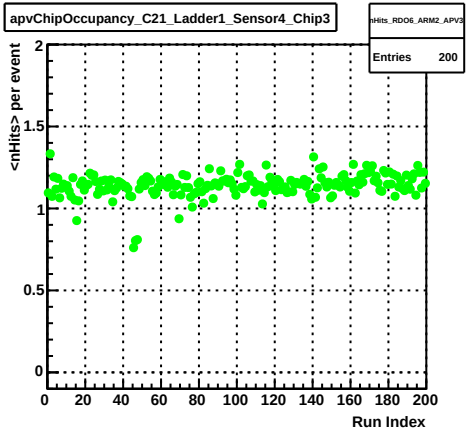
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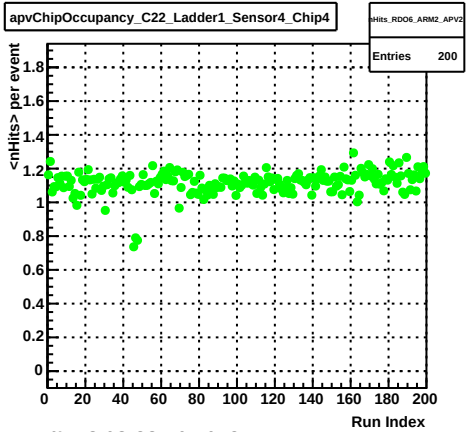
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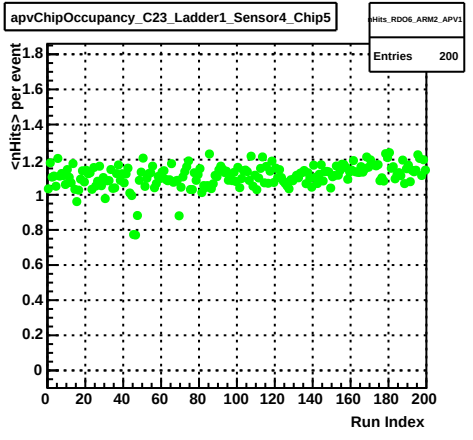
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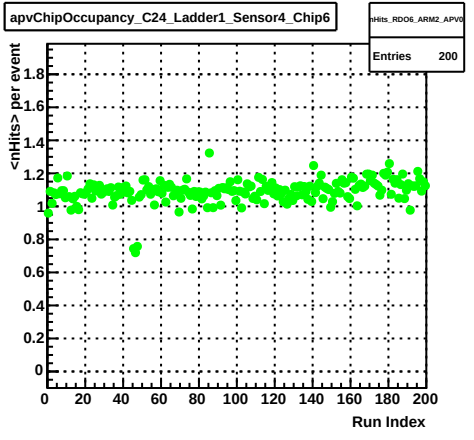
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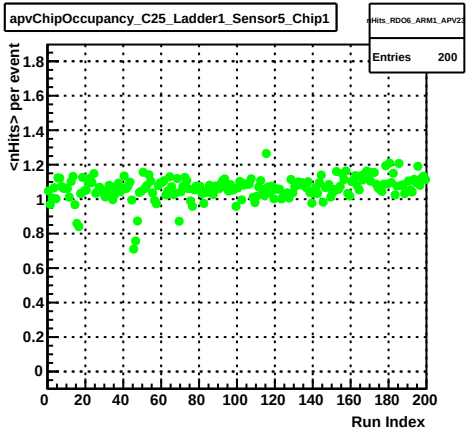
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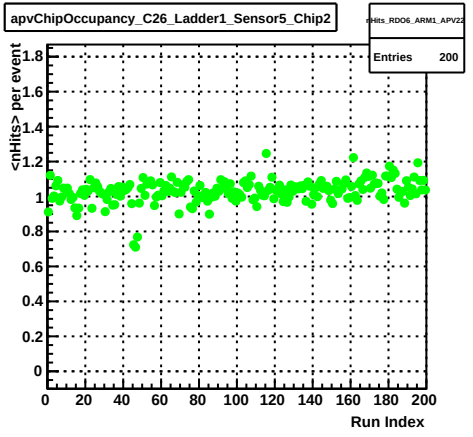
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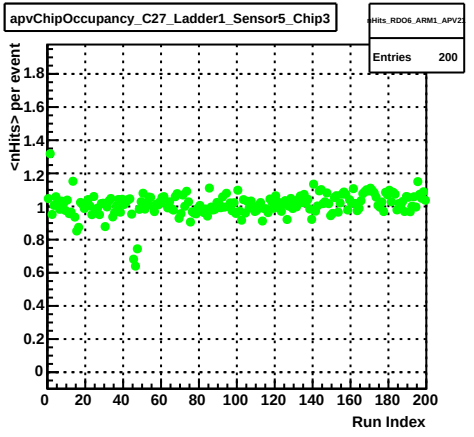
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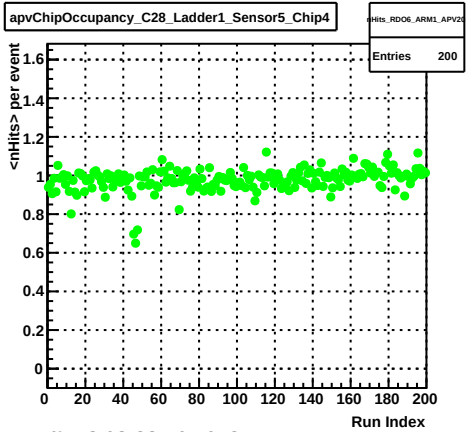
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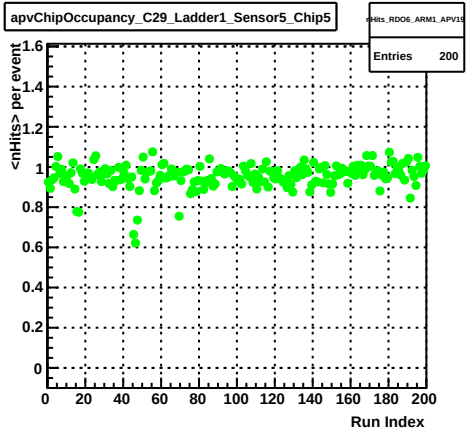
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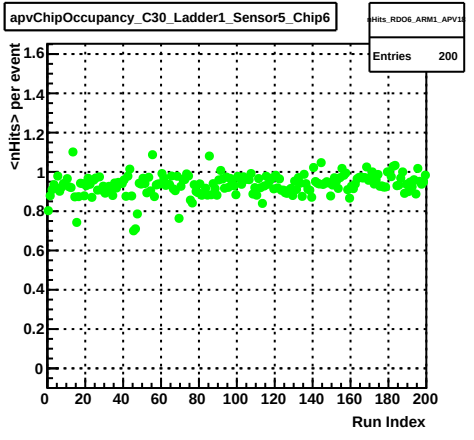
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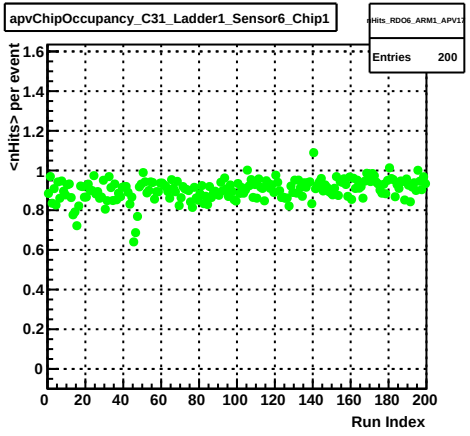
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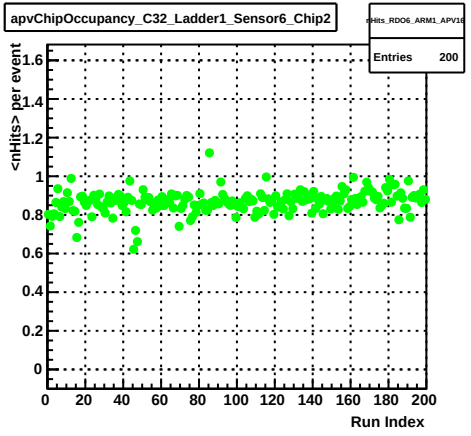
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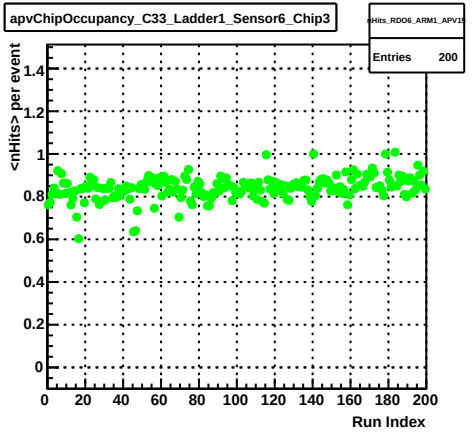
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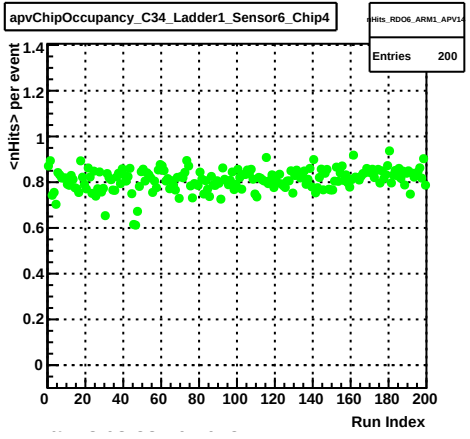
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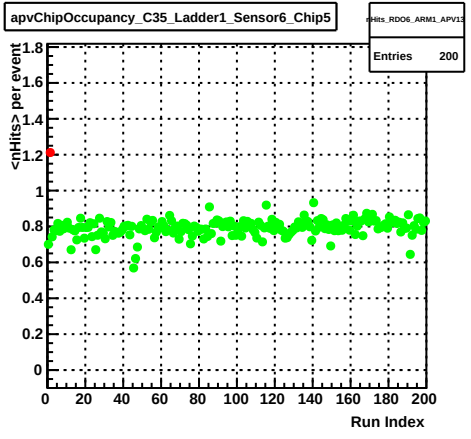
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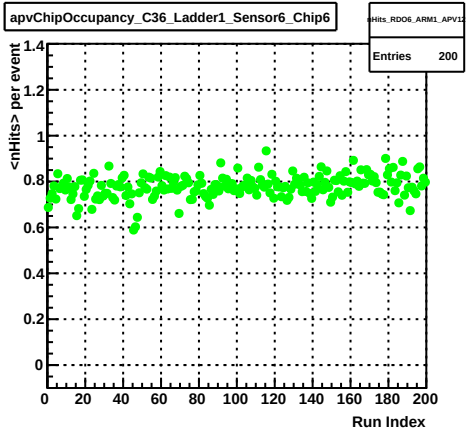
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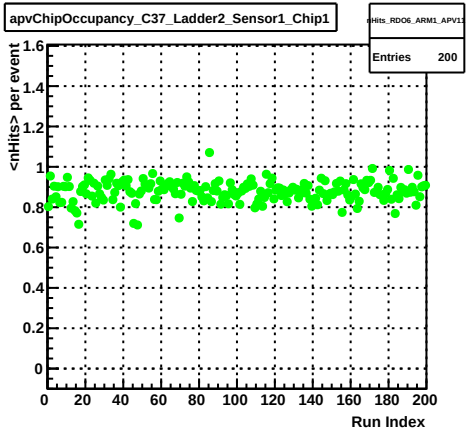
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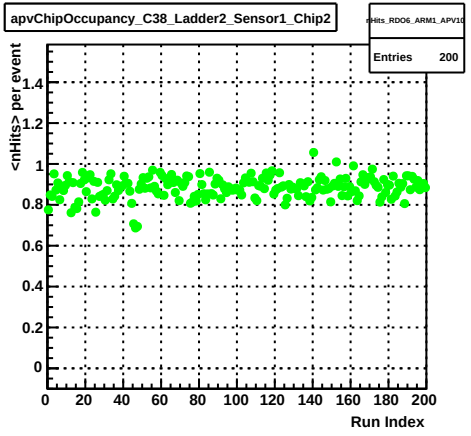
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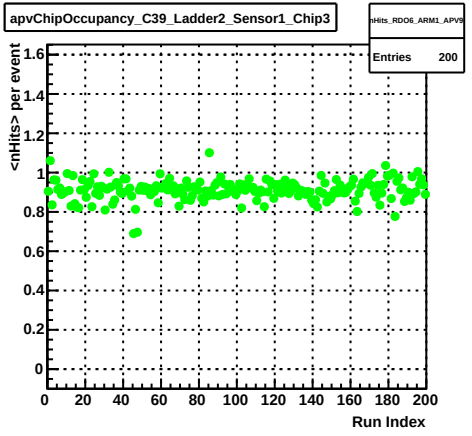
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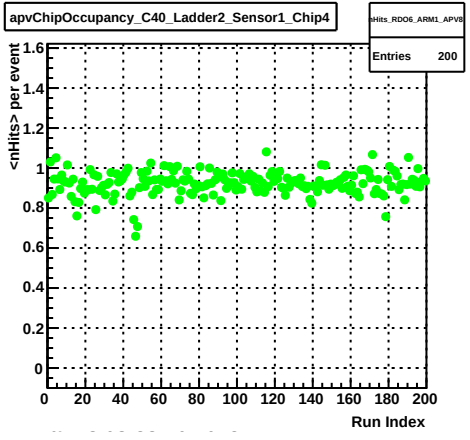
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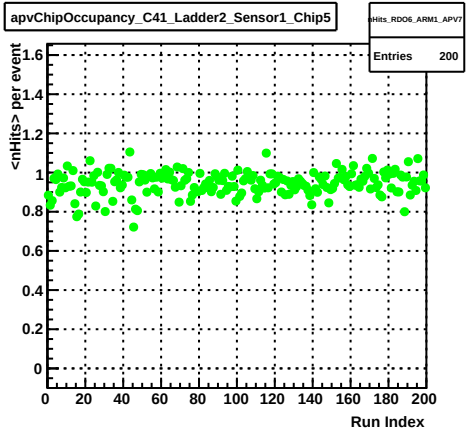
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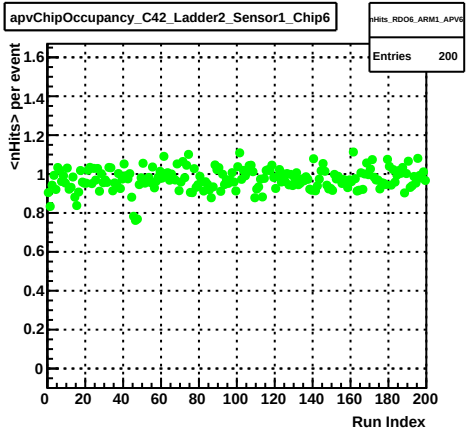
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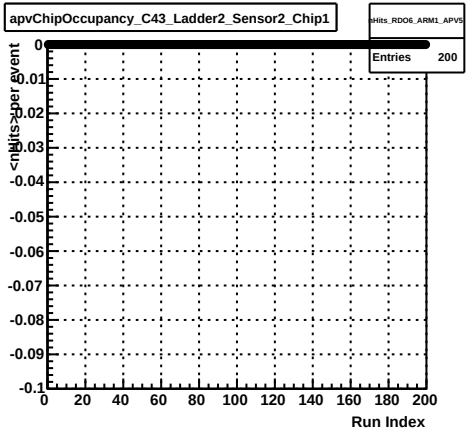
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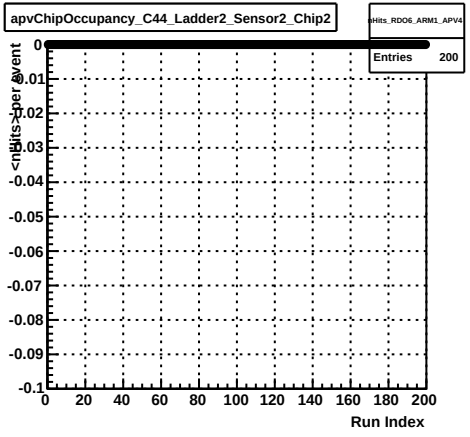
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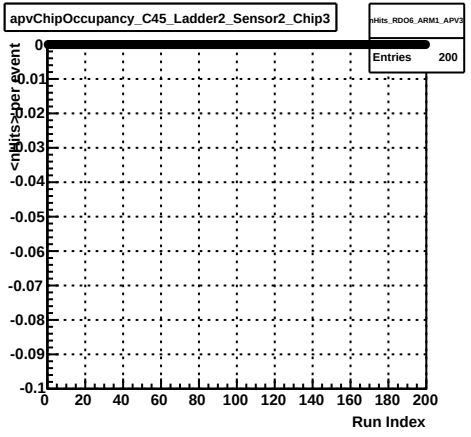
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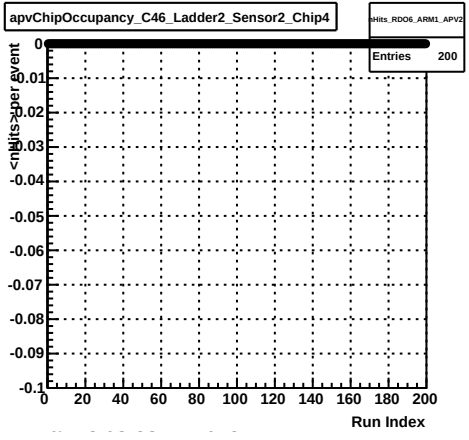
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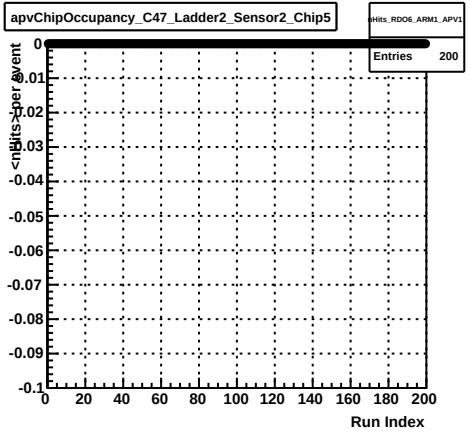
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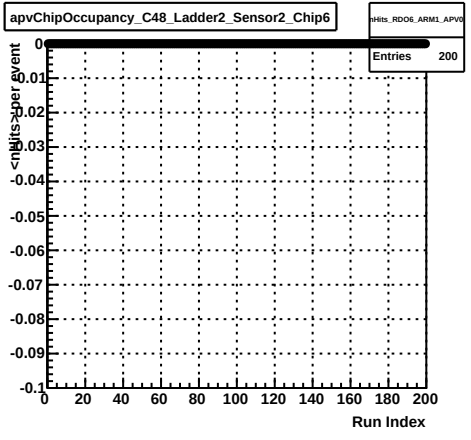
Run Index


apvChipOccupancy_C48_Ladder2_Sensor2_Chip6
nHits_RDO6_ARM1_APV0
Entries 200
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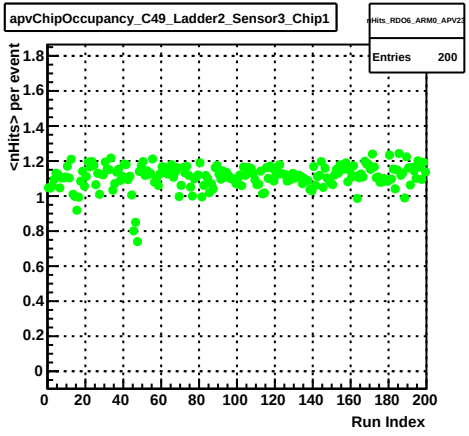
Run Index


apvChipOccupancy_C49_Ladder2_Sensor3_Chip1
nHits_RDO6_ARM0_APV25
Entries 200
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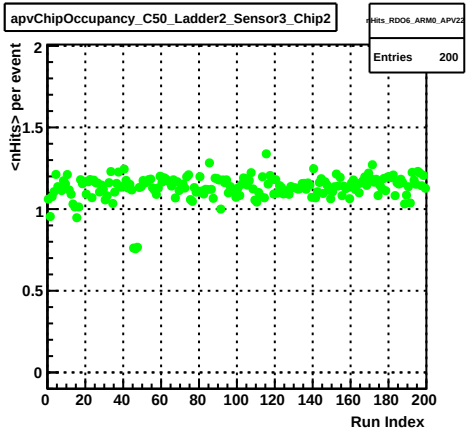
Run Index


apvChipOccupancy_C50_Ladder2_Sensor3_Chip2
nHits_RDO6_ARM0_APV22
Entries 200
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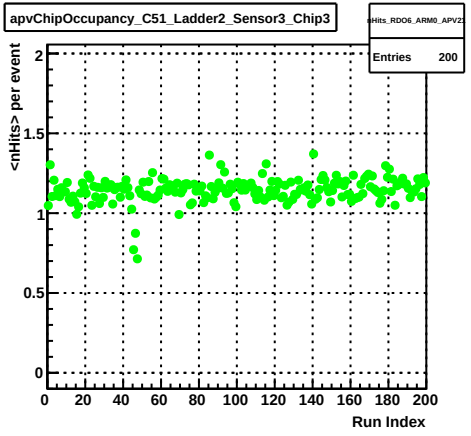
Run Index


apvChipOccupancy_C51_Ladder2_Sensor3_Chip3
nHits_RDO6_ARM0_APV23
Entries 200
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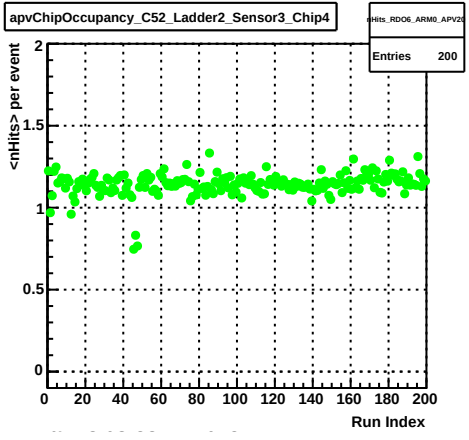
Run Index


apvChipOccupancy_C52_Ladder2_Sensor3_Chip4
nHits_RDO6_ARM0_APV20
Entries 200
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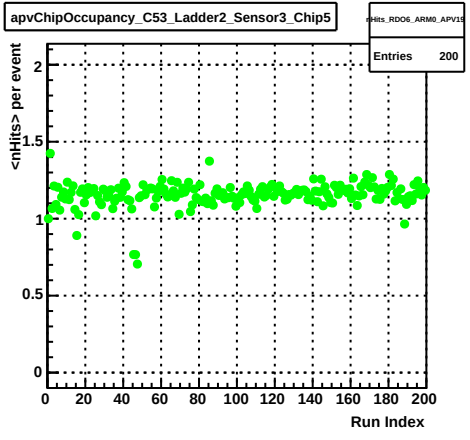
Run Index


apvChipOccupancy_C53_Ladder2_Sensor3_Chip5
nHits_RDO6_ARM0_APV18
Entries 200
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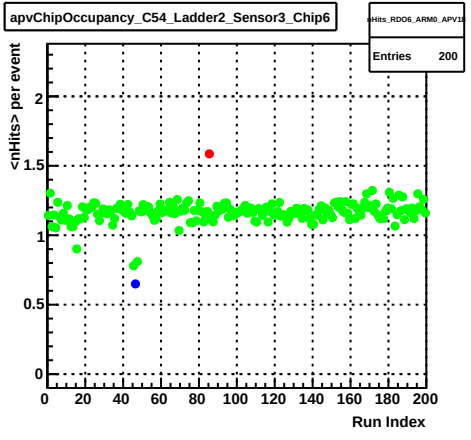
Run Index


apvChipOccupancy_C54_Ladder2_Sensor3_Chip6
nHits_RDO6_ARM0_APV16
Entries 200
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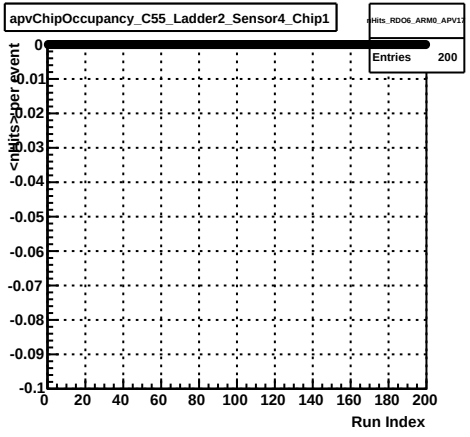
Run Index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Entries 200
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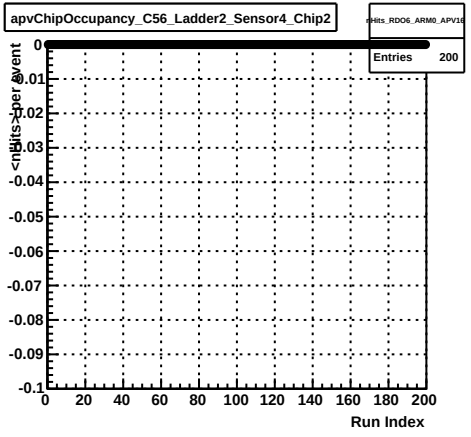
apvChipOccupancy_C56_Ladder2_Sensor4_Chip2
Entries 200
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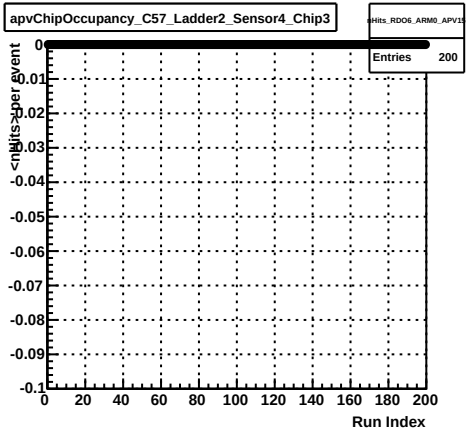
Run Index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Entries 200
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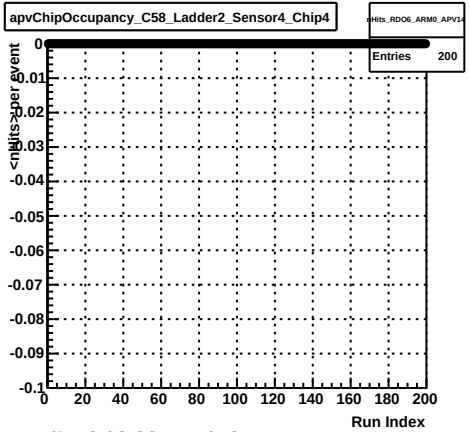
Run Index


apvChipOccupancy_C58_Ladder2_Sensor4_Chip4
Entries 200
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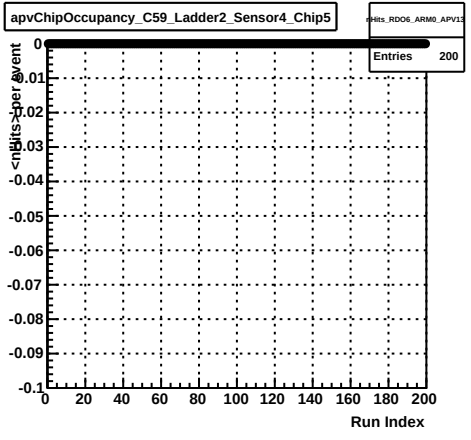
Run Index


apvChipOccupancy_C59_Ladder2_Sensor4_Chip5
Entries 200
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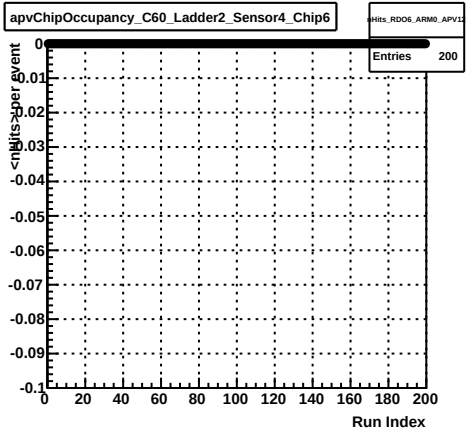
Run Index


apvChipOccupancy_C60_Ladder2_Sensor4_Chip6
Entries 200
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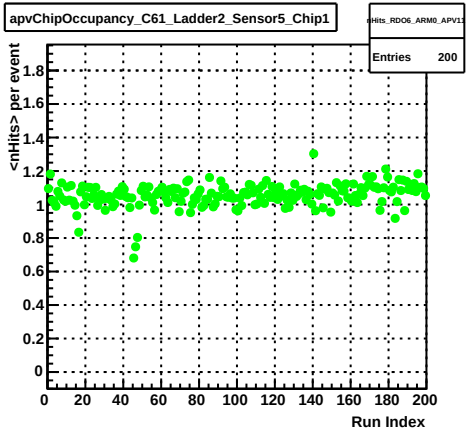
Run Index


apvChipOccupancy_C61_Ladder2_Sensor5_Chip1
Entries 200
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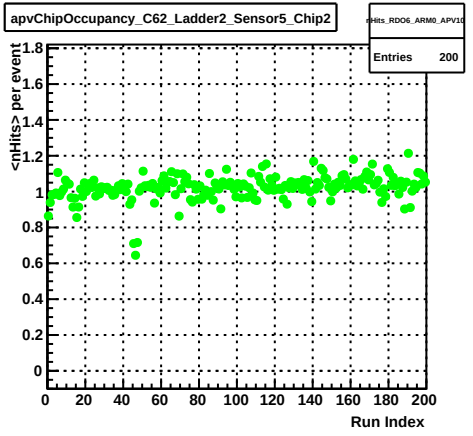
Run Index


apvChipOccupancy_C62_Ladder2_Sensor5_Chip2
Entries 200
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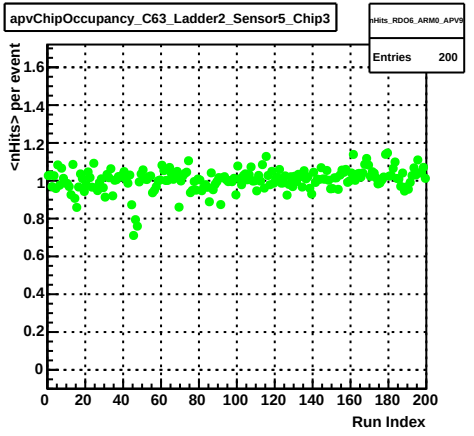
Run Index


apvChipOccupancy_C63_Ladder2_Sensor5_Chip3
Entries 200
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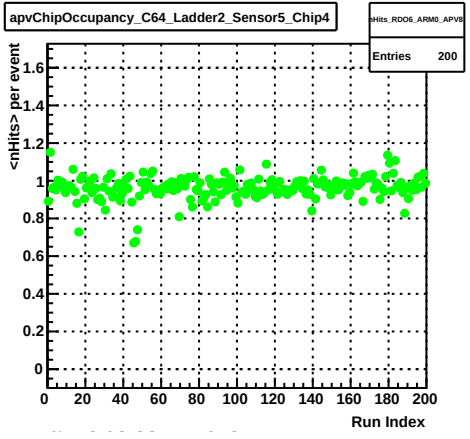
Run Index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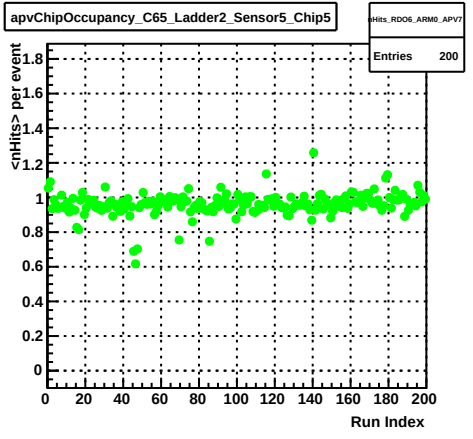
Run Index


apvChipOccupancy_C65_Ladder2_Sensor5_Chip5
Entries 200
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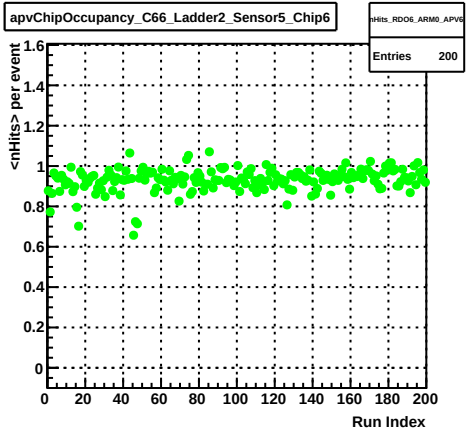
Run Index


apvChipOccupancy_C66_Ladder2_Sensor5_Chip6
Entries 200
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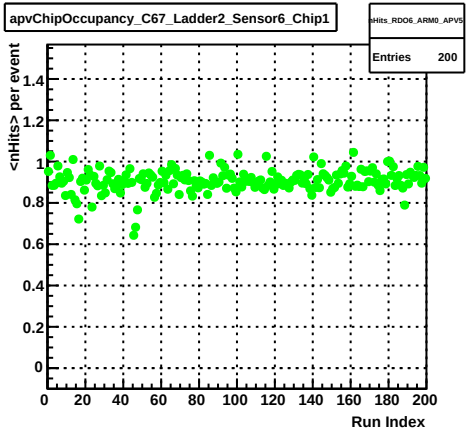
Run Index


apvChipOccupancy_C67_Ladder2_Sensor6_Chip1
nHits_RDO6_ARM0_APV5
Entries 200
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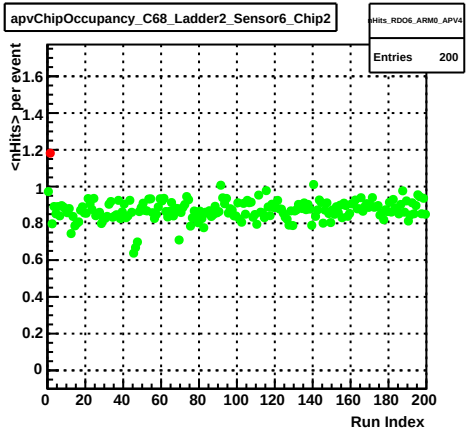
Run Index


apvChipOccupancy_C68_Ladder2_Sensor6_Chip2
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Entries 200
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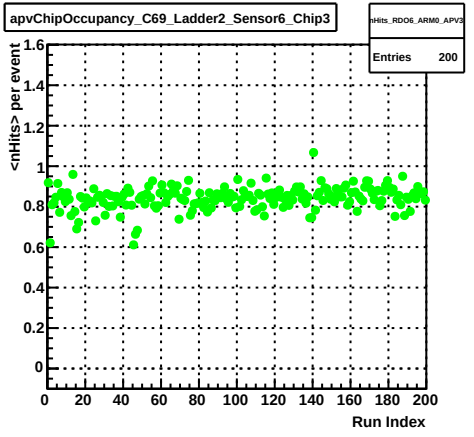
Run Index


apvChipOccupancy_C69_Ladder2_Sensor6_Chip3
nHits_RDO6_ARM0_APV3
Entries 200
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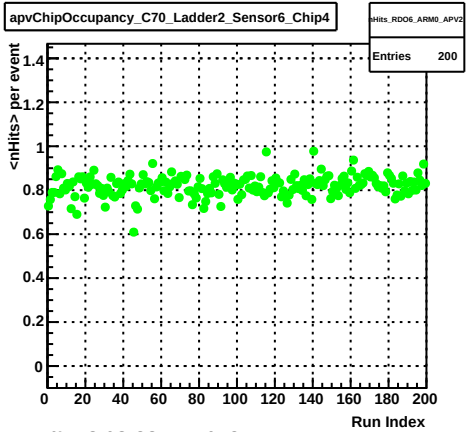
Run Index


apvChipOccupancy_C70_Ladder2_Sensor6_Chip4
nHits_RDO6_ARM0_APV2
Entries 200
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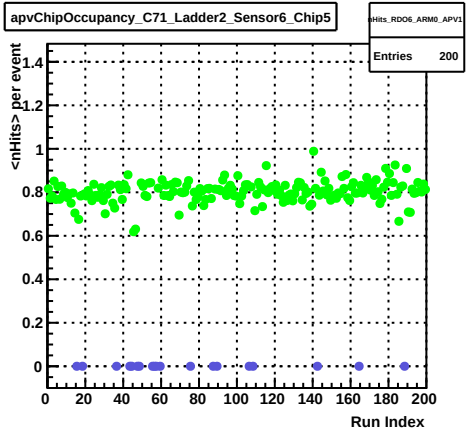
Run Index


apvChipOccupancy_C71_Ladder2_Sensor6_Chip5
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Entries 200
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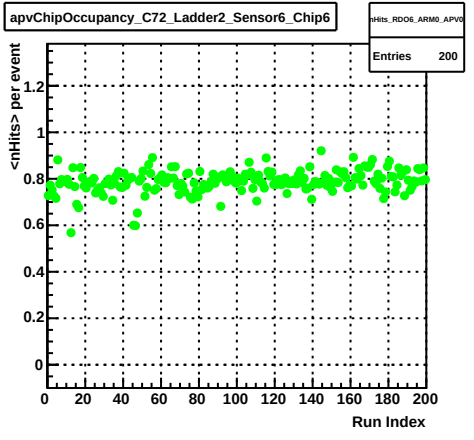
Run Index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Entries 200
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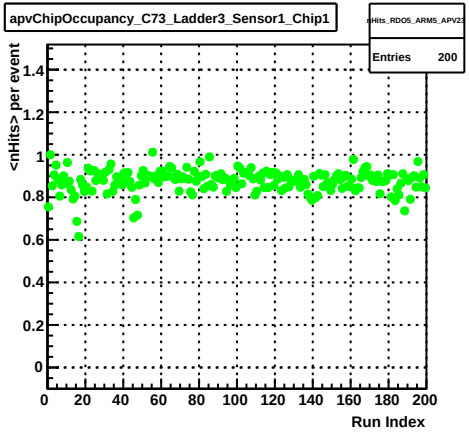
Run Index


apvChipOccupancy_C73_Ladder3_Sensor1_Chip1
Entries 200
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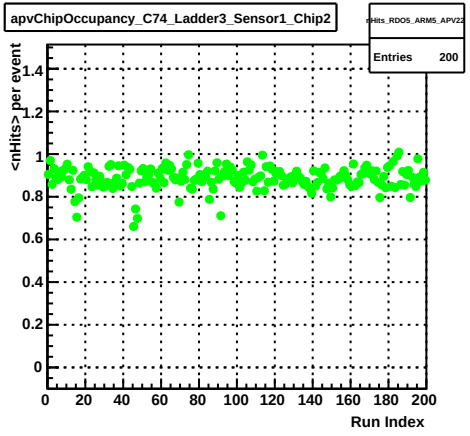
Run Index


apvChipOccupancy_C74_Ladder3_Sensor1_Chip2
Entries 200
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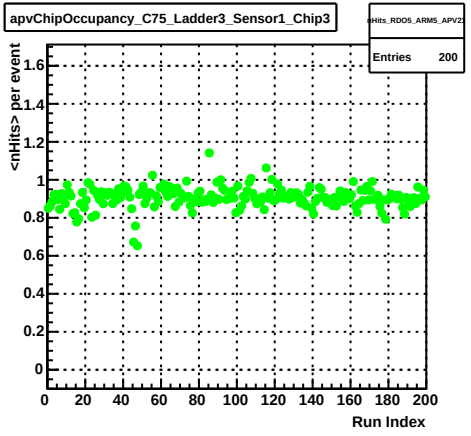
Run Index


apvChipOccupancy_C75_Ladder3_Sensor1_Chip3
Entries 200
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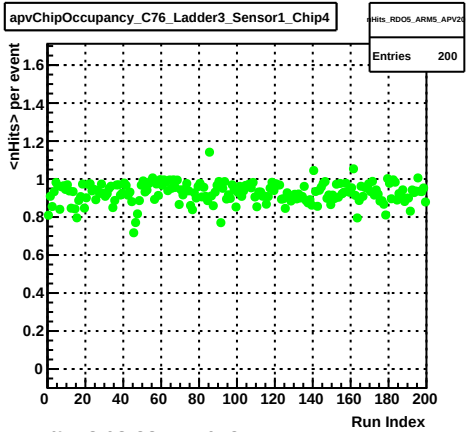
Run Index


apvChipOccupancy_C76_Ladder3_Sensor1_Chip4
Entries 200
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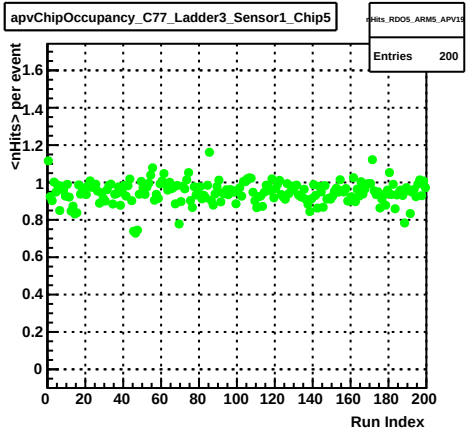
Run Index


apvChipOccupancy_C77_Ladder3_Sensor1_Chip5
Entries 200
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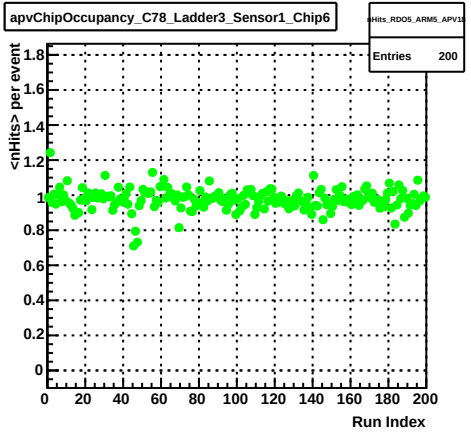
Run Index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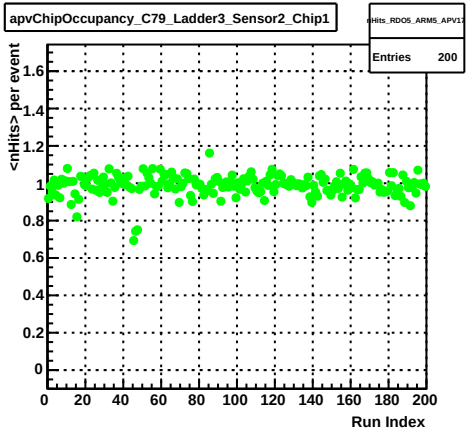
Run Index


apvChipOccupancy_C79_Ladder3_Sensor2_Chip1
Entries 200
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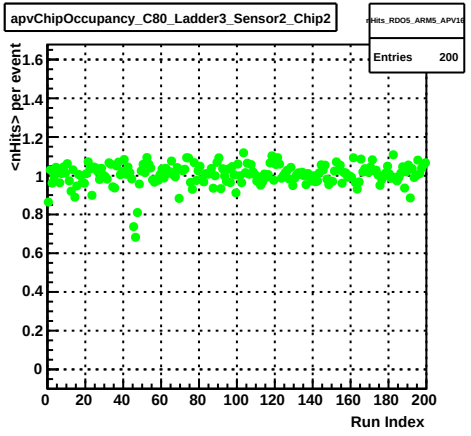
Run Index


apvChipOccupancy_C80_Ladder3_Sensor2_Chip2
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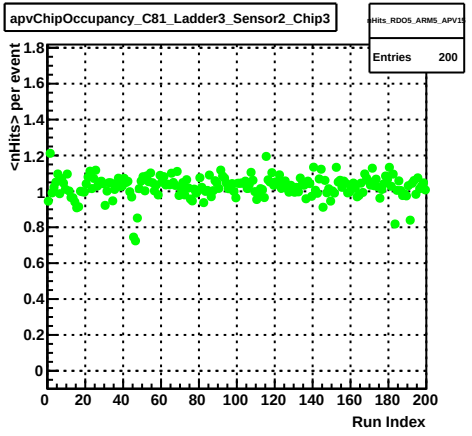
Run Index


apvChipOccupancy_C81_Ladder3_Sensor2_Chip3
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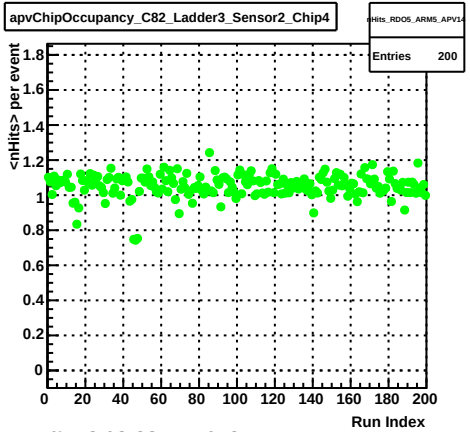
Run Index


apvChipOccupancy_C82_Ladder3_Sensor2_Chip4
Entries 200
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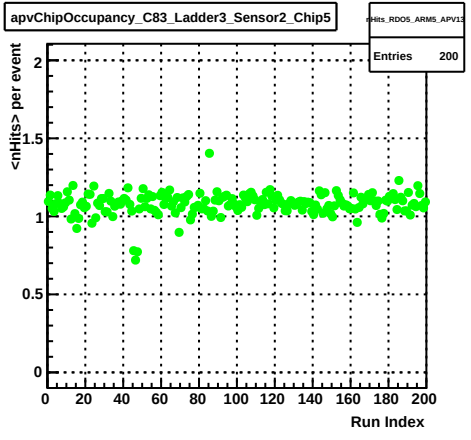
Run Index


apvChipOccupancy_C83_Ladder3_Sensor2_Chip5
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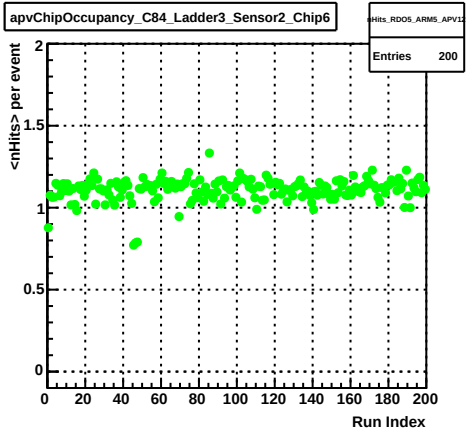
Run Index


apvChipOccupancy_C84_Ladder3_Sensor2_Chip6
Entries 200
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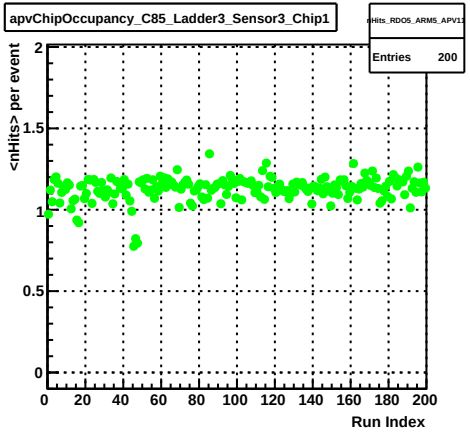
Run Index


apvChipOccupancy_C85_Ladder3_Sensor3_Chip1
Entries 200
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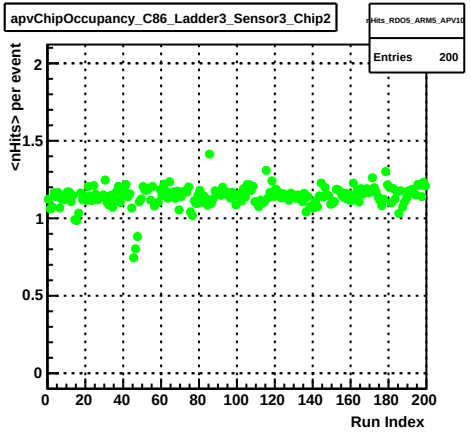
Run Index


apvChipOccupancy_C86_Ladder3_Sensor3_Chip2
Entries 200
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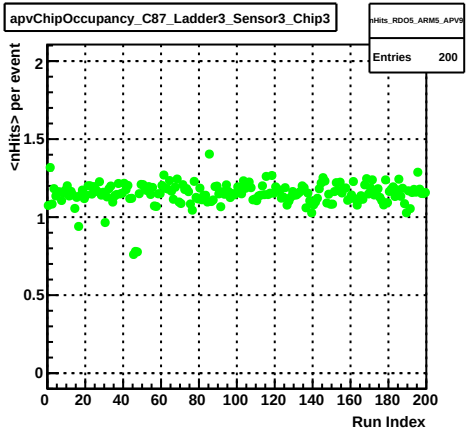
Run Index


apvChipOccupancy_C87_Ladder3_Sensor3_Chip3
Entries 200
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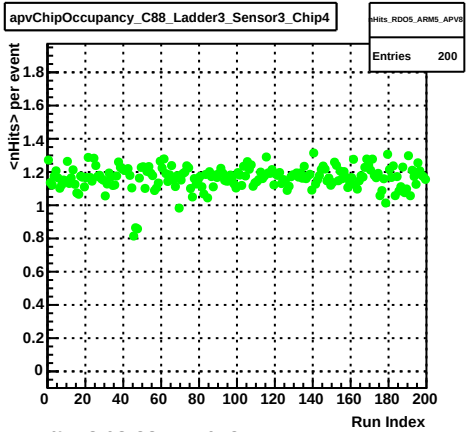
Run Index


apvChipOccupancy_C88_Ladder3_Sensor3_Chip4
Entries 200
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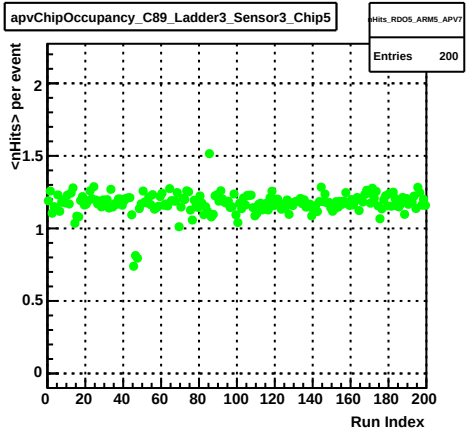
Run Index


apvChipOccupancy_C89_Ladder3_Sensor3_Chip5
Entries 200
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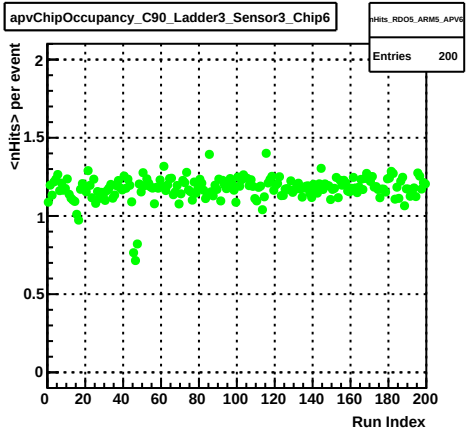
Run Index


apvChipOccupancy_C90_Ladder3_Sensor3_Chip6
Entries 200
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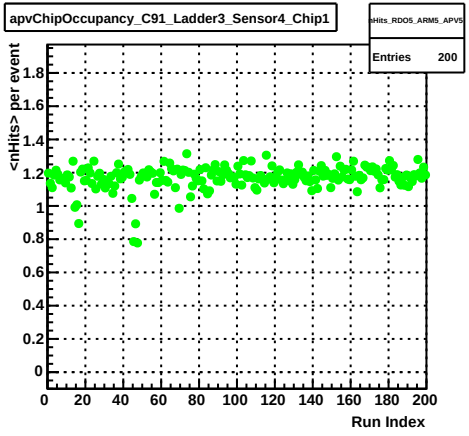
Run Index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Entries 200
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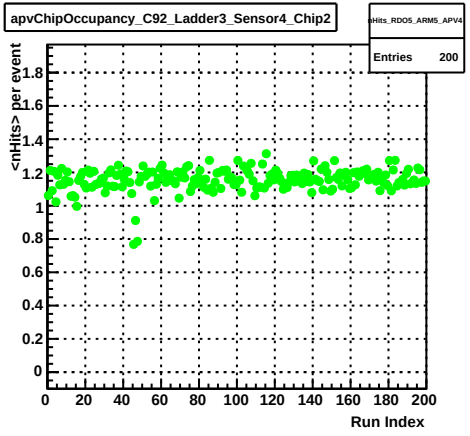
Run Index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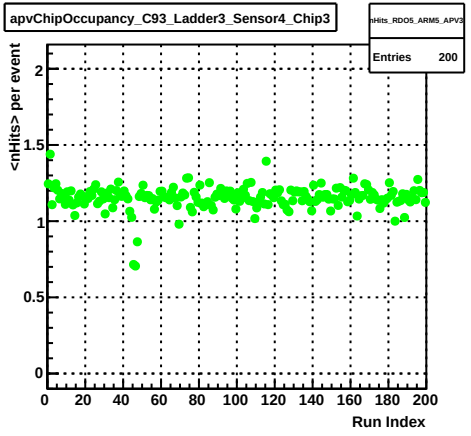
Run Index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Entries 200
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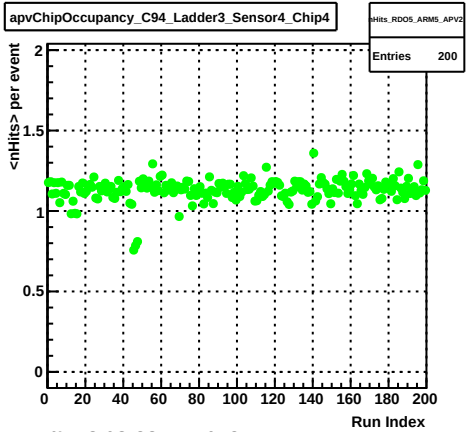
Run Index


apvChipOccupancy_C94_Ladder3_Sensor4_Chip4
nHits_RDO5_ARM5_APV2
Entries 200
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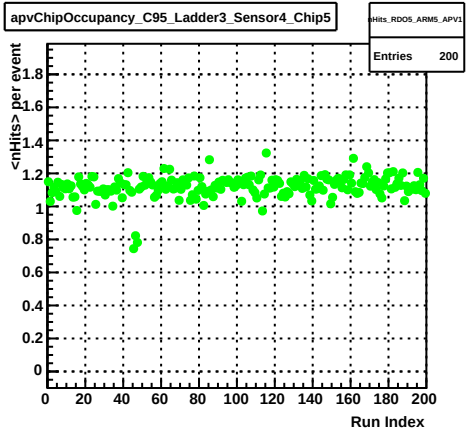
Run Index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Entries 200
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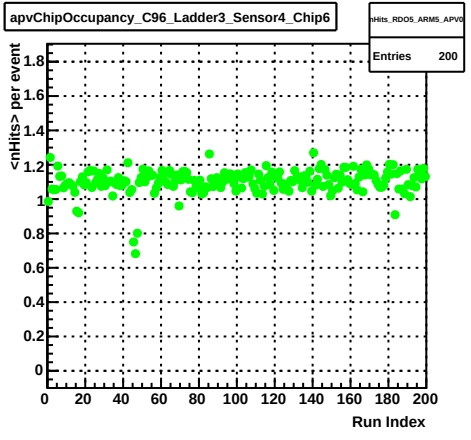
Run Index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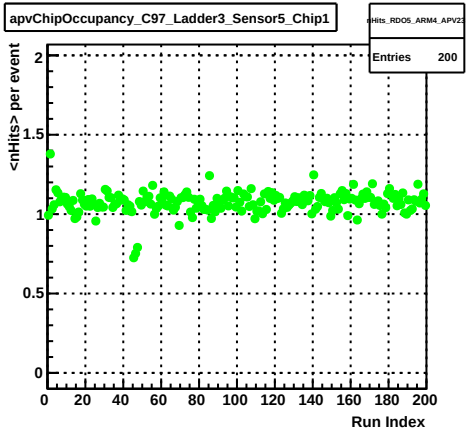
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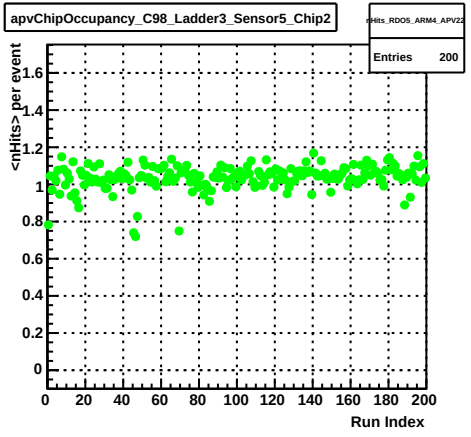
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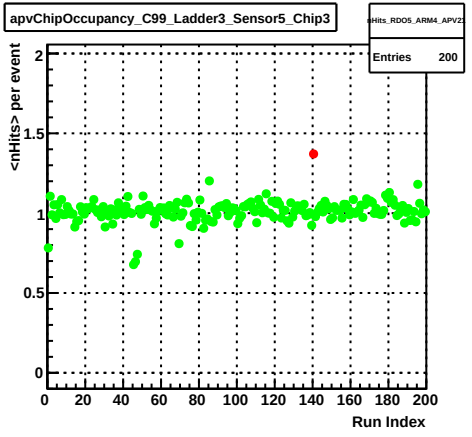
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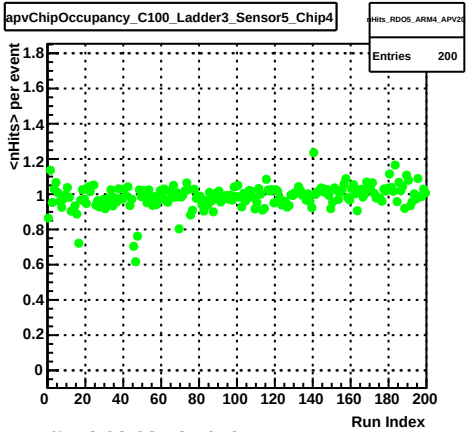
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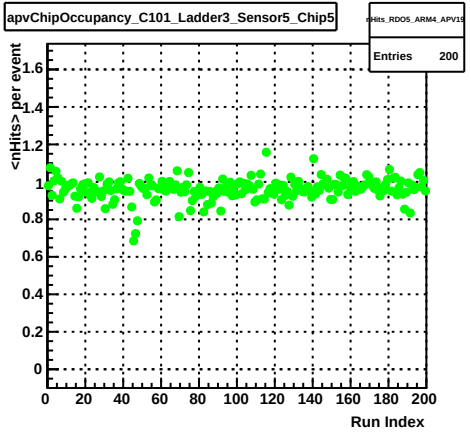
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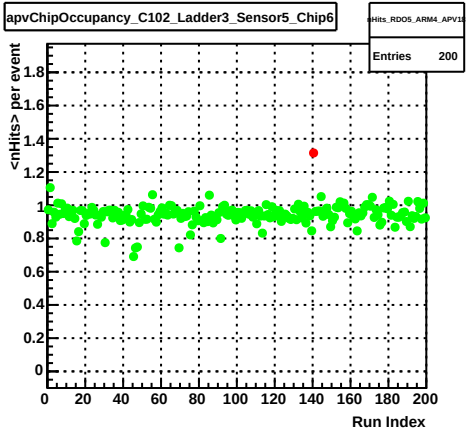
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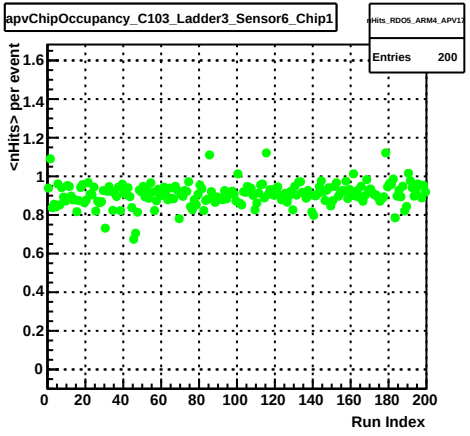
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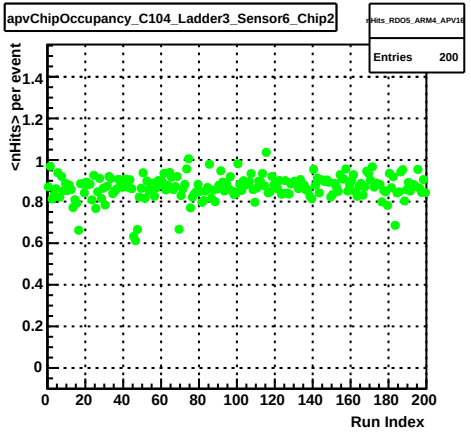
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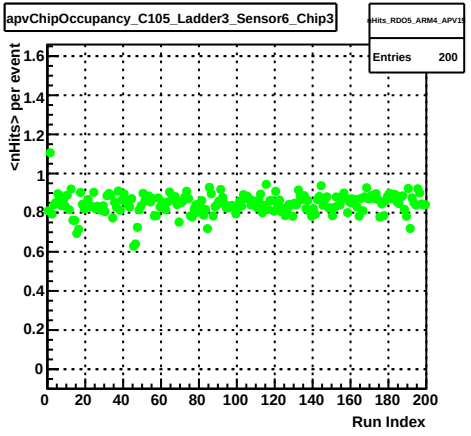
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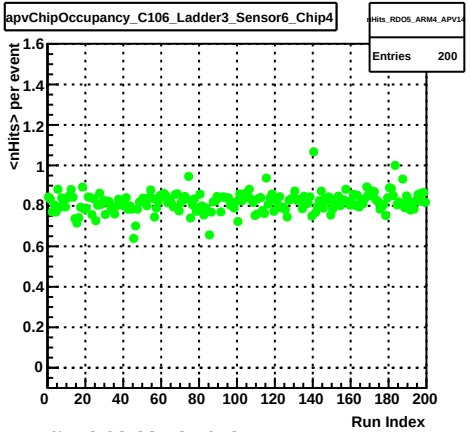
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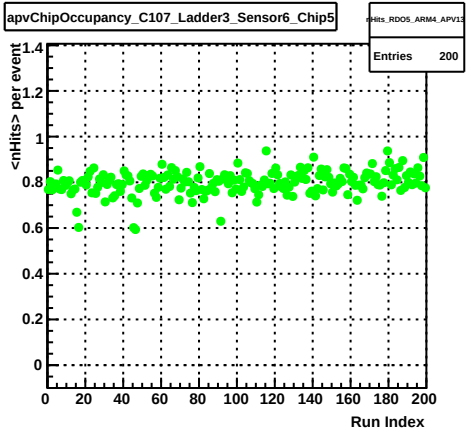
Run Index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nHits_RDO5_ARM4_APV17
Entries 200
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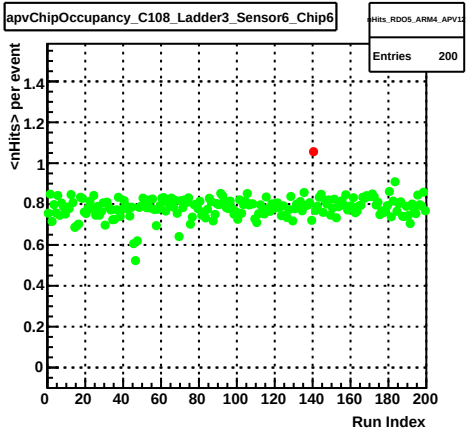
Run Index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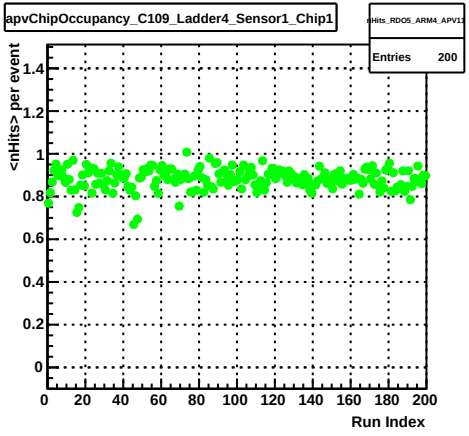
Run Index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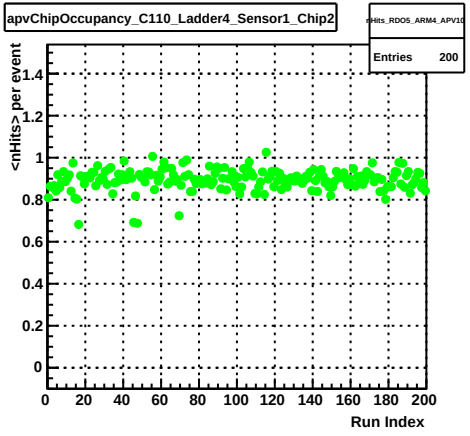
Run Index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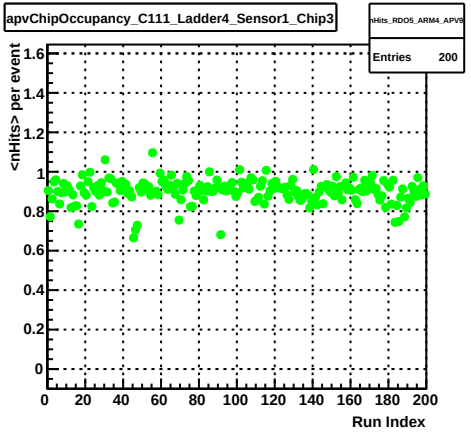
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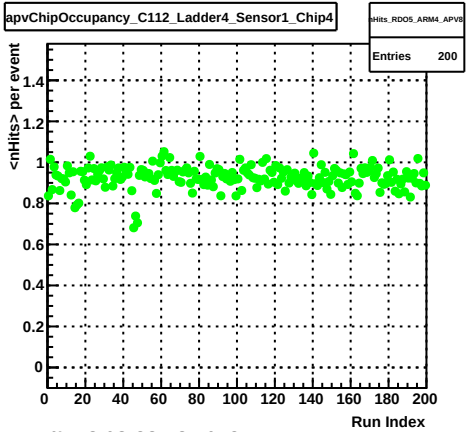
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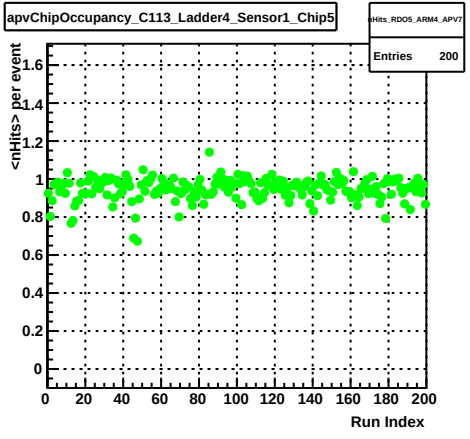
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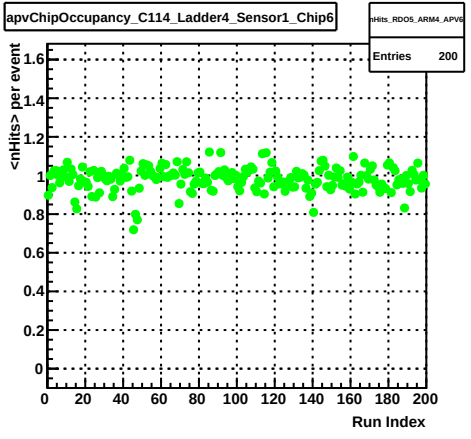
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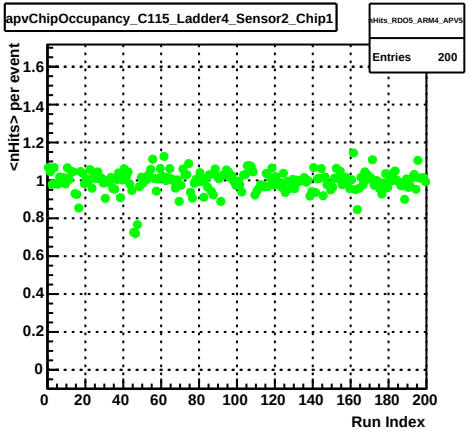
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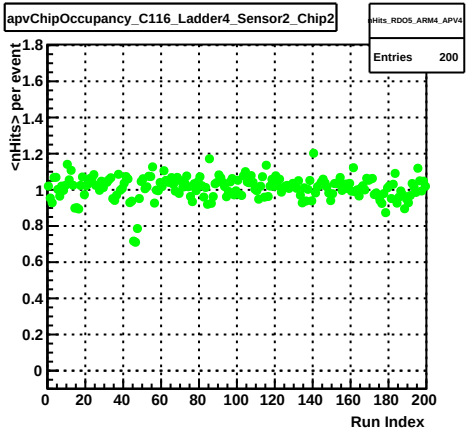
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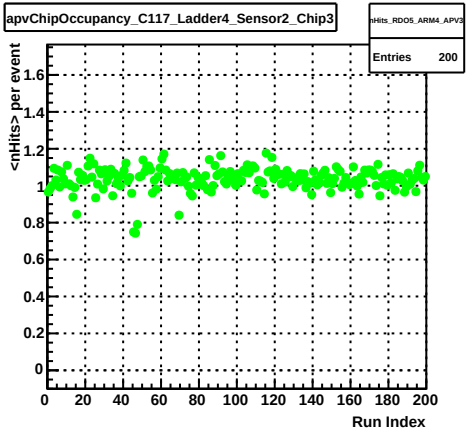
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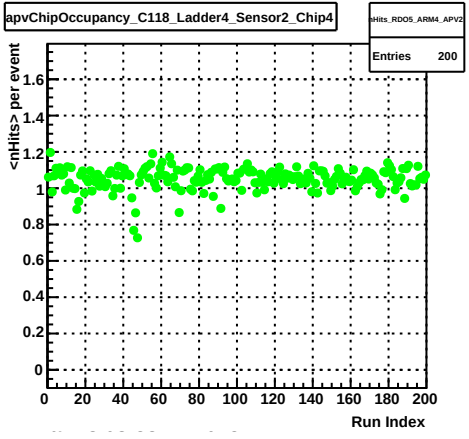
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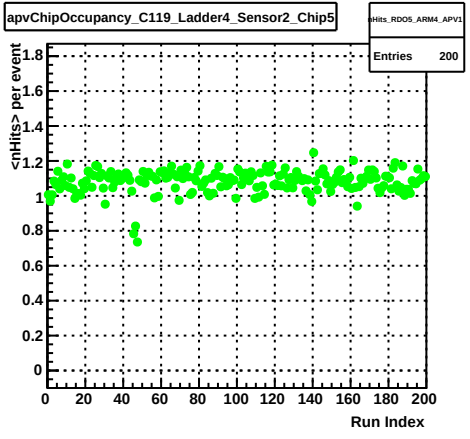
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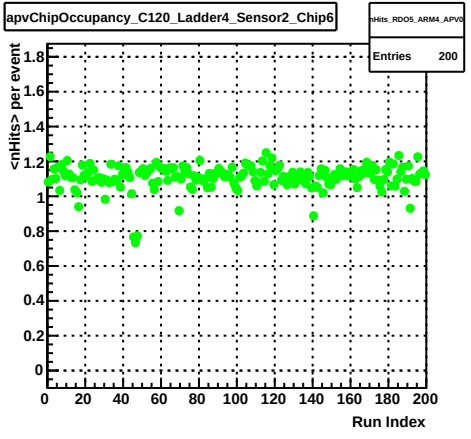
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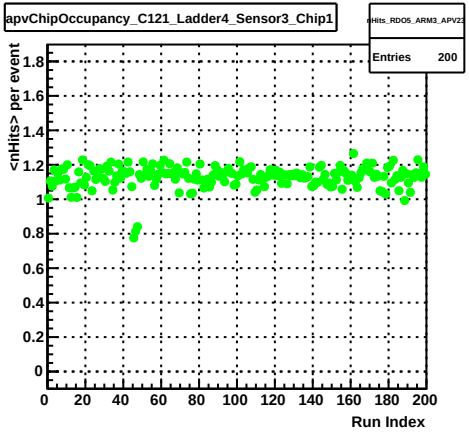
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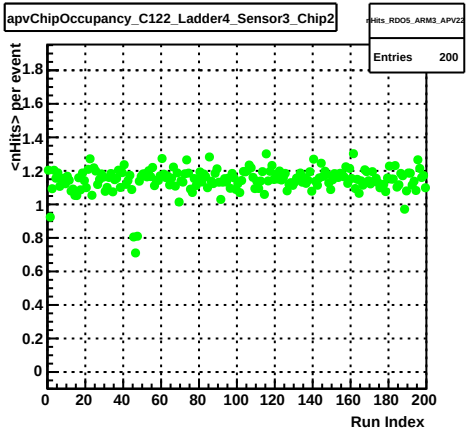
Run Index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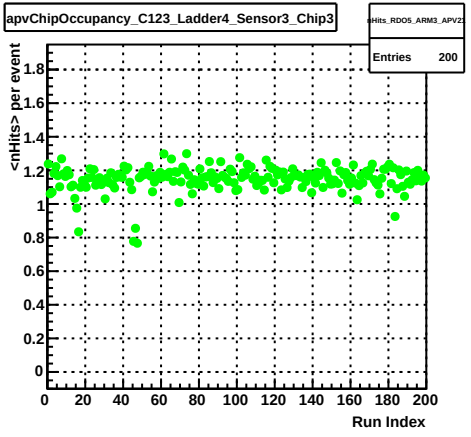
Run Index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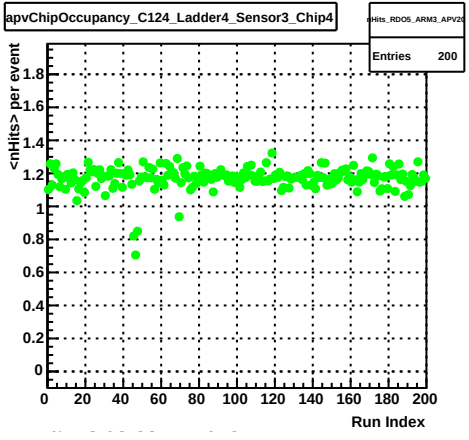
Run Index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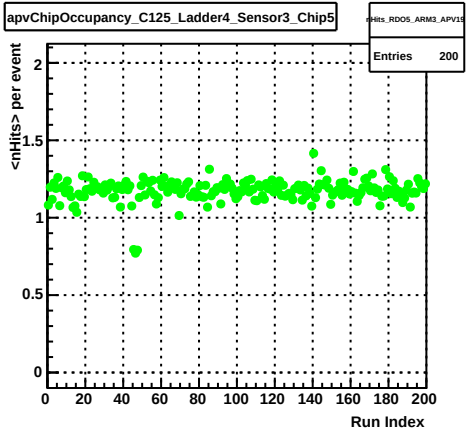
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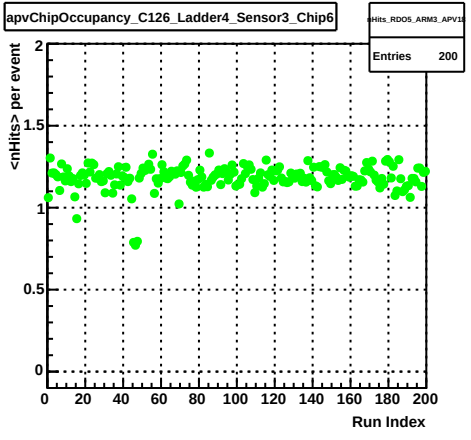
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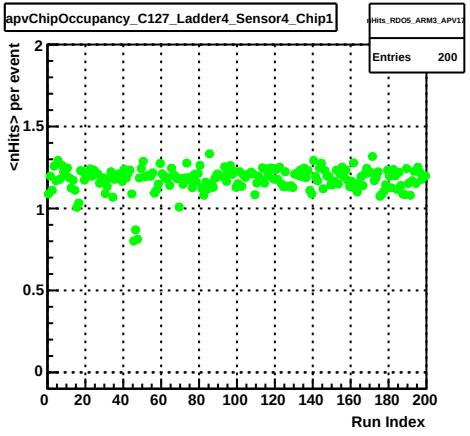
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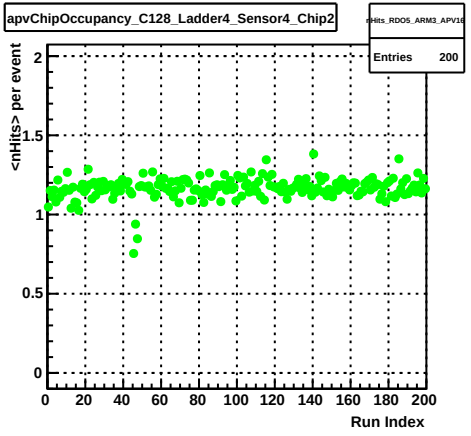
Run Index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Entries 200
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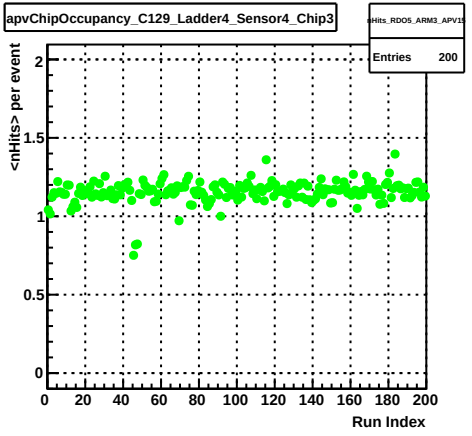
Run Index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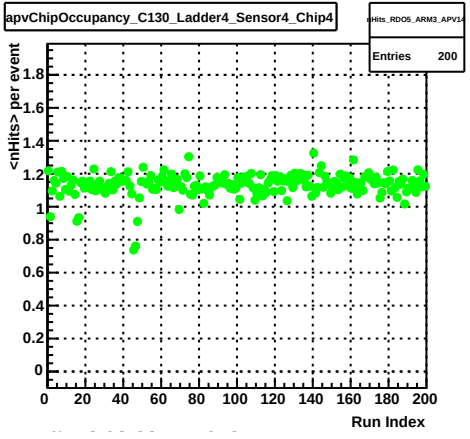
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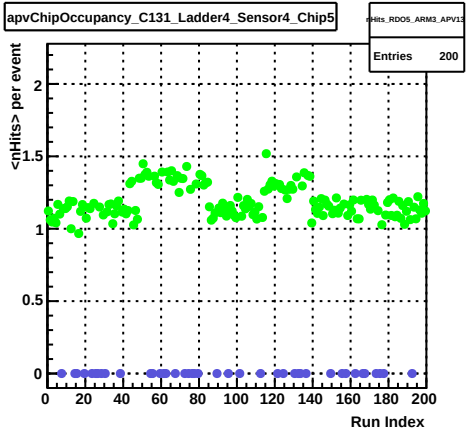
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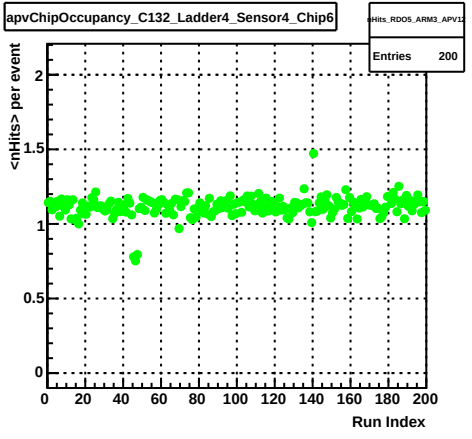
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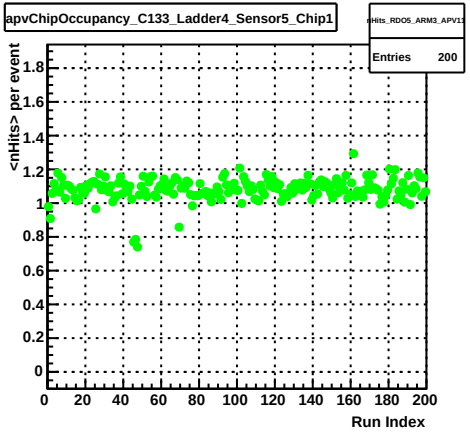
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Entries 200
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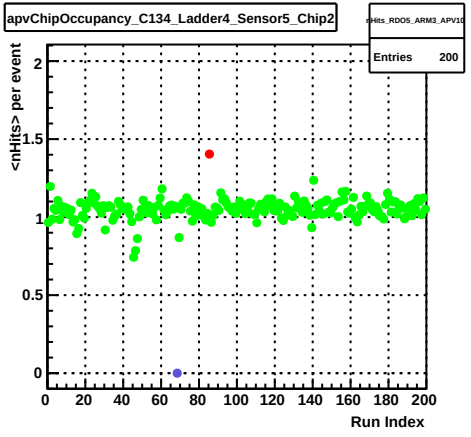
Run Index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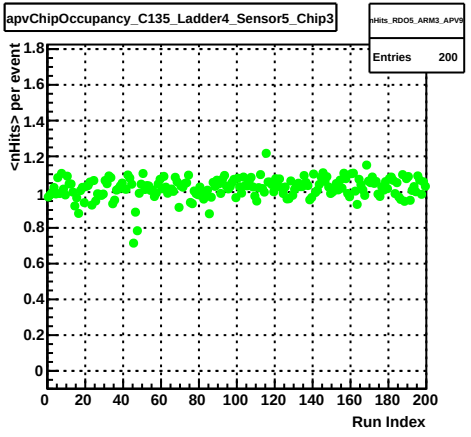
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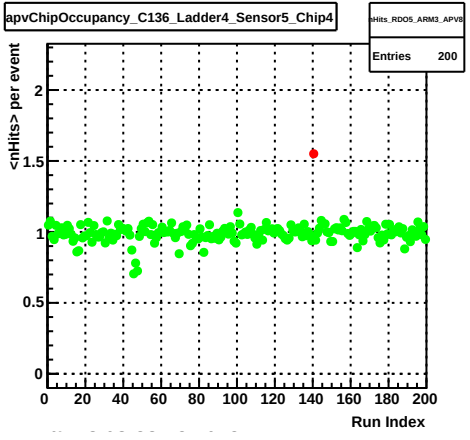
Run Index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Entries 200
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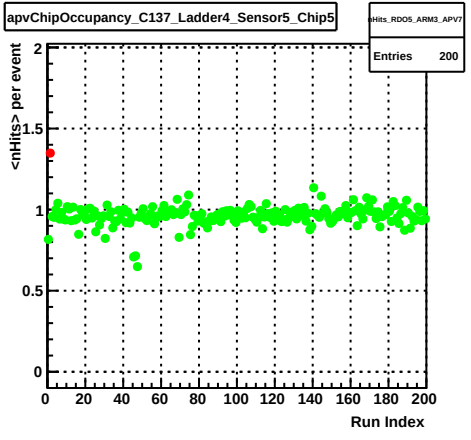
Run Index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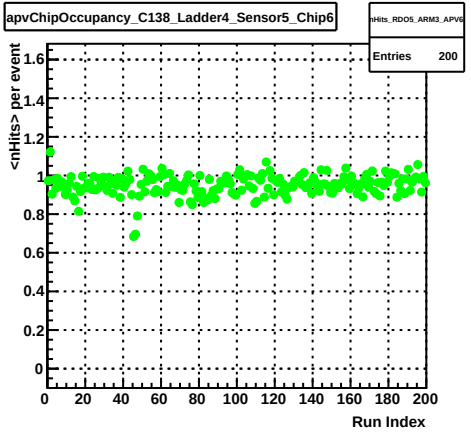
Run Index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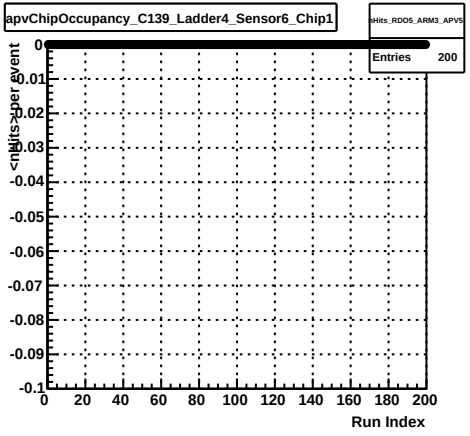
Run Index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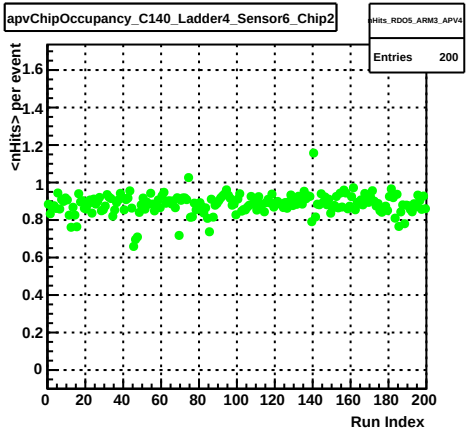
Run Index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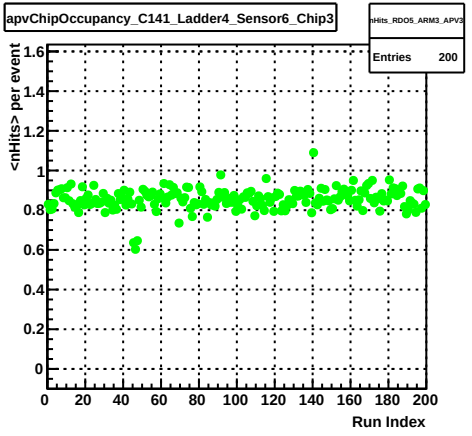
Run Index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Entries 200
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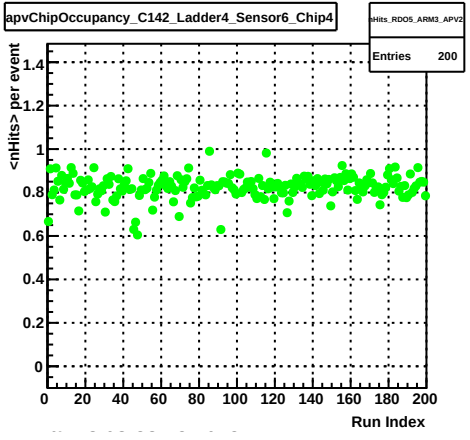
Run Index


apvChipOccupancy_C142_Ladder4_Sensor6_Chip4
nHits_RD05_ARM3_APV2
Entries 200
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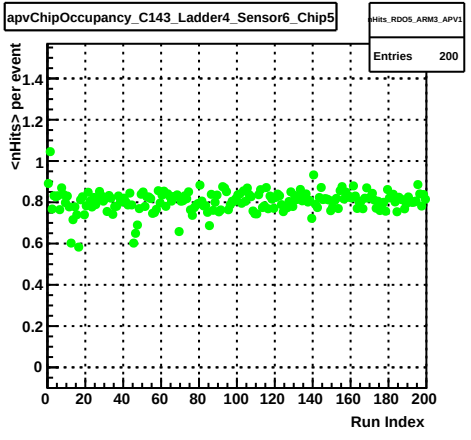
Run Index


apvChipOccupancy_C143_Ladder4_Sensor6_Chip5
nHits_RD05_ARM3_APV1
Entries 200
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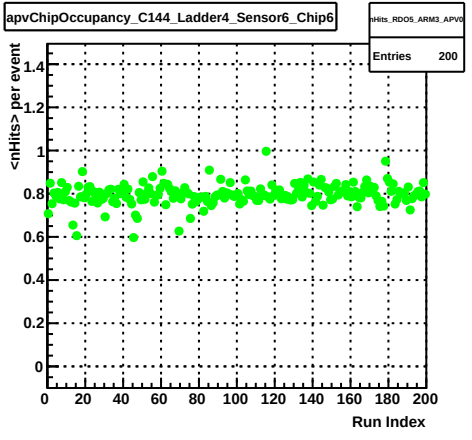
Run Index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nHits_RD05_ARM3_APV0
Entries 200
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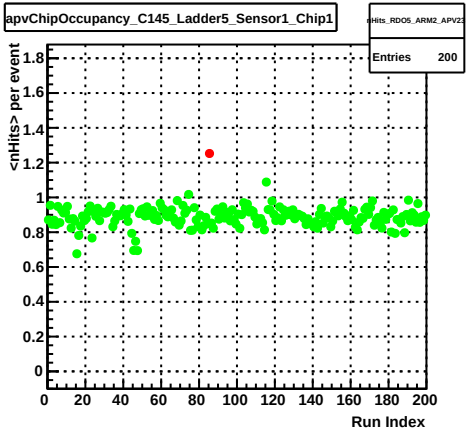
Run Index


apvChipOccupancy_C145_Ladder5_Sensor1_Chip1
Entries 200
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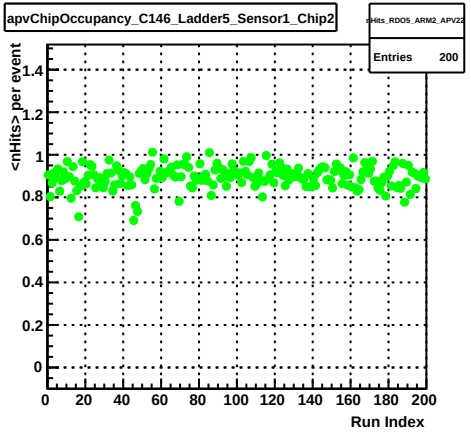
Run Index


apvChipOccupancy_C146_Ladder5_Sensor1_Chip2
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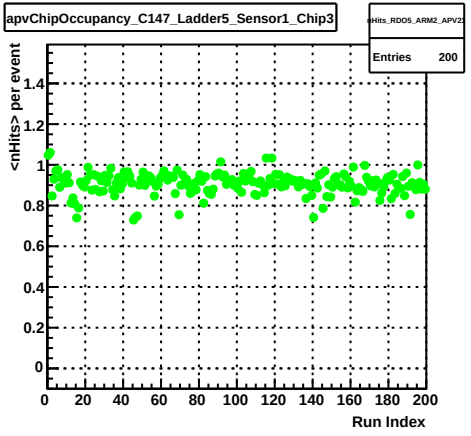
Run Index


apvChipOccupancy_C147_Ladder5_Sensor1_Chip3
Entries 200
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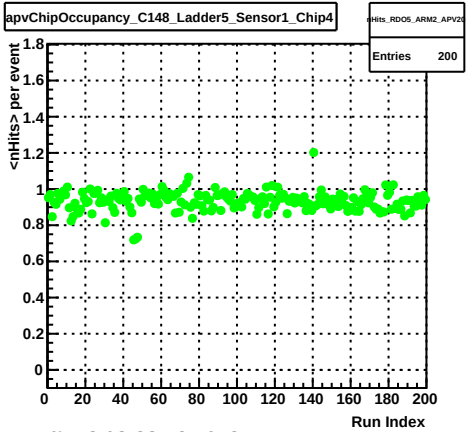
Run Index


apvChipOccupancy_C148_Ladder5_Sensor1_Chip4
Entries 200
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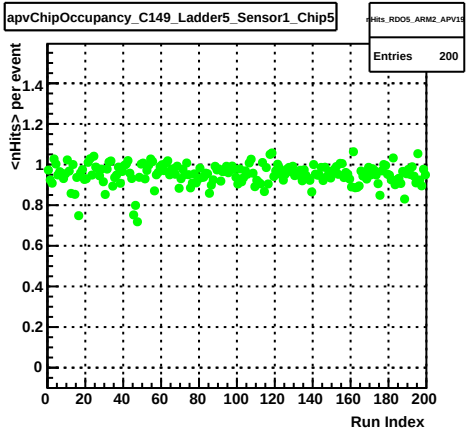
Run Index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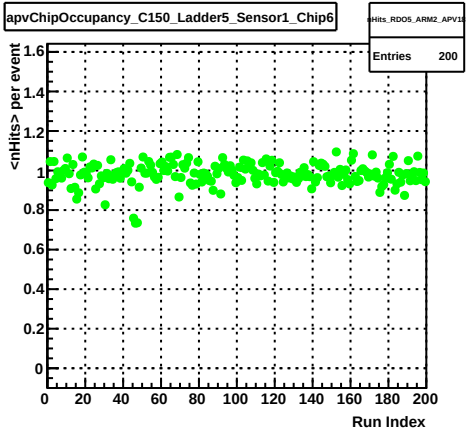
apvChipOccupancy_C150_Ladder5_Sensor1_Chip6
Entries 200
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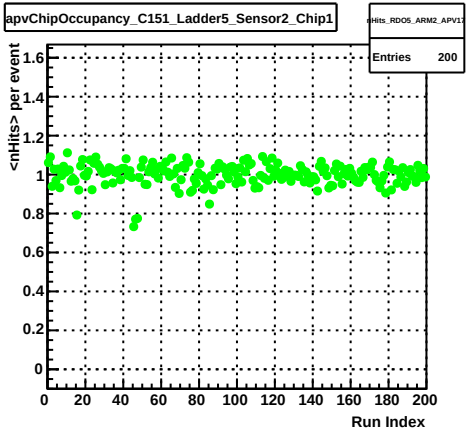
Run Index


apvChipOccupancy_C151_Ladder5_Sensor2_Chip1
Entries 200
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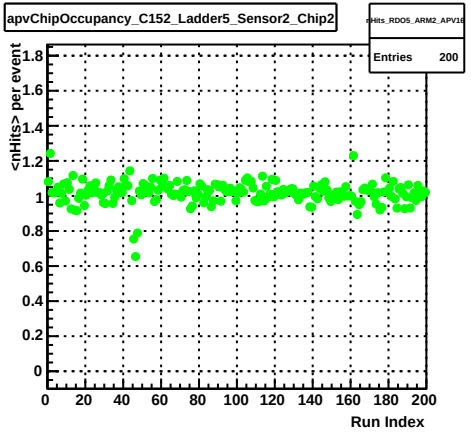
Run Index


apvChipOccupancy_C152_Ladder5_Sensor2_Chip2
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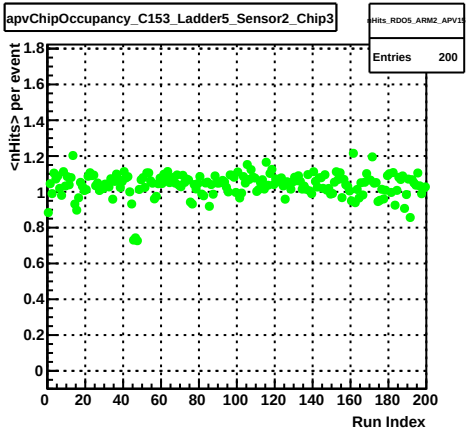
Run Index


apvChipOccupancy_C153_Ladder5_Sensor2_Chip3
Entries 200
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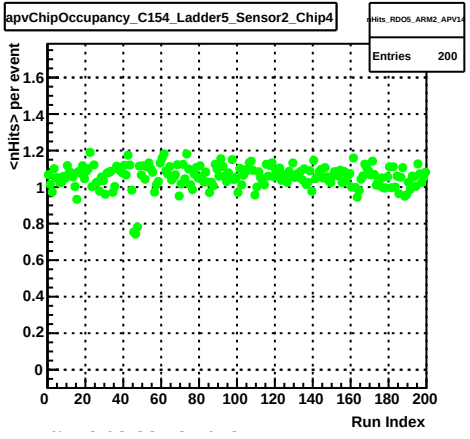
Run Index


apvChipOccupancy_C154_Ladder5_Sensor2_Chip4
Entries 200
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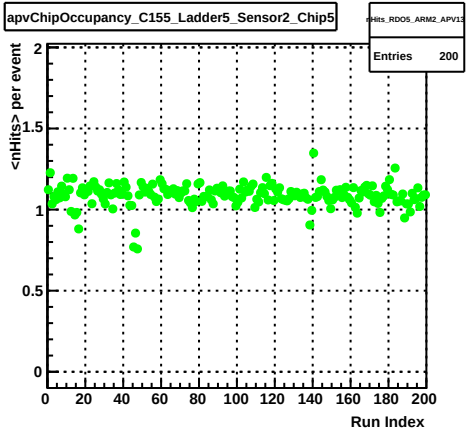
Run Index


apvChipOccupancy_C155_Ladder5_Sensor2_Chip5
Entries 200
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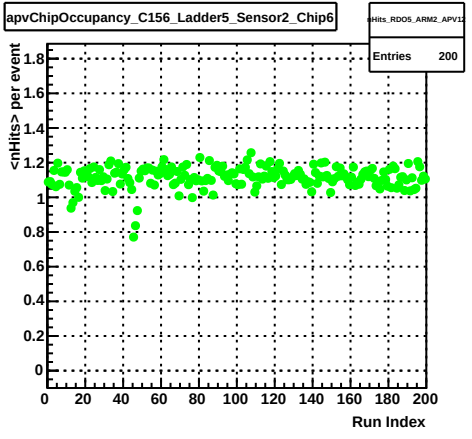
Run Index


apvChipOccupancy_C156_Ladder5_Sensor2_Chip6
Entries 200
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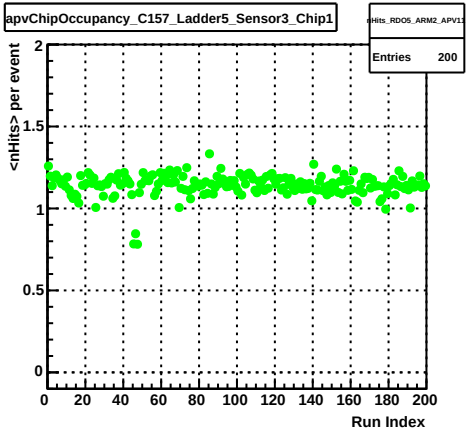
Run Index


apvChipOccupancy_C157_Ladder5_Sensor3_Chip1
Entries 200
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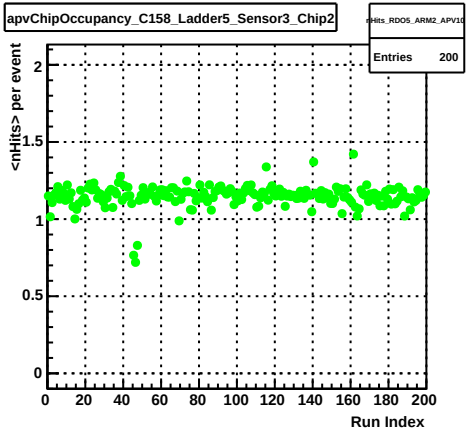
Run Index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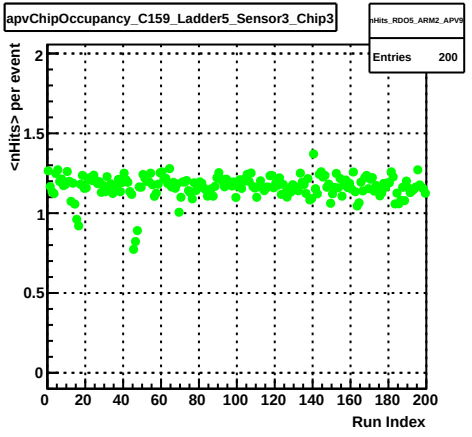
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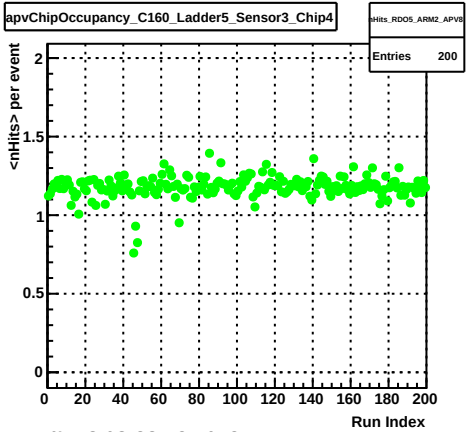
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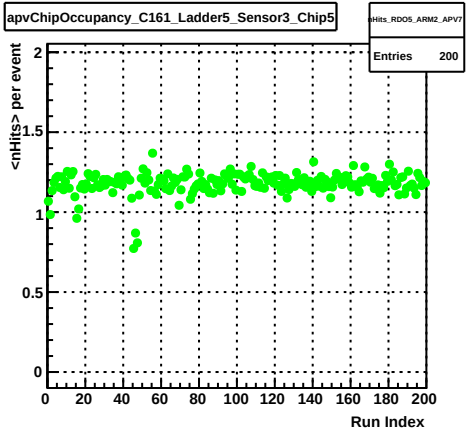
Run Index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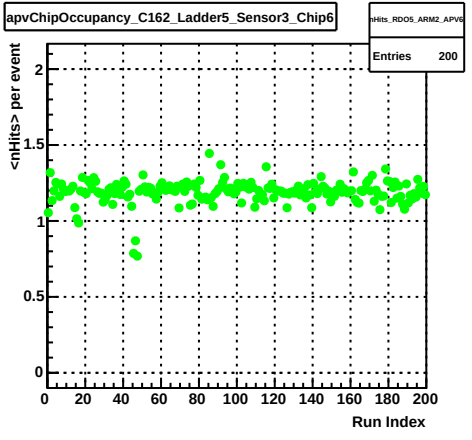
apvChipOccupancy_C162_Ladder5_Sensor3_Chip6
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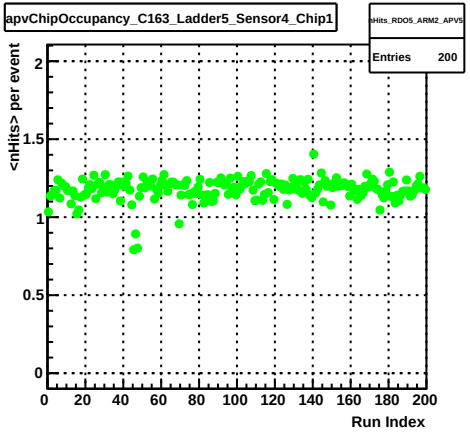
apvChipOccupancy_C163_Ladder5_Sensor4_Chip1
nHits_RDO5_ARM2_APV5
Entries 200
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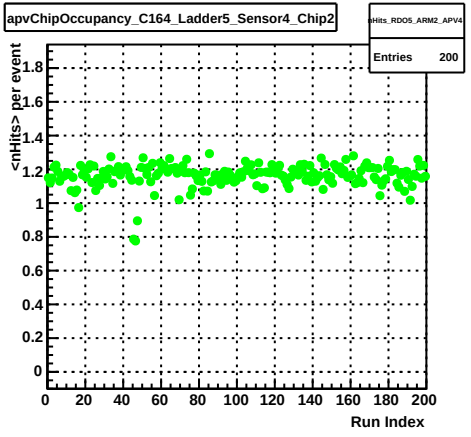
Run Index


apvChipOccupancy_C164_Ladder5_Sensor4_Chip2
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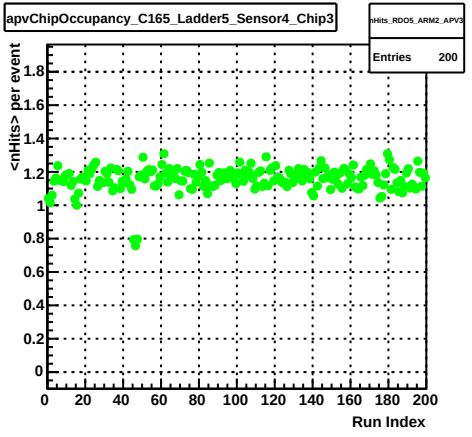
Run Index


apvChipOccupancy_C165_Ladder5_Sensor4_Chip3
nHits_RDO5_ARM2_APV3
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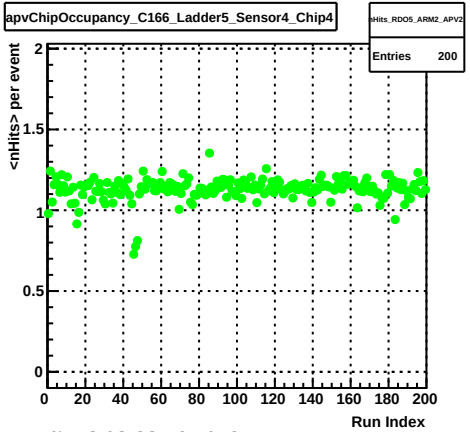
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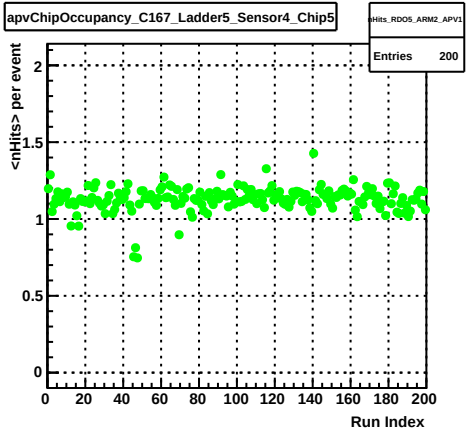
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nHits_RDO5_ARM2_APV1
Entries 200
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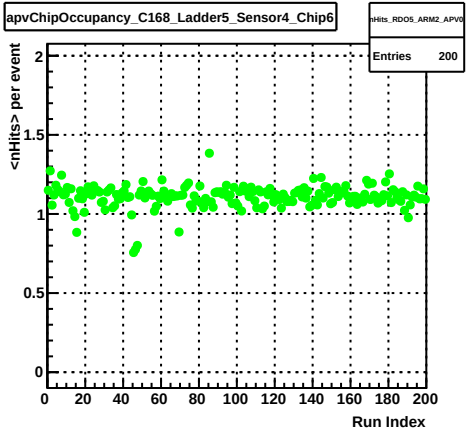
apvChipOccupancy_C168_Ladder5_Sensor4_Chip6
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Entries 200
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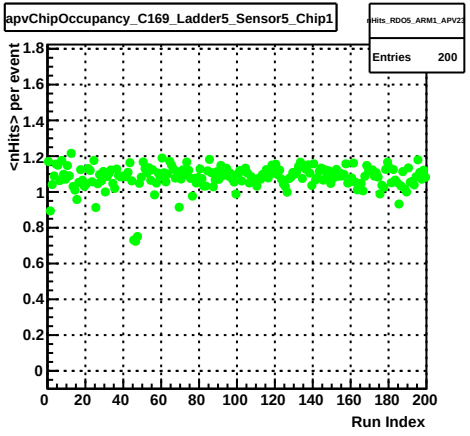
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Entries 200
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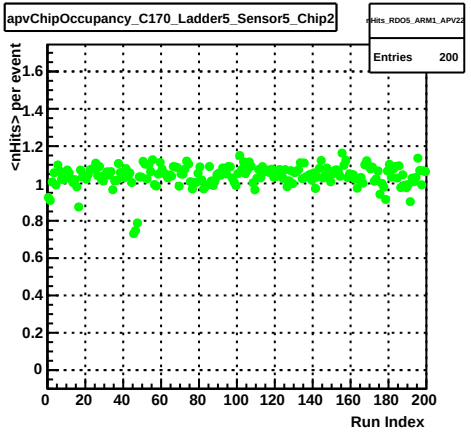
Run Index


apvChipOccupancy_C170_Ladder5_Sensor5_Chip2
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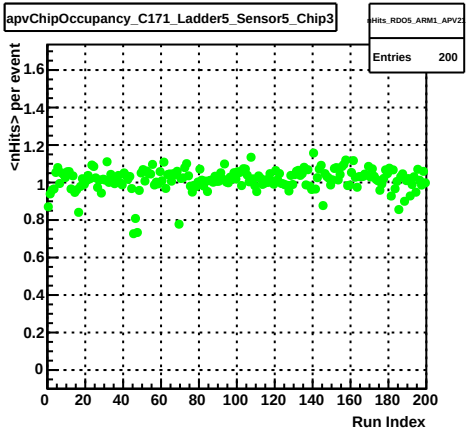
Run Index


apvChipOccupancy_C171_Ladder5_Sensor5_Chip3
Entries 200
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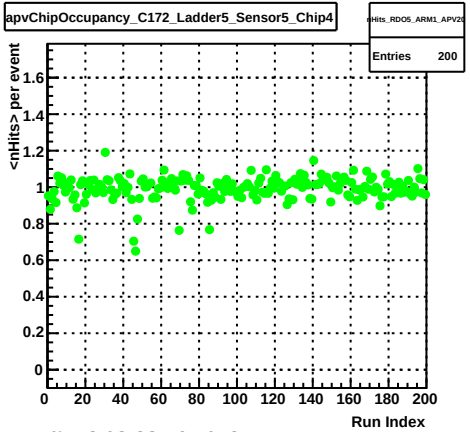
Run Index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Entries 200
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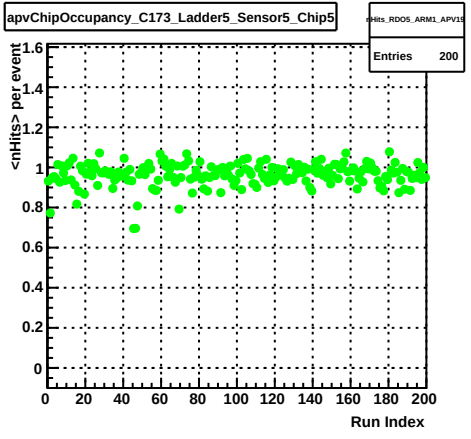
Run Index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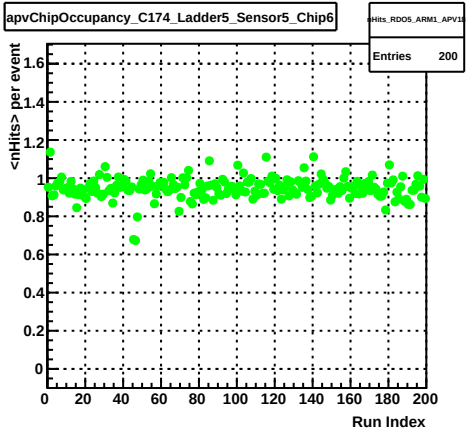
Run Index


apvChipOccupancy_C174_Ladder5_Sensor5_Chip6
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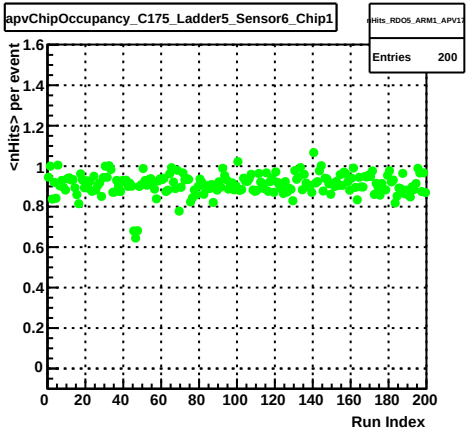
Run Index


apvChipOccupancy_C175_Ladder5_Sensor6_Chip1
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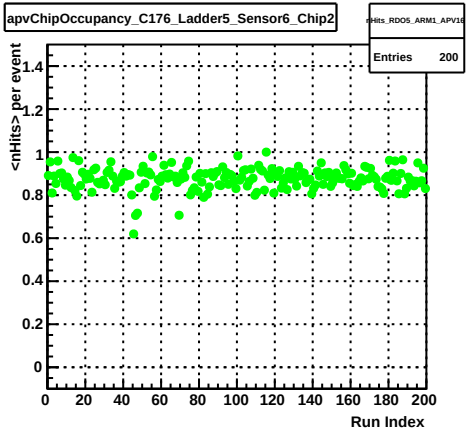
Run Index


apvChipOccupancy_C176_Ladder5_Sensor6_Chip2
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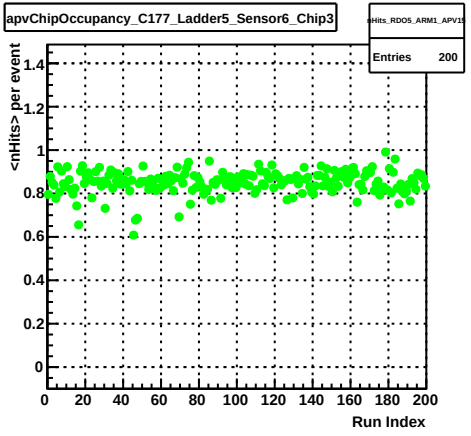
Run Index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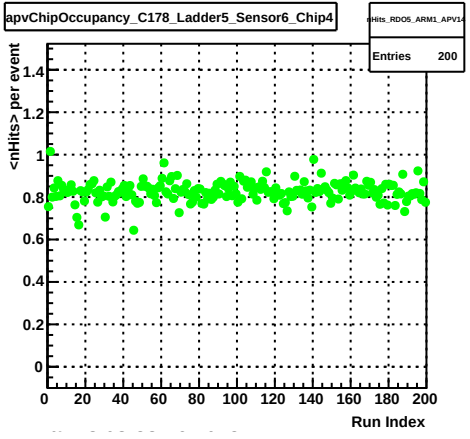
Run Index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Entries 200
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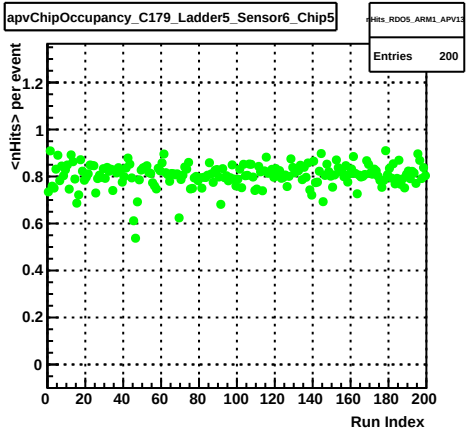
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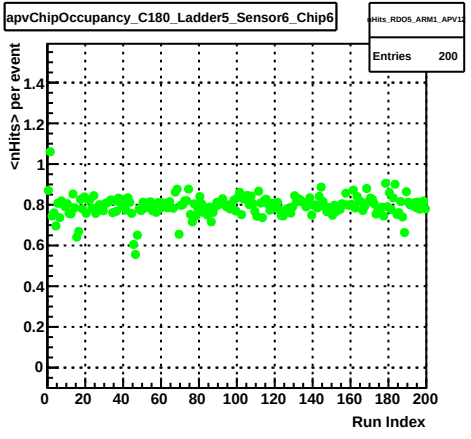
Run Index


apvChipOccupancy_C180_Ladder5_Sensor6_Chip6
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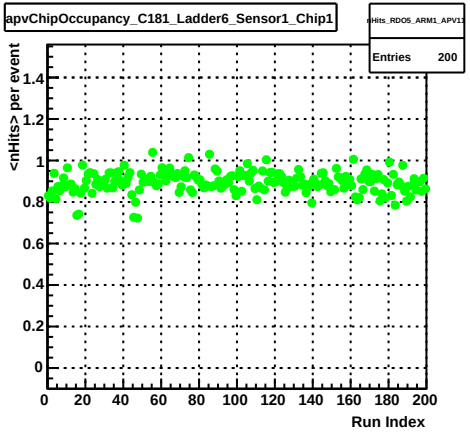
apvChipOccupancy_C181_Ladder6_Sensor1_Chip1
Entries 200
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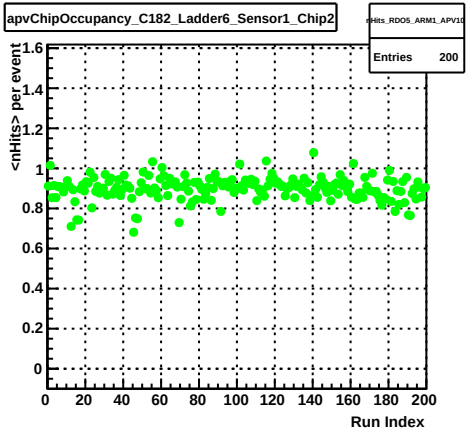
Run Index


apvChipOccupancy_C182_Ladder6_Sensor1_Chip2
Entries 200
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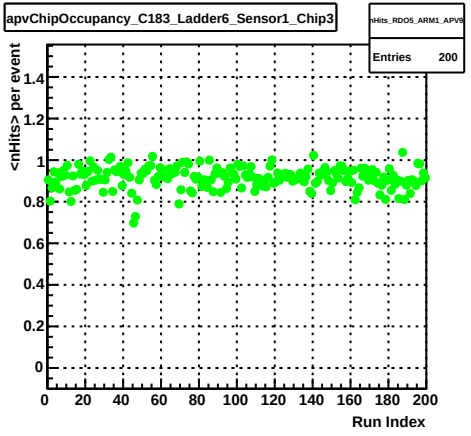
Run Index


apvChipOccupancy_C183_Ladder6_Sensor1_Chip3
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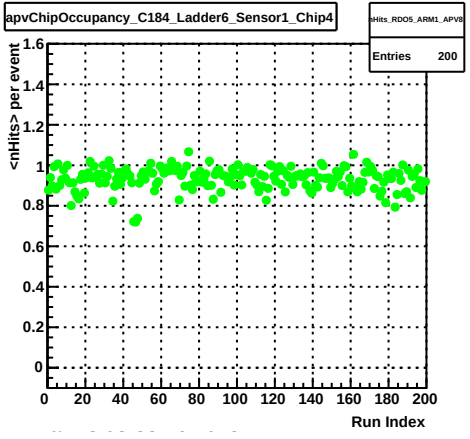
Run Index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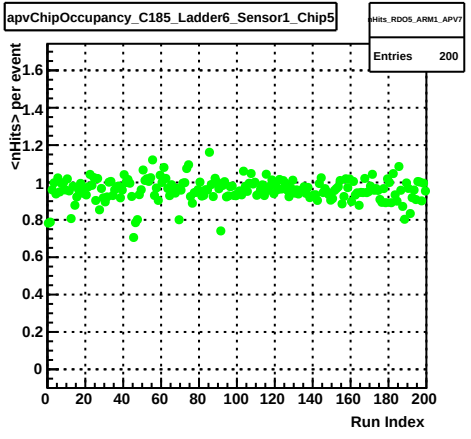
Run Index


apvChipOccupancy_C185_Ladder6_Sensor1_Chip5
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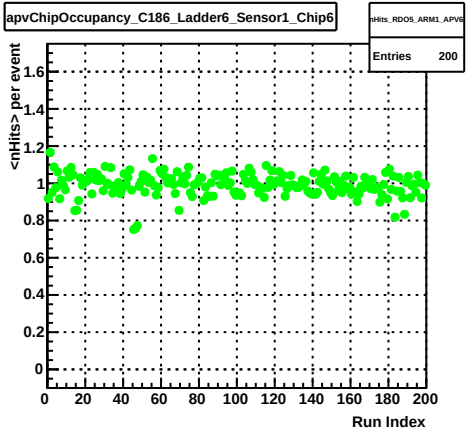
Run Index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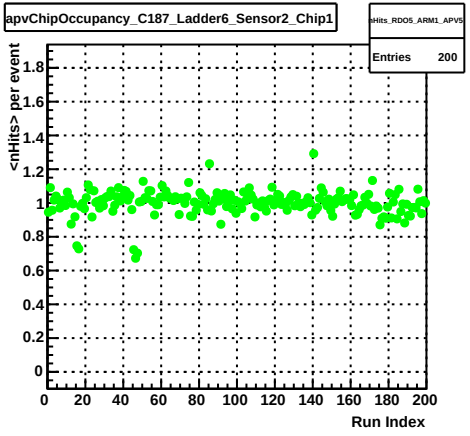
Run Index


apvChipOccupancy_C187_Ladder6_Sensor2_Chip1
nHits_RD05_ARM1_APV5
Entries 200
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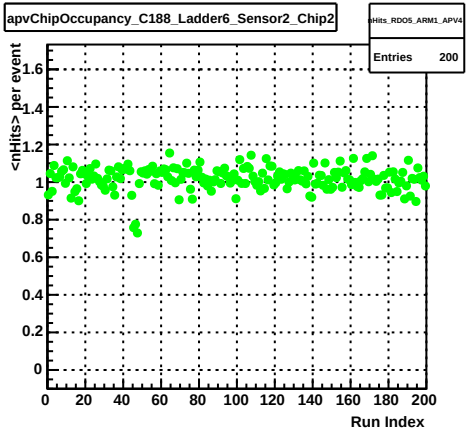
Run Index


apvChipOccupancy_C188_Ladder6_Sensor2_Chip2
nHits_RD05_ARM1_APV4
Entries 200
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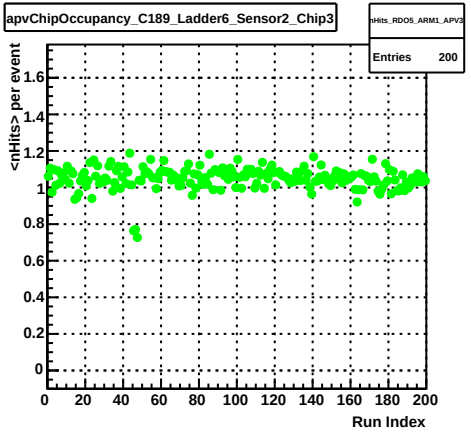
Run Index


apvChipOccupancy_C189_Ladder6_Sensor2_Chip3
nHits_RD05_ARM1_APV3
Entries 200
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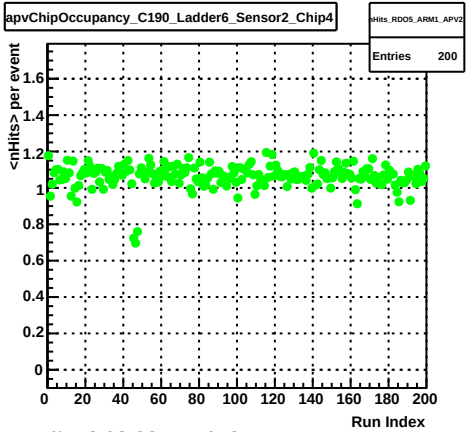
Run Index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nHits_RD05_ARM1_APV2
Entries 200
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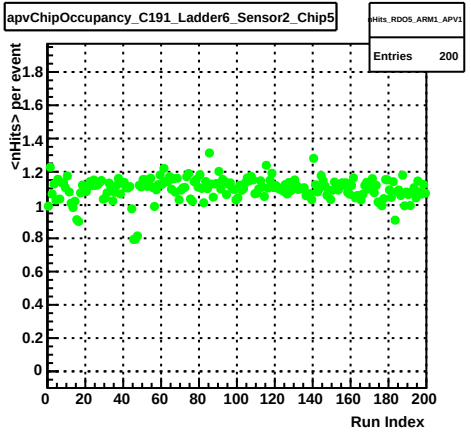
Run Index


apvChipOccupancy_C191_Ladder6_Sensor2_Chip5
nHits_RD05_ARM1_APV1
Entries 200
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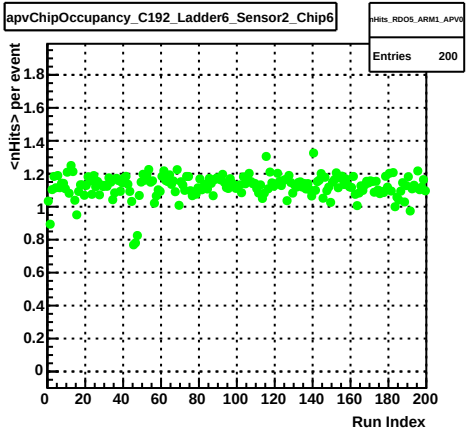
Run Index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nHits_RD05_ARM1_APV0
Entries 200
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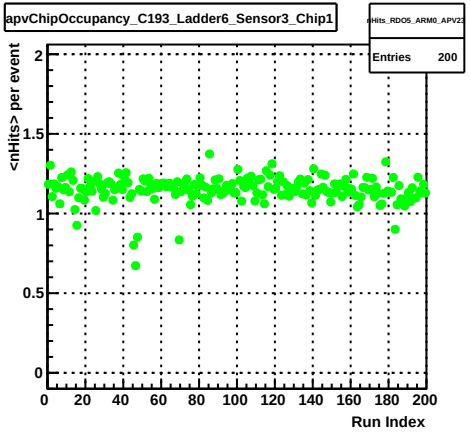
apvChipOccupancy_C193_Ladder6_Sensor3_Chip1
Entries 200
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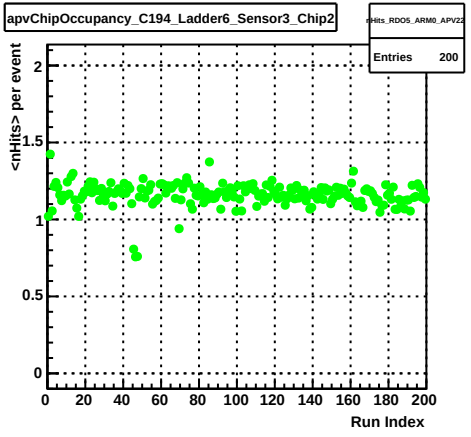
Run Index


apvChipOccupancy_C194_Ladder6_Sensor3_Chip2
Entries 200
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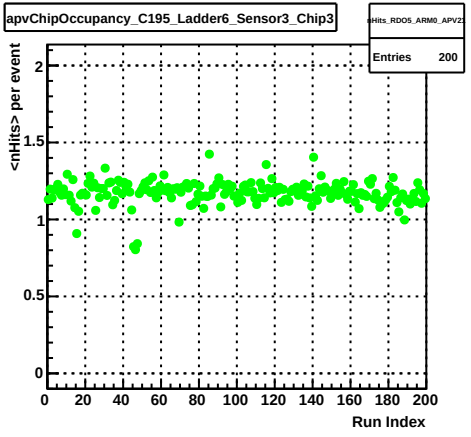
Run Index


apvChipOccupancy_C195_Ladder6_Sensor3_Chip3
Entries 200
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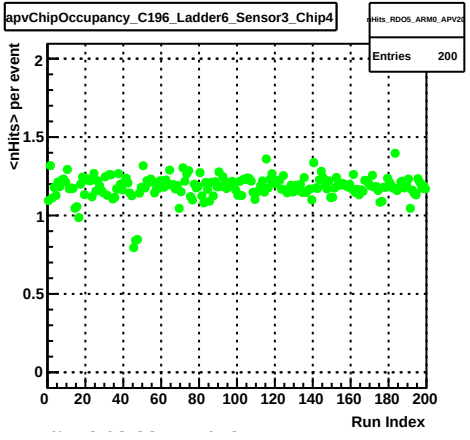
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Entries 200
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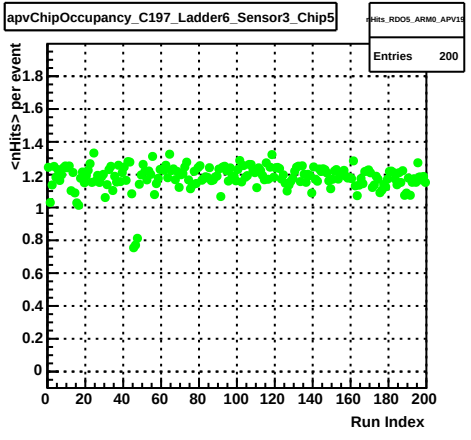
apvChipOccupancy_C197_Ladder6_Sensor3_Chip5
Entries 200
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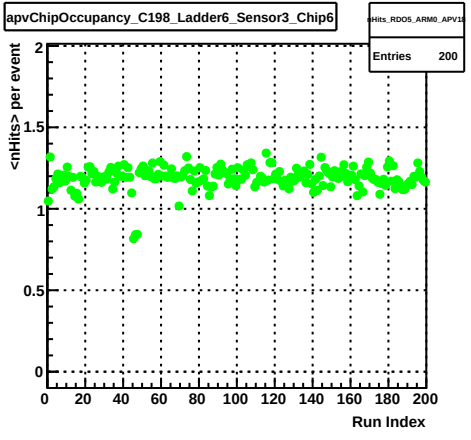
apvChipOccupancy_C198_Ladder6_Sensor3_Chip6
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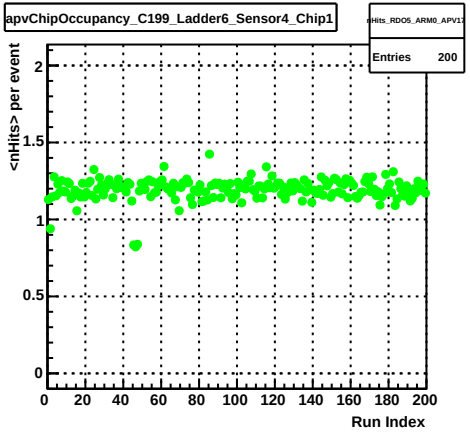
Run Index


apvChipOccupancy_C199_Ladder6_Sensor4_Chip1
Entries 200
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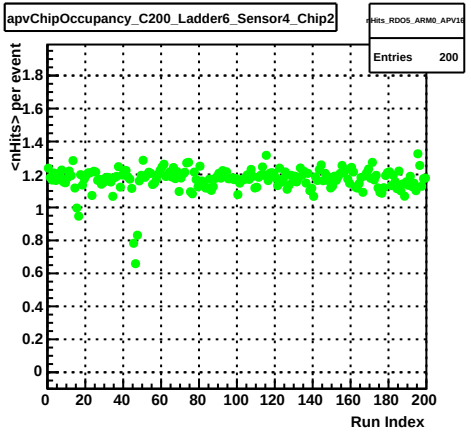
Run Index


apvChipOccupancy_C200_Ladder6_Sensor4_Chip2
Entries 200
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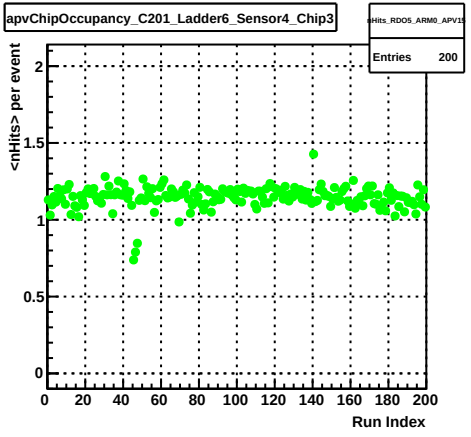
Run Index


apvChipOccupancy_C201_Ladder6_Sensor4_Chip3
Entries 200
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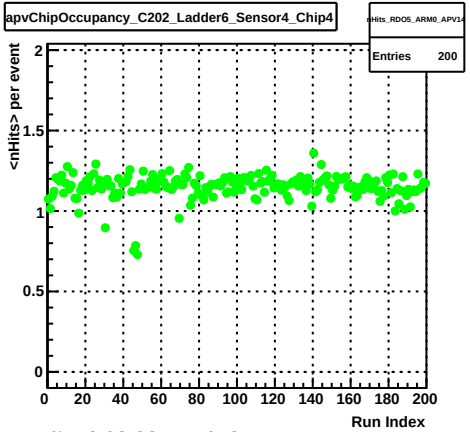
Run Index


apvChipOccupancy_C202_Ladder6_Sensor4_Chip4
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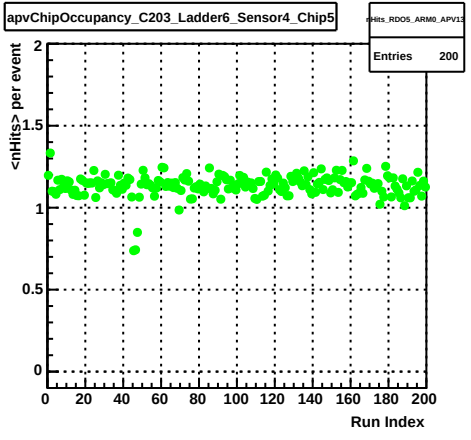
Run Index


apvChipOccupancy_C203_Ladder6_Sensor4_Chip5
Entries 200
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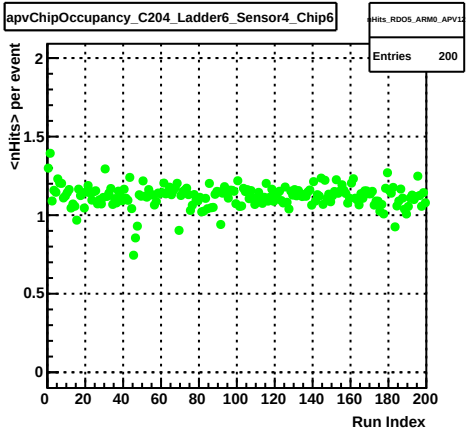
Run Index


apvChipOccupancy_C204_Ladder6_Sensor4_Chip6
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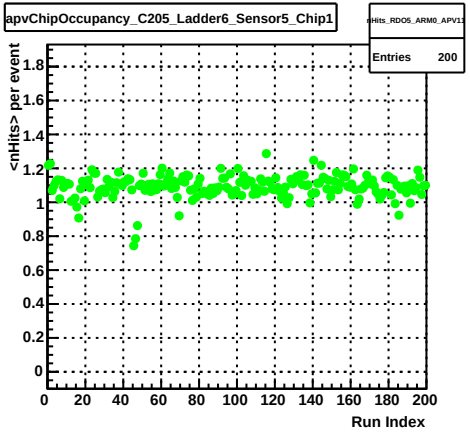
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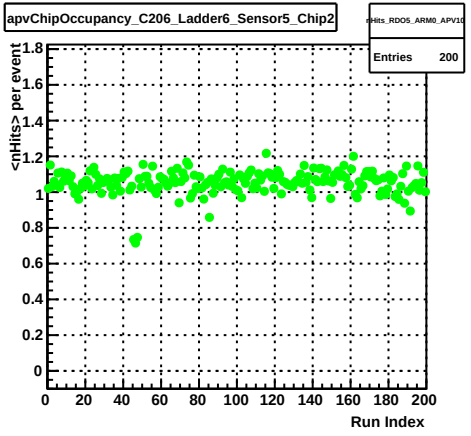
apvChipOccupancy_C206_Ladder6_Sensor5_Chip2
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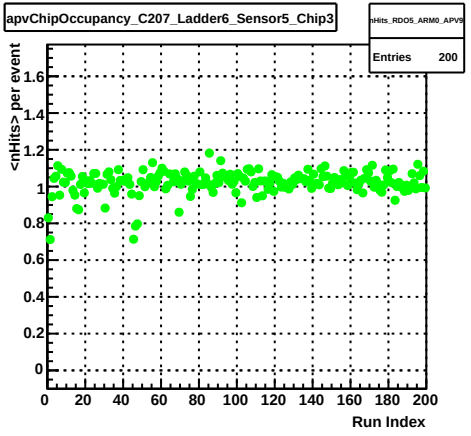
apvChipOccupancy_C207_Ladder6_Sensor5_Chip3
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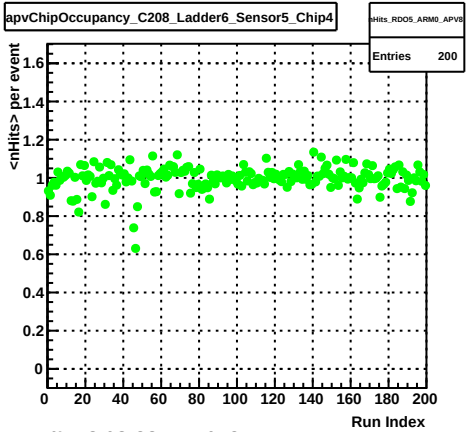
apvChipOccupancy_C208_Ladder6_Sensor5_Chip4
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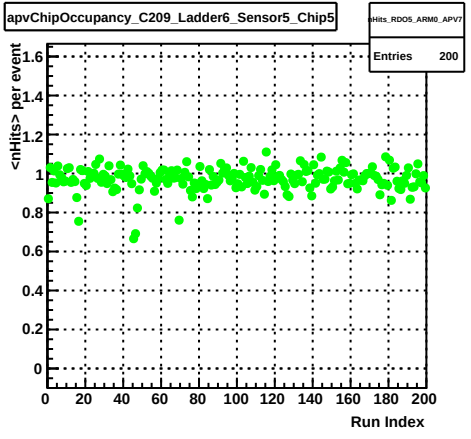
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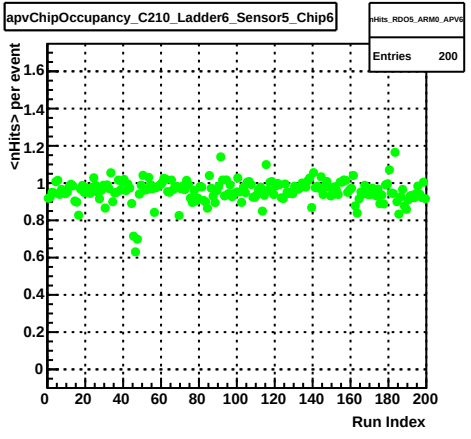
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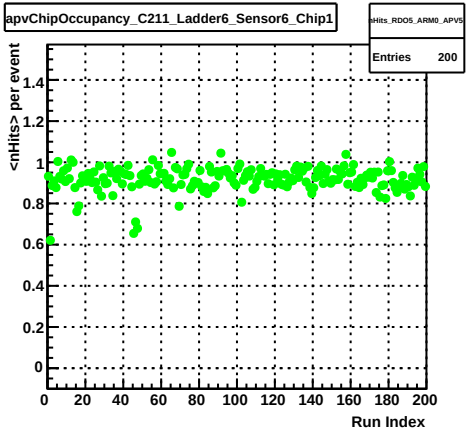
apvChipOccupancy_C211_Ladder6_Sensor6_Chip1
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Entries 200
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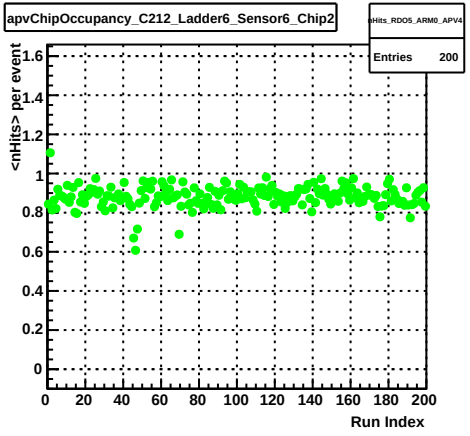
apvChipOccupancy_C212_Ladder6_Sensor6_Chip2
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Entries 200
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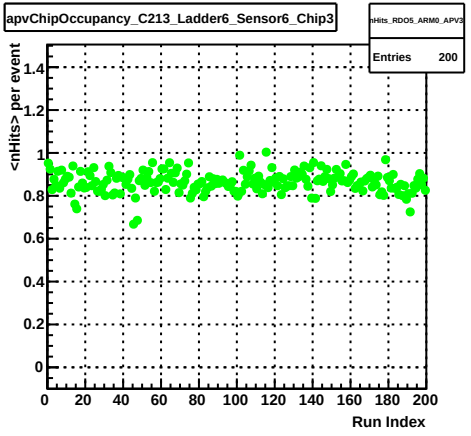
apvChipOccupancy_C213_Ladder6_Sensor6_Chip3
nHits_RDO5_ARM0_APV3
Entries 200
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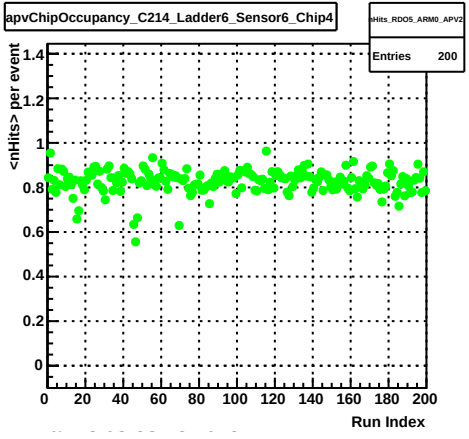
Run Index


apvChipOccupancy_C214_Ladder6_Sensor6_Chip4
nHits_RDO5_ARM0_APV2
Entries 200
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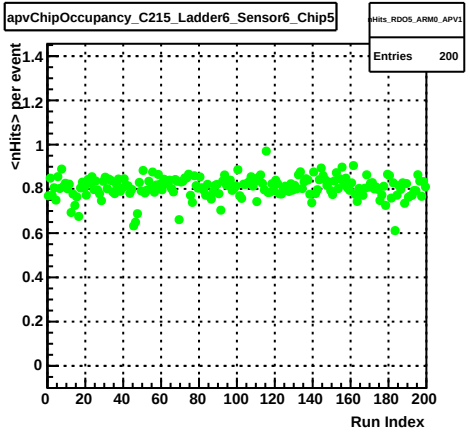
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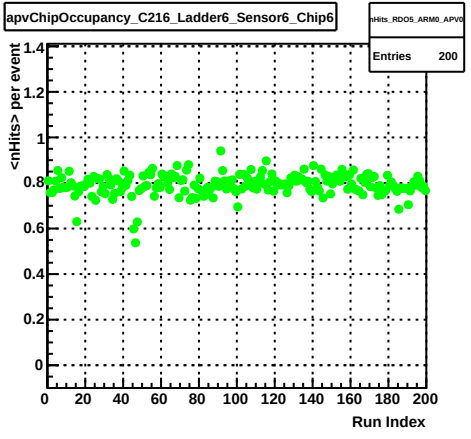
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Entries 200
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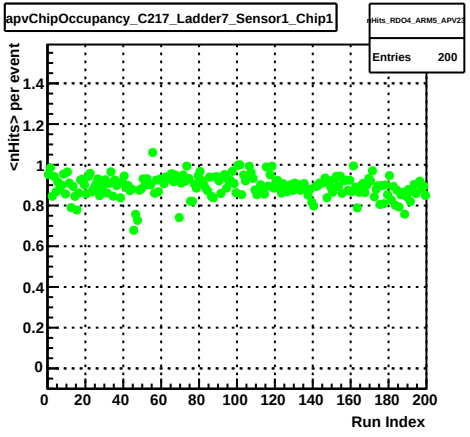
apvChipOccupancy_C217_Ladder7_Sensor1_Chip1
Entries 200
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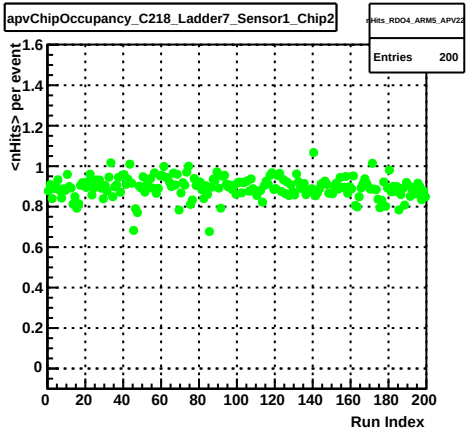
Run Index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Entries 200
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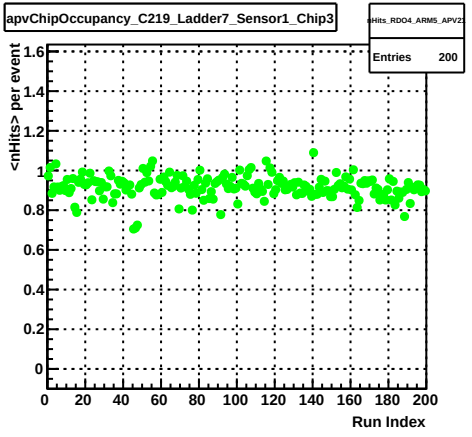
Run Index


apvChipOccupancy_C219_Ladder7_Sensor1_Chip3
Entries 200
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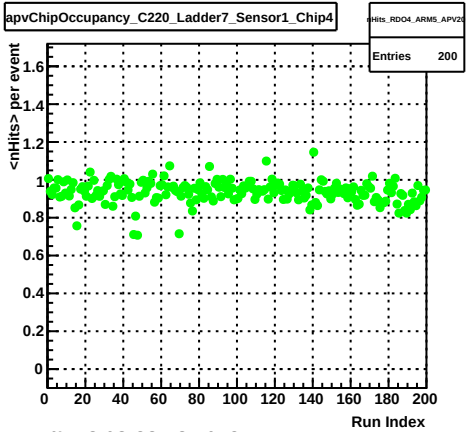
Run Index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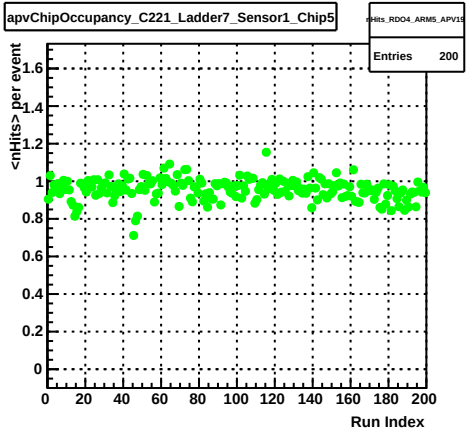
Run Index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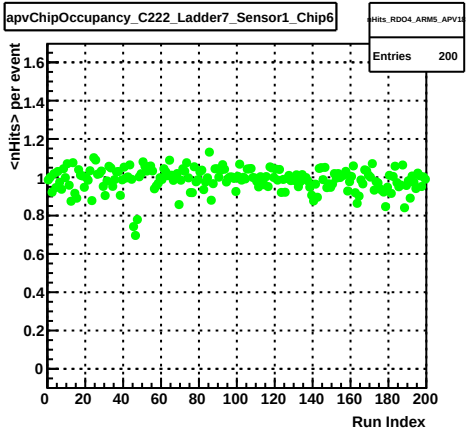
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Entries 200
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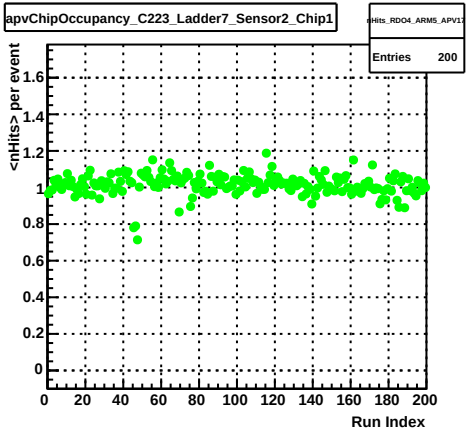
Run Index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Entries 200
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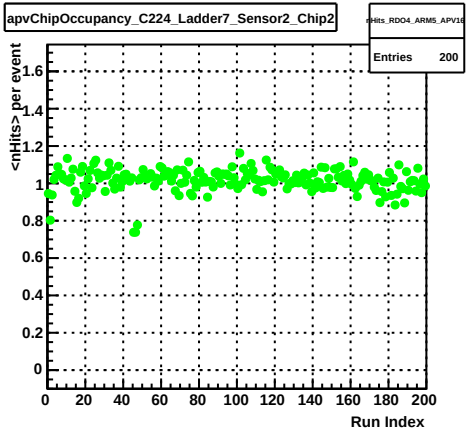
Run Index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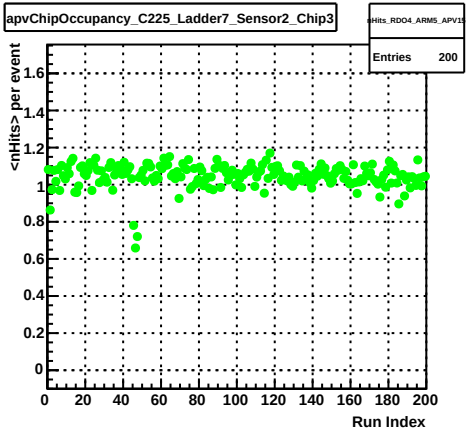
Run Index


apvChipOccupancy_C225_Ladder7_Sensor2_Chip3
Entries 200
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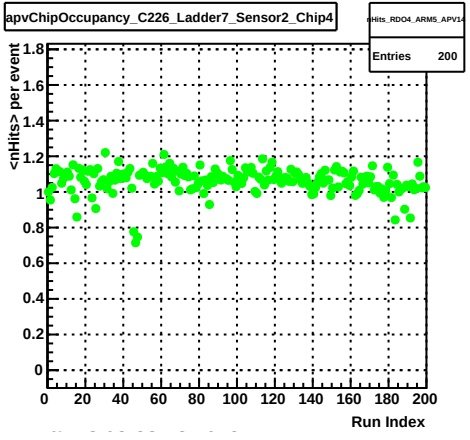
Run Index


apvChipOccupancy_C226_Ladder7_Sensor2_Chip4
Entries 200
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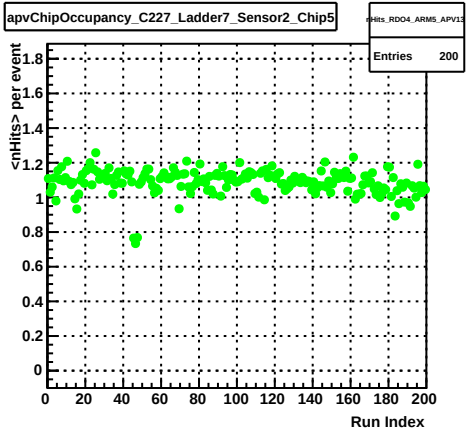
Run Index


apvChipOccupancy_C227_Ladder7_Sensor2_Chip5
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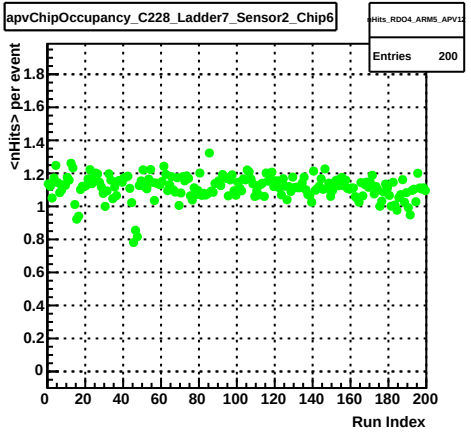
Run Index


apvChipOccupancy_C228_Ladder7_Sensor2_Chip6
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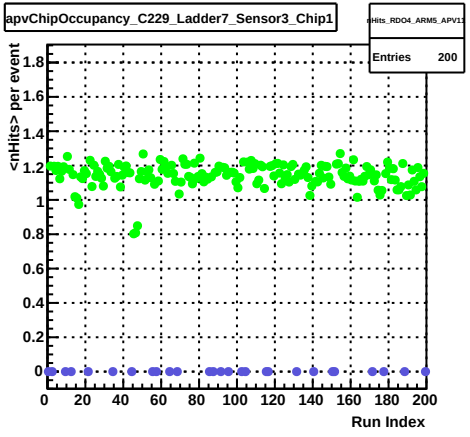
Run Index


apvChipOccupancy_C229_Ladder7_Sensor3_Chip1
nHits_RDO4_ARM5_APV13
Entries 200
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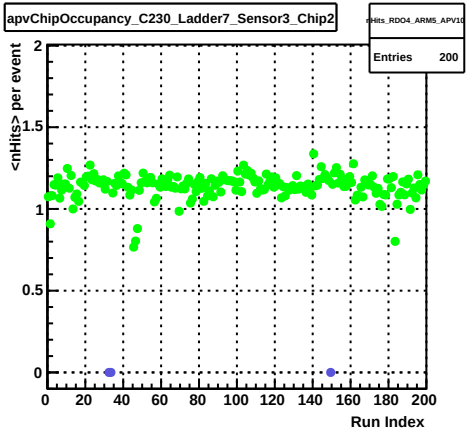
Run Index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nHits_RDO4_ARM5_APV10
Entries 200
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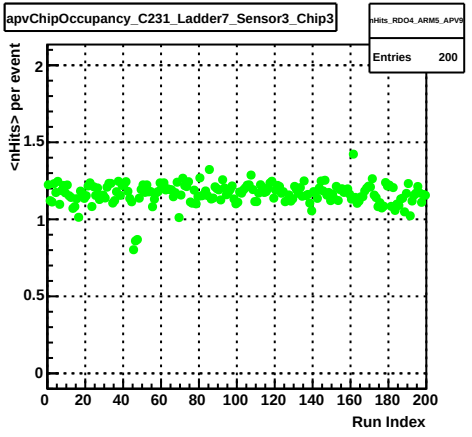
Run Index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nHits_RDO4_ARM5_APV9
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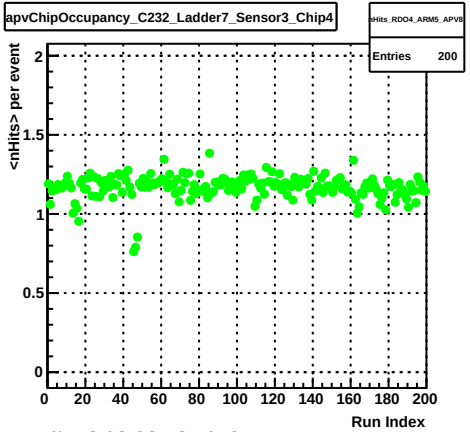
Run Index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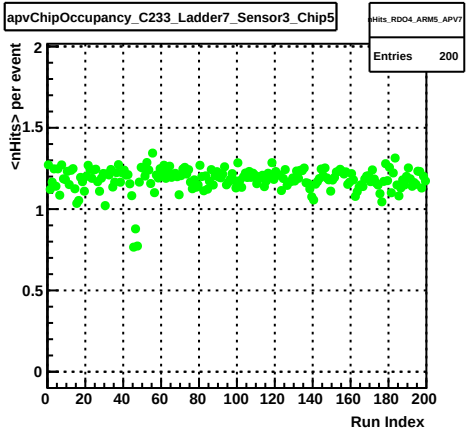
Run Index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nHits_RDO4_ARM5_APV7
Entries 200
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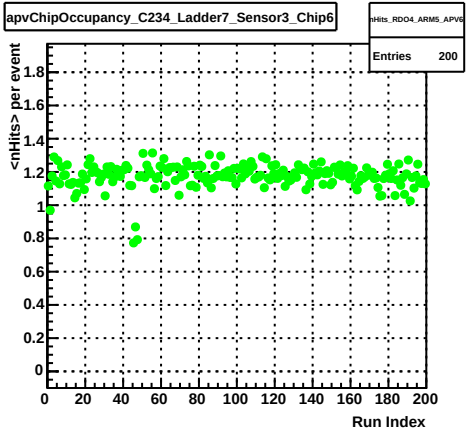
Run Index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nHits_RDO4_ARM5_APV6
Entries 200
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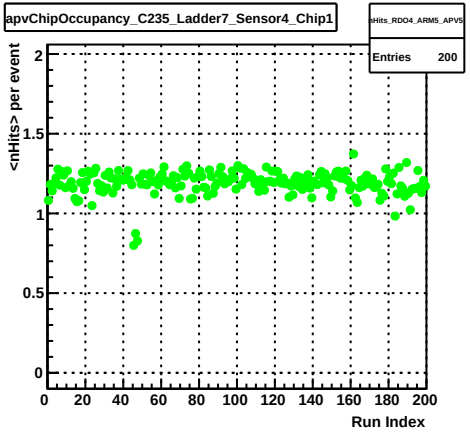
Run Index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nHits_RDO4_ARM5_APV5
Entries 200
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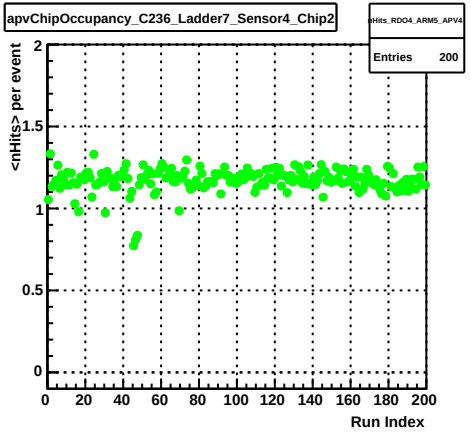
Run Index


apvChipOccupancy_C236_Ladder7_Sensor4_Chip2
nHits_RDO4_ARM5_APV4
Entries 200
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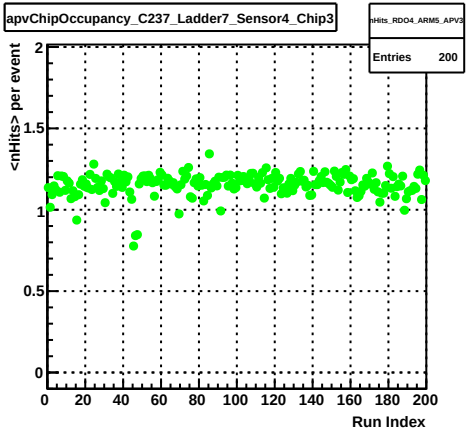
Run Index


apvChipOccupancy_C237_Ladder7_Sensor4_Chip3
nHits_RDO4_ARM5_APV3
Entries 200
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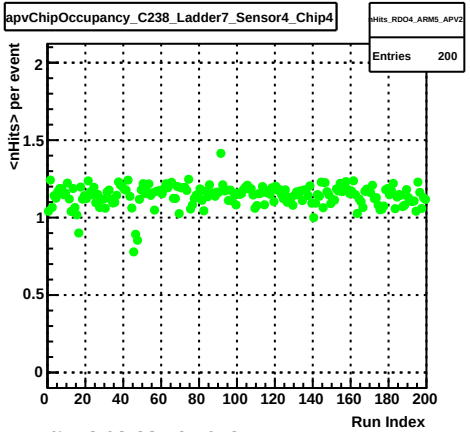
Run Index


apvChipOccupancy_C238_Ladder7_Sensor4_Chip4
nHits_RDO4_ARM5_APV2
Entries 200
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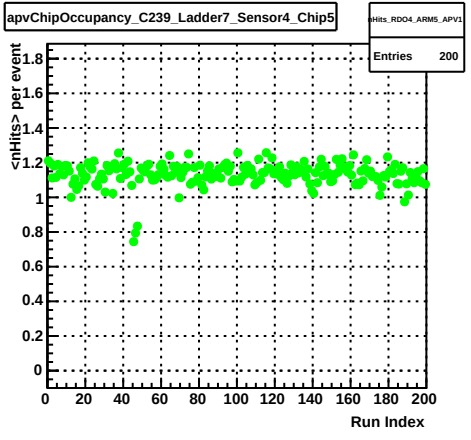
Run Index


apvChipOccupancy_C239_Ladder7_Sensor4_Chip5
nHits_RDO4_ARM5_APV1
Entries 200
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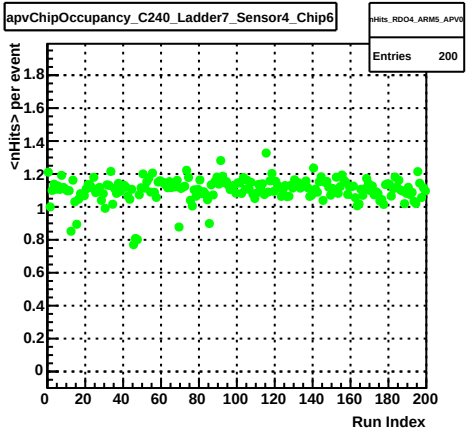
Run Index


apvChipOccupancy_C240_Ladder7_Sensor4_Chip6
nHits_RDO4_ARM5_APV0
Entries 200
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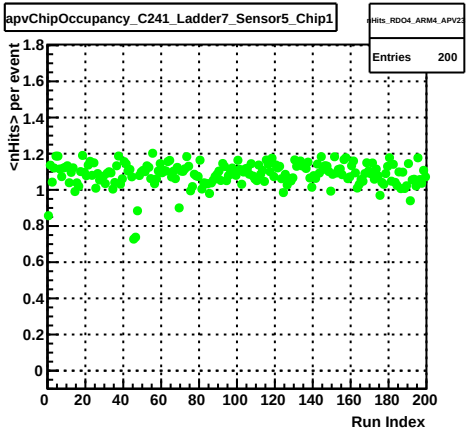
Run Index


apvChipOccupancy_C241_Ladder7_Sensor5_Chip1
Entries 200
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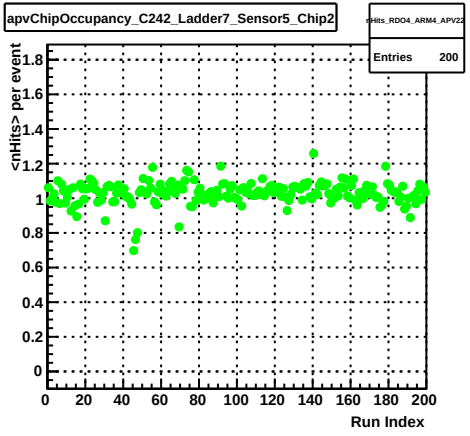
Run Index


apvChipOccupancy_C242_Ladder7_Sensor5_Chip2
Entries 200
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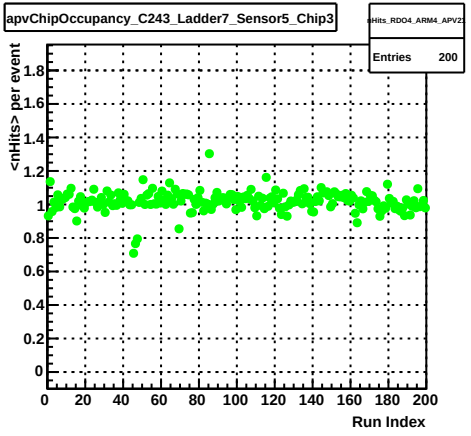
Run Index


apvChipOccupancy_C243_Ladder7_Sensor5_Chip3
Entries 200
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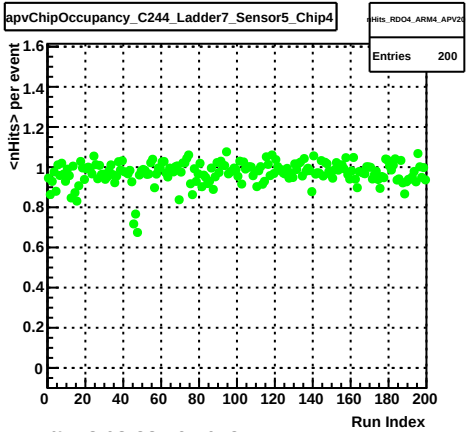
Run Index


apvChipOccupancy_C244_Ladder7_Sensor5_Chip4
Entries 200
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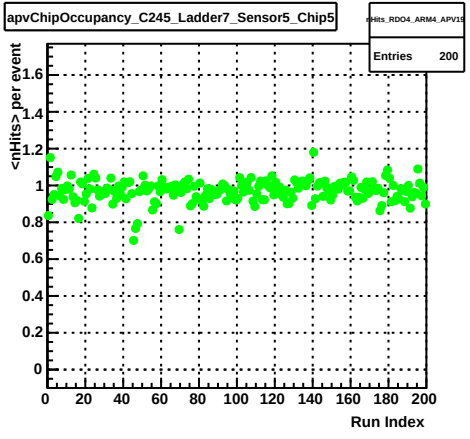
Run Index


apvChipOccupancy_C245_Ladder7_Sensor5_Chip5
Entries 200
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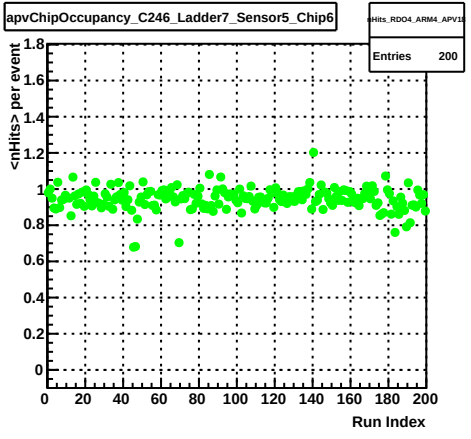
Run Index


apvChipOccupancy_C246_Ladder7_Sensor5_Chip6
Entries 200
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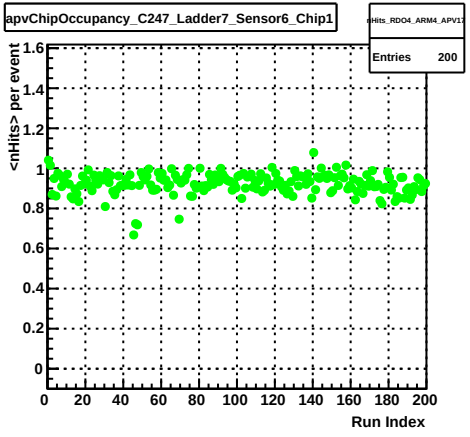
Run Index


apvChipOccupancy_C247_Ladder7_Sensor6_Chip1
Entries 200
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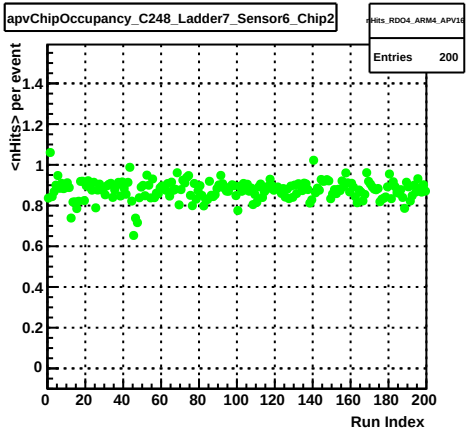
Run Index


apvChipOccupancy_C248_Ladder7_Sensor6_Chip2
Entries 200
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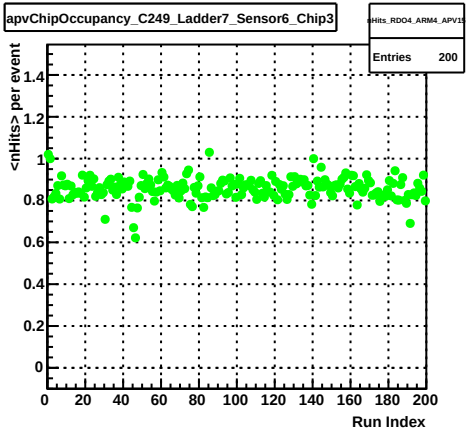
Run Index


apvChipOccupancy_C249_Ladder7_Sensor6_Chip3
Entries 200
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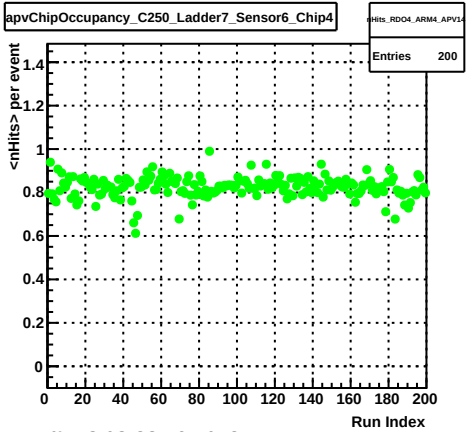
Run Index


apvChipOccupancy_C250_Ladder7_Sensor6_Chip4
Entries 200
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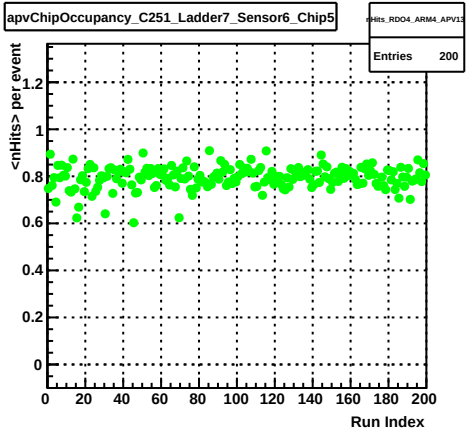
Run Index


apvChipOccupancy_C251_Ladder7_Sensor6_Chip5
Entries 200
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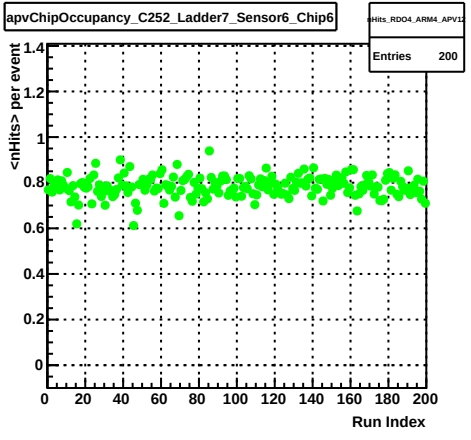
Run Index


apvChipOccupancy_C252_Ladder7_Sensor6_Chip6
Entries 200
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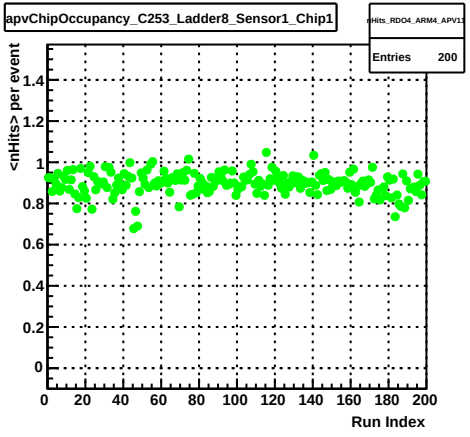
Run Index


apvChipOccupancy_C253_Ladder8_Sensor1_Chip1
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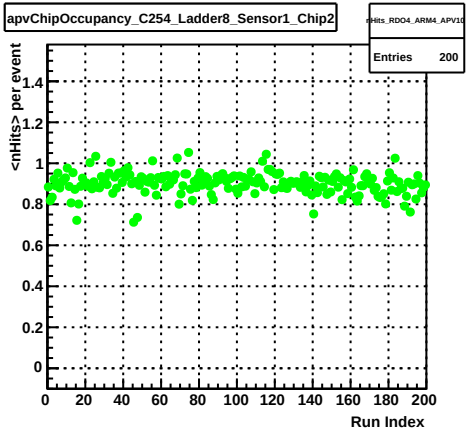
Run Index


apvChipOccupancy_C254_Ladder8_Sensor1_Chip2
Entries 200
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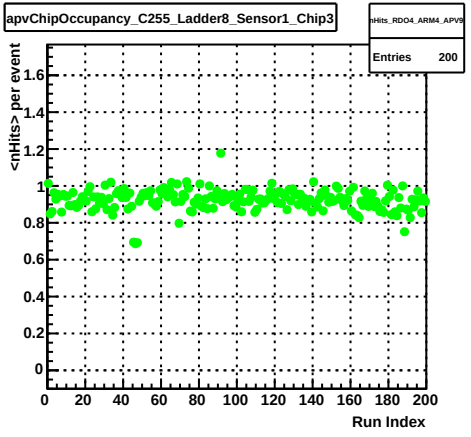
Run Index


apvChipOccupancy_C255_Ladder8_Sensor1_Chip3
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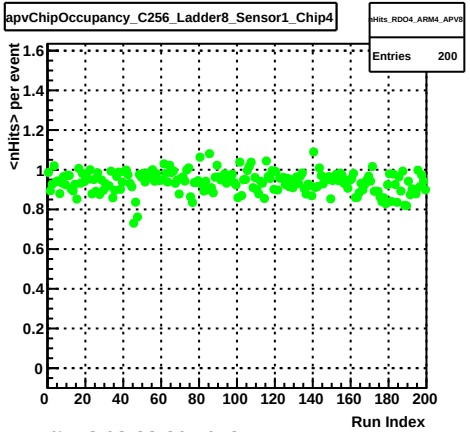
Run Index


apvChipOccupancy_C256_Ladder8_Sensor1_Chip4
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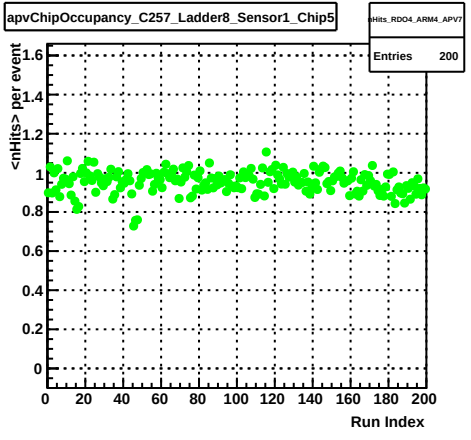
Run Index


apvChipOccupancy_C257_Ladder8_Sensor1_Chip5
Entries 200
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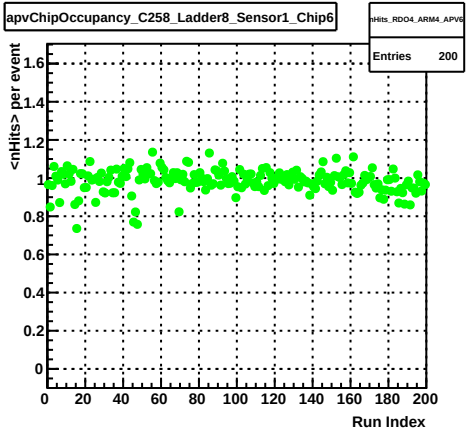
Run Index


apvChipOccupancy_C258_Ladder8_Sensor1_Chip6
Entries 200
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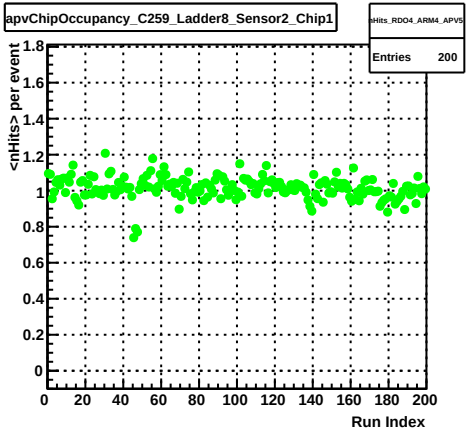
Run Index


apvChipOccupancy_C259_Ladder8_Sensor2_Chip1
nHits_RDO4_ARM4_APV5
Entries 200
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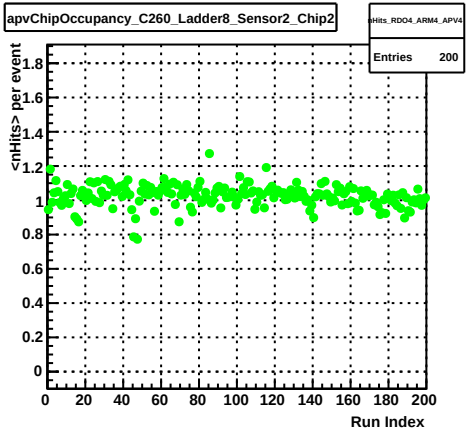
Run Index


apvChipOccupancy_C260_Ladder8_Sensor2_Chip2
nHits_RDO4_ARM4_APV4
Entries 200
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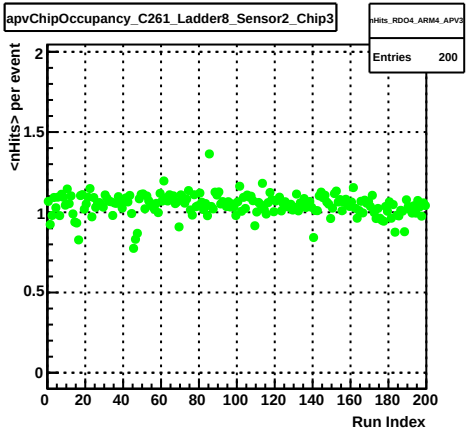
Run Index


apvChipOccupancy_C261_Ladder8_Sensor2_Chip3
nHits_RDO4_ARM4_APV3
Entries 200
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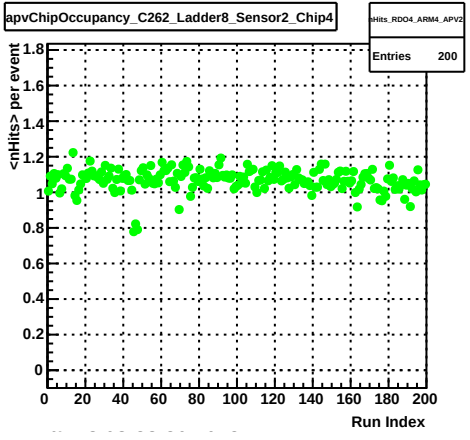
Run Index


apvChipOccupancy_C262_Ladder8_Sensor2_Chip4
nHits_RDO4_ARM4_APV2
Entries 200
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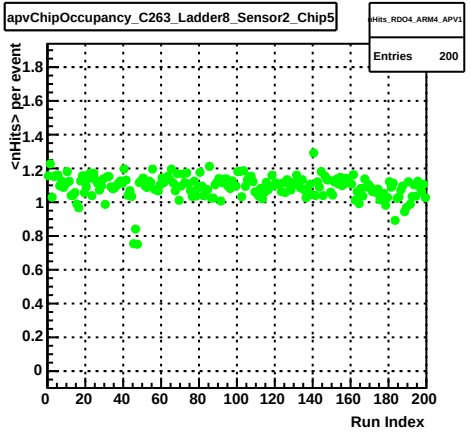
Run Index


apvChipOccupancy_C263_Ladder8_Sensor2_Chip5
nHits_RDO4_ARM4_APV1
Entries 200
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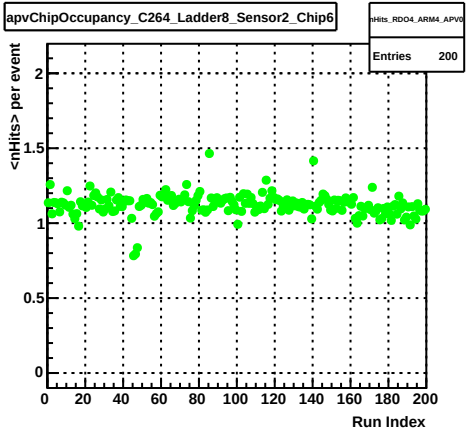
Run Index


apvChipOccupancy_C264_Ladder8_Sensor2_Chip6
nHits_RDO4_ARM4_APV0
Entries 200
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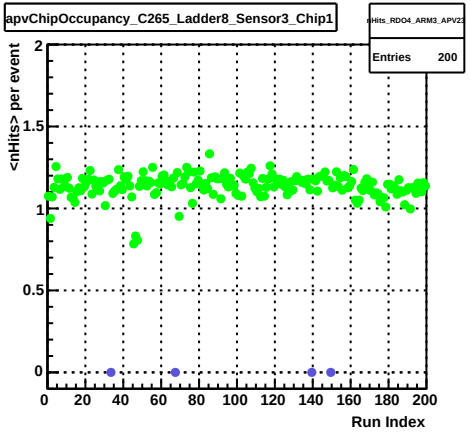
Run Index


apvChipOccupancy_C265_Ladder8_Sensor3_Chip1
Entries 200
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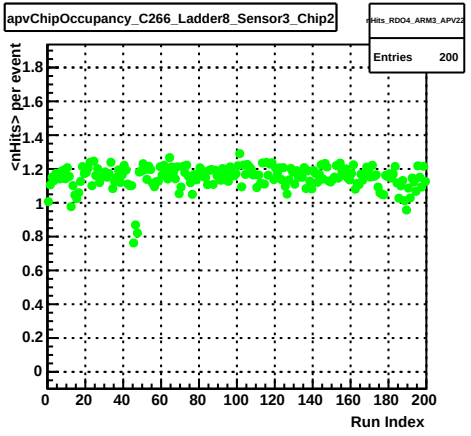
Run Index


apvChipOccupancy_C266_Ladder8_Sensor3_Chip2
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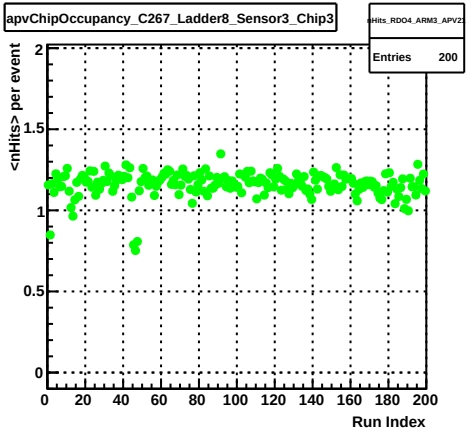
Run Index


apvChipOccupancy_C267_Ladder8_Sensor3_Chip3
Entries 200
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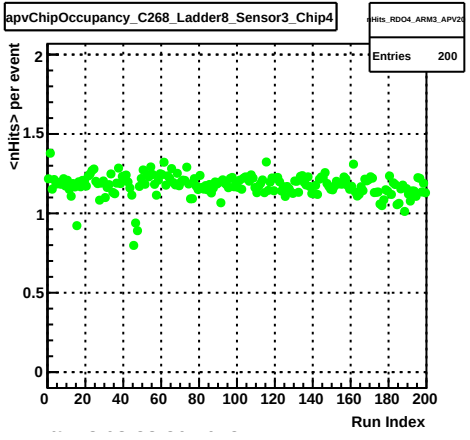
apvChipOccupancy_C268_Ladder8_Sensor3_Chip4
Entries 200
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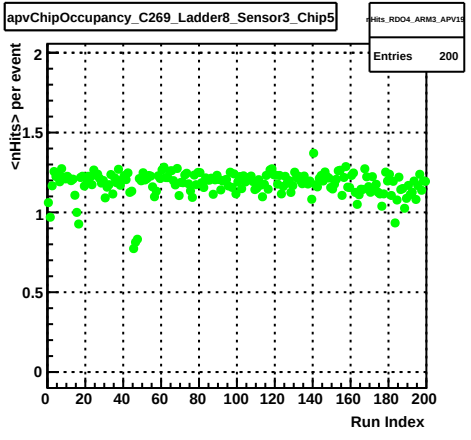
Run Index


apvChipOccupancy_C269_Ladder8_Sensor3_Chip5
Entries 200
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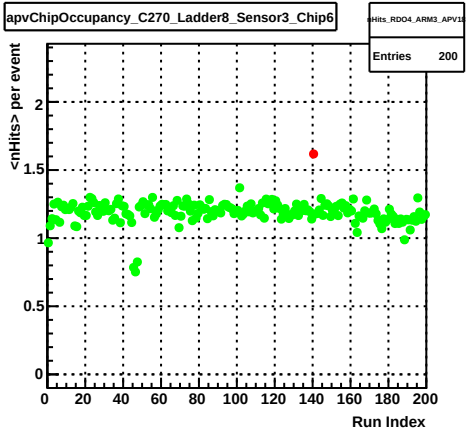
Run Index


apvChipOccupancy_C270_Ladder8_Sensor3_Chip6
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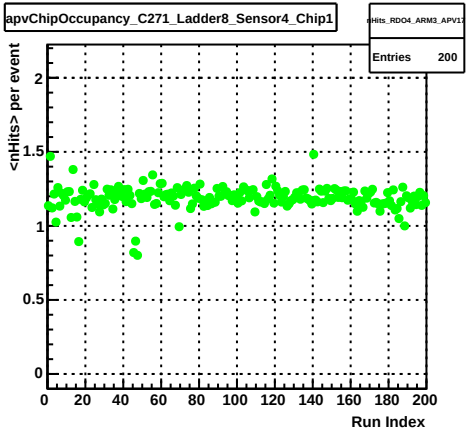
apvChipOccupancy_C271_Ladder8_Sensor4_Chip1
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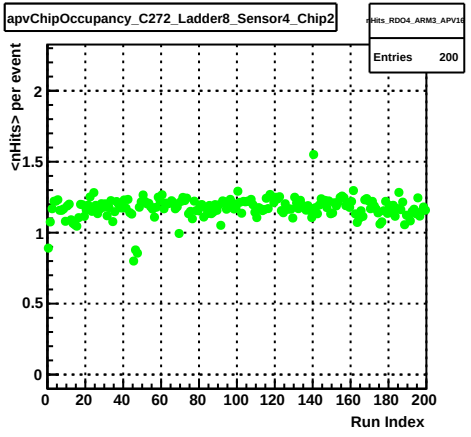
apvChipOccupancy_C272_Ladder8_Sensor4_Chip2
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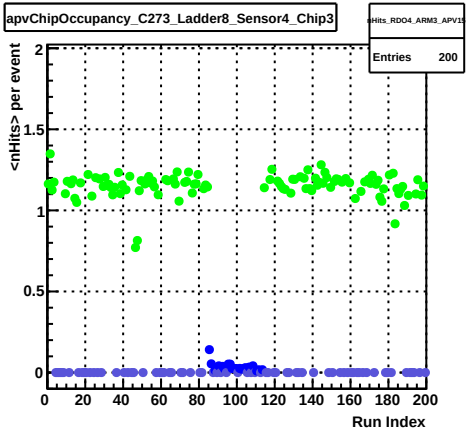
Run Index


apvChipOccupancy_C273_Ladder8_Sensor4_Chip3
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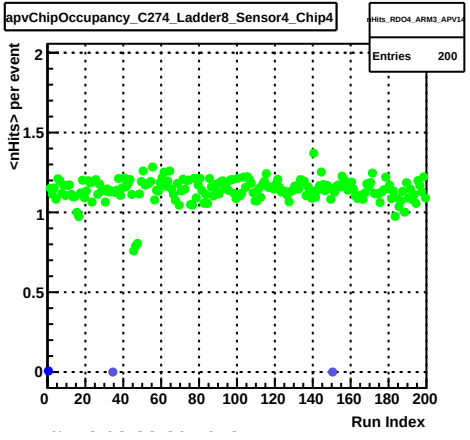
Run Index


apvChipOccupancy_C274_Ladder8_Sensor4_Chip4
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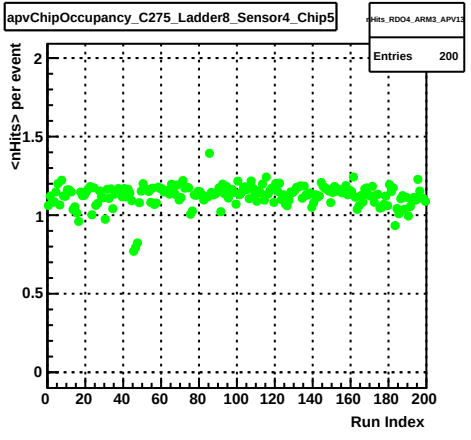
Run Index


apvChipOccupancy_C275_Ladder8_Sensor4_Chip5
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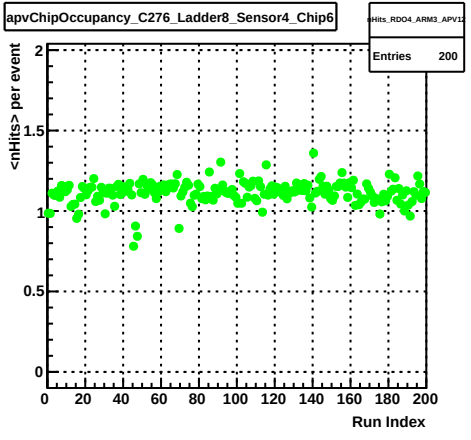
Run Index


apvChipOccupancy_C276_Ladder8_Sensor4_Chip6
Entries 200
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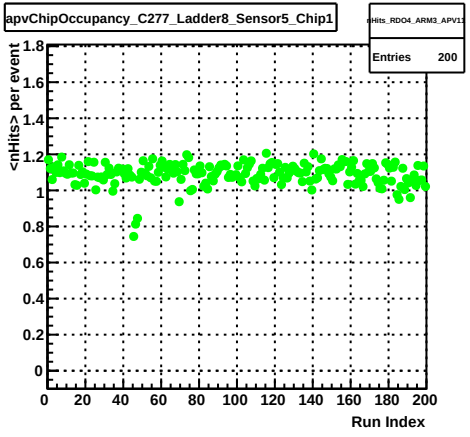
Run Index


apvChipOccupancy_C277_Ladder8_Sensor5_Chip1
Entries 200
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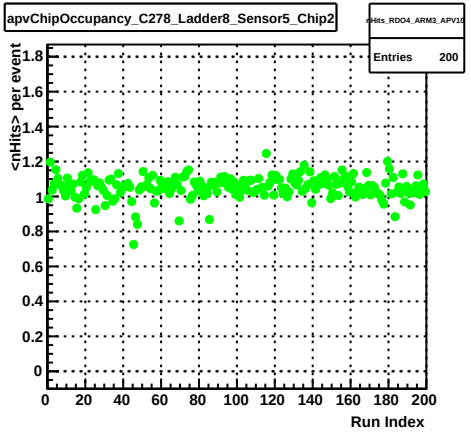
Run Index


apvChipOccupancy_C278_Ladder8_Sensor5_Chip2
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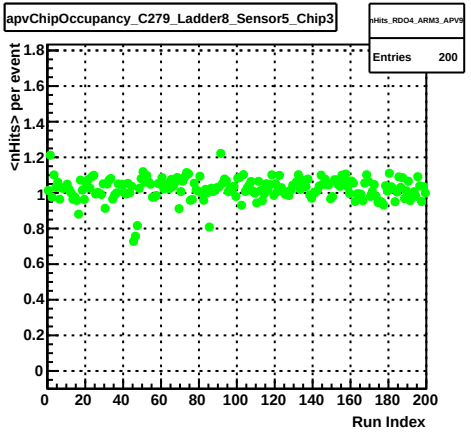
Run Index


apvChipOccupancy_C279_Ladder8_Sensor5_Chip3
Entries 200
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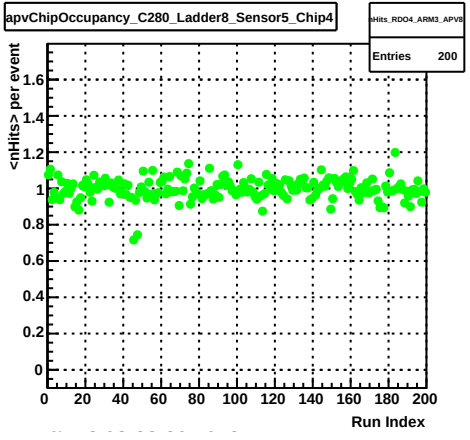
Run Index


apvChipOccupancy_C280_Ladder8_Sensor5_Chip4
Entries 200
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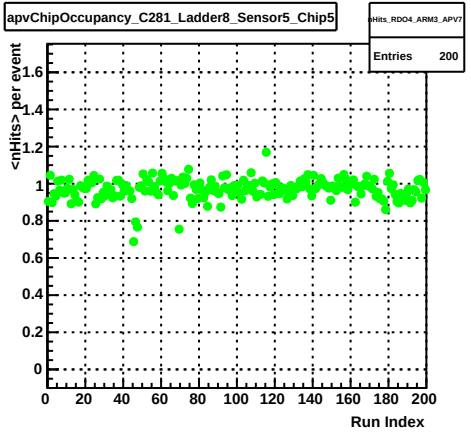
Run Index


apvChipOccupancy_C281_Ladder8_Sensor5_Chip5
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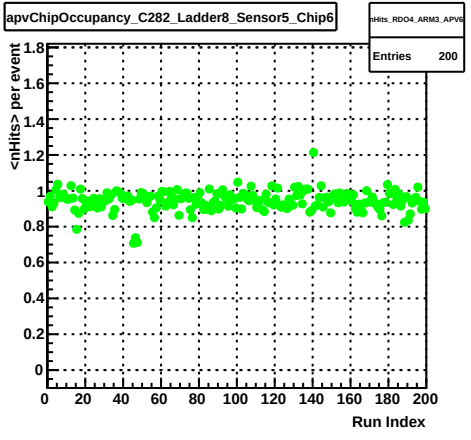
Run Index


apvChipOccupancy_C282_Ladder8_Sensor5_Chip6
Entries 200
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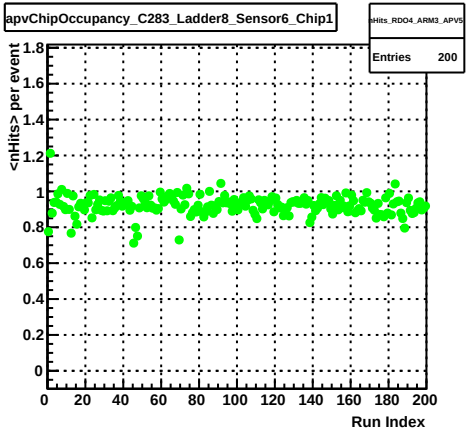
Run Index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Entries 200
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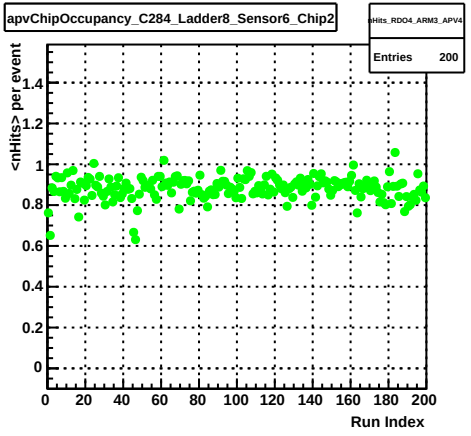
Run Index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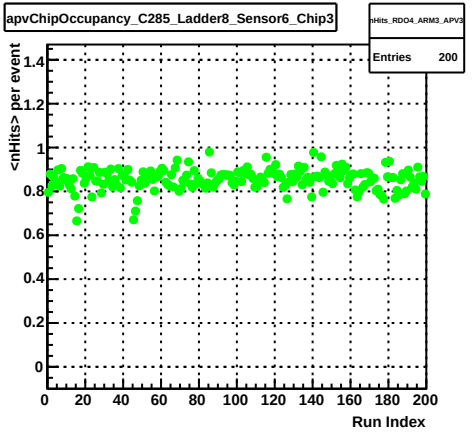
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Entries 200
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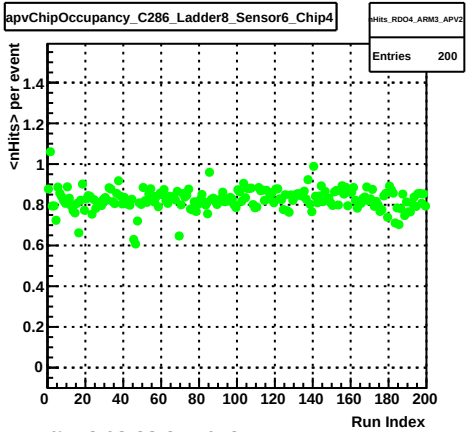
Run Index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nHits_RDO4_ARM3_APV2
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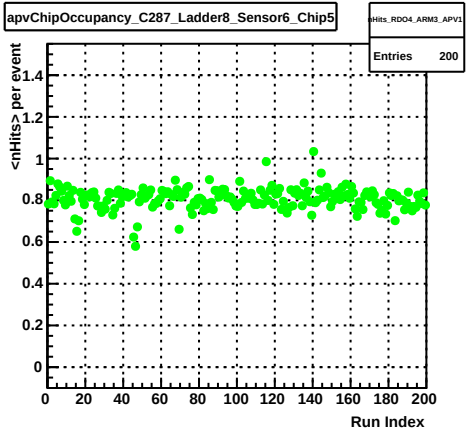
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Entries 200
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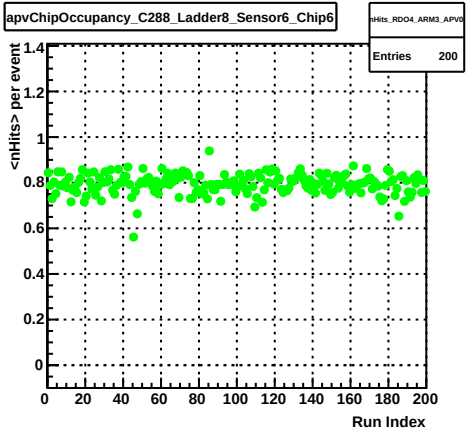
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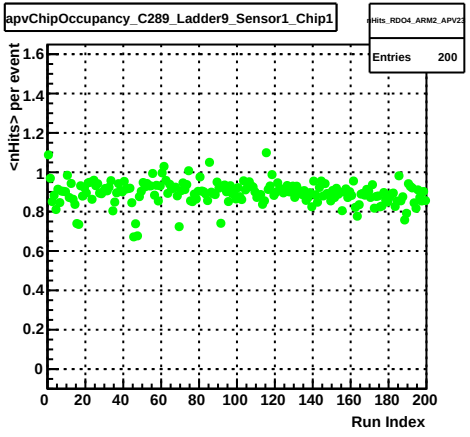
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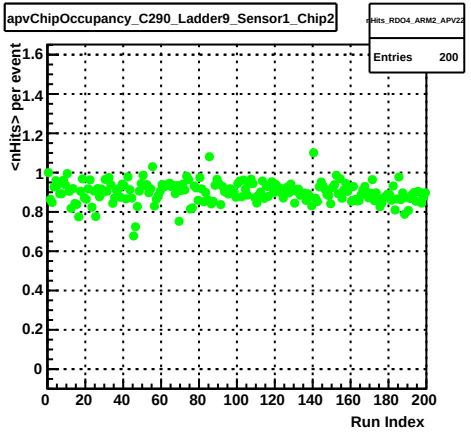
Run Index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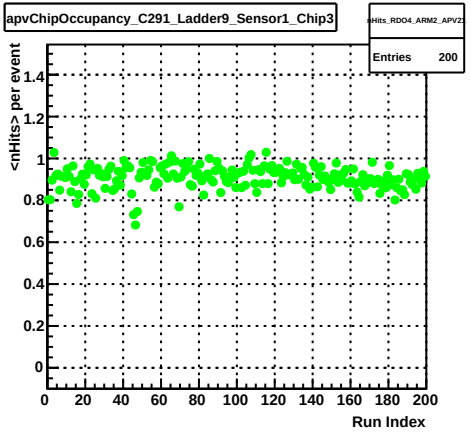
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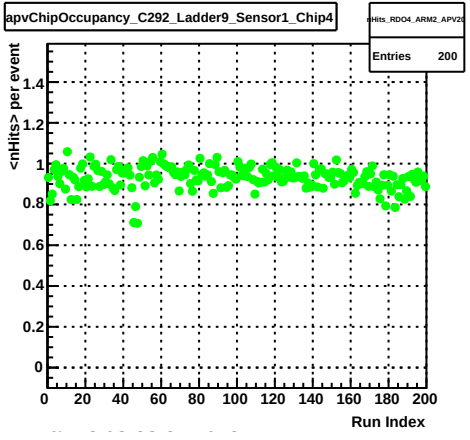
Run Index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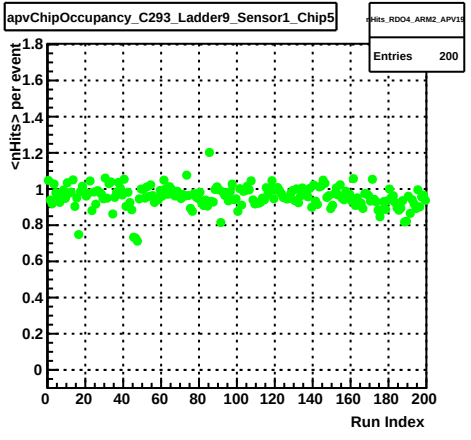
Run Index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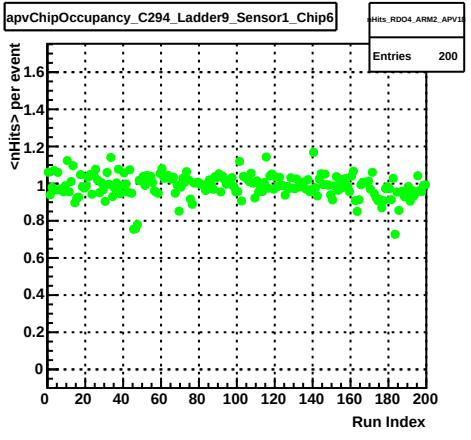
apvChipOccupancy_C294_Ladder9_Sensor1_Chip6
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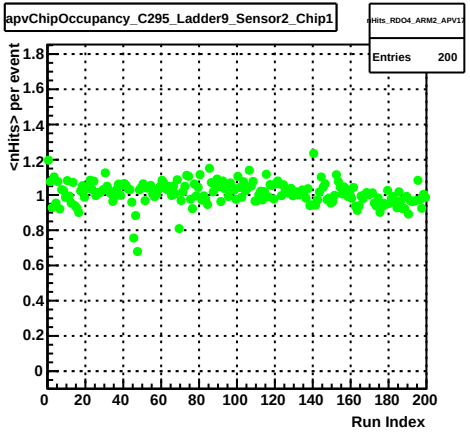
Run Index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Entries 200
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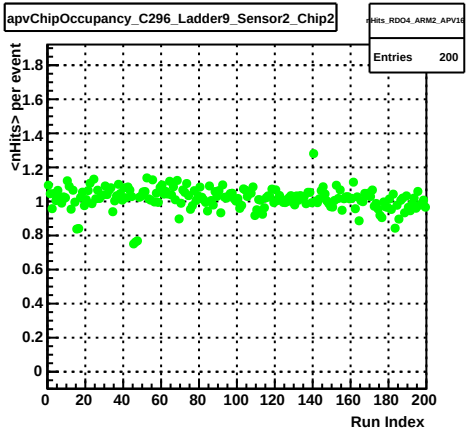
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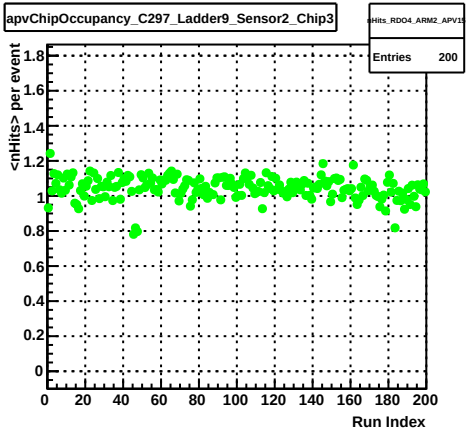
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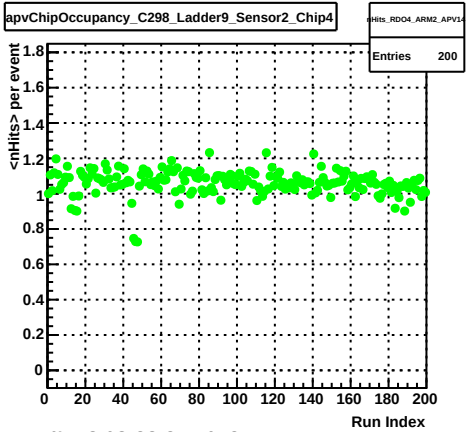
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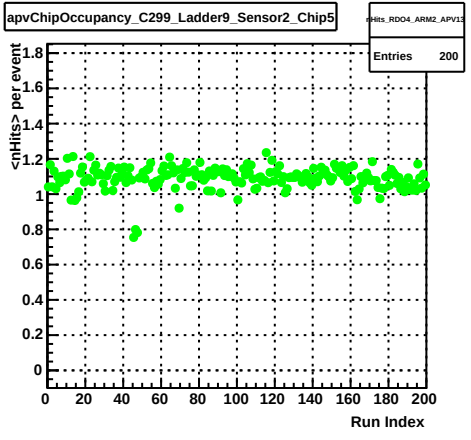
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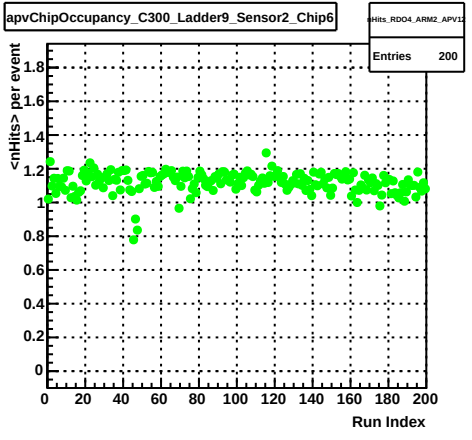
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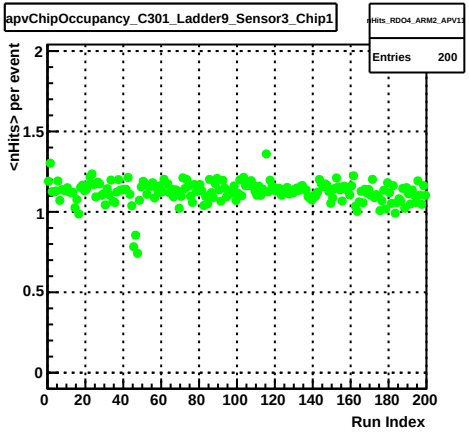
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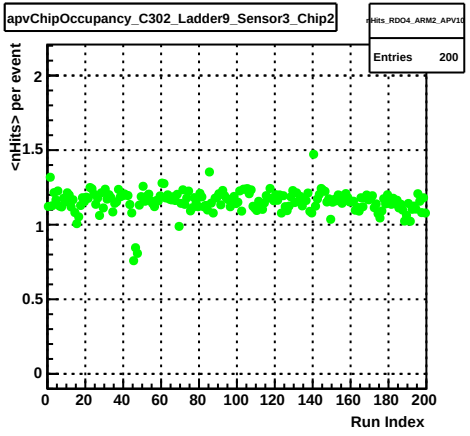
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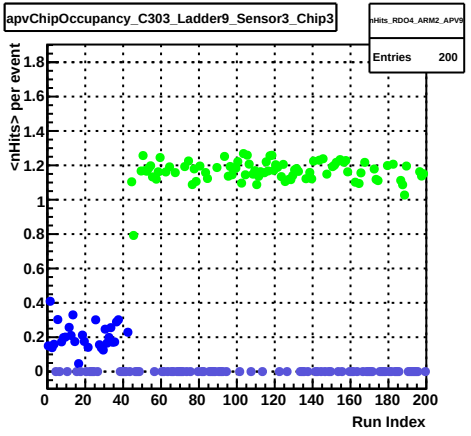
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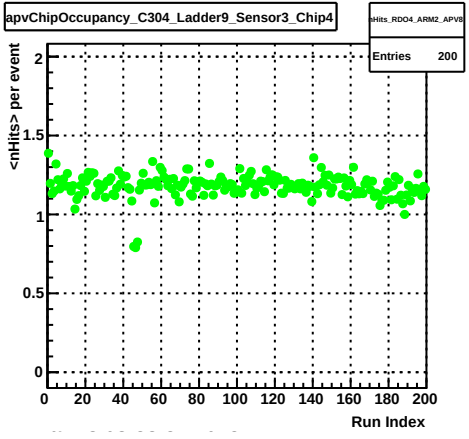
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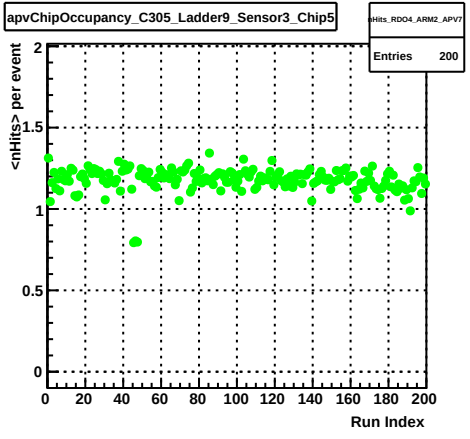
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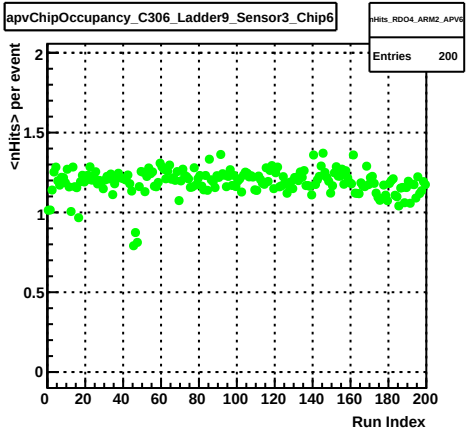
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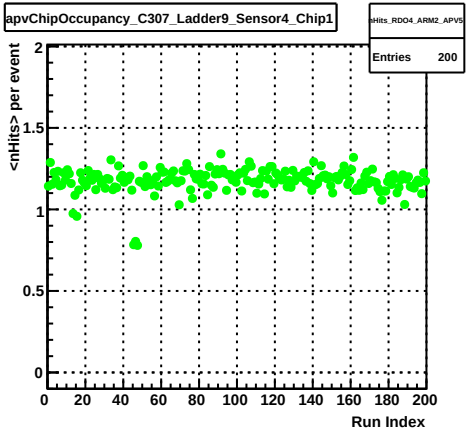
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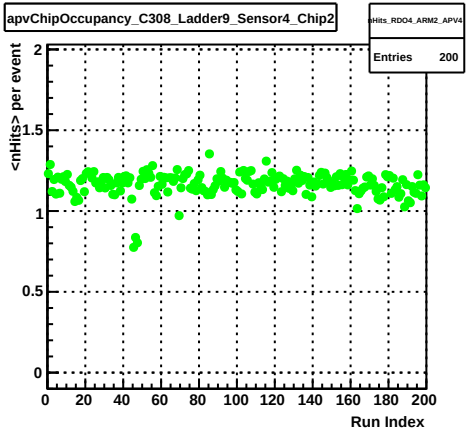
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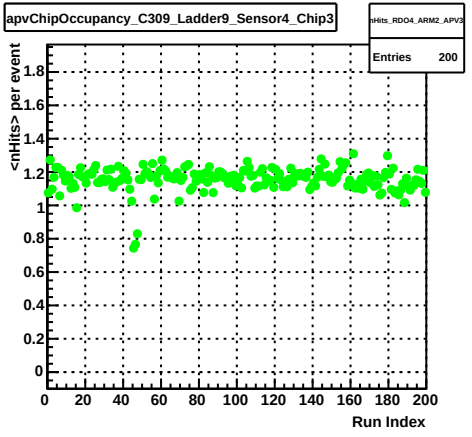
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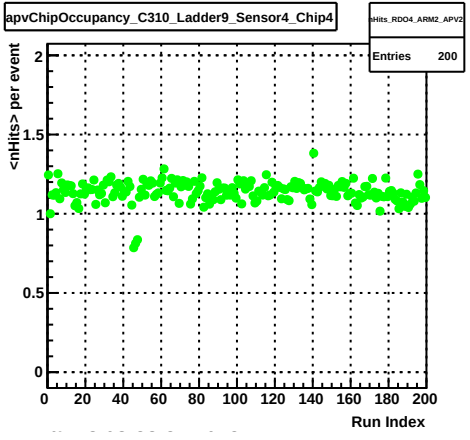
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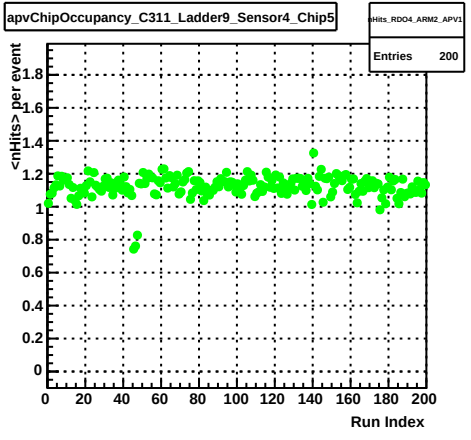
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Entries 200
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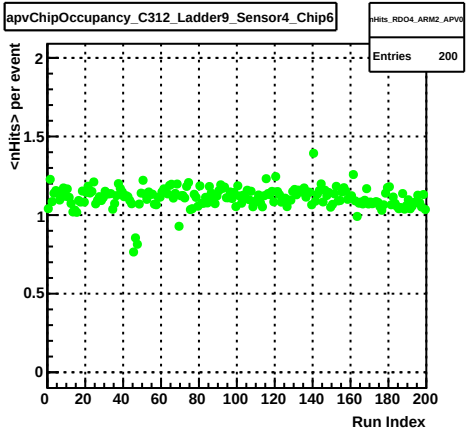
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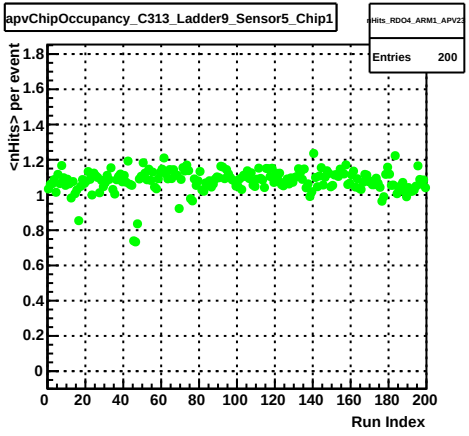
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Entries 200
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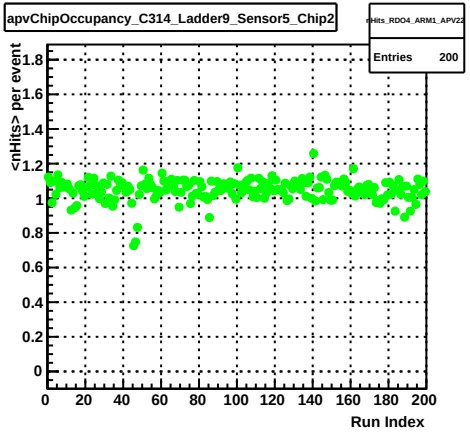
Run Index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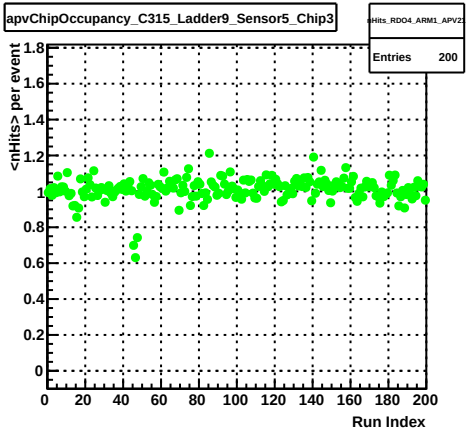
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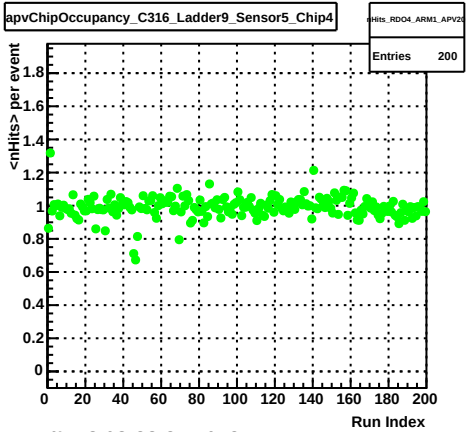
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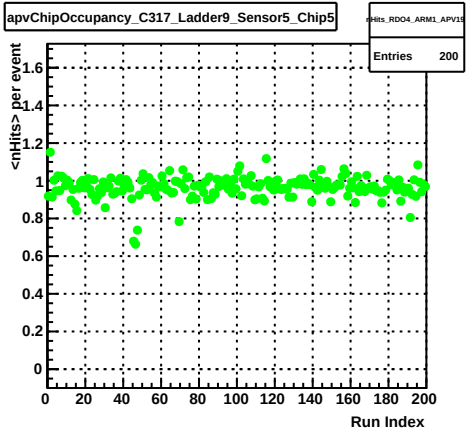
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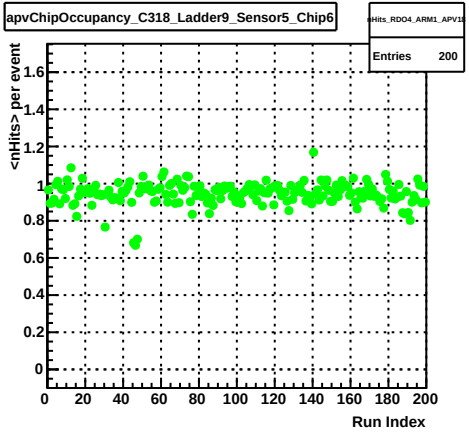
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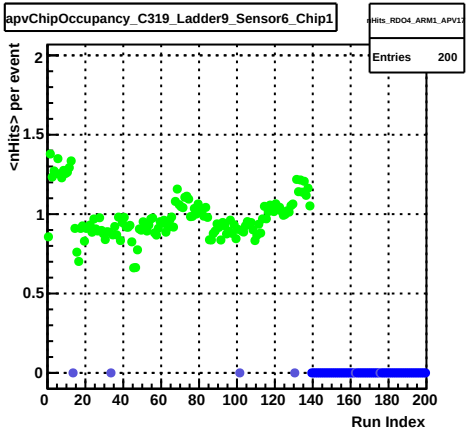
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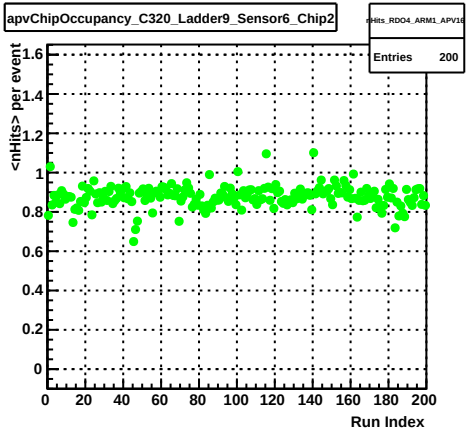
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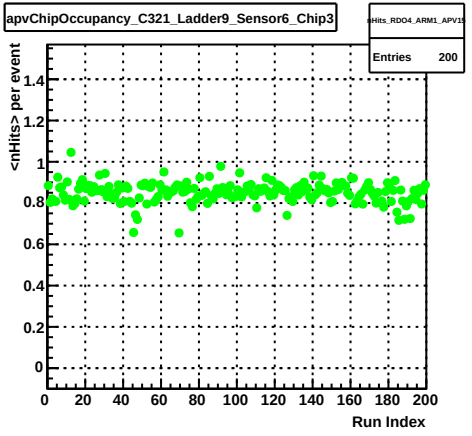
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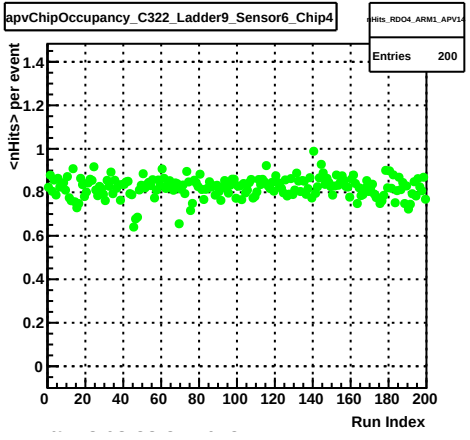
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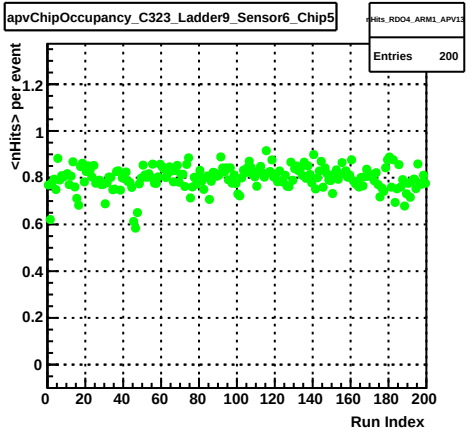
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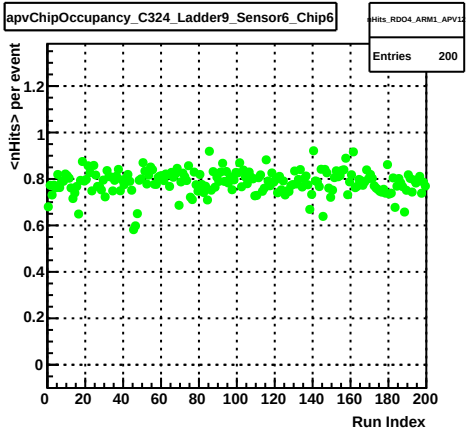
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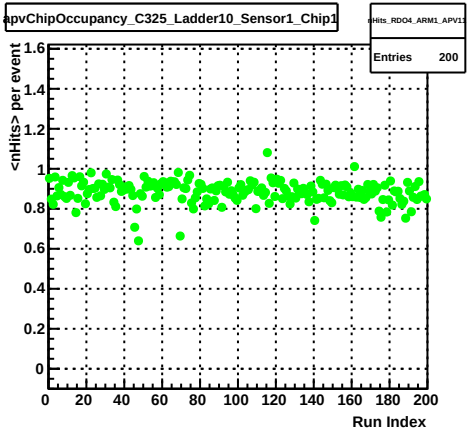
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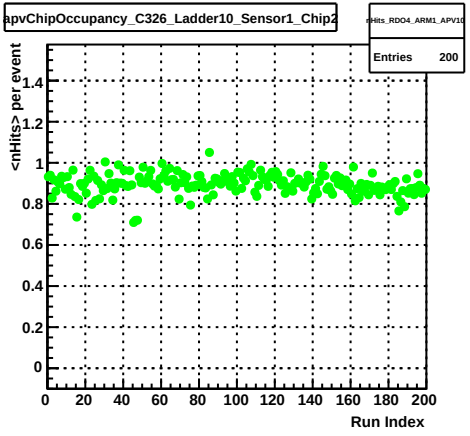
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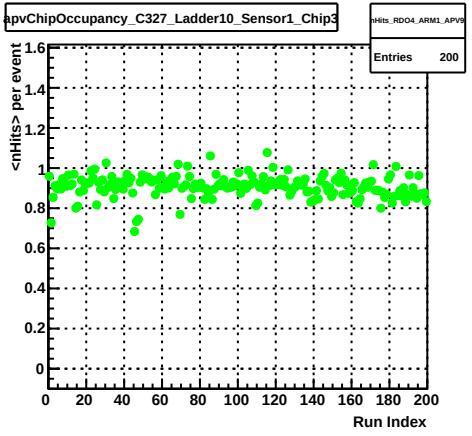
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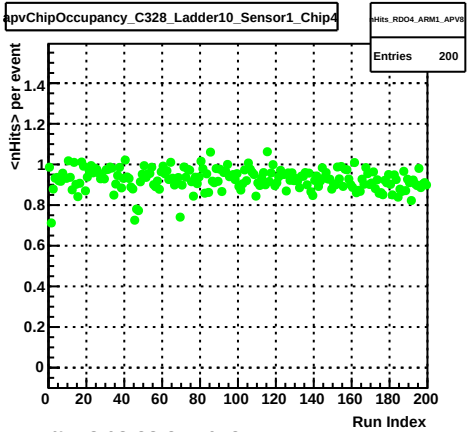
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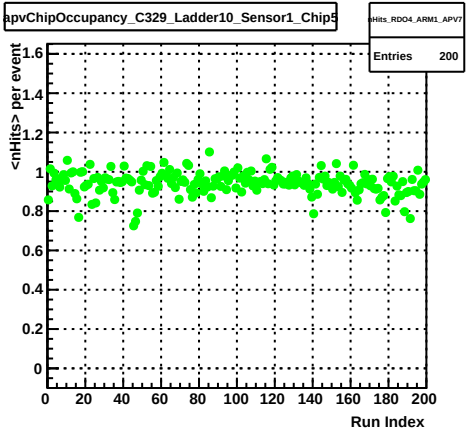
Run Index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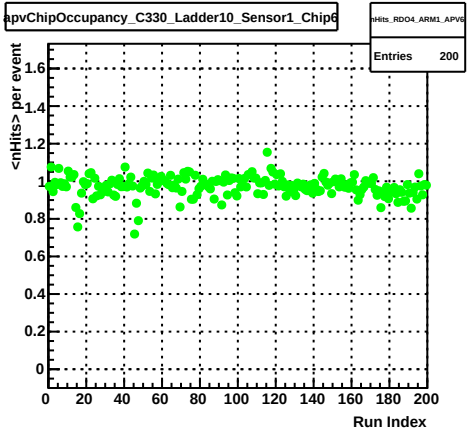
apvChipOccupancy_C330_Ladder10_Sensor1_Chip6
nHits_RDO4_ARM1_APV6
Entries 200
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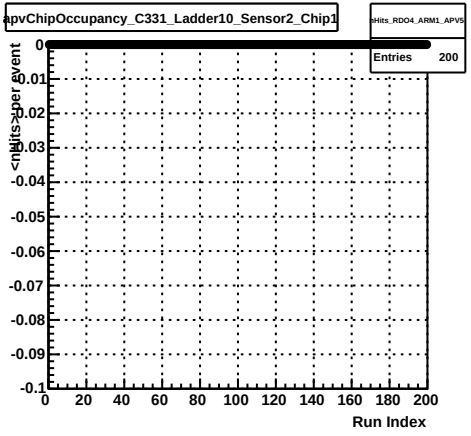
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nHits_RD04_ARM1_APV5
Entries 200
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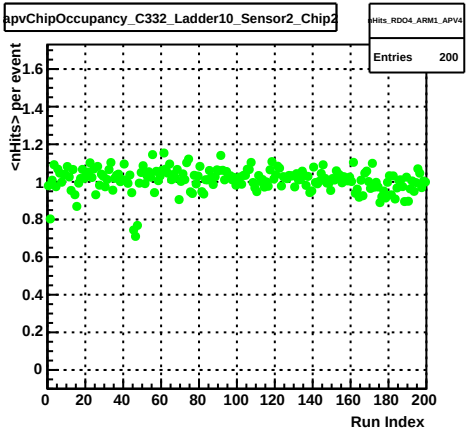
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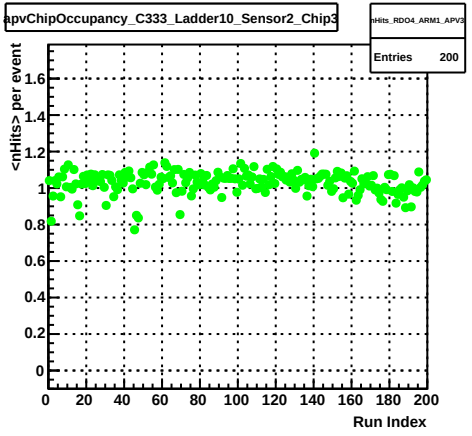
apvChipOccupancy_C333_Ladder10_Sensor2_Chip3
nHits_RD04_ARM1_APV3
Entries 200
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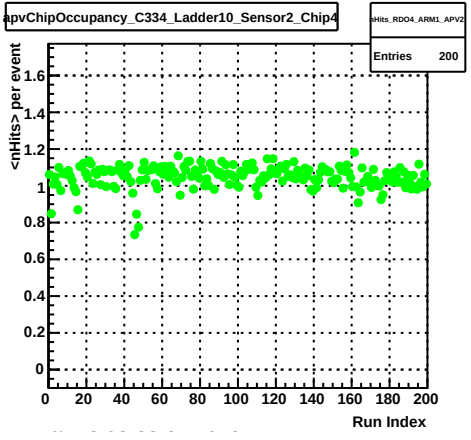
Run Index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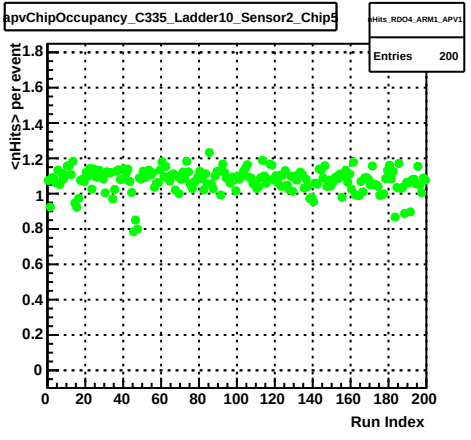
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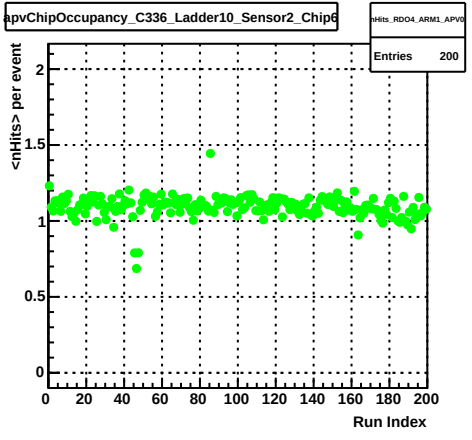
apvChipOccupancy_C336_Ladder10_Sensor2_Chip6
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Entries 200
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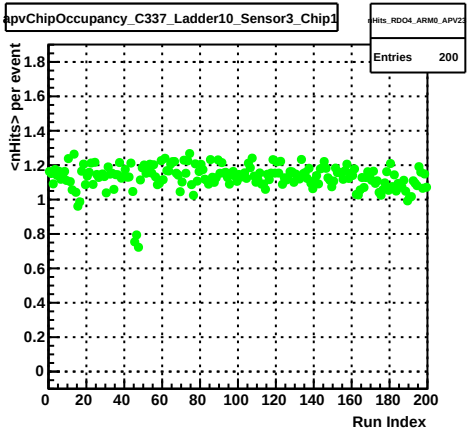
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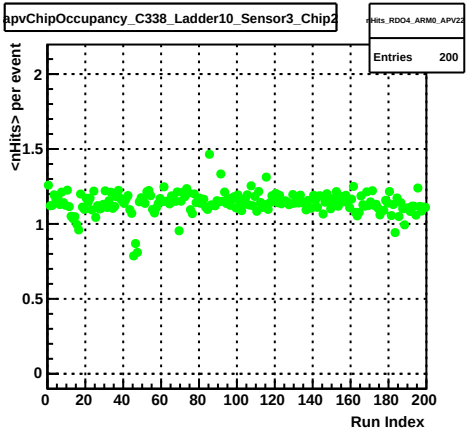
Run Index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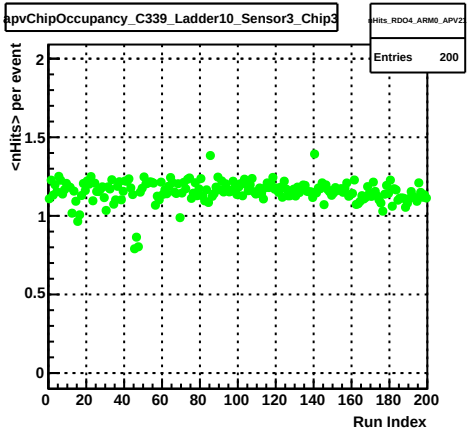
apvChipOccupancy_C339_Ladder10_Sensor3_Chip3
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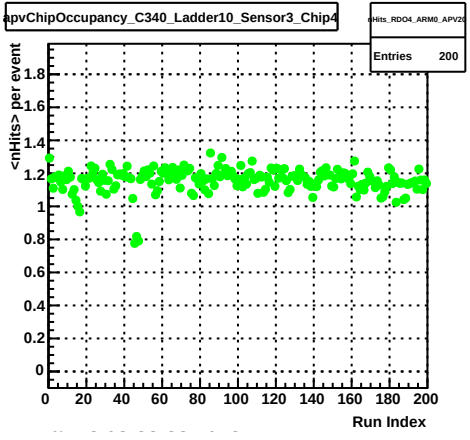
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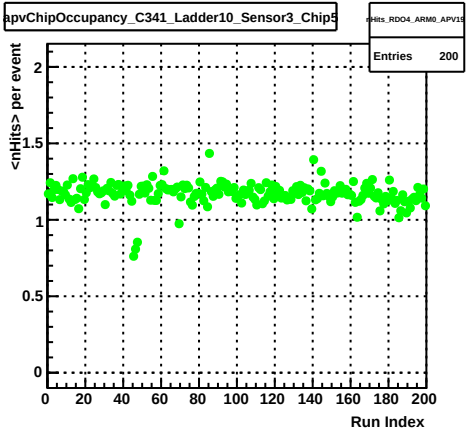
apvChipOccupancy_C341_Ladder10_Sensor3_Chip5
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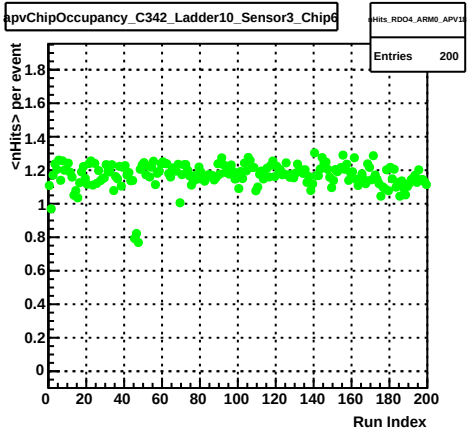
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Entries 200
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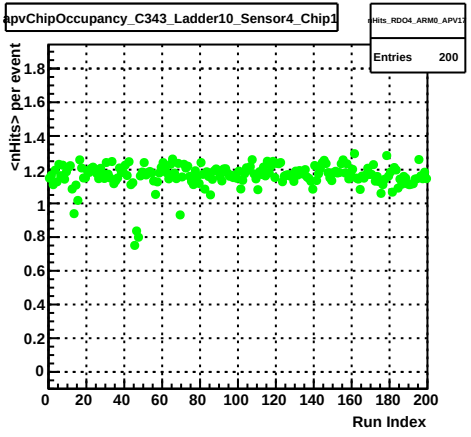
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Entries 200
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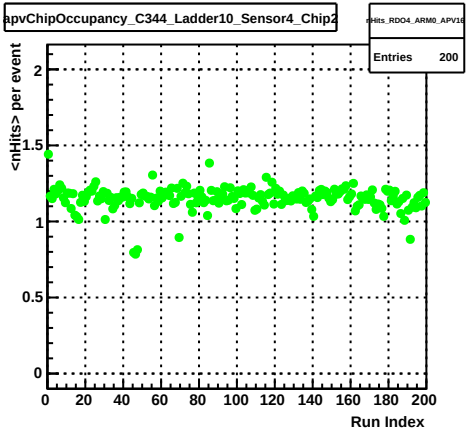
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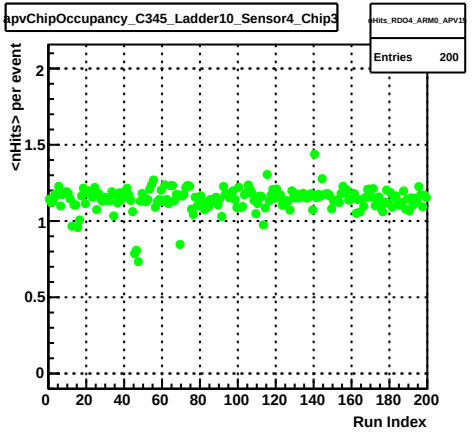
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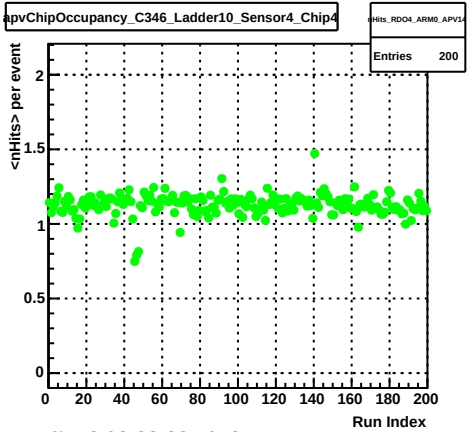
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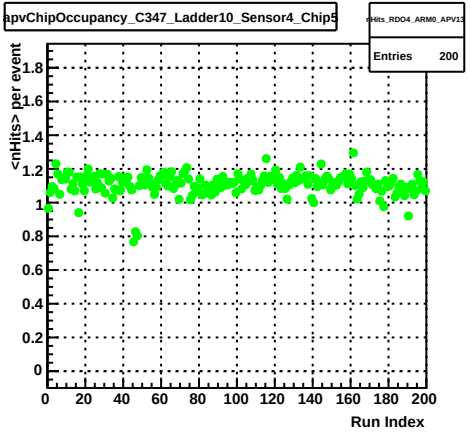
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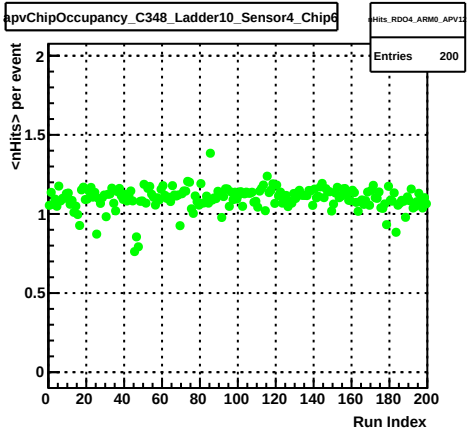
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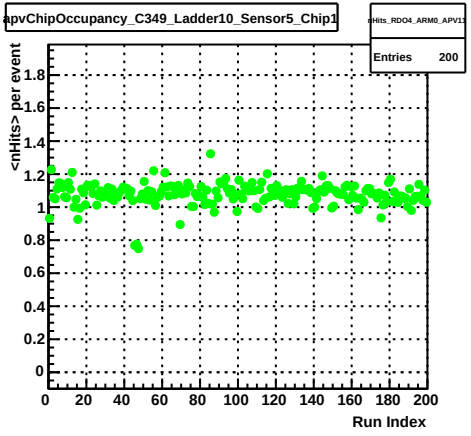
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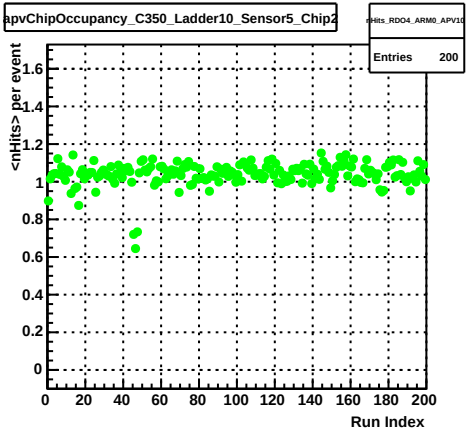
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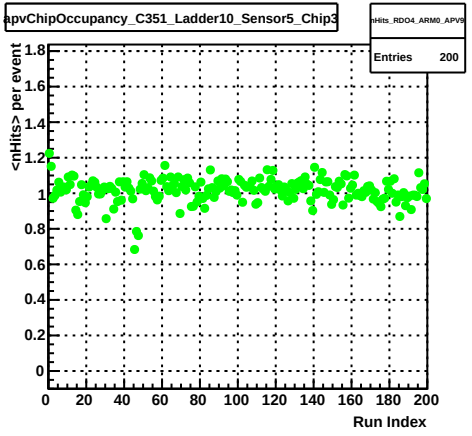
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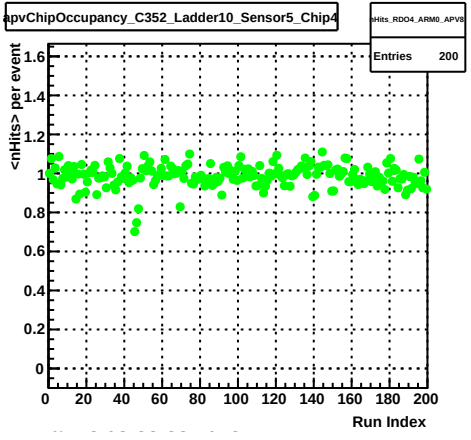
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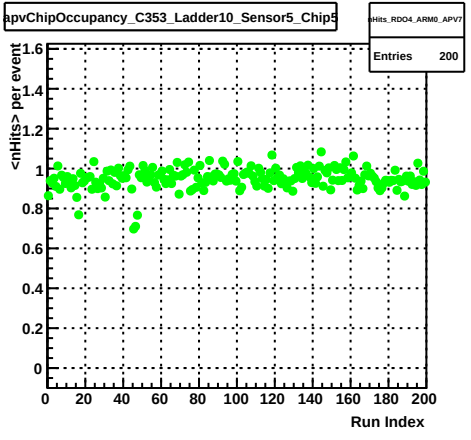
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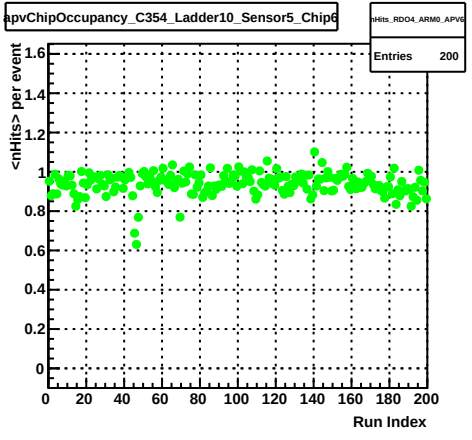
Run Index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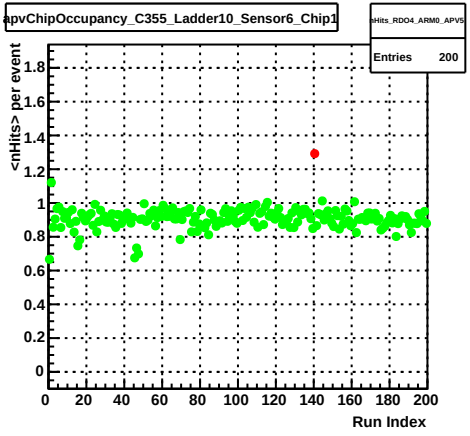
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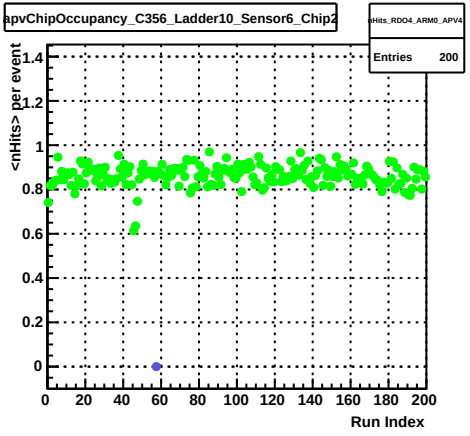
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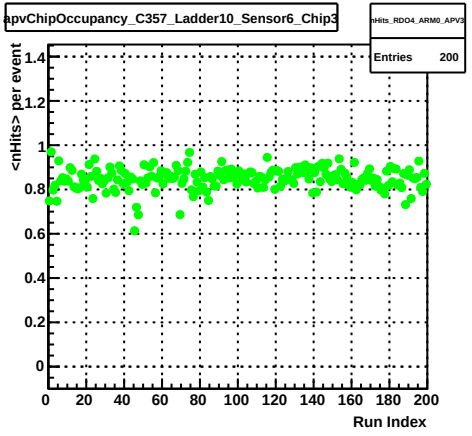
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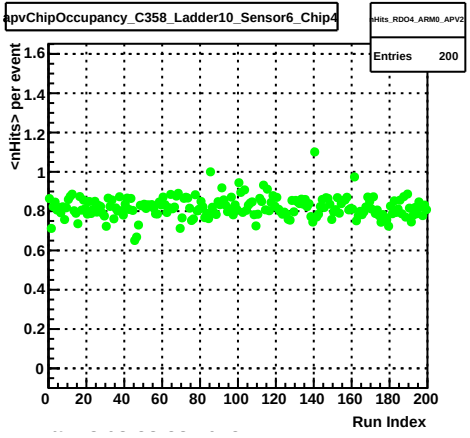
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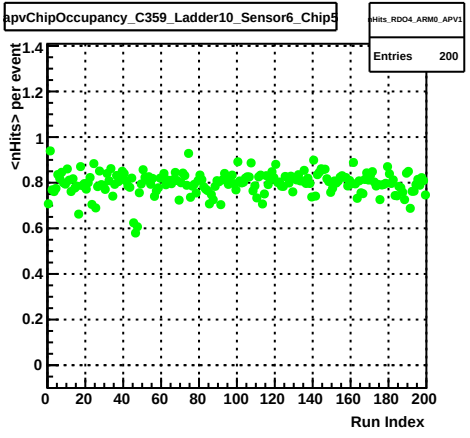
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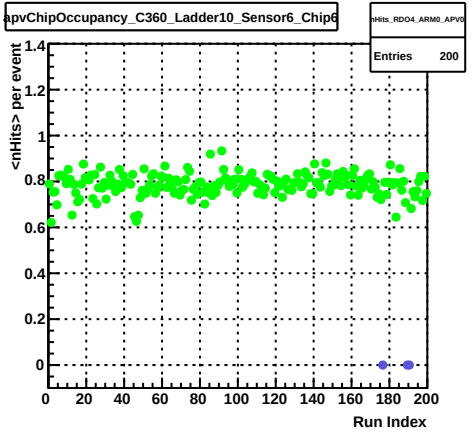
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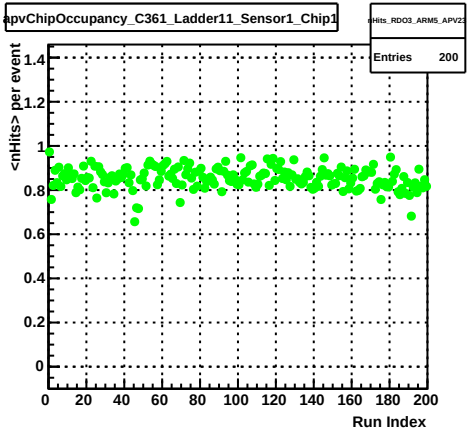
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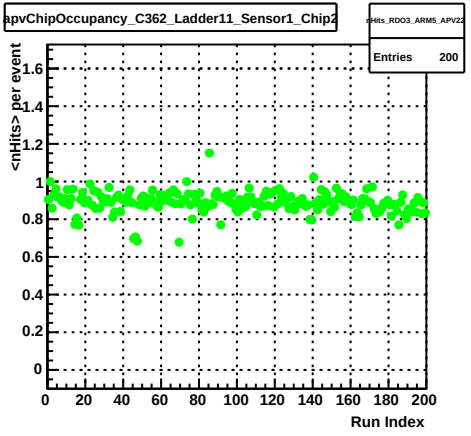
Run Index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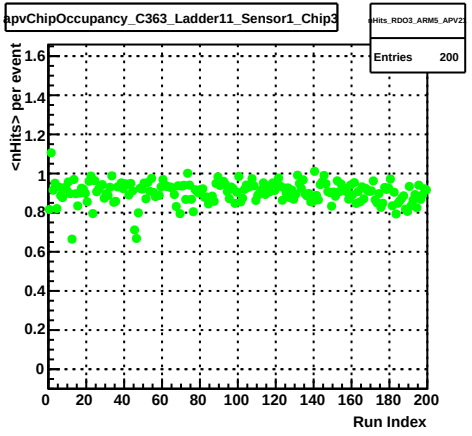
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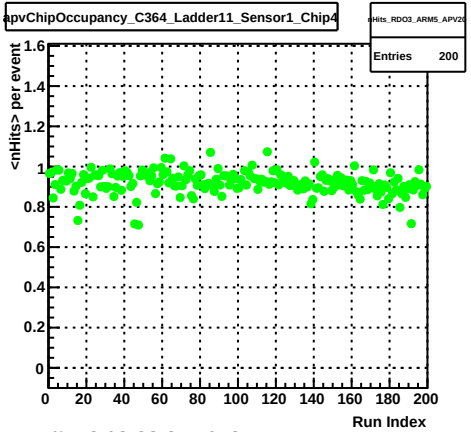
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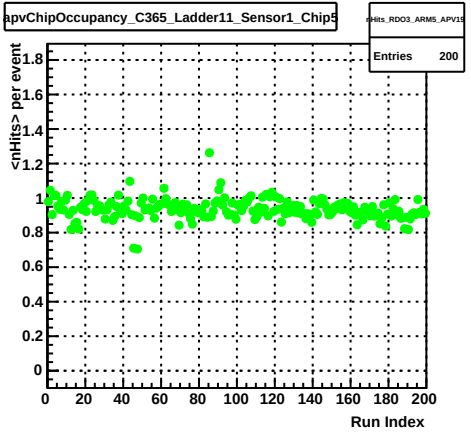
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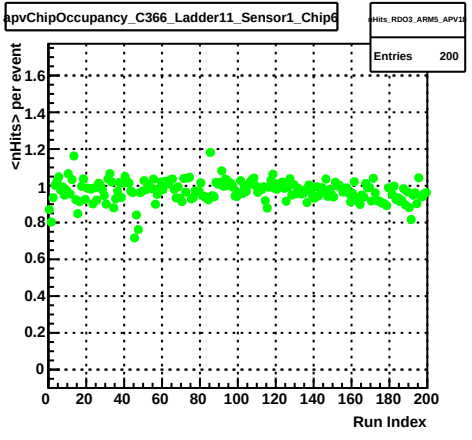
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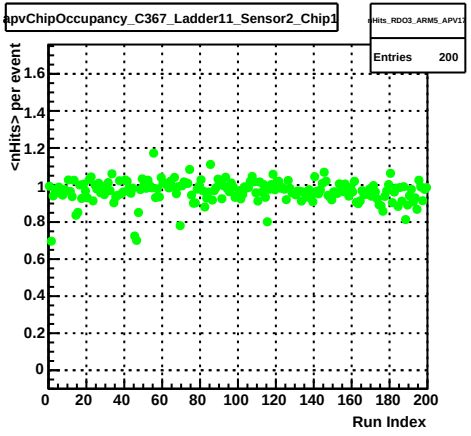
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Entries 200
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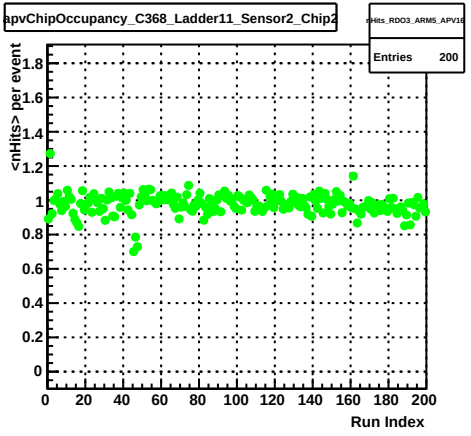
Run Index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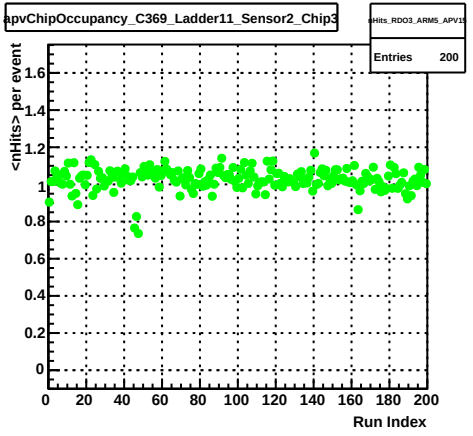
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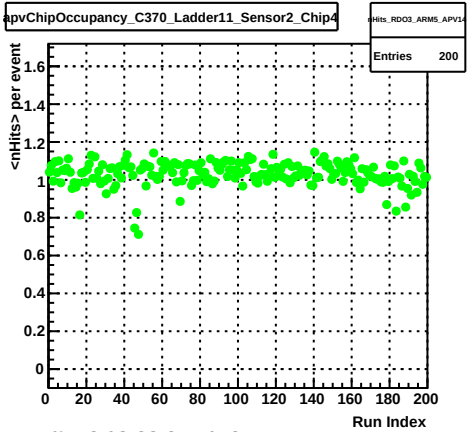
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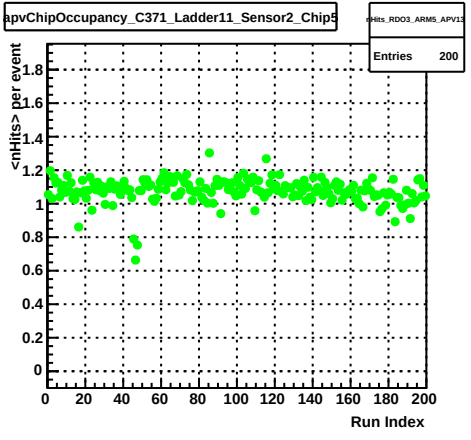
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Entries 200
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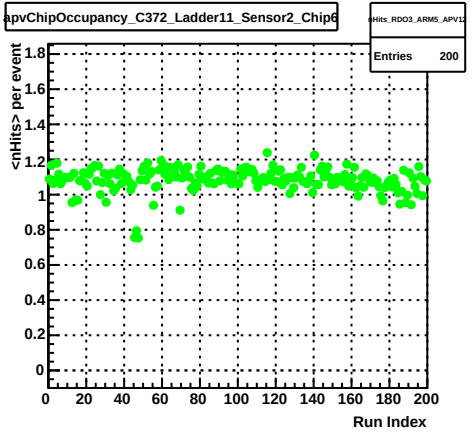
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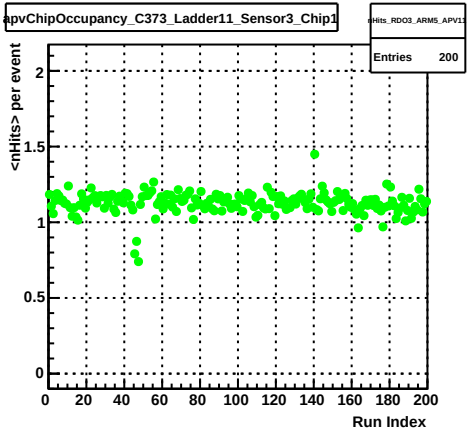
Run Index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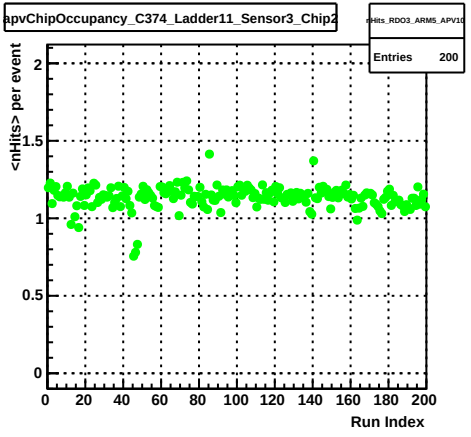
Run Index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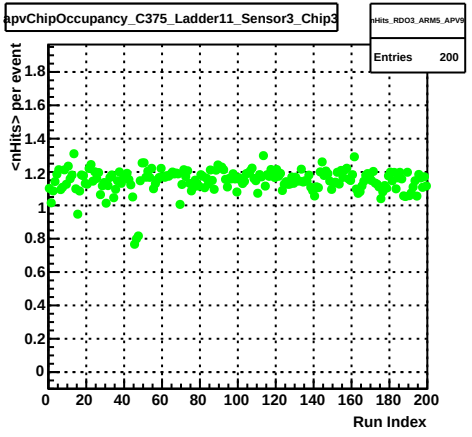
Run Index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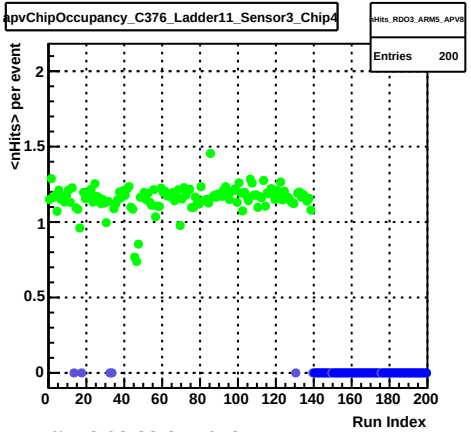
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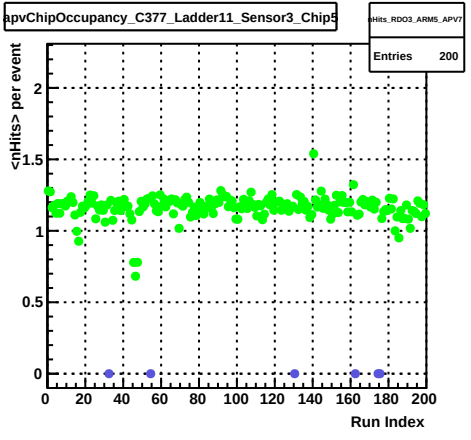
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nHits_RDO3_ARM5_APV7
Entries 200
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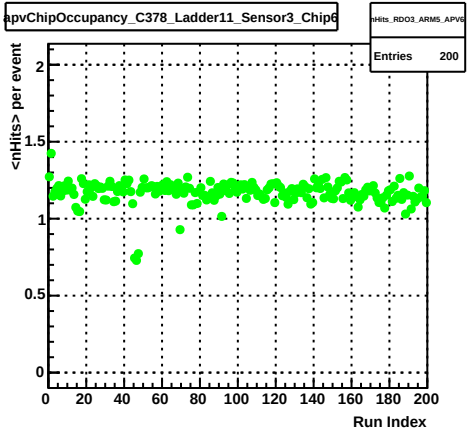
apvChipOccupancy_C378_Ladder11_Sensor3_Chip6
nHits_RDO3_ARM5_APV6
Entries 200
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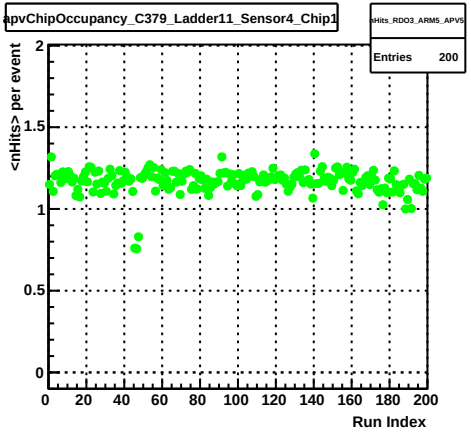
Run Index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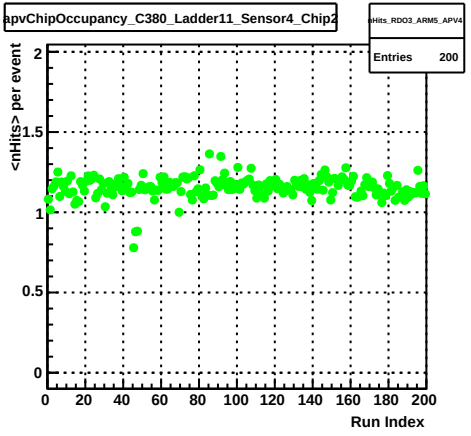
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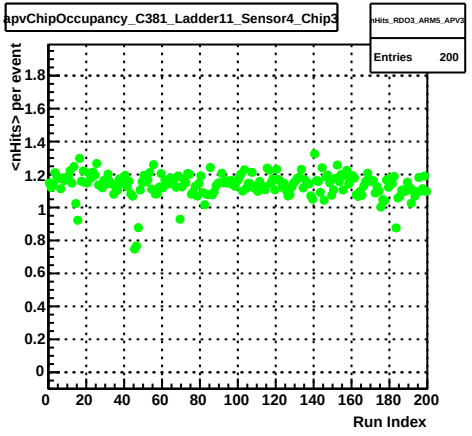
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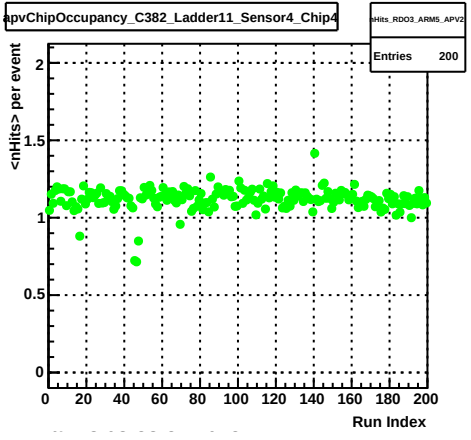
Run Index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nHits_RD03_ARM5_APV2
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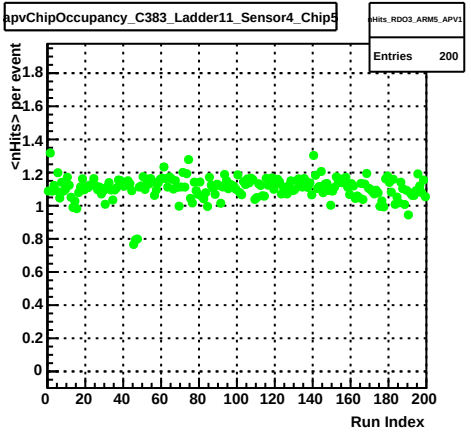
Run Index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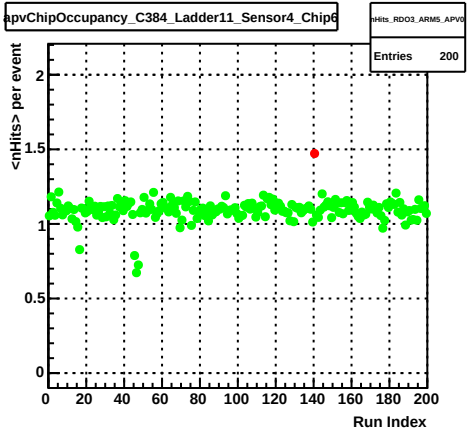
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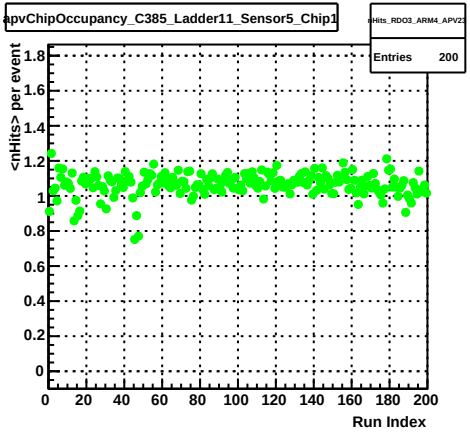
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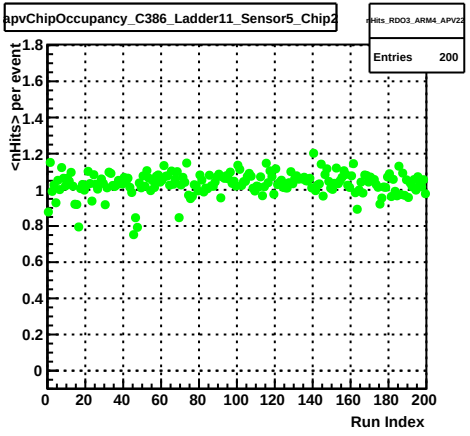
Run Index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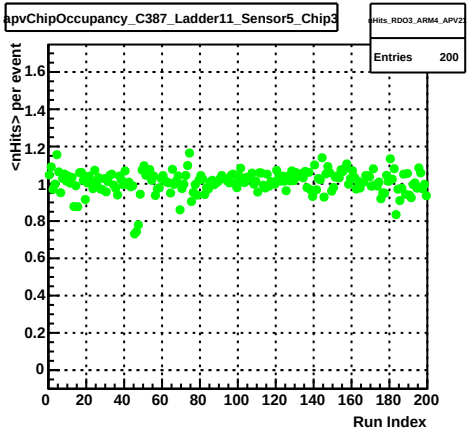
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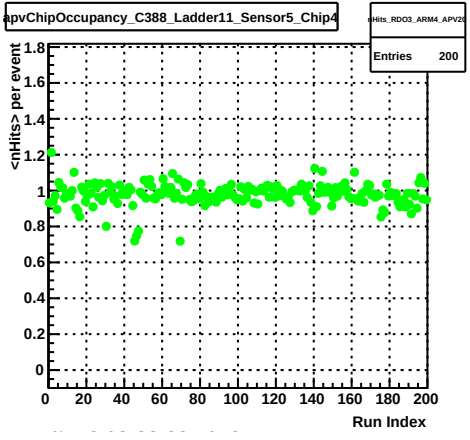
Run Index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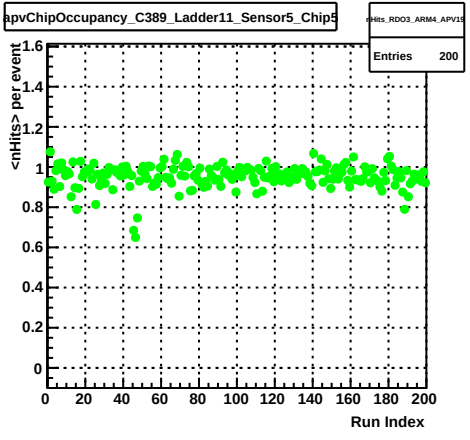
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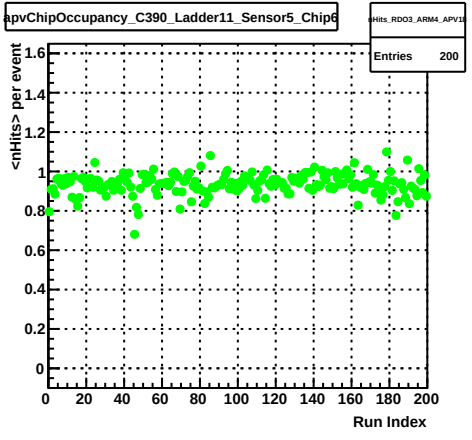
Run Index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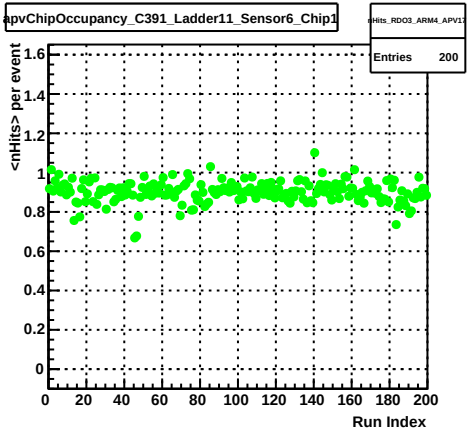
Run Index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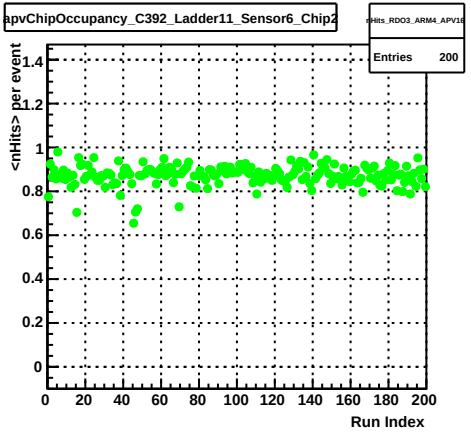
Run Index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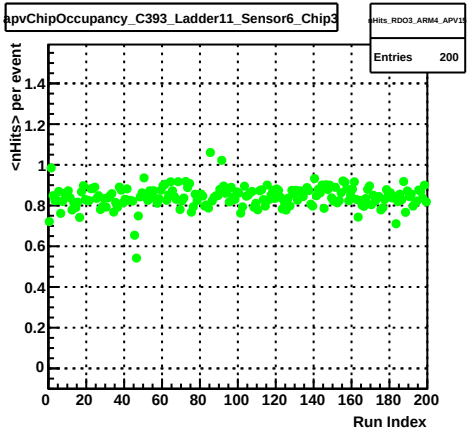
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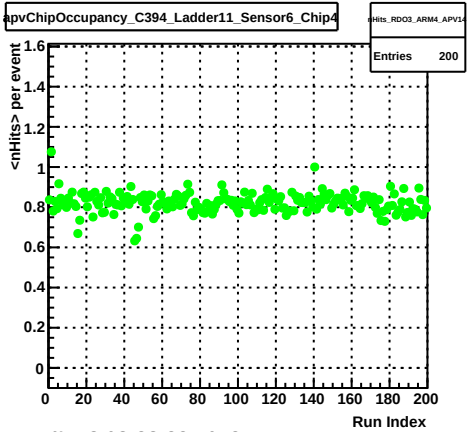
Run Index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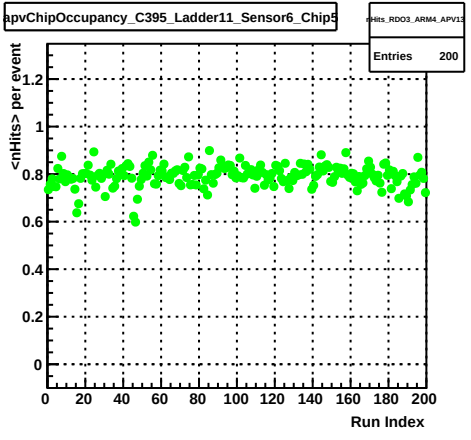
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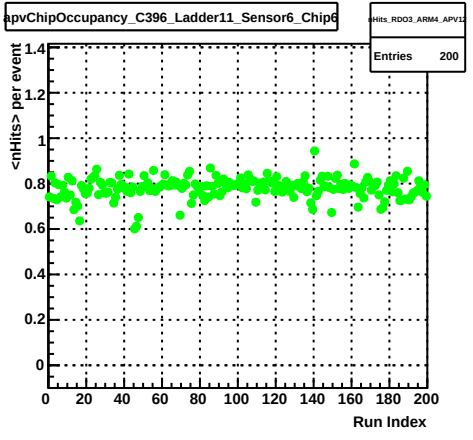
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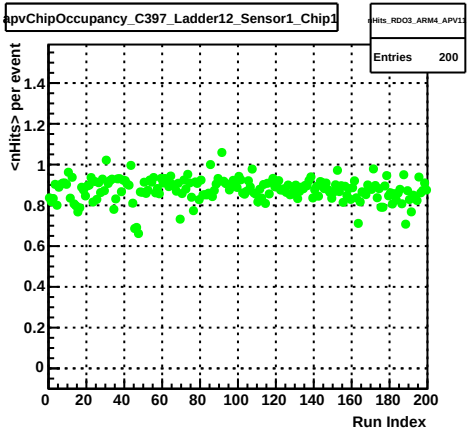
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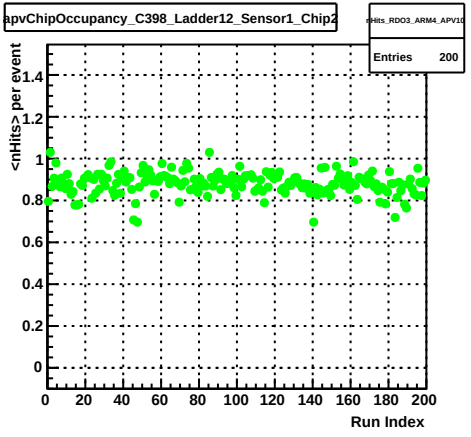
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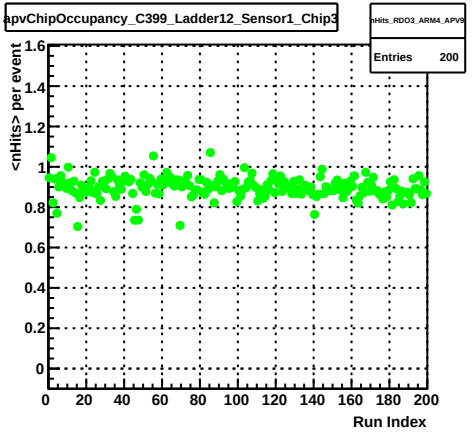
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Entries 200
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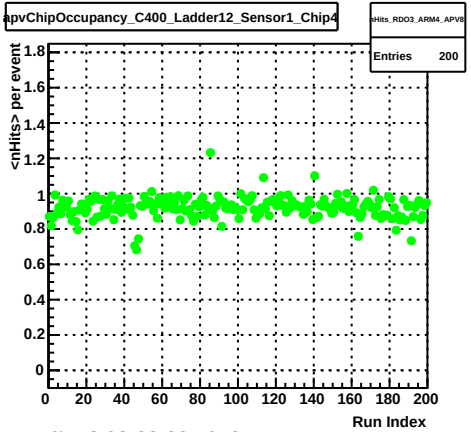
Run Index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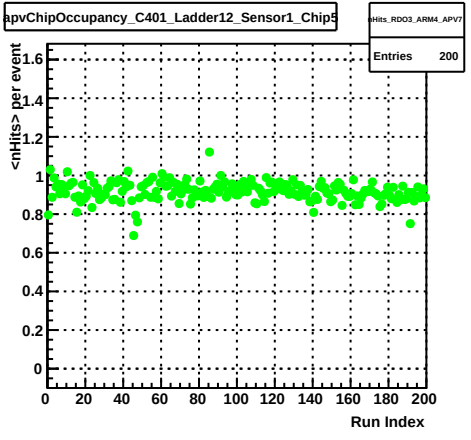
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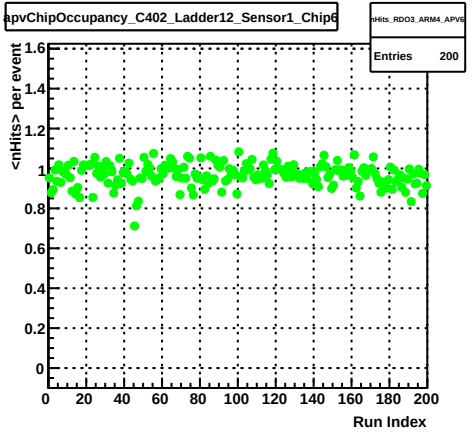
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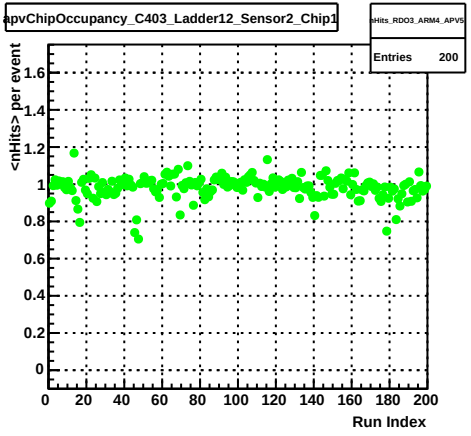
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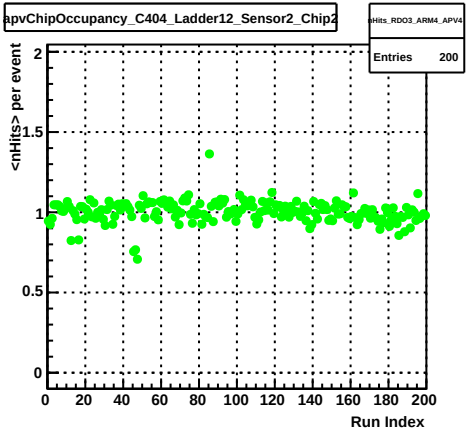
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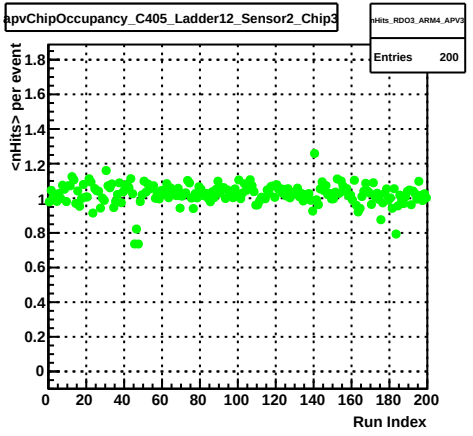
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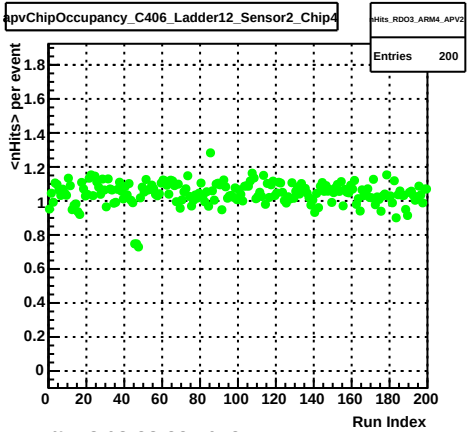
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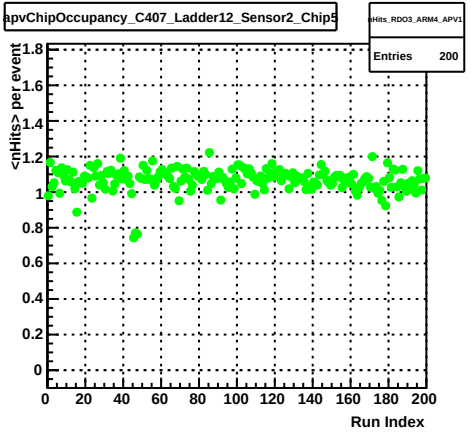
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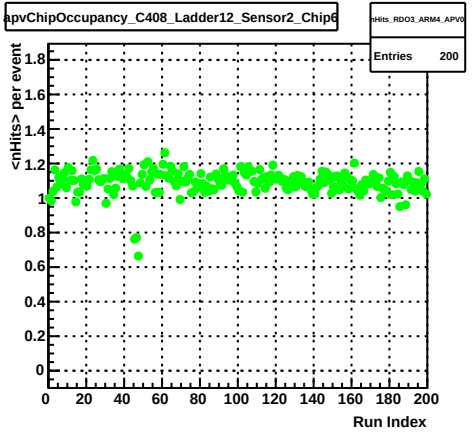
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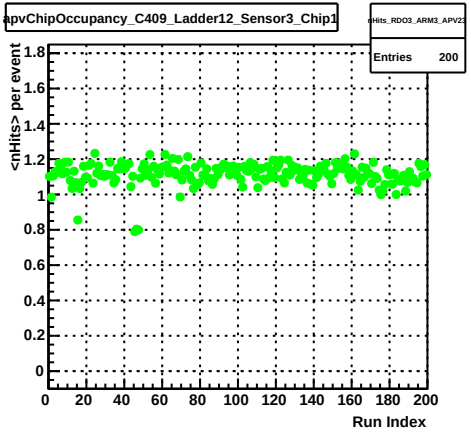
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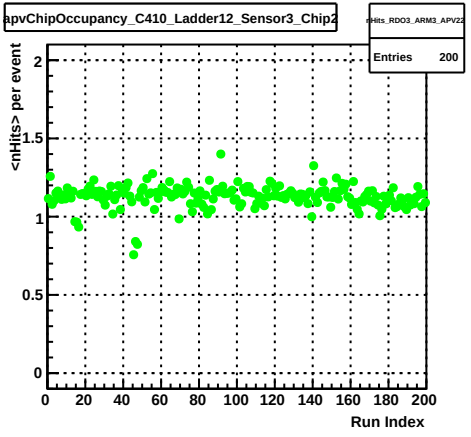
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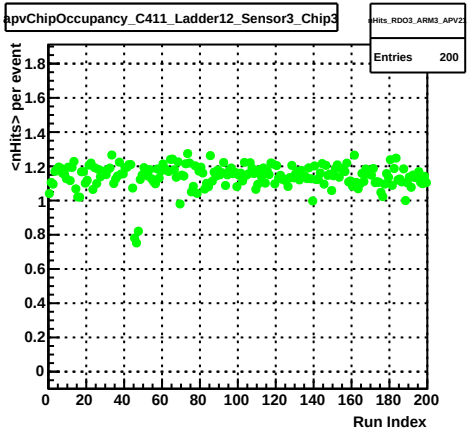
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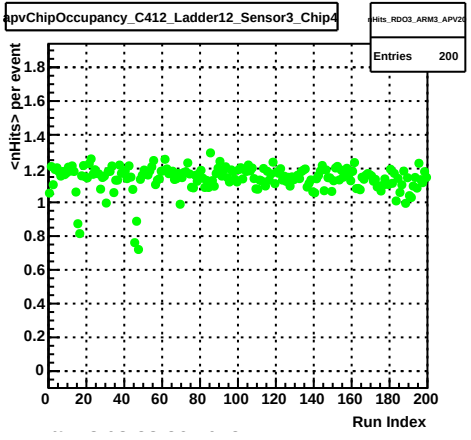
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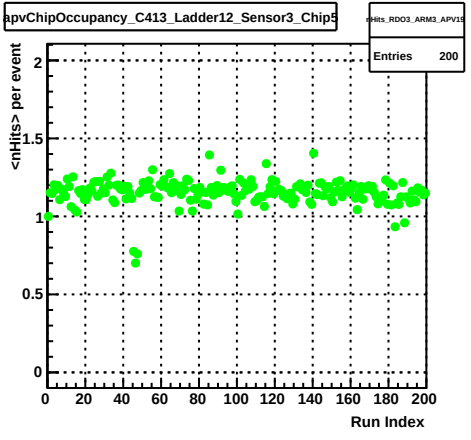
Run Index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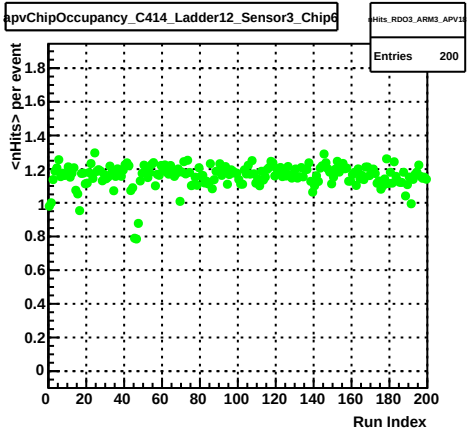
Run Index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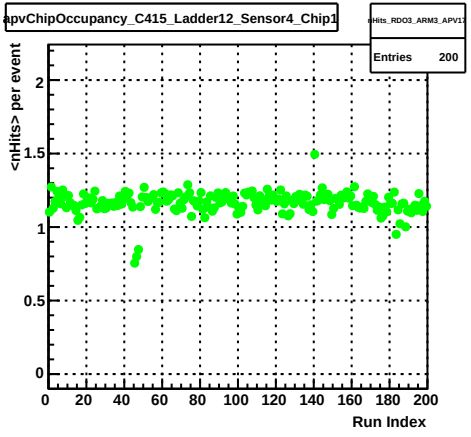
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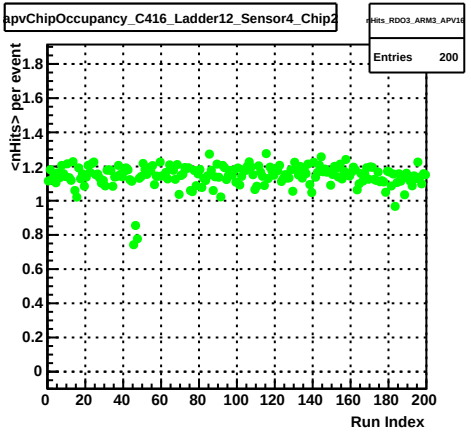
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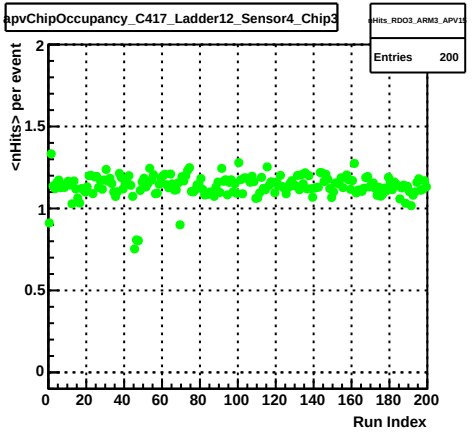
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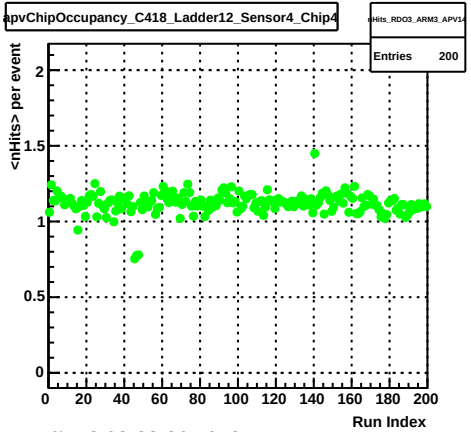
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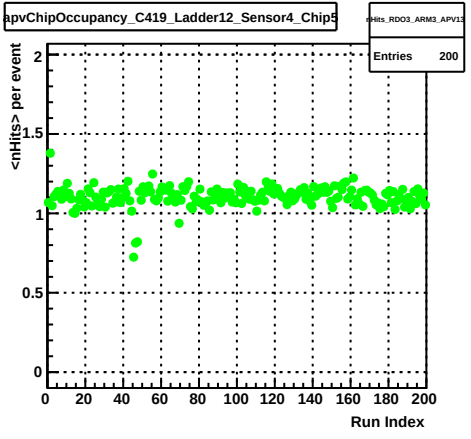
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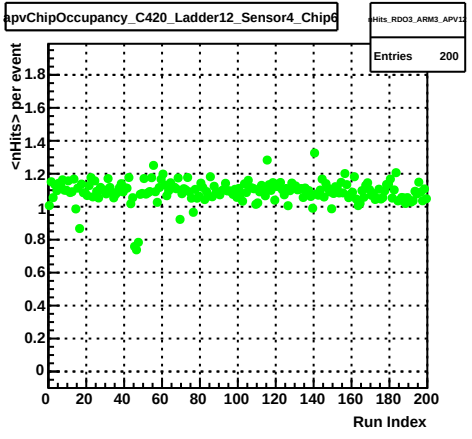
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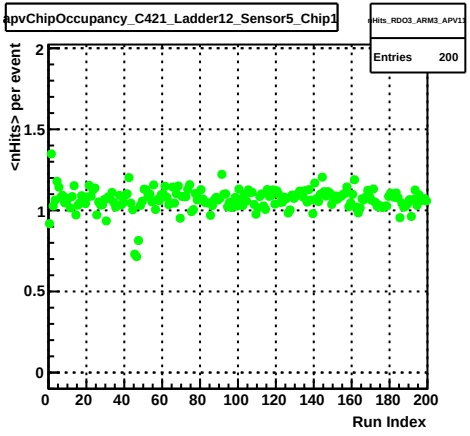
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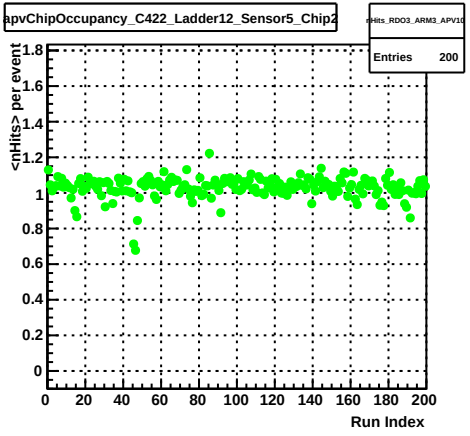
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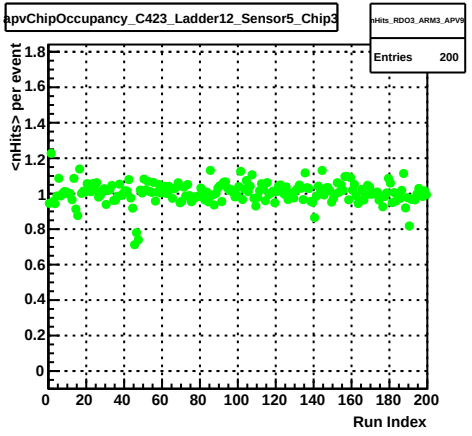
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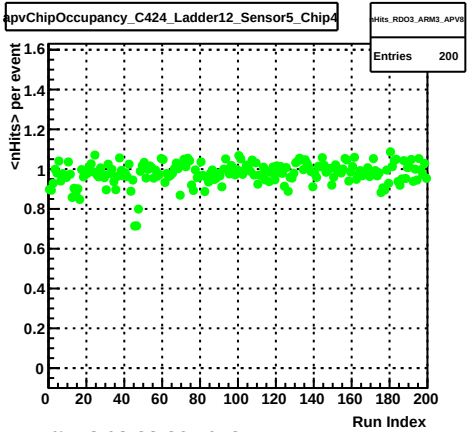
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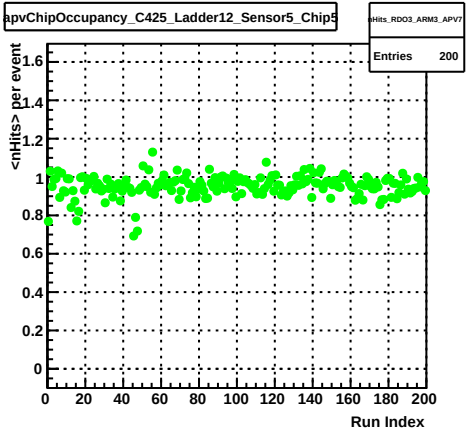
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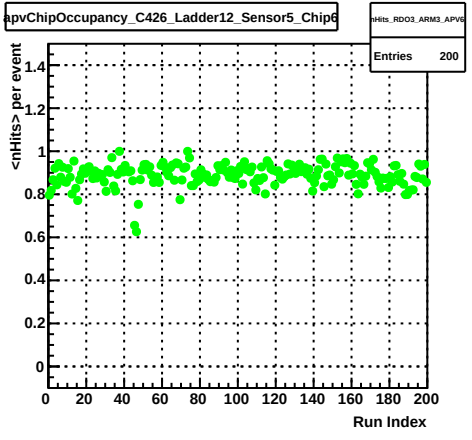
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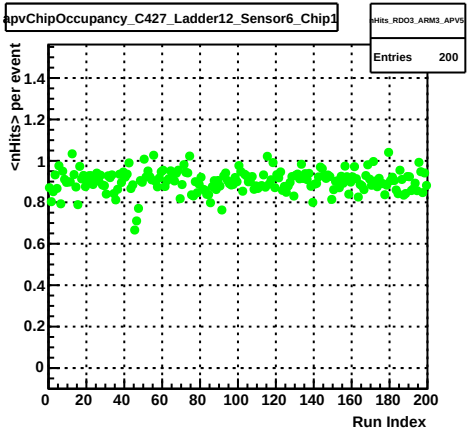
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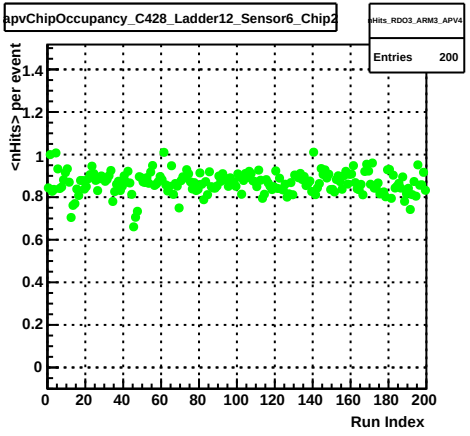
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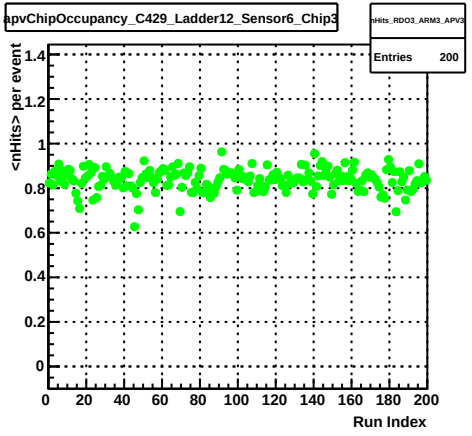
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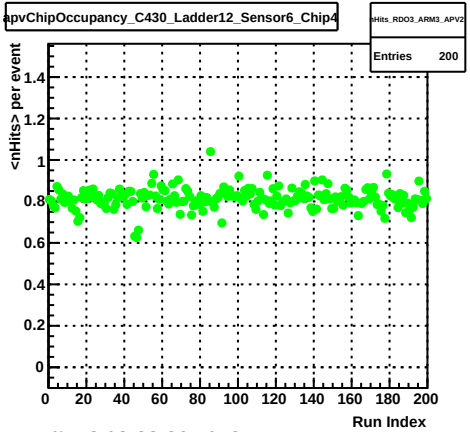
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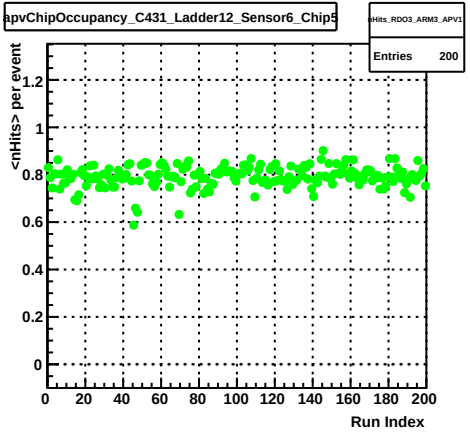
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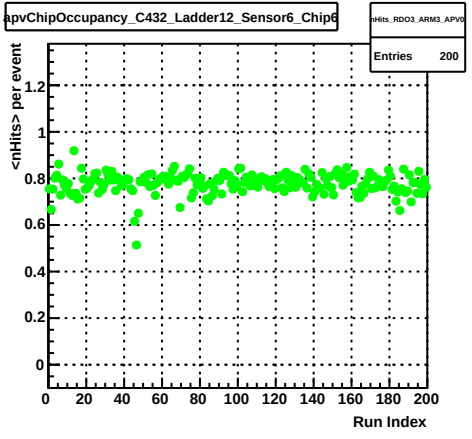
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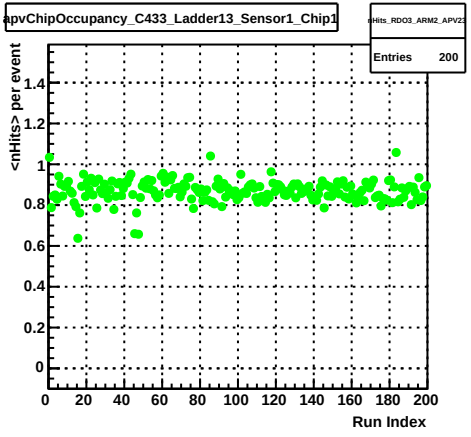
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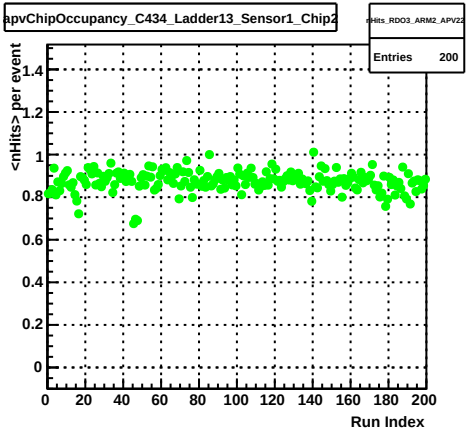
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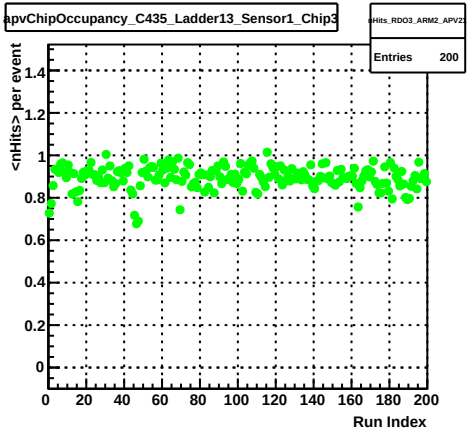
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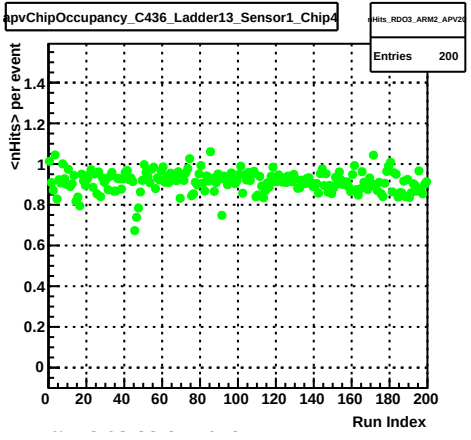
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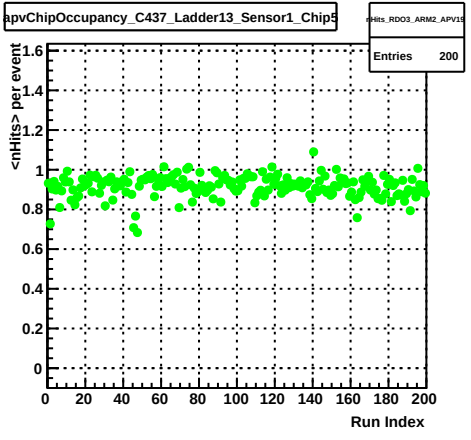
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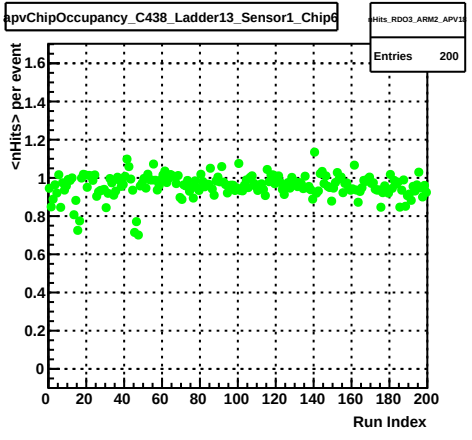
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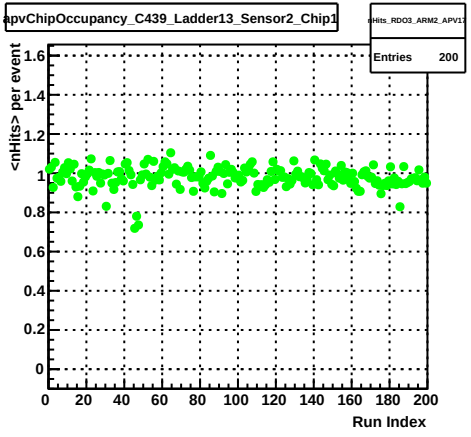
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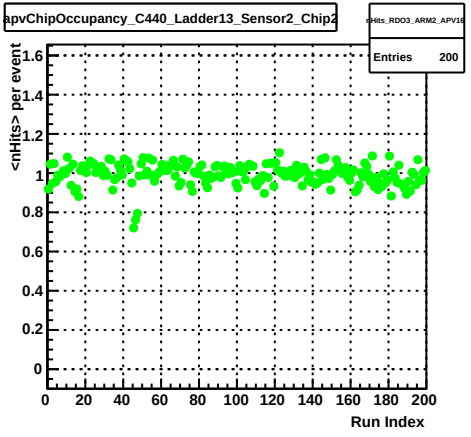
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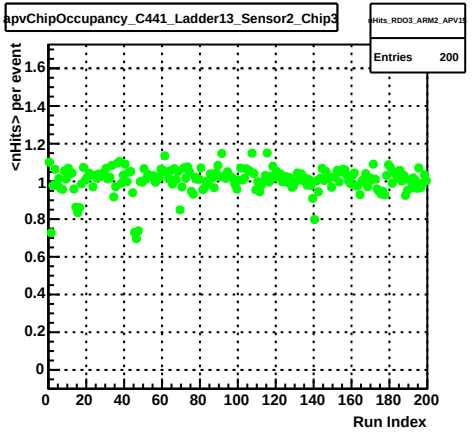
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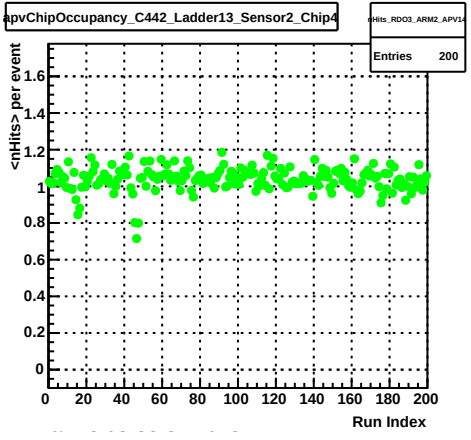
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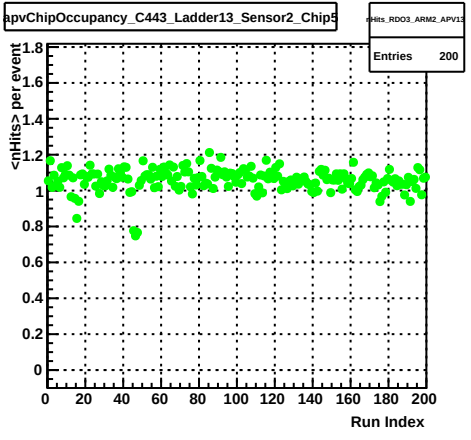
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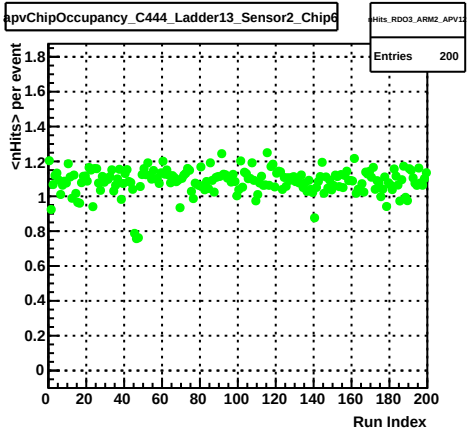
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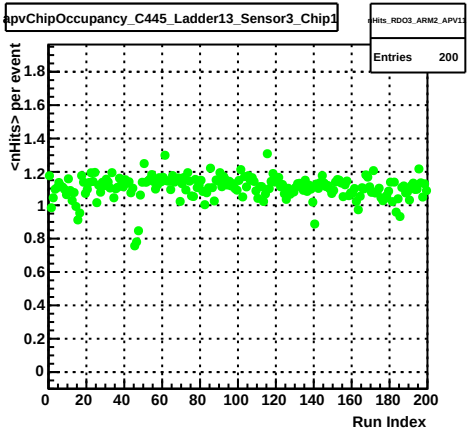
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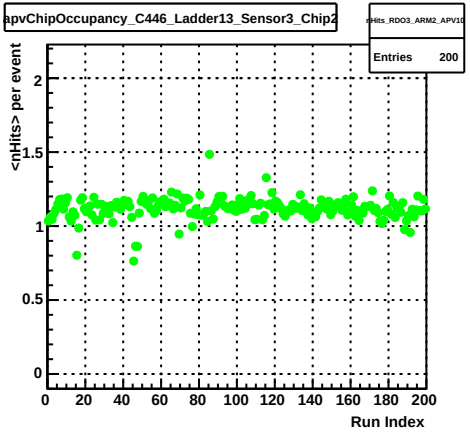
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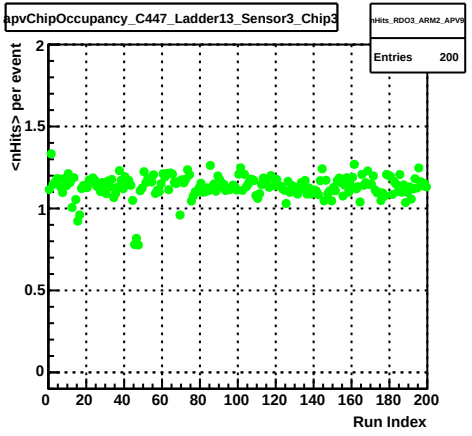
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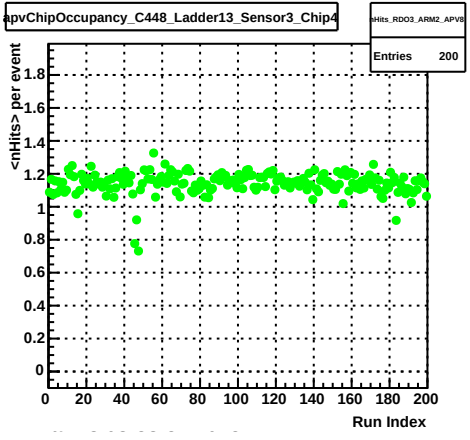
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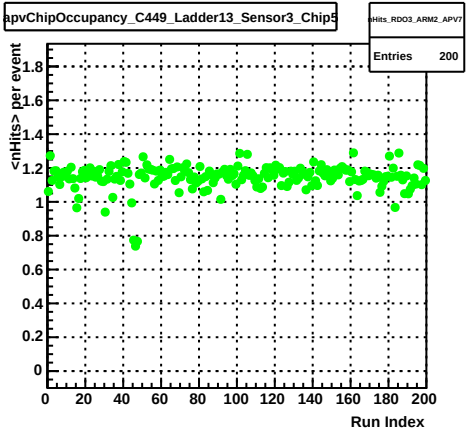
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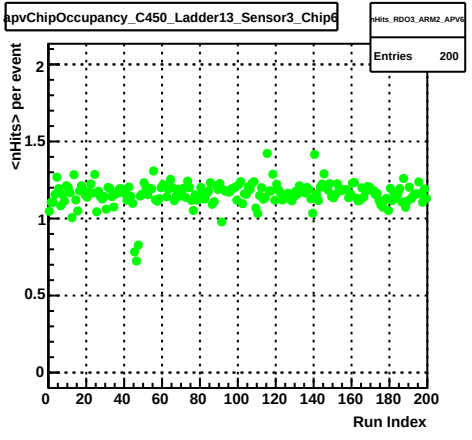
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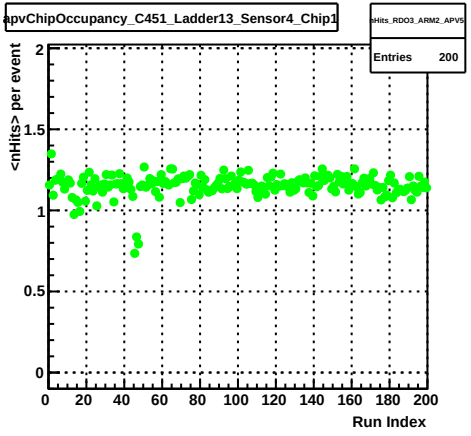
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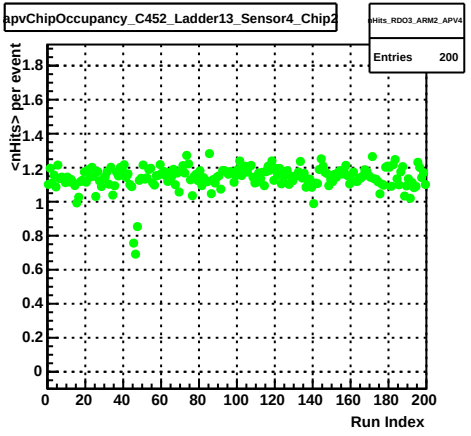
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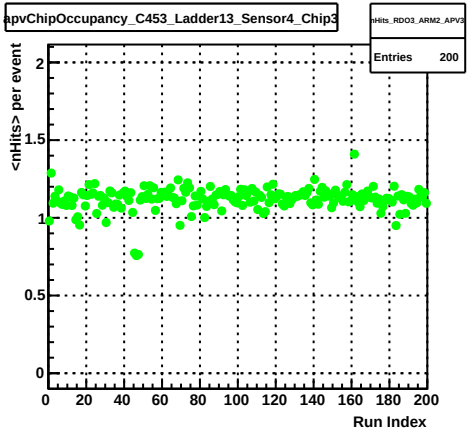
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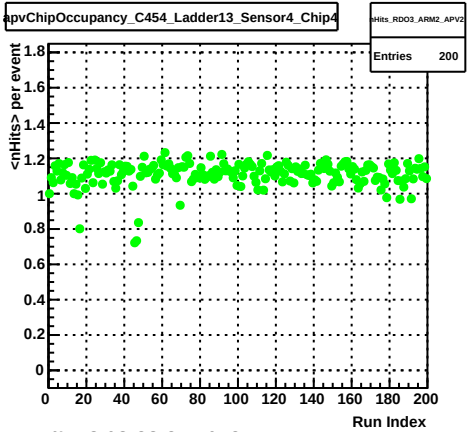
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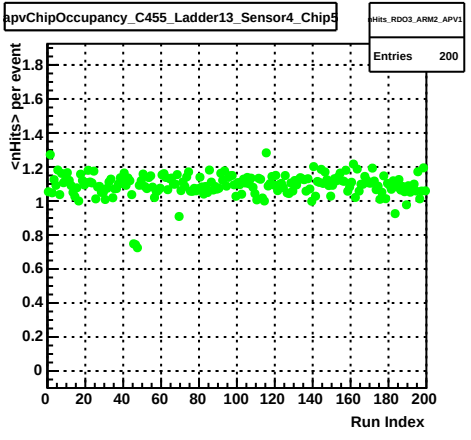
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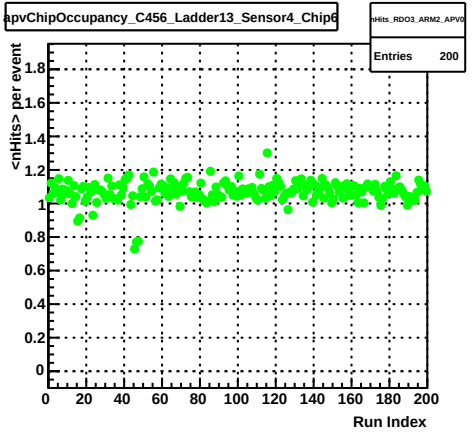
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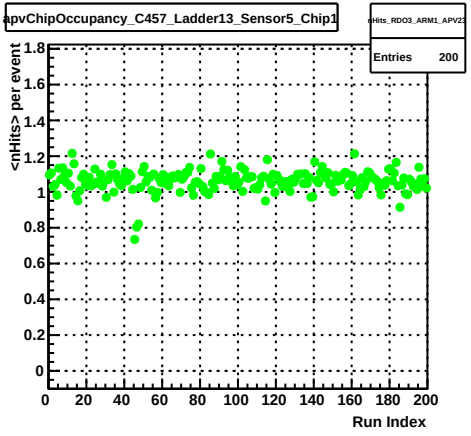
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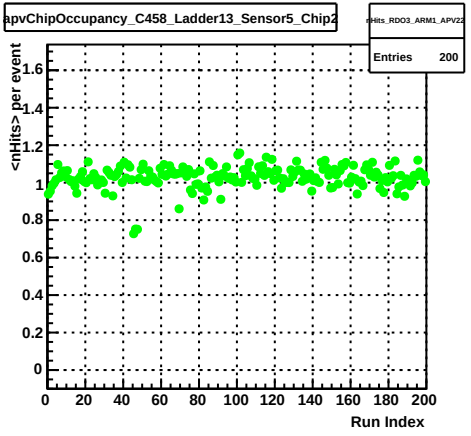
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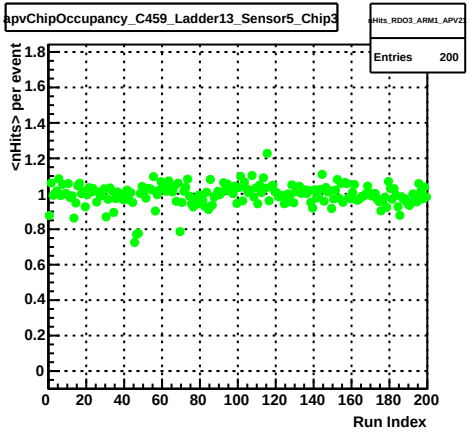
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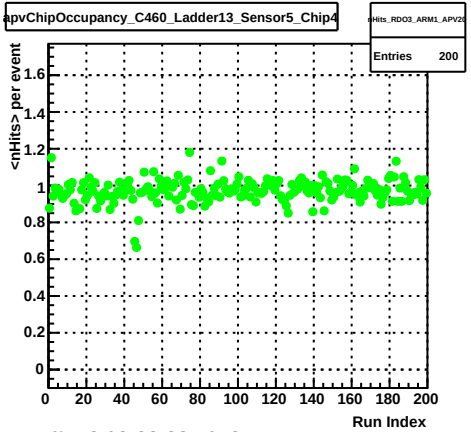
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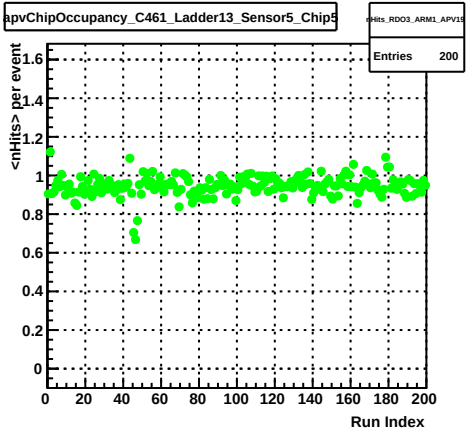
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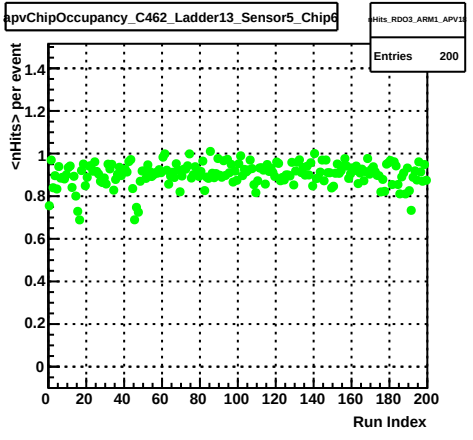
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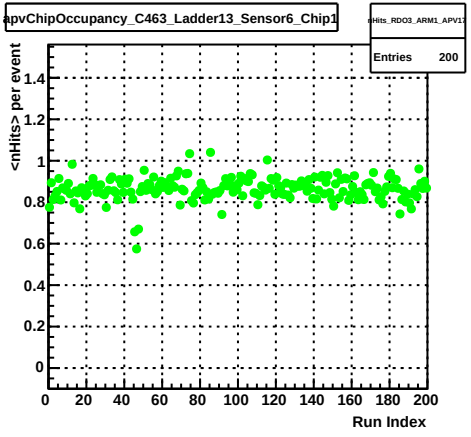
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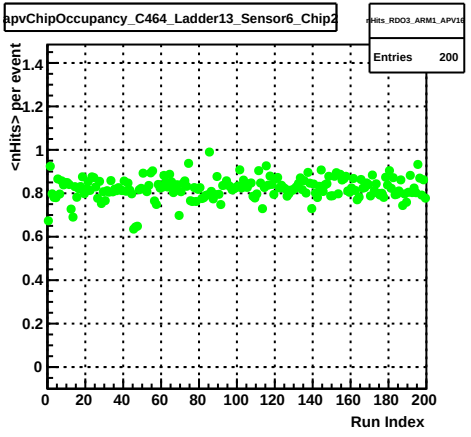
Run Index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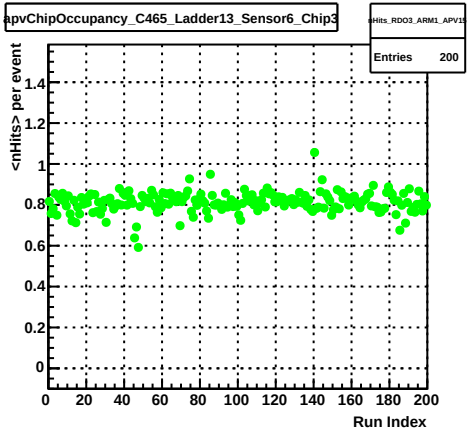
Run Index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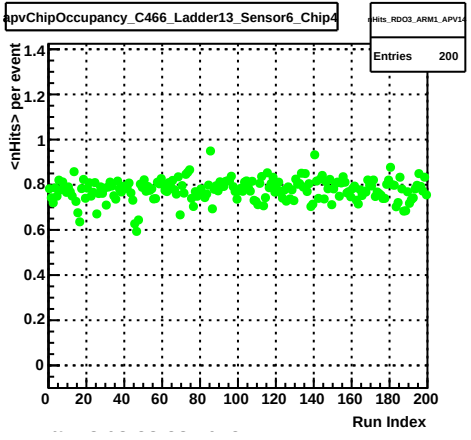
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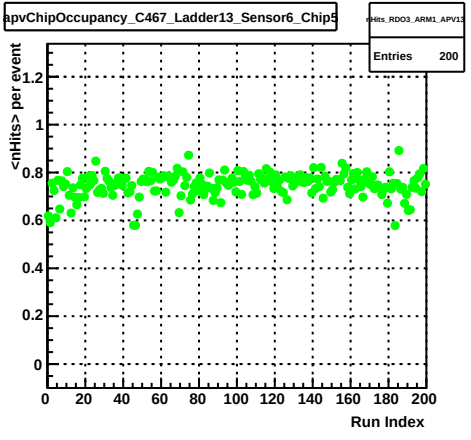
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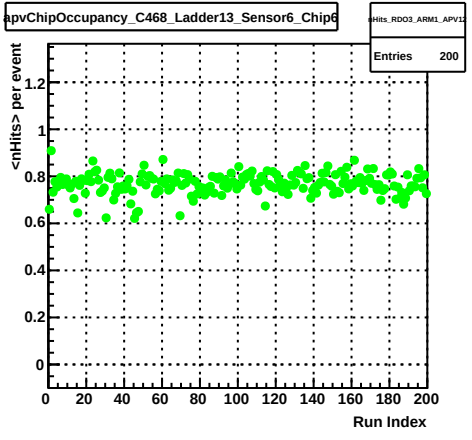
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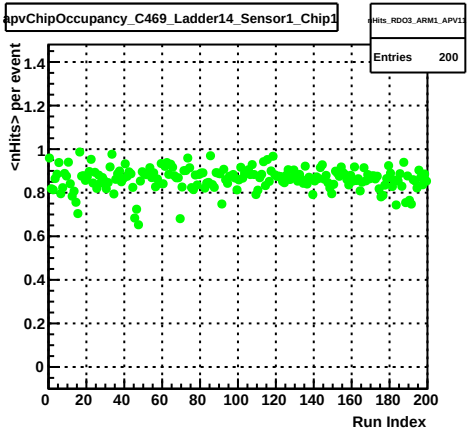
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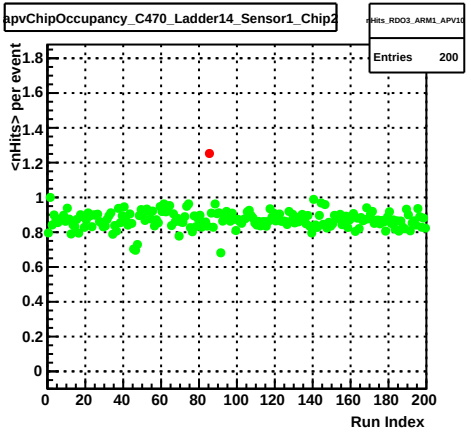
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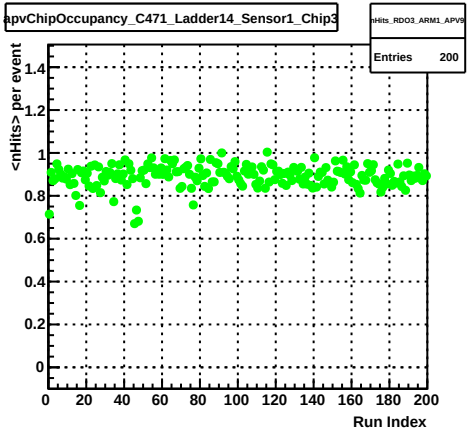
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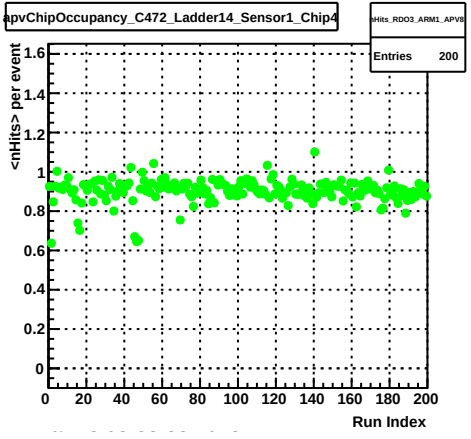
Run Index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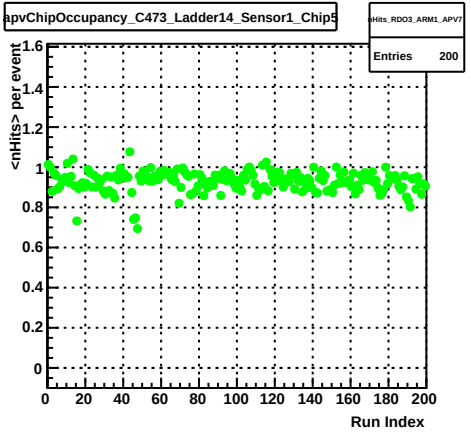
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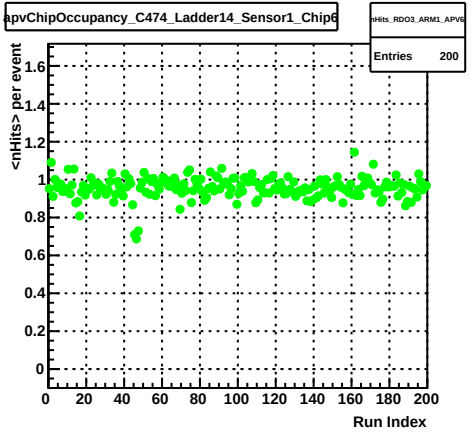
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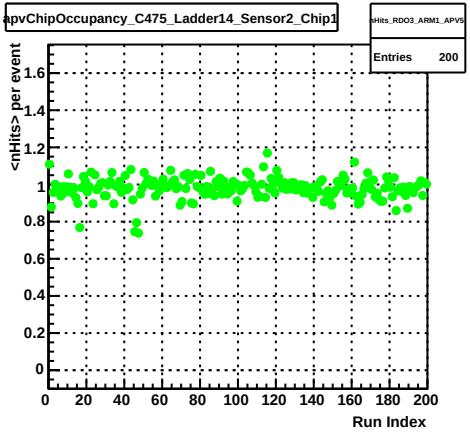
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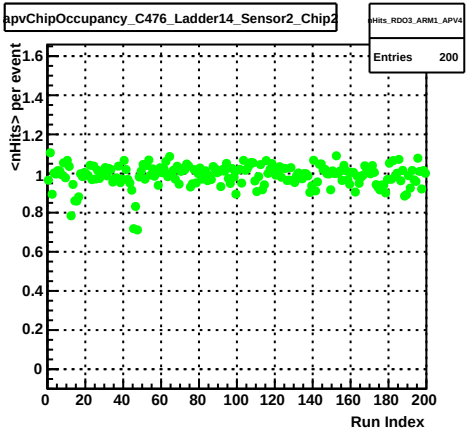
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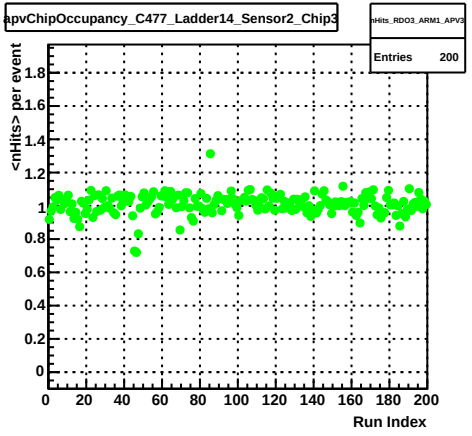
Run Index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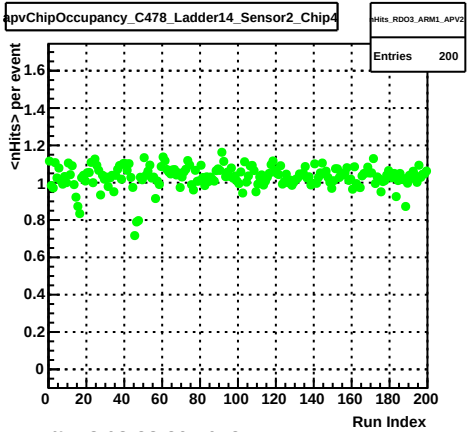
Run Index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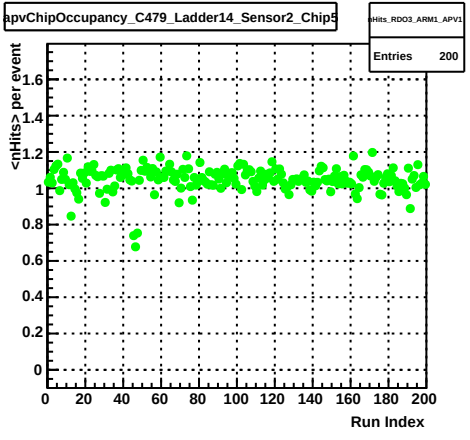
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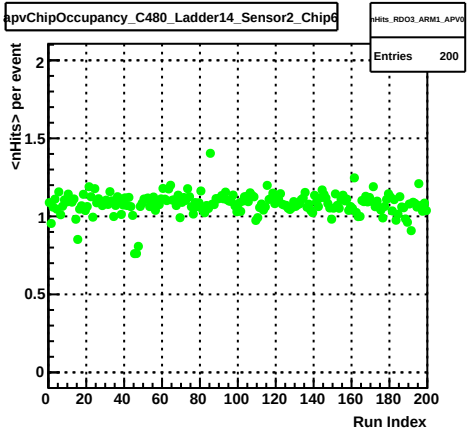
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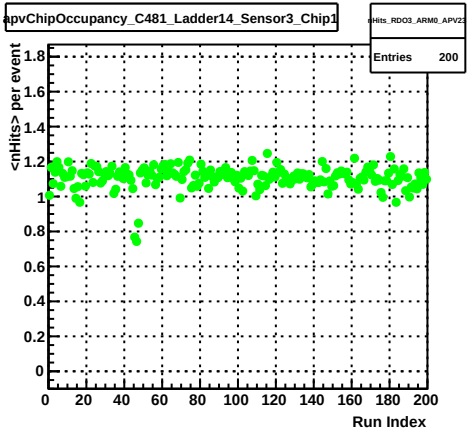
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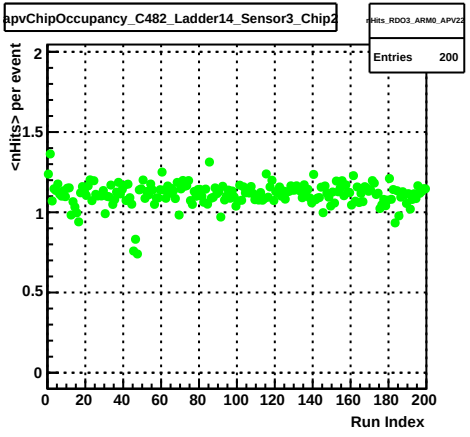
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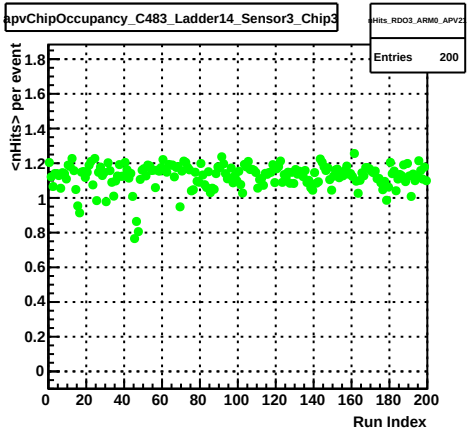
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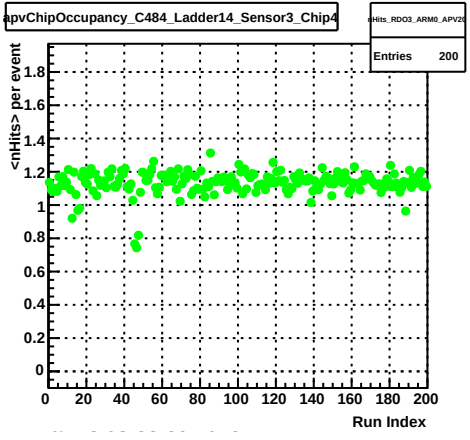
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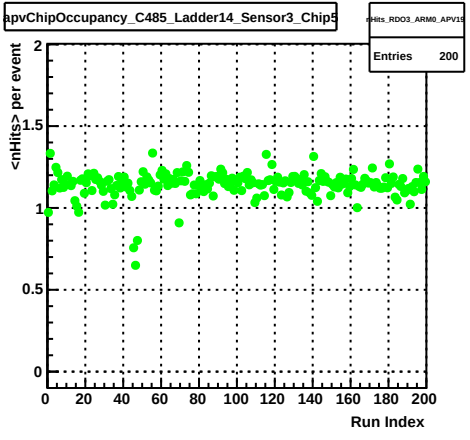
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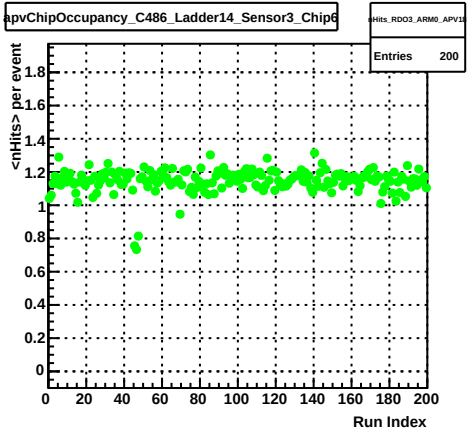
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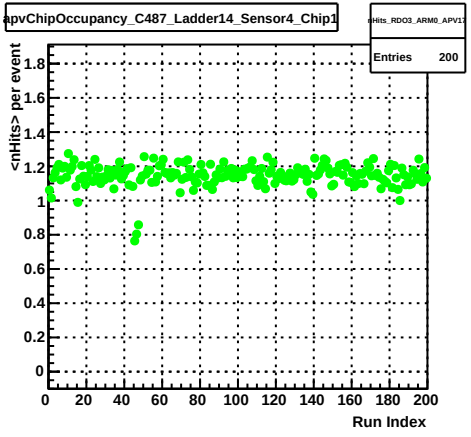
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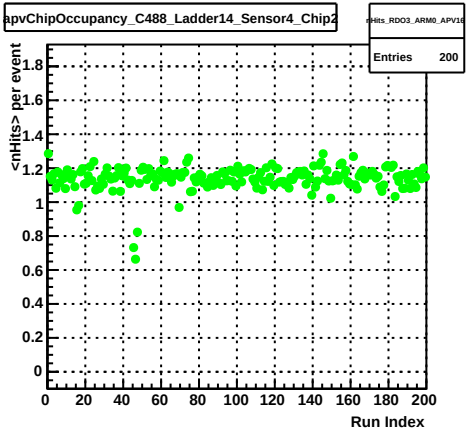
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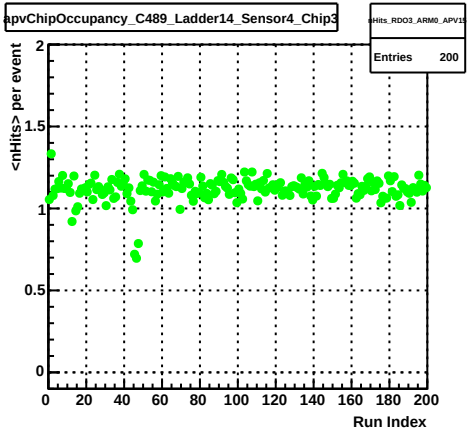
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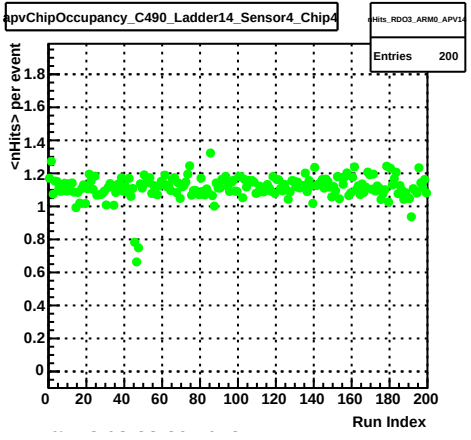
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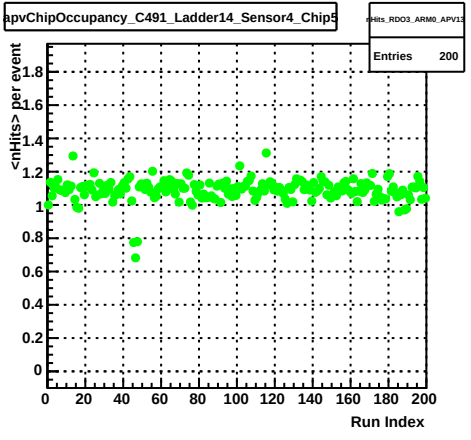
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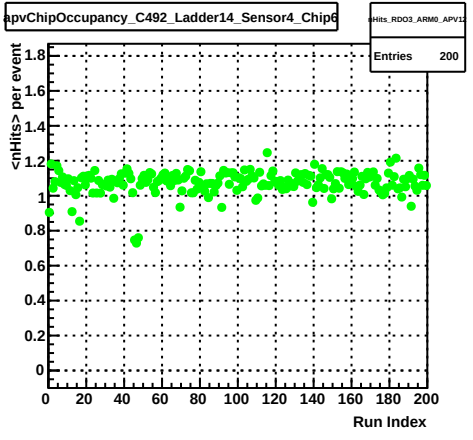
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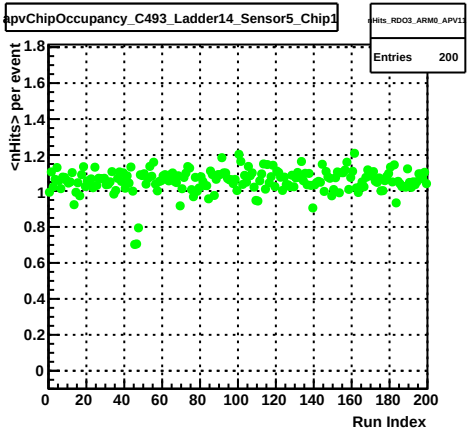
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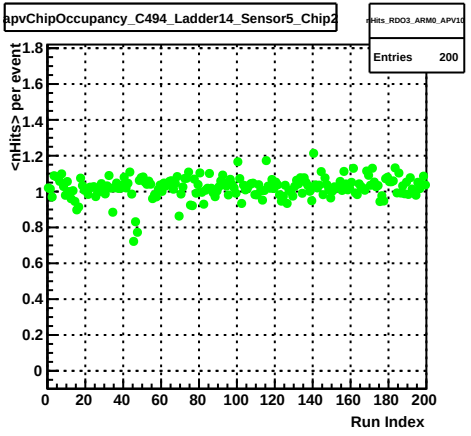
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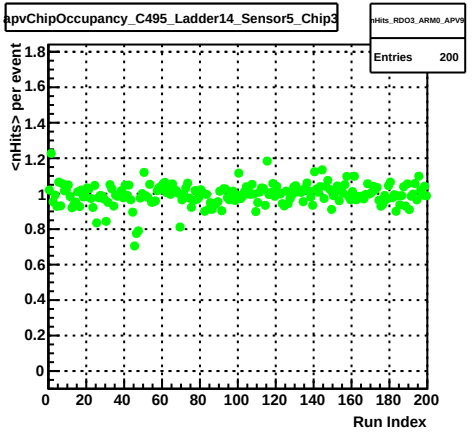
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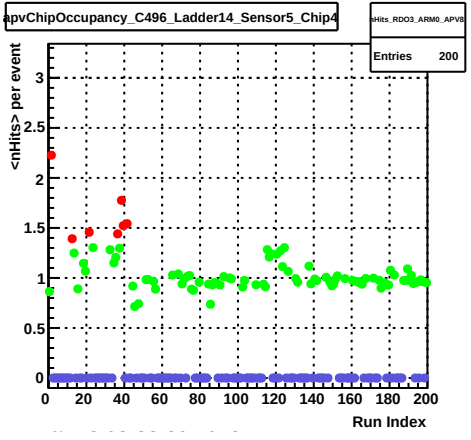
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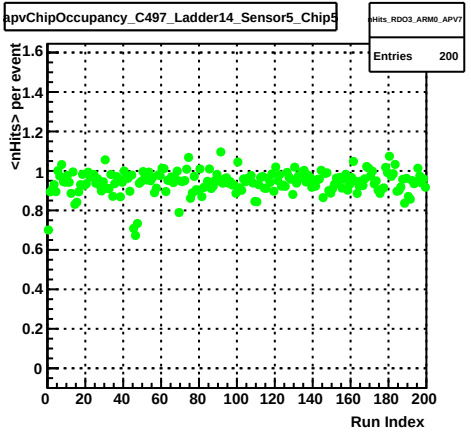
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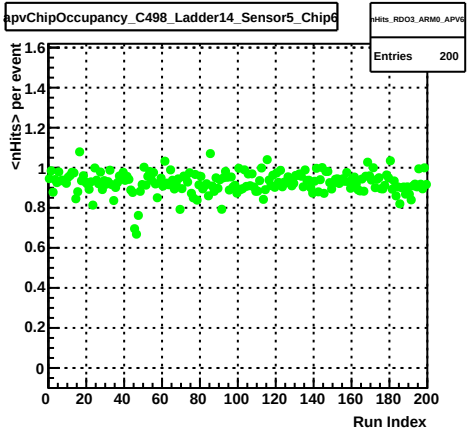
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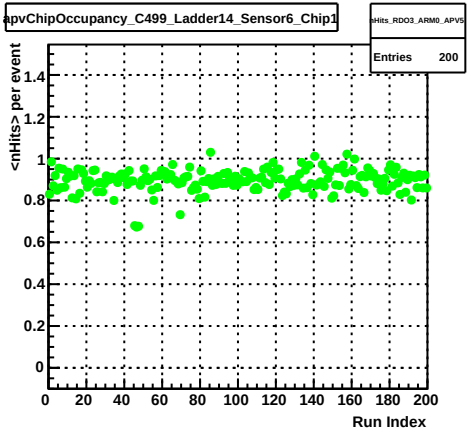
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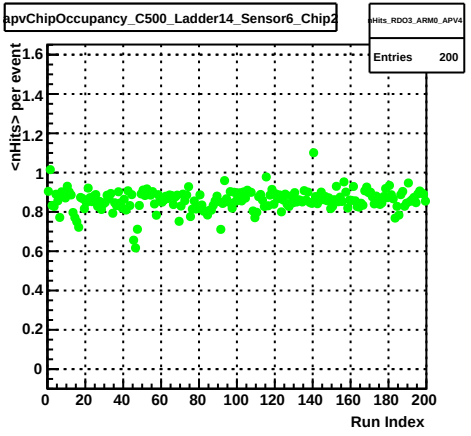
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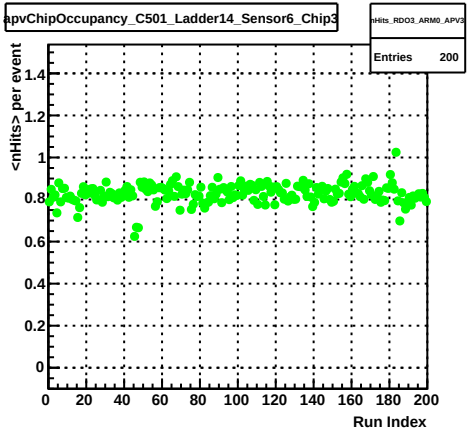
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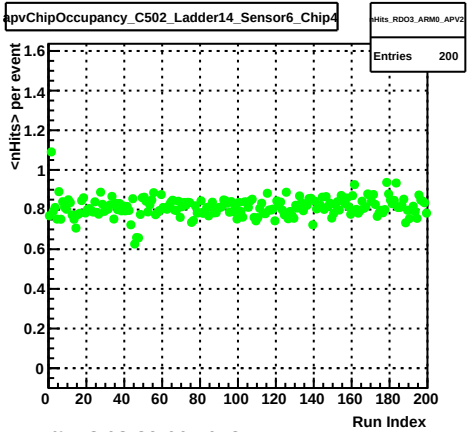
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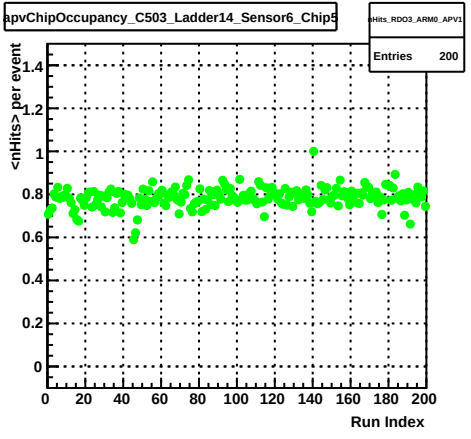
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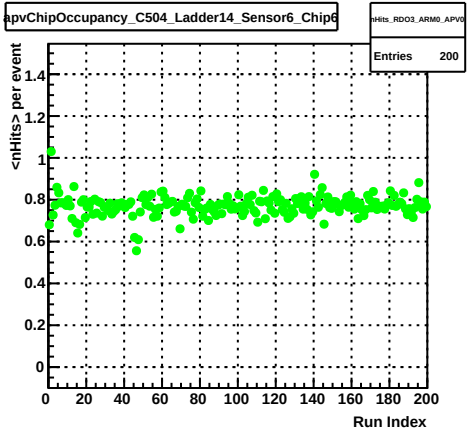
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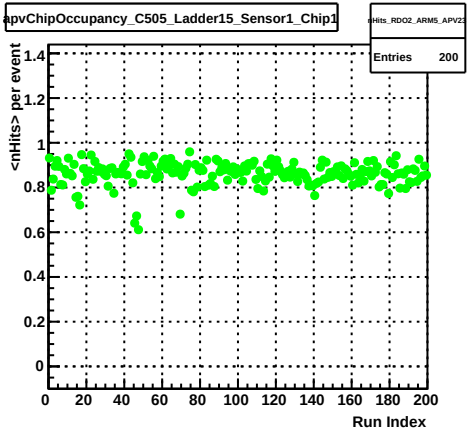
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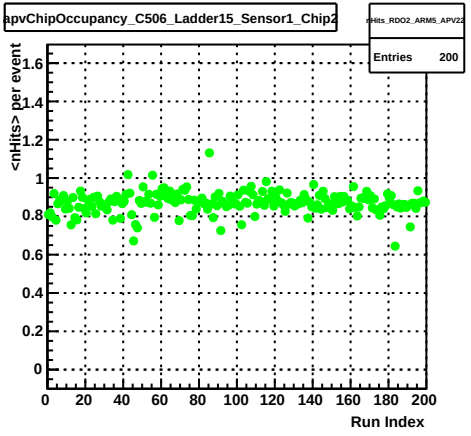
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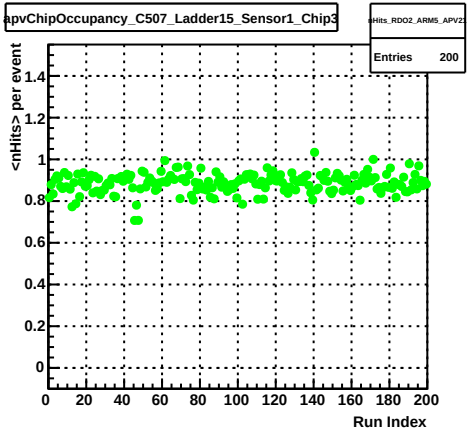
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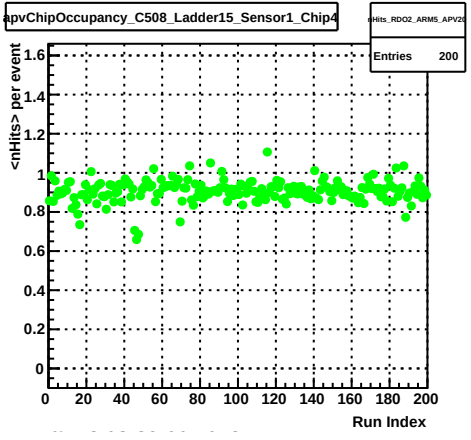
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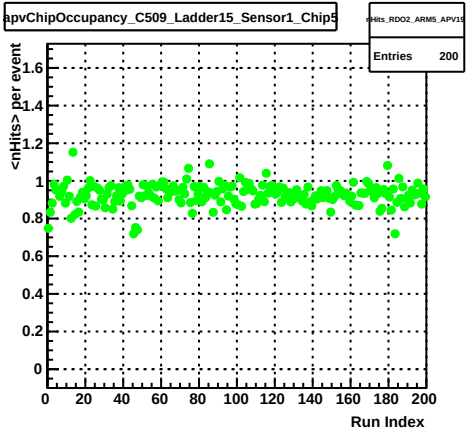
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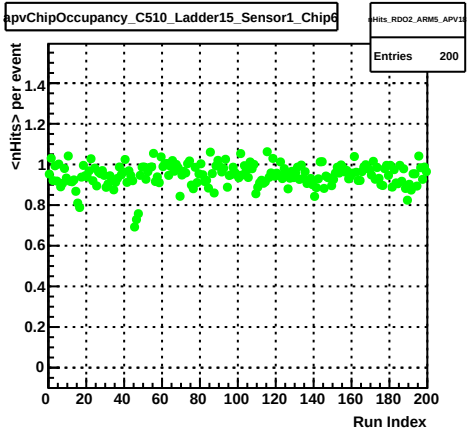
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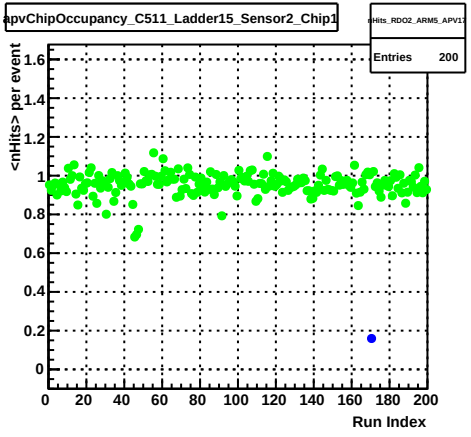
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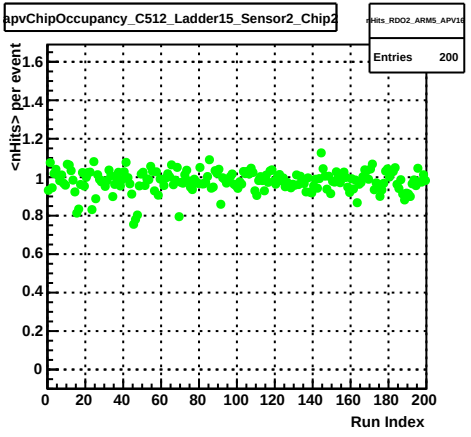
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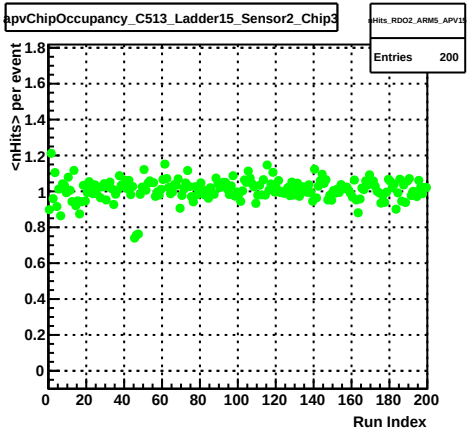
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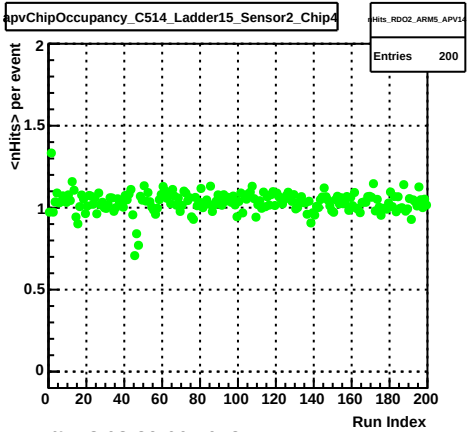
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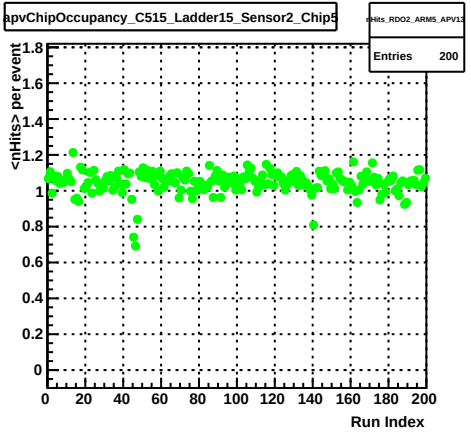
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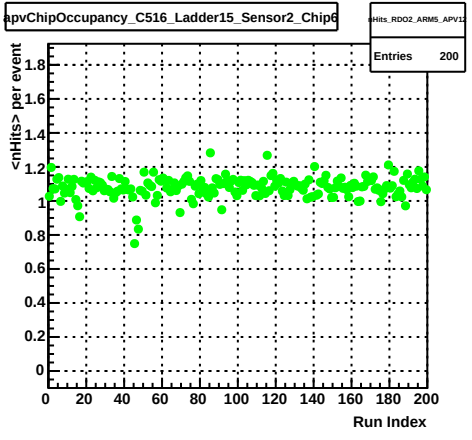
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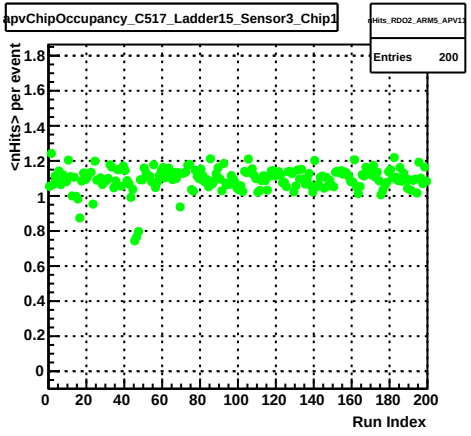
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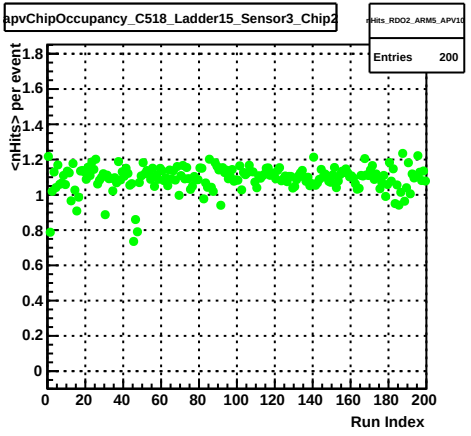
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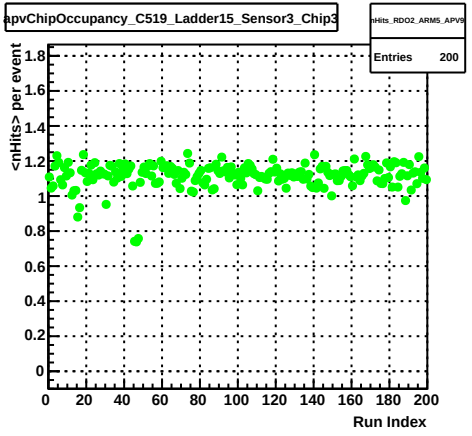
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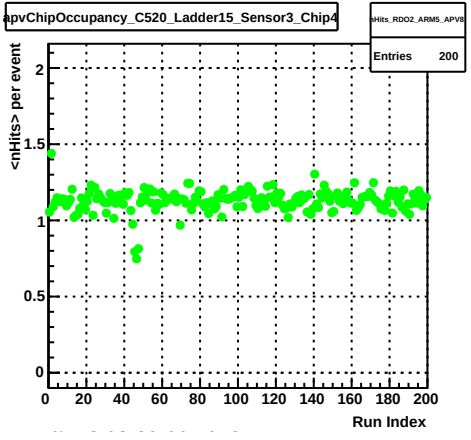
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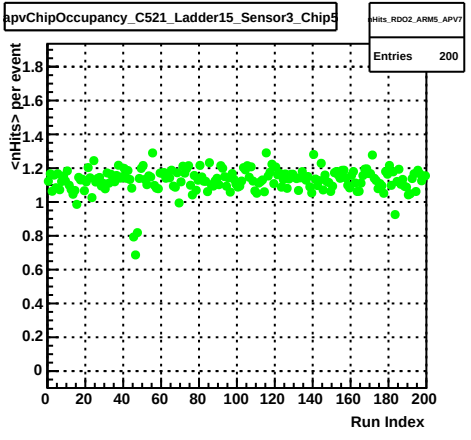
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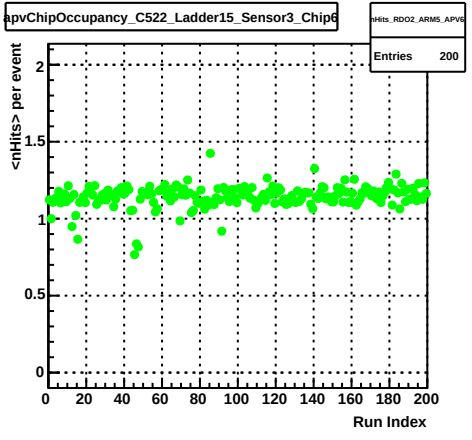
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Entries 200
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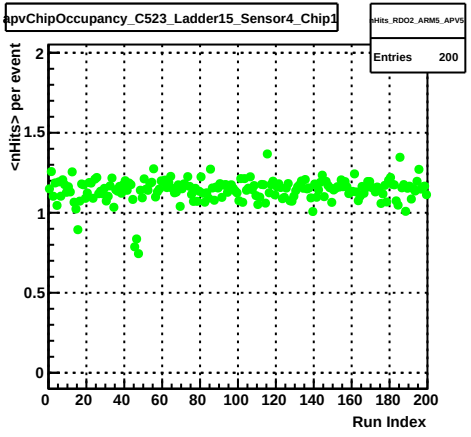
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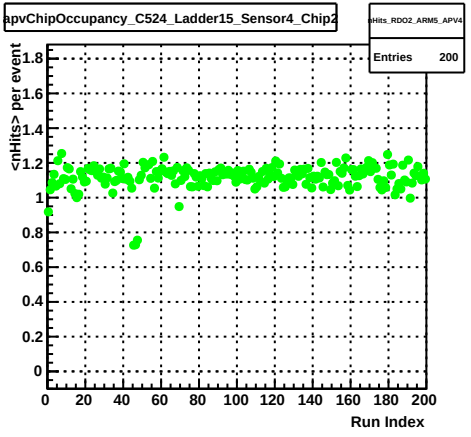
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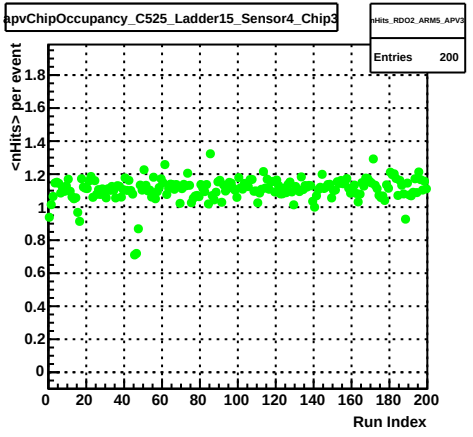
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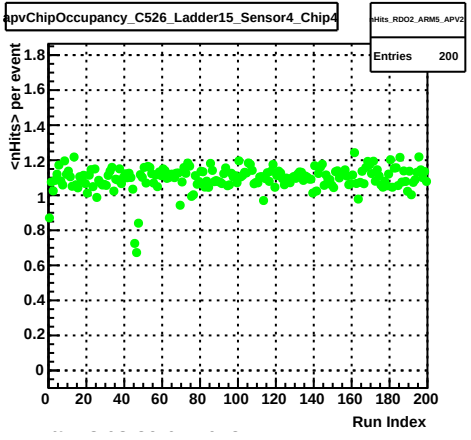
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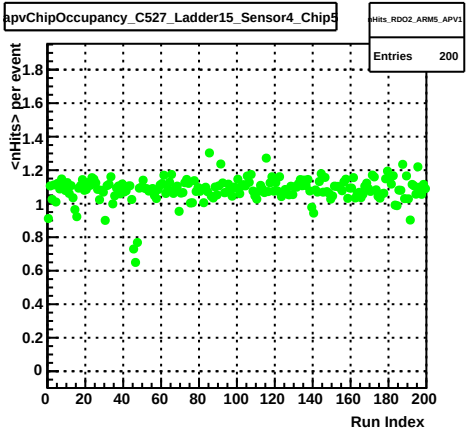
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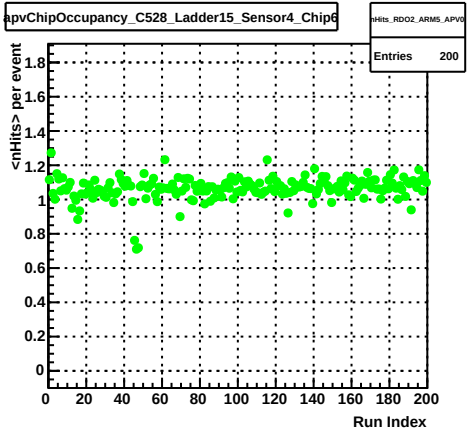
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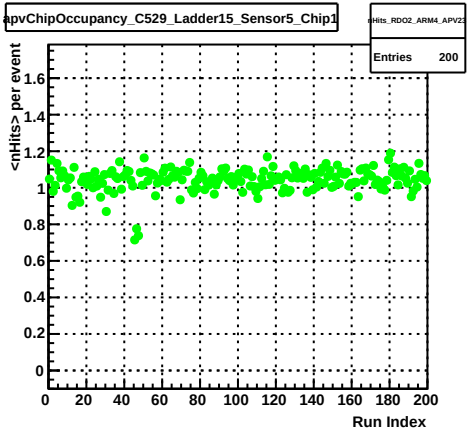
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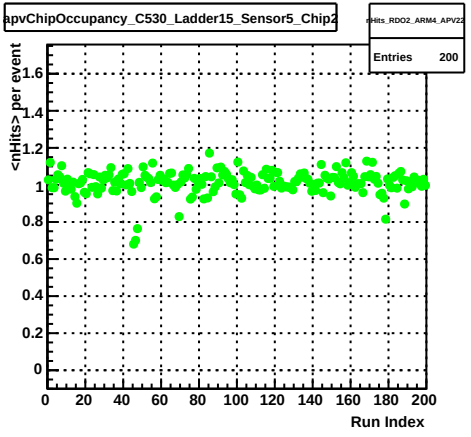
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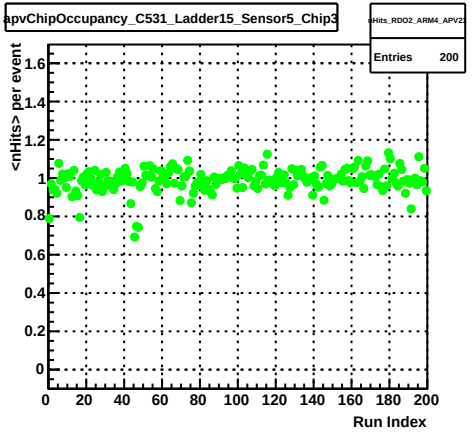
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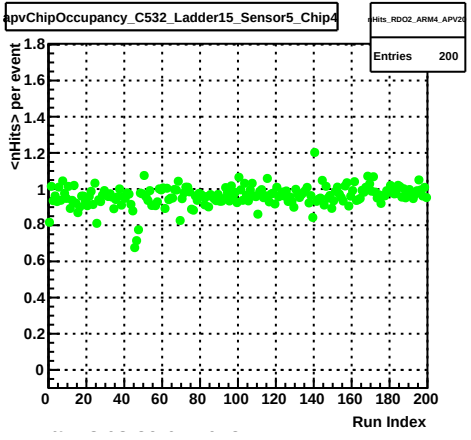
Run Index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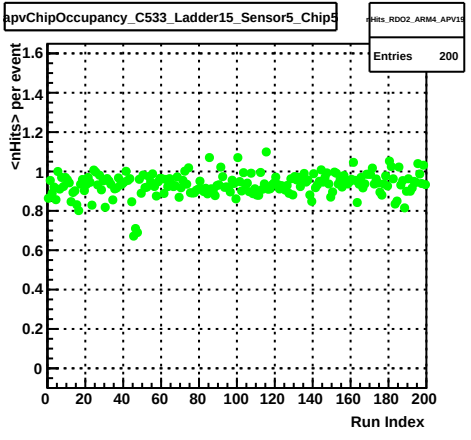
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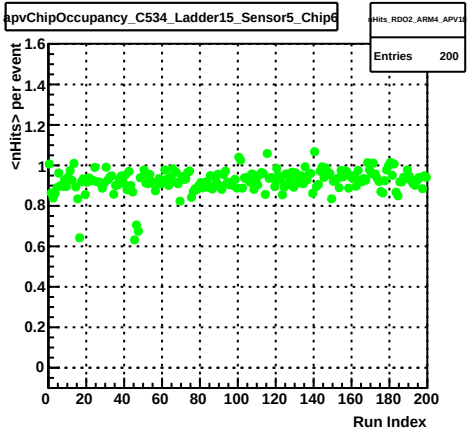
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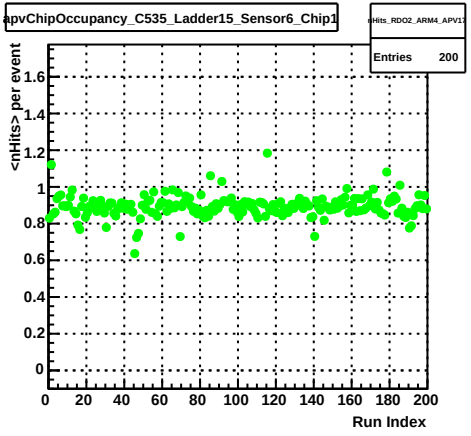
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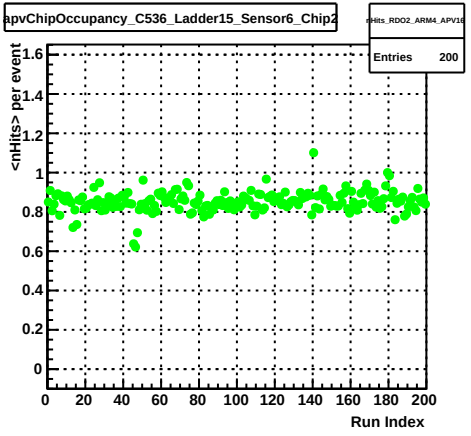
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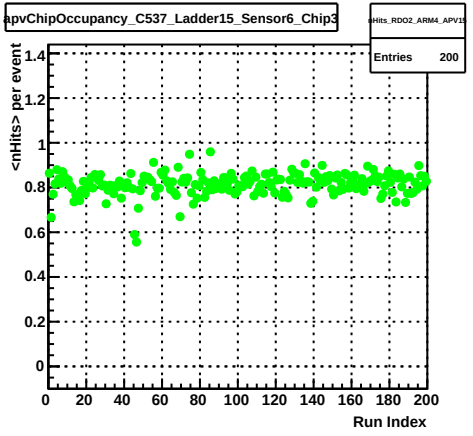
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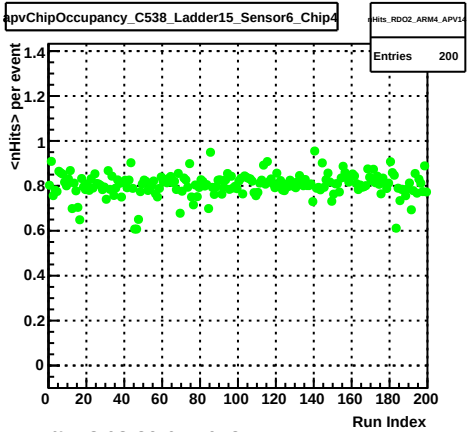
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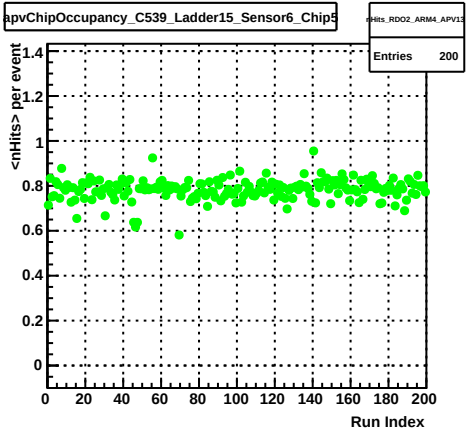
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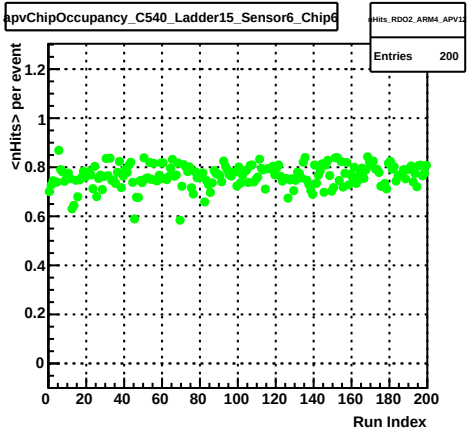
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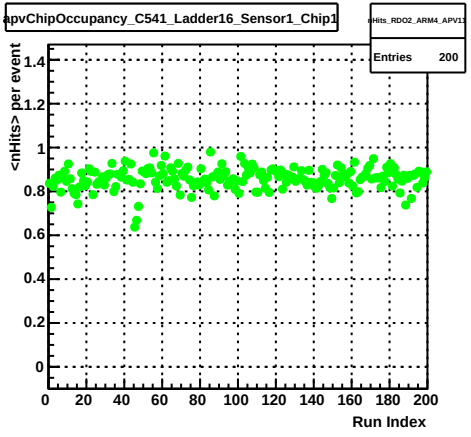
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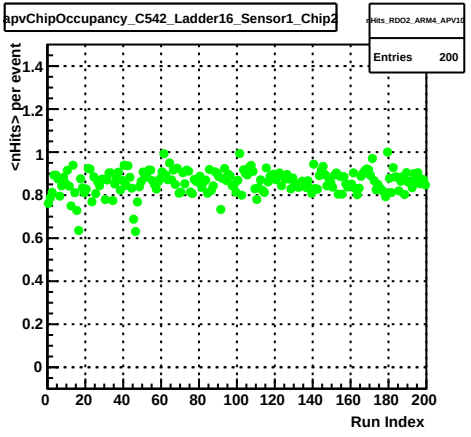
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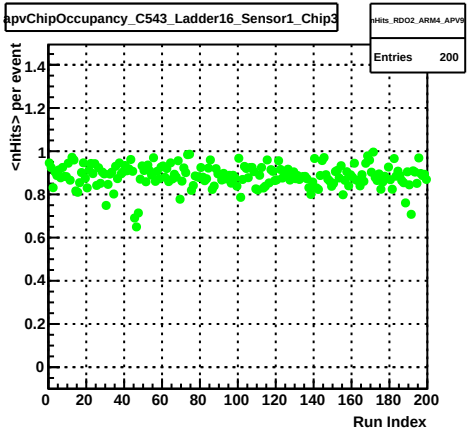
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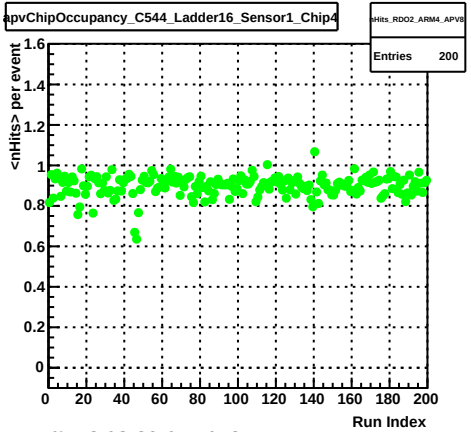
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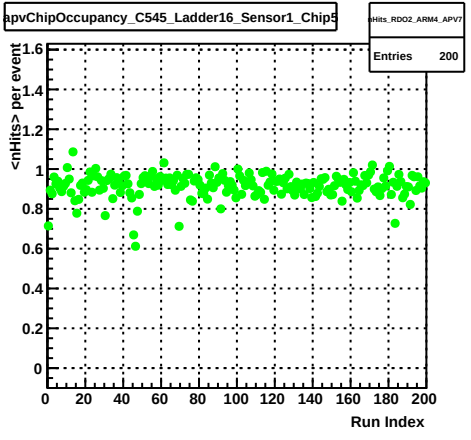
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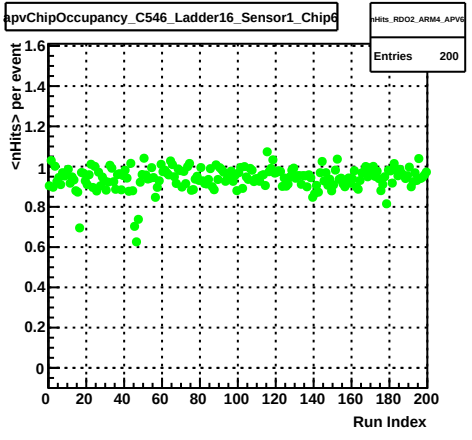
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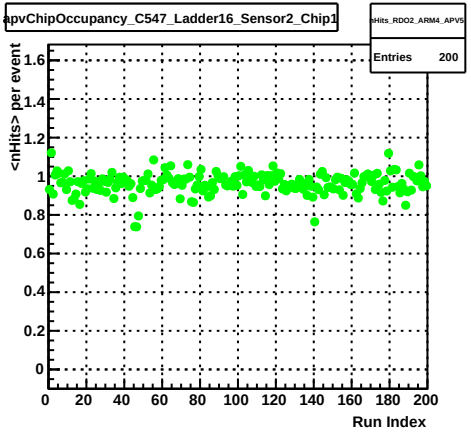
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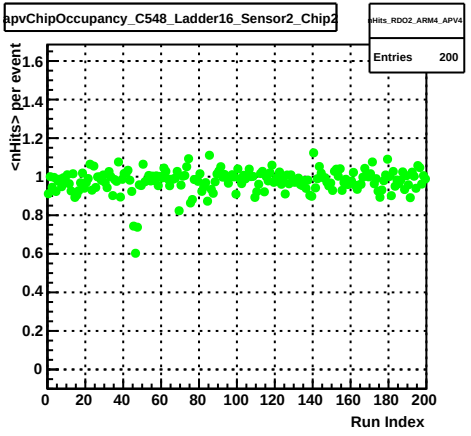
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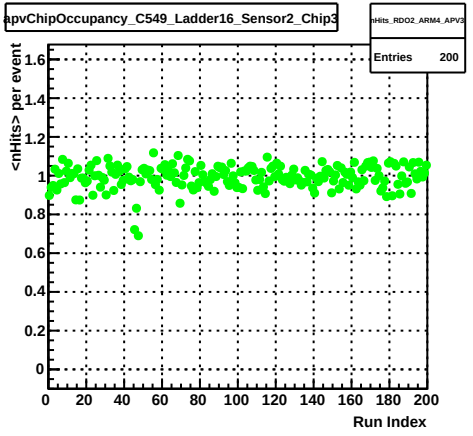
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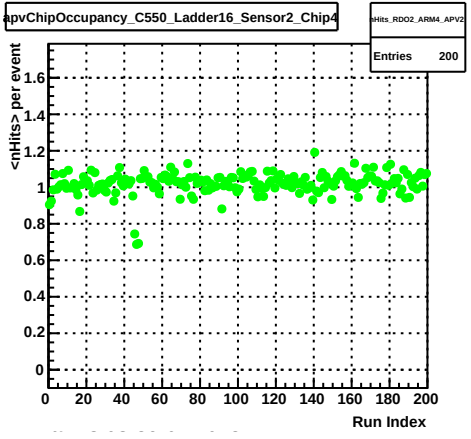
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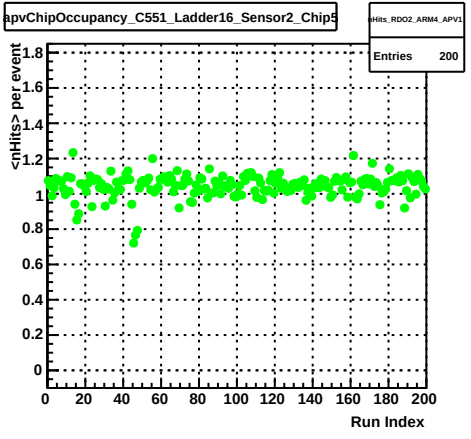
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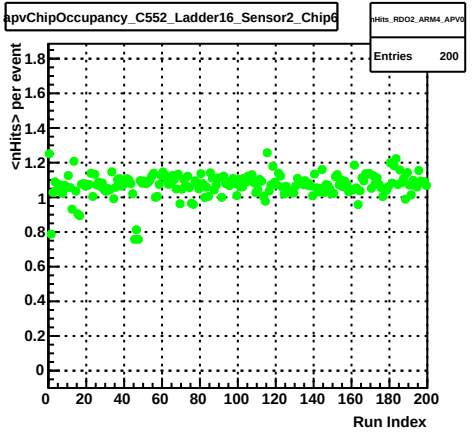
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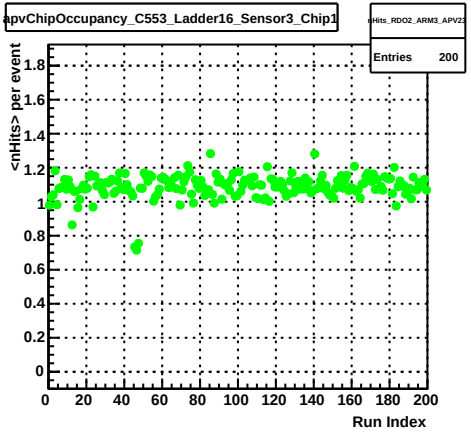
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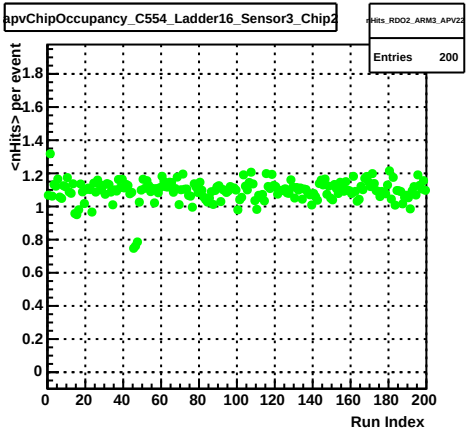
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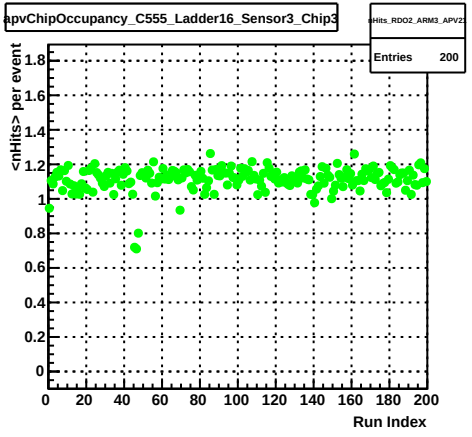
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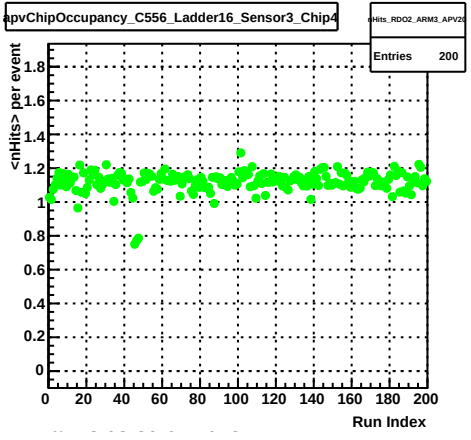
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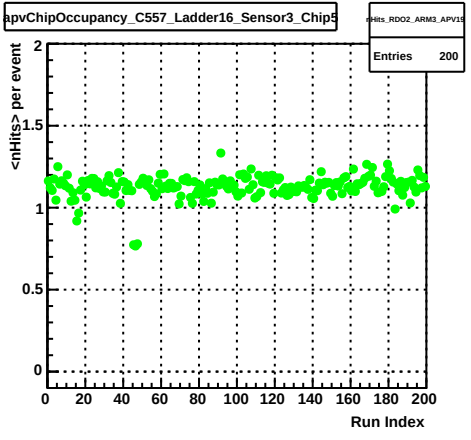
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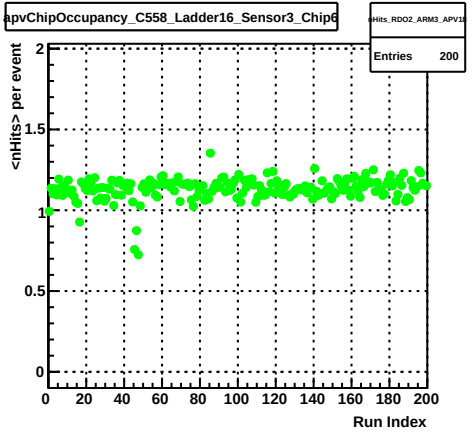
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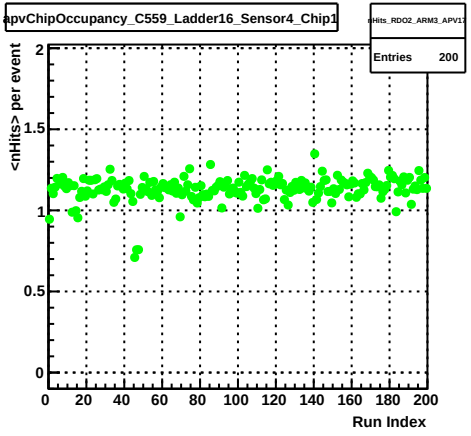
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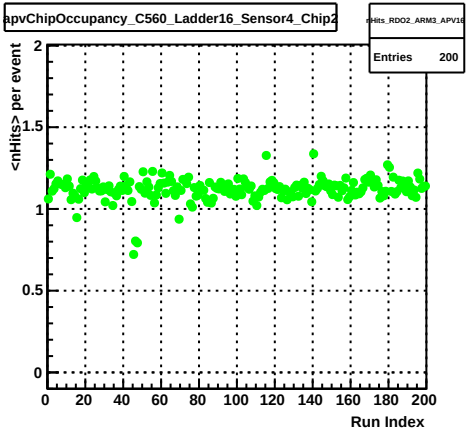
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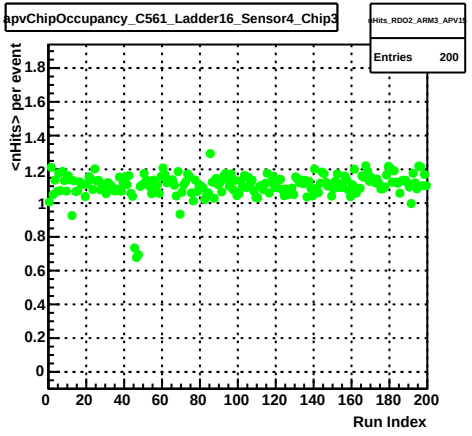
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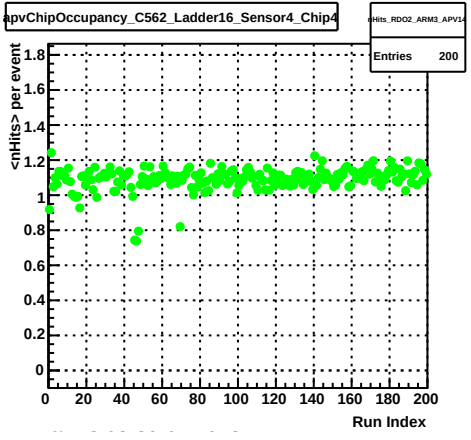
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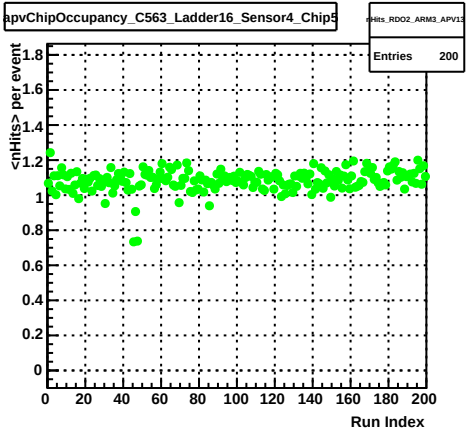
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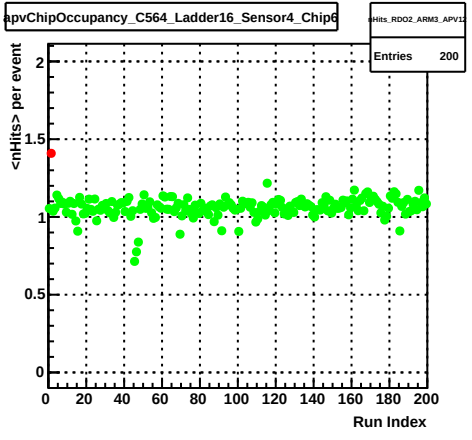
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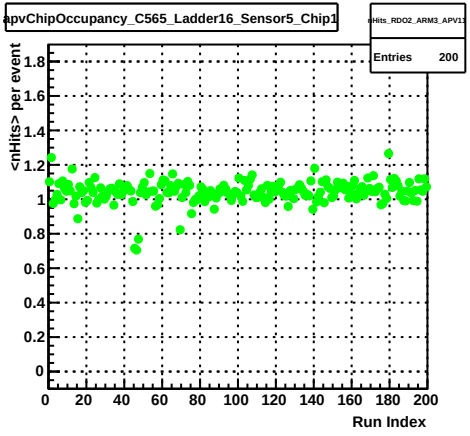
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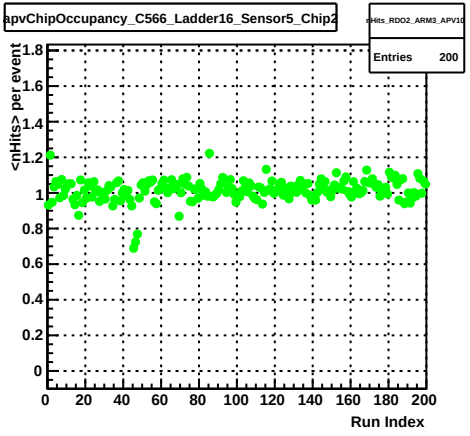
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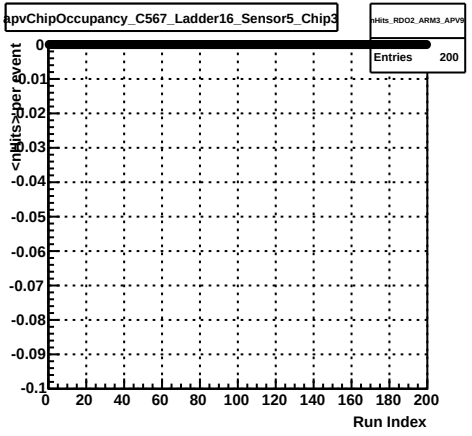
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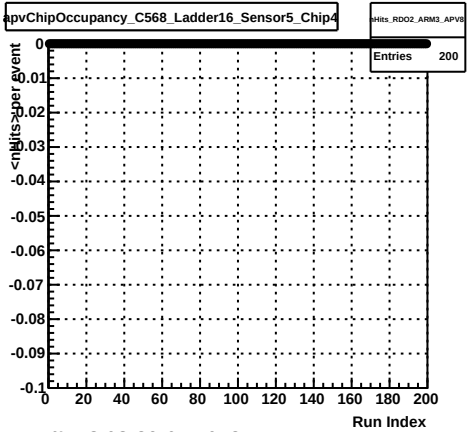
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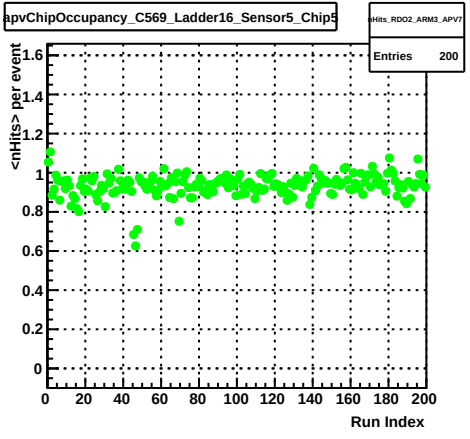
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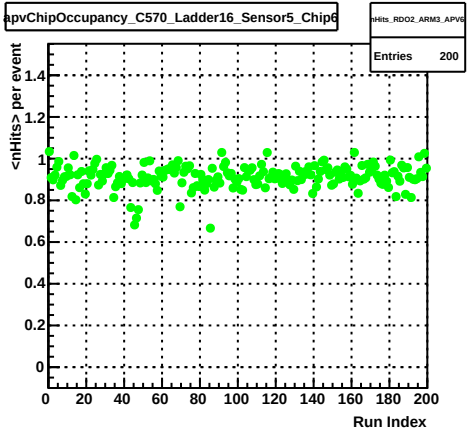
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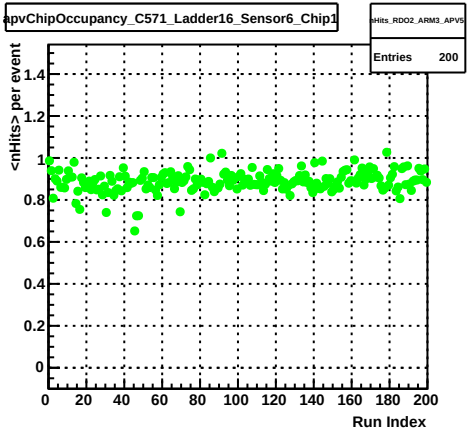
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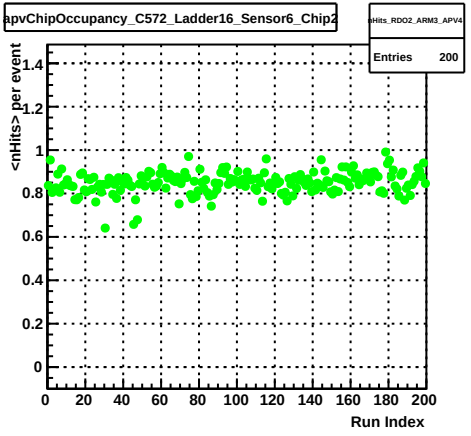
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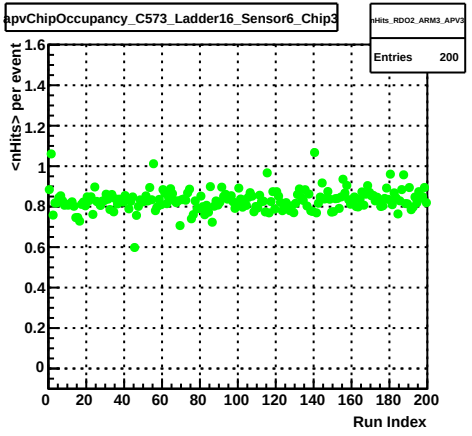
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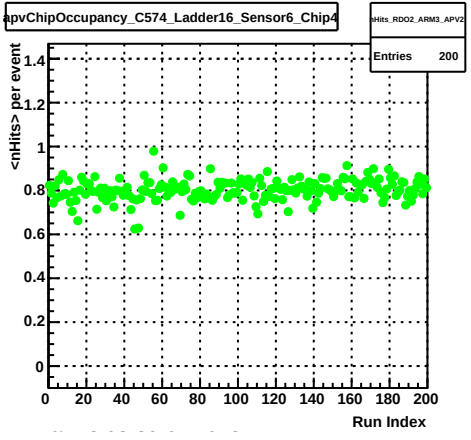
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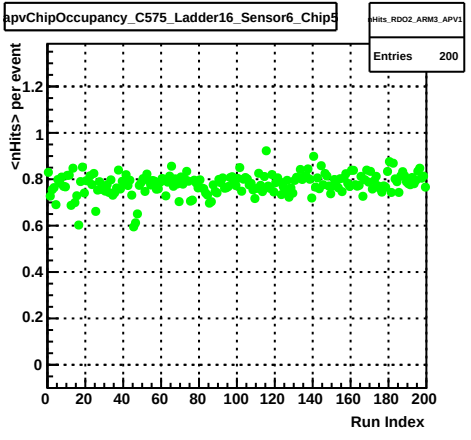
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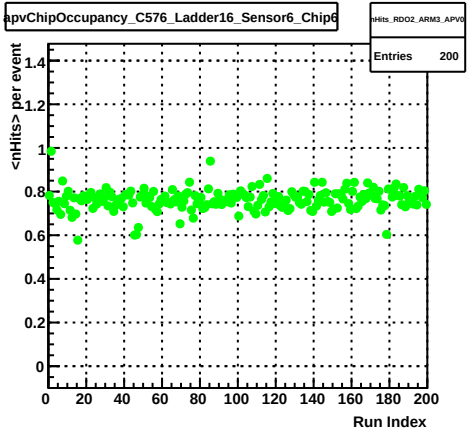
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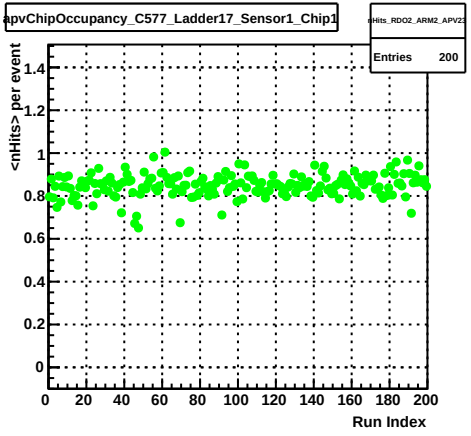
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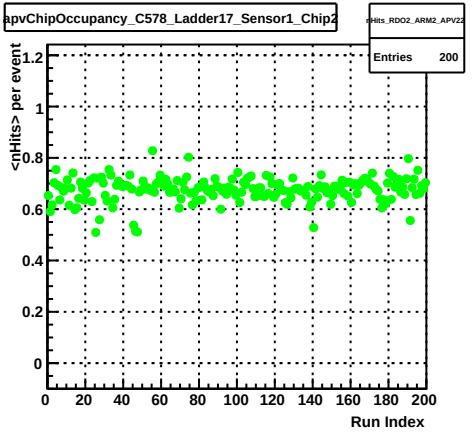
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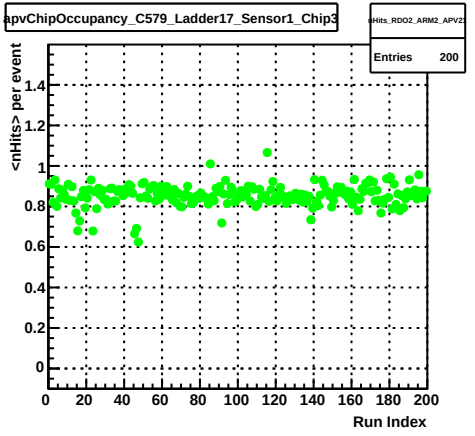
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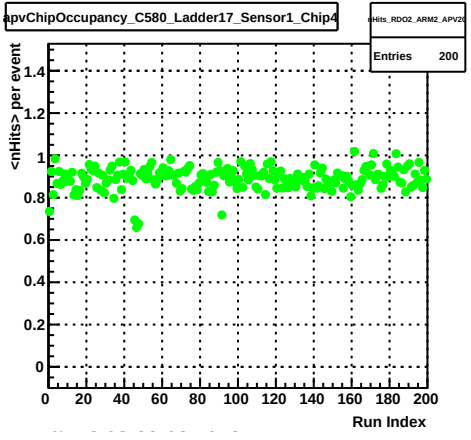
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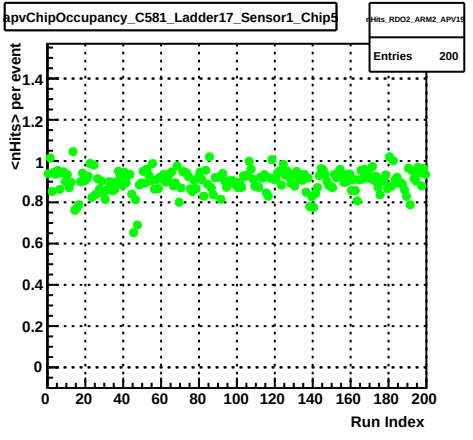
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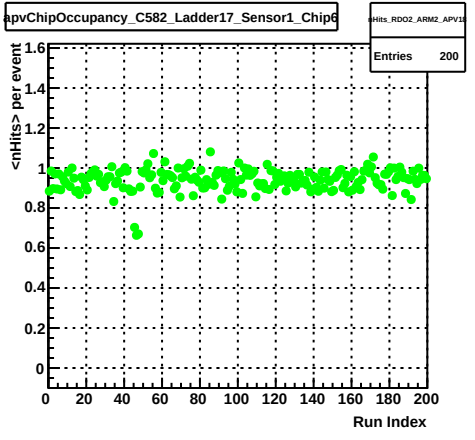
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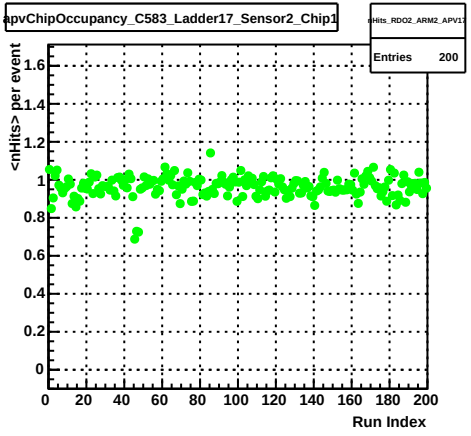
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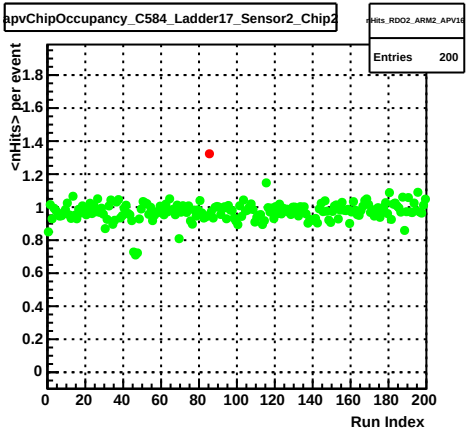
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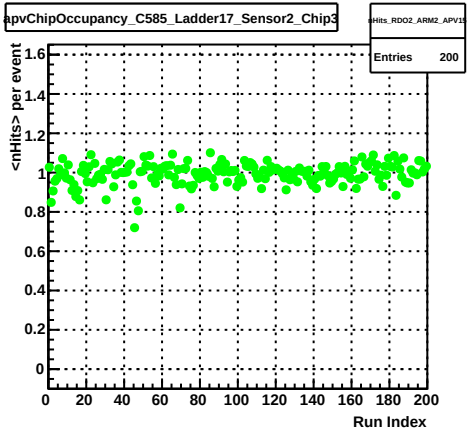
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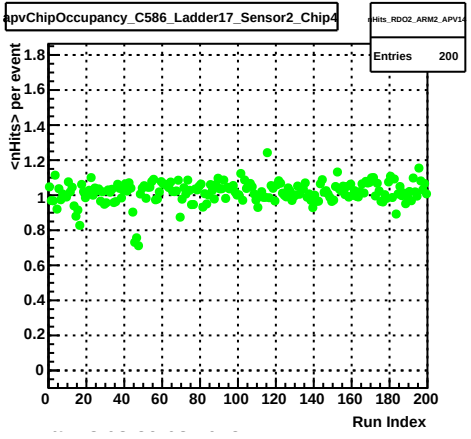
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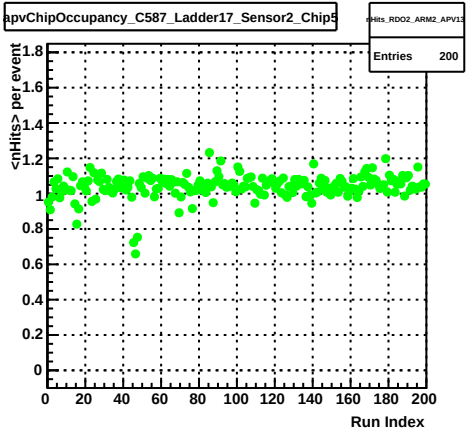
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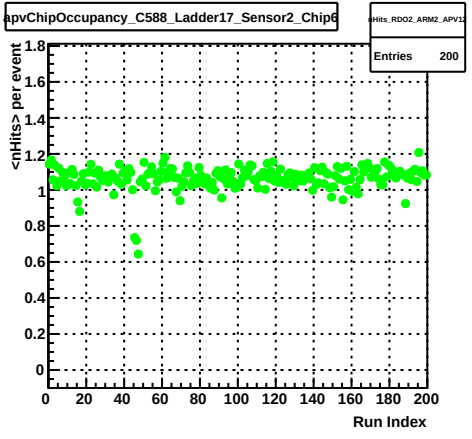
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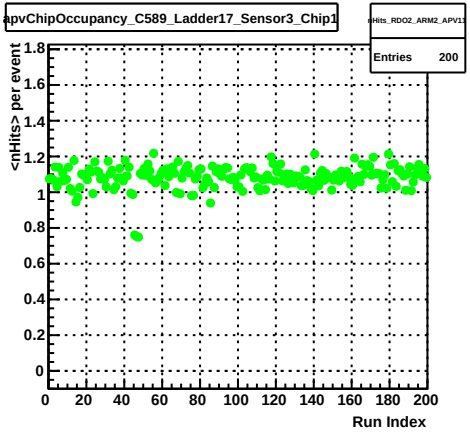
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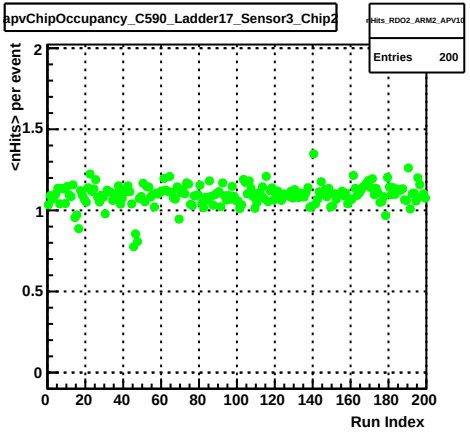
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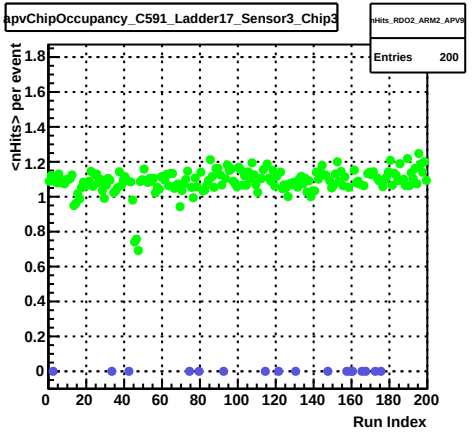
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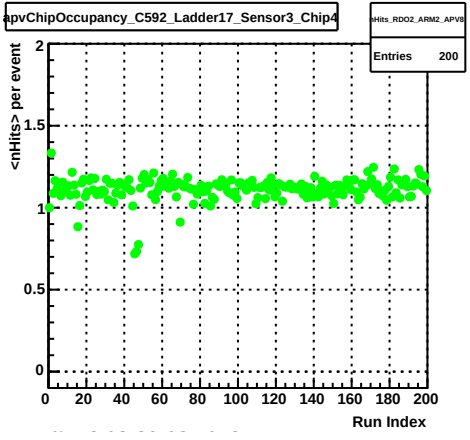
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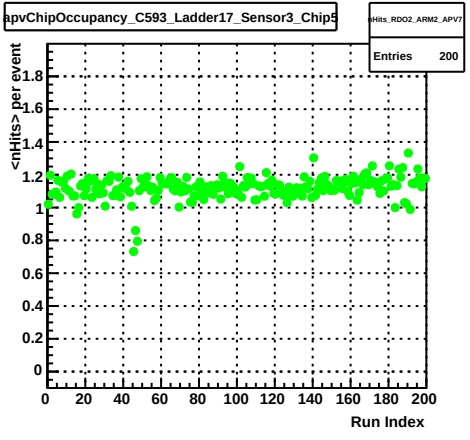
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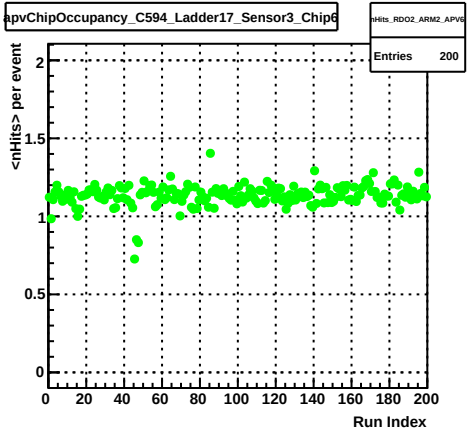
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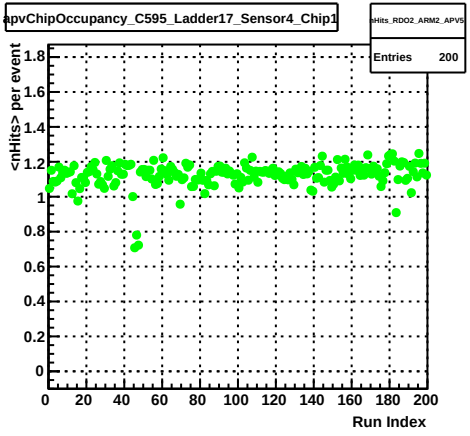
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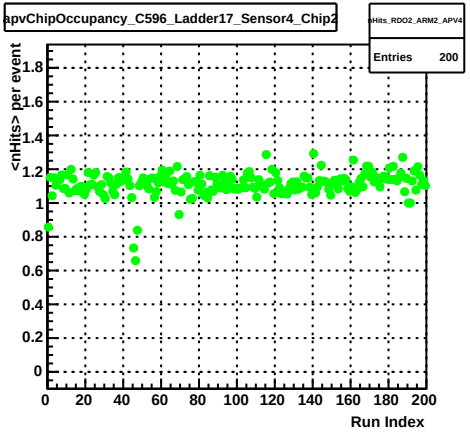
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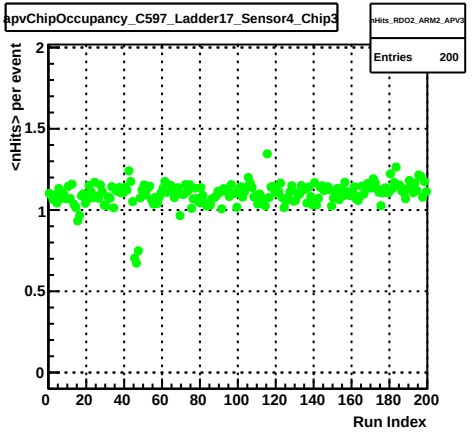
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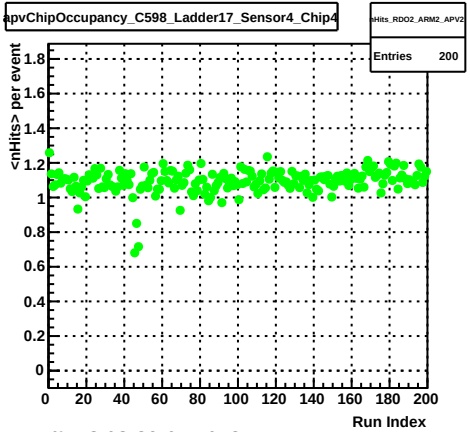
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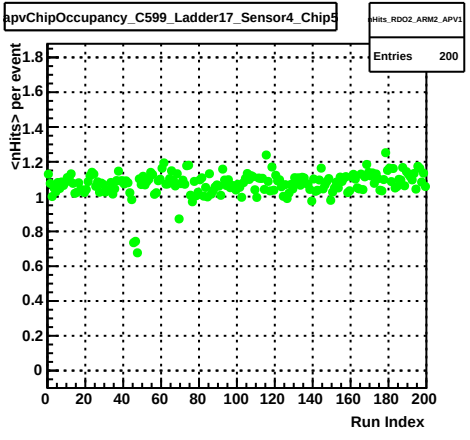
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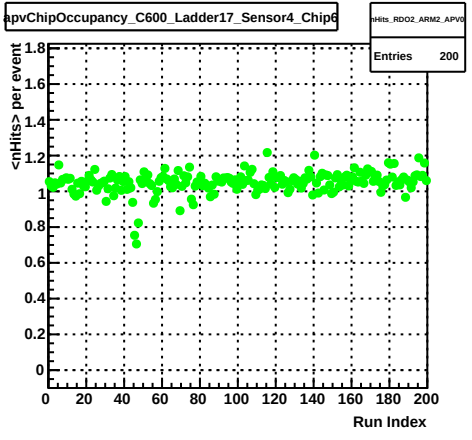
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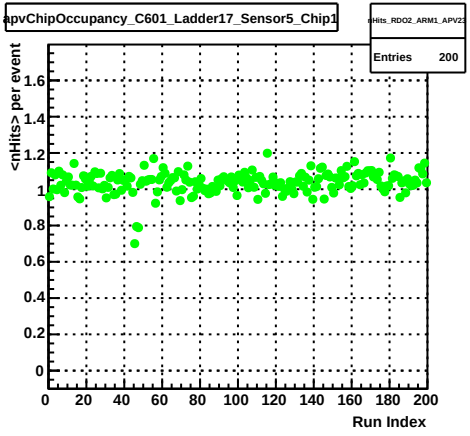
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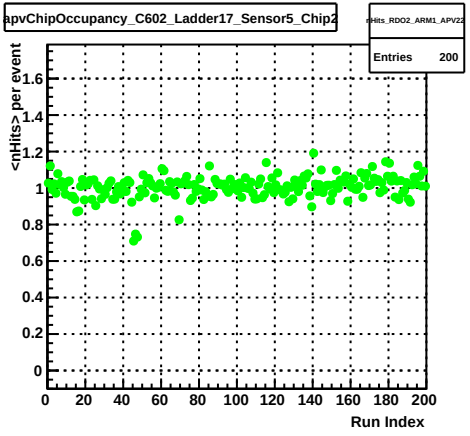
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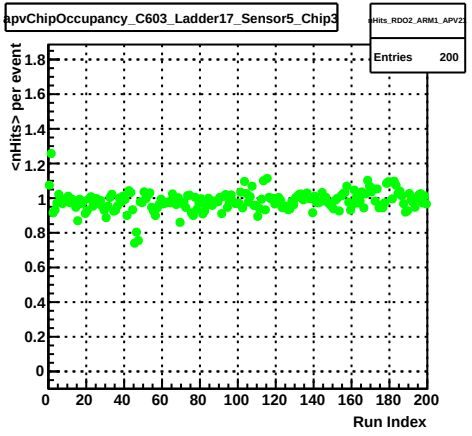
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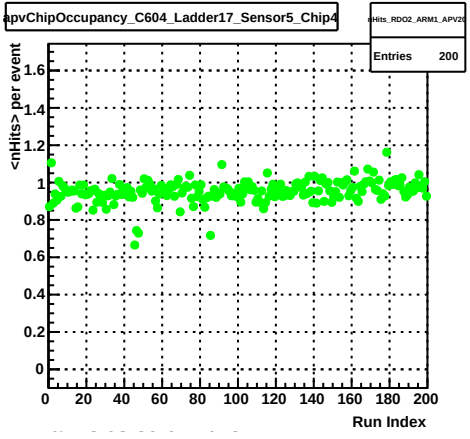
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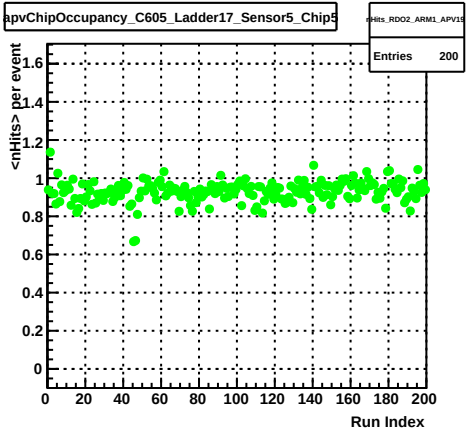
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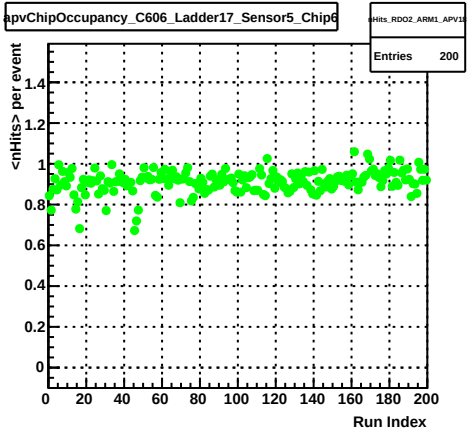
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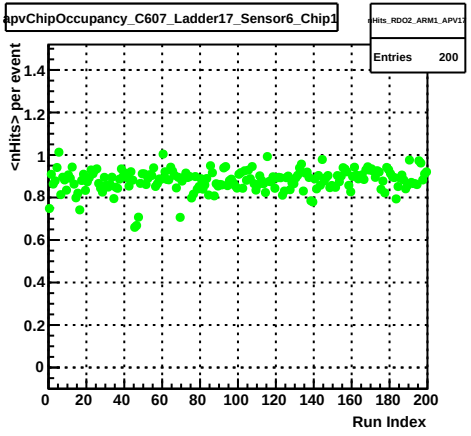
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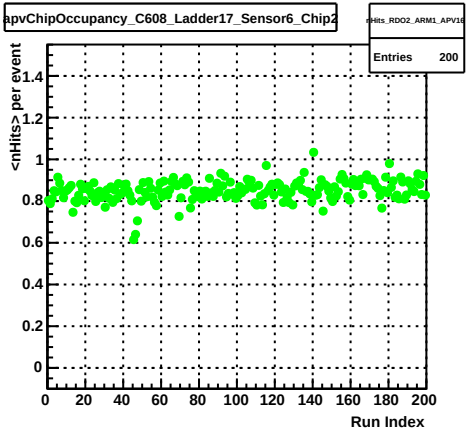
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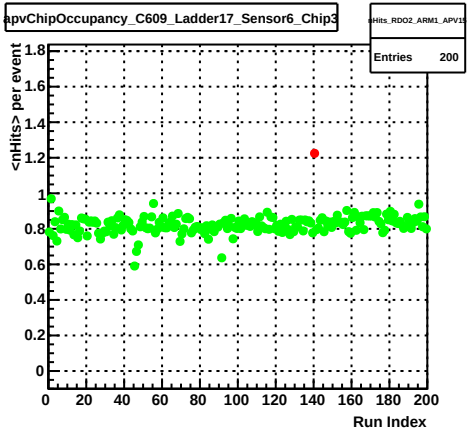
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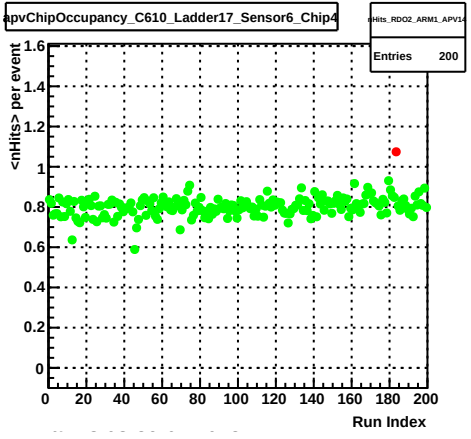
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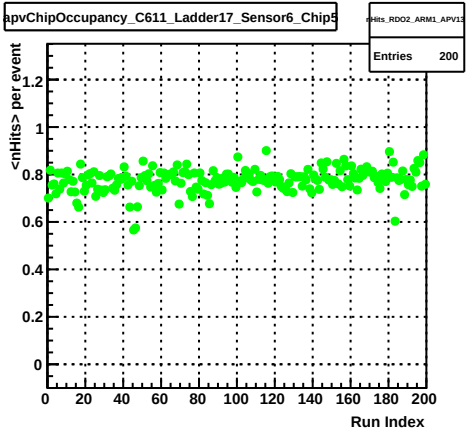
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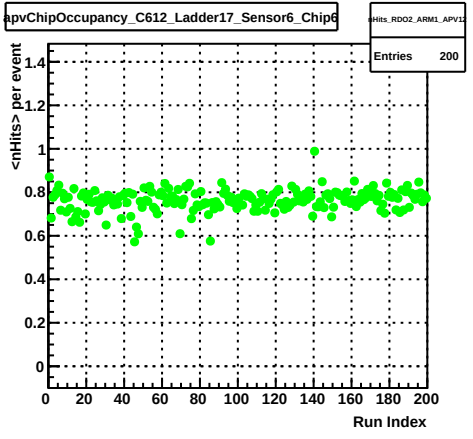
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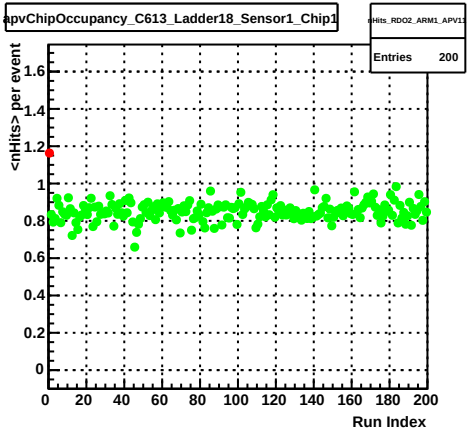
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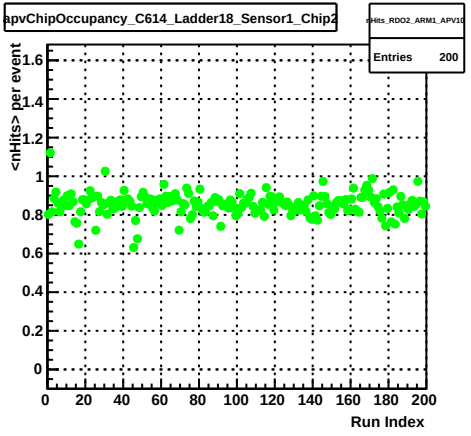
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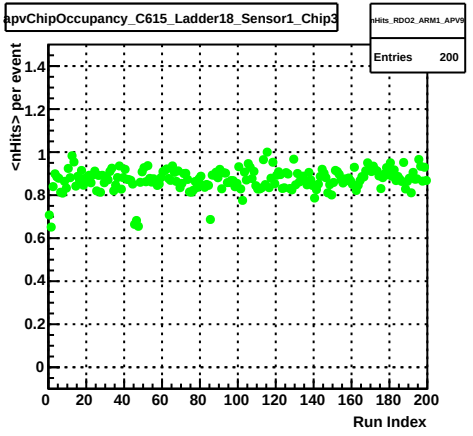
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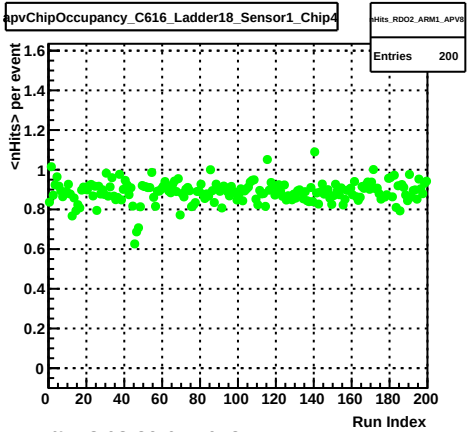
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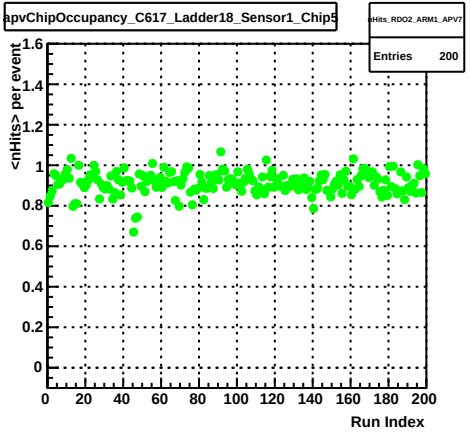
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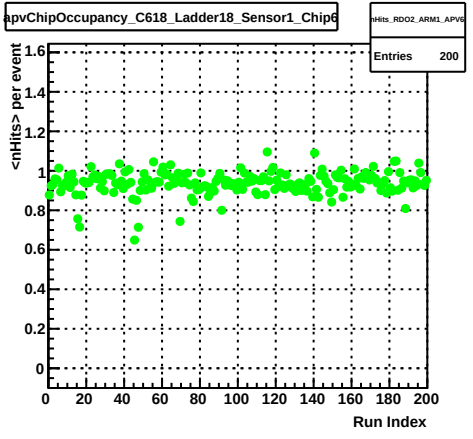
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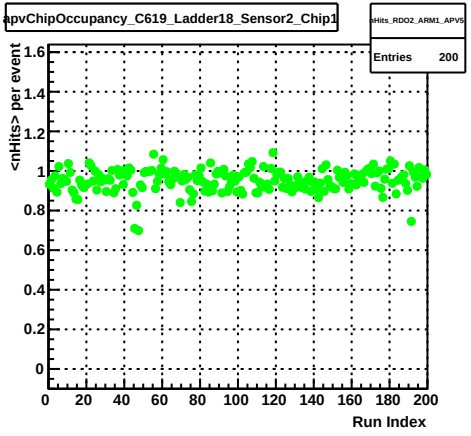
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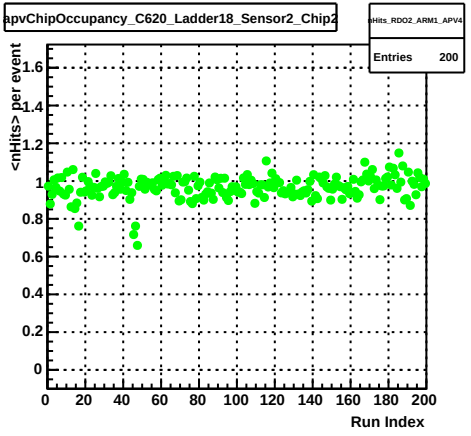
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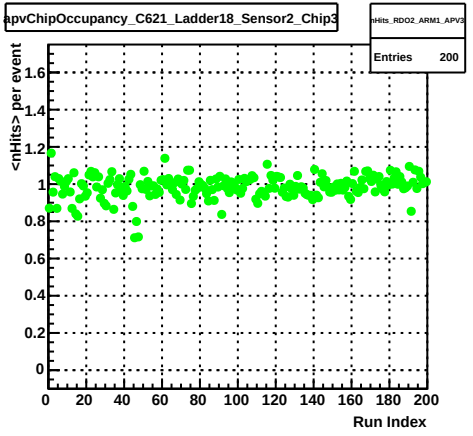
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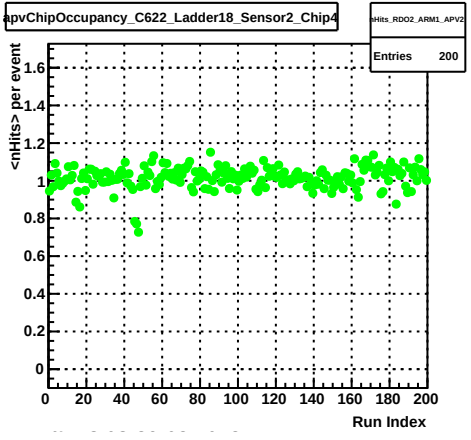
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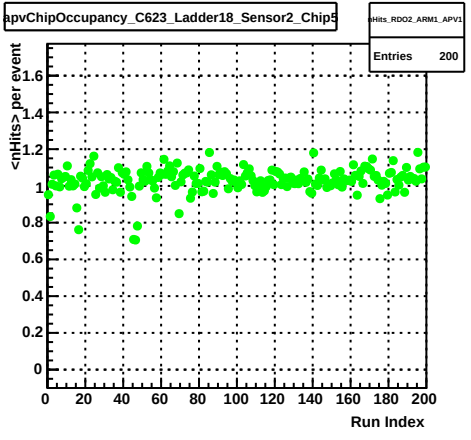
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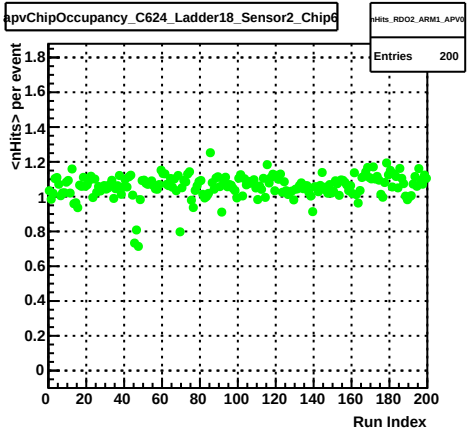
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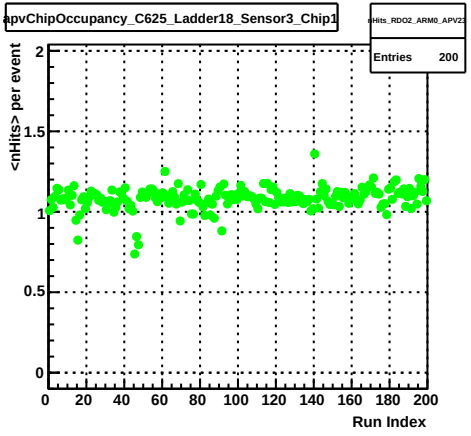
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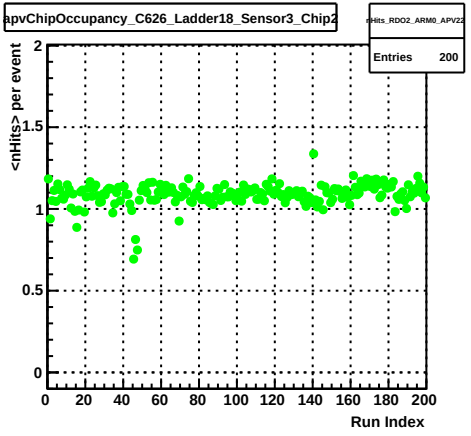
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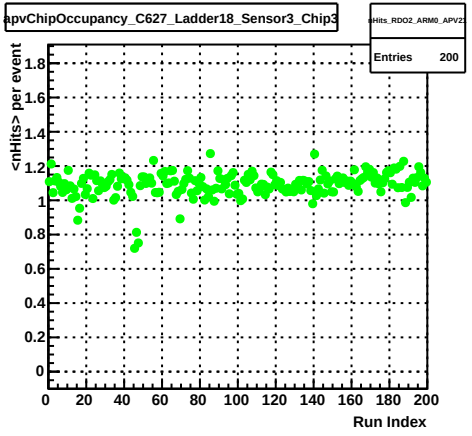
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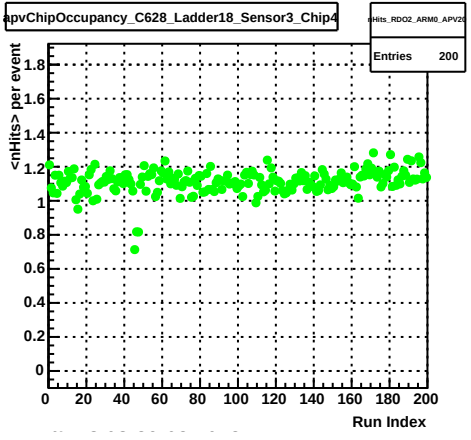
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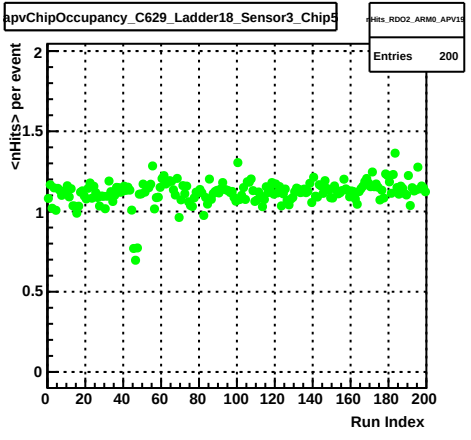
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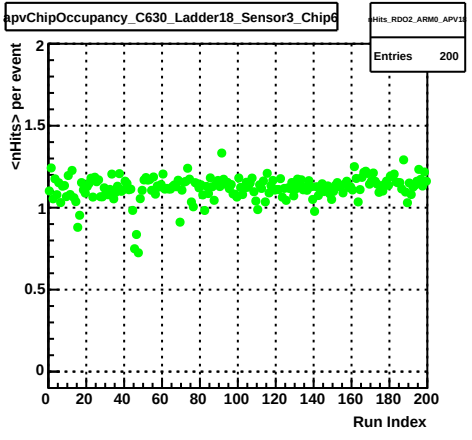
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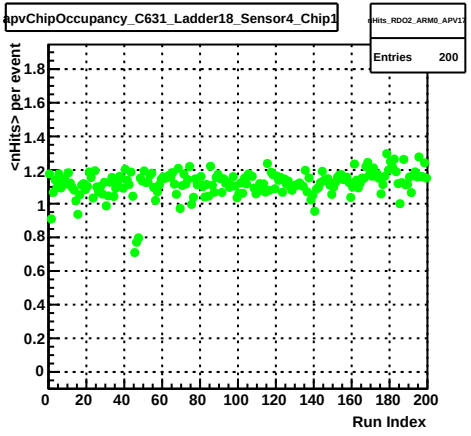
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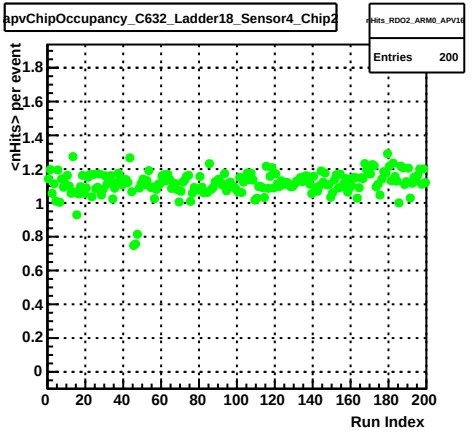
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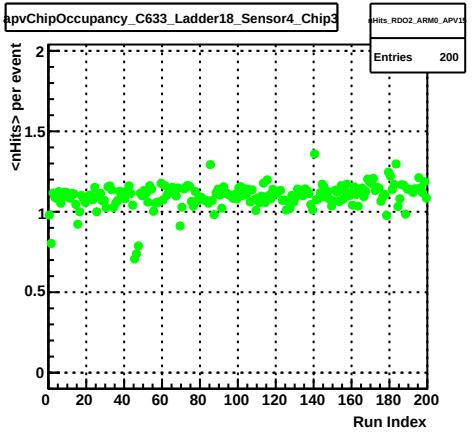
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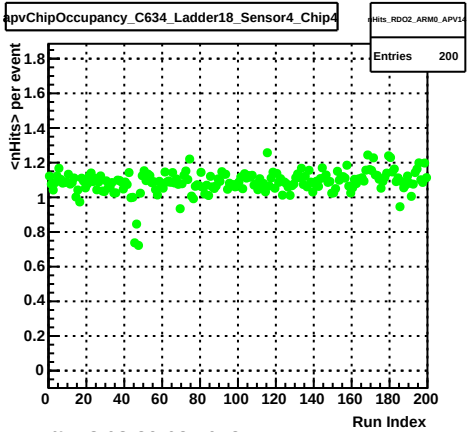
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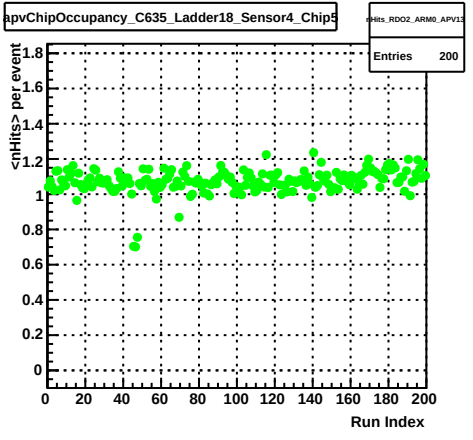
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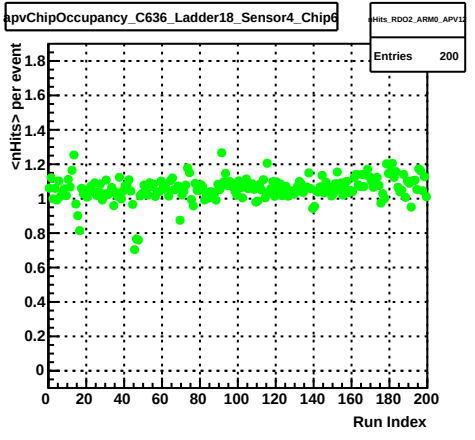
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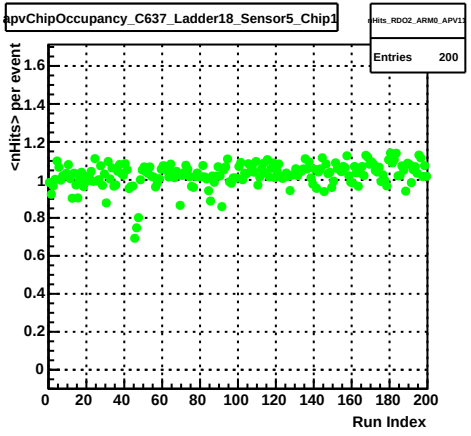
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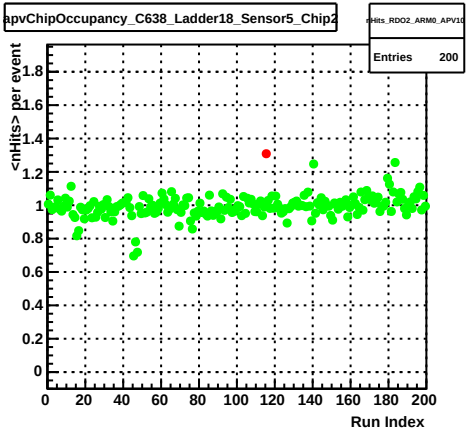
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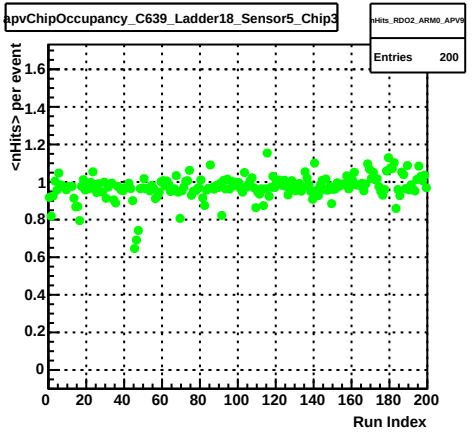
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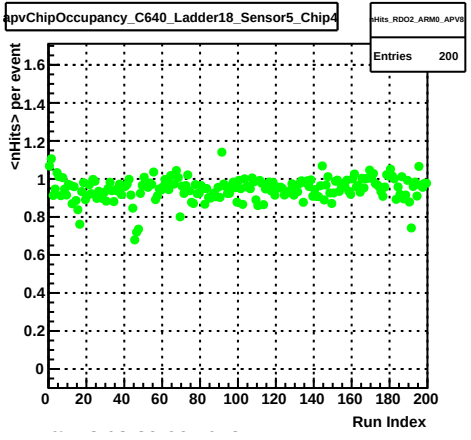
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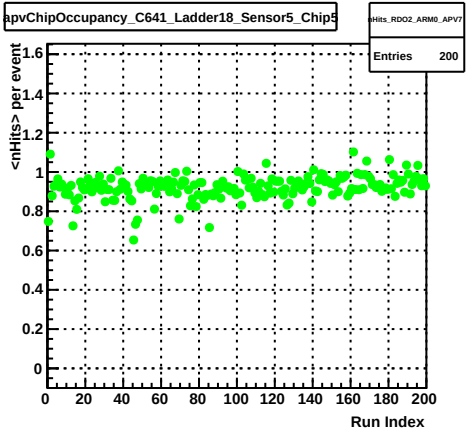
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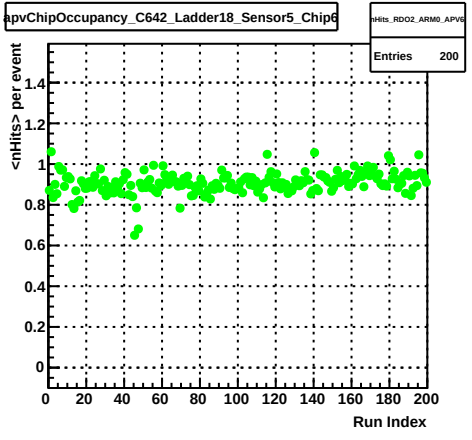
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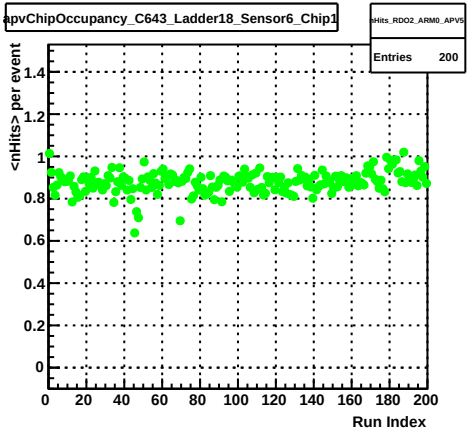
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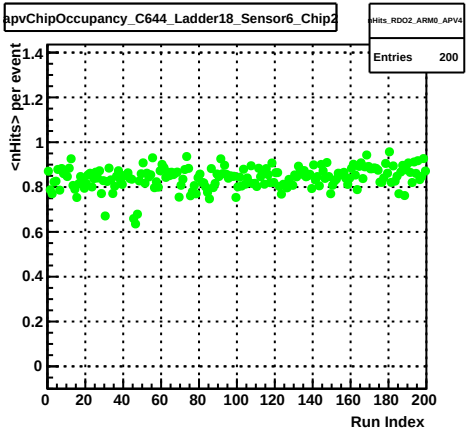
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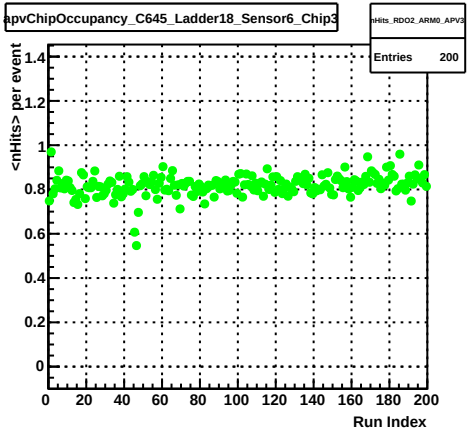
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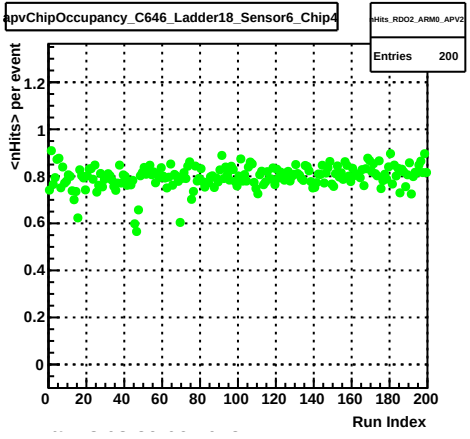
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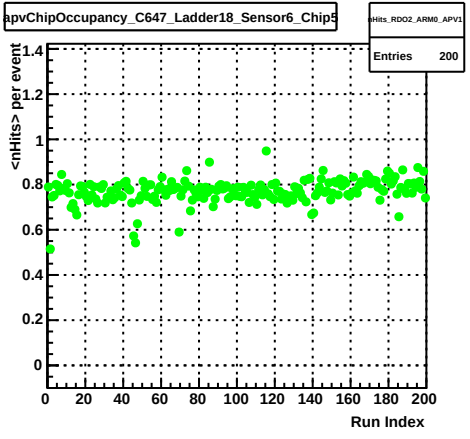
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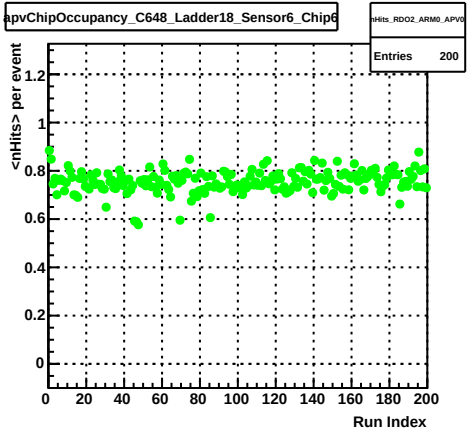
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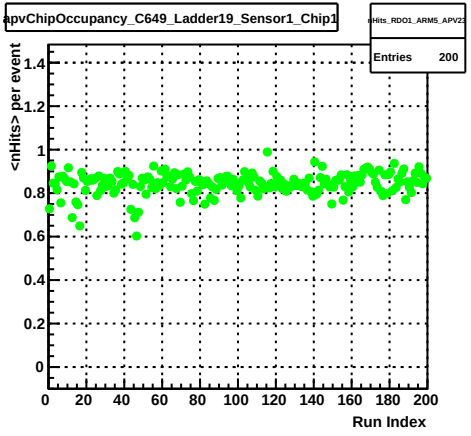
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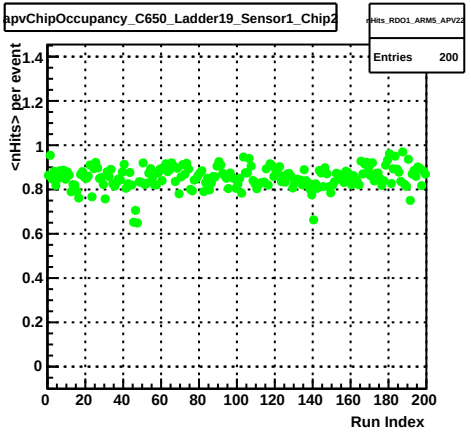
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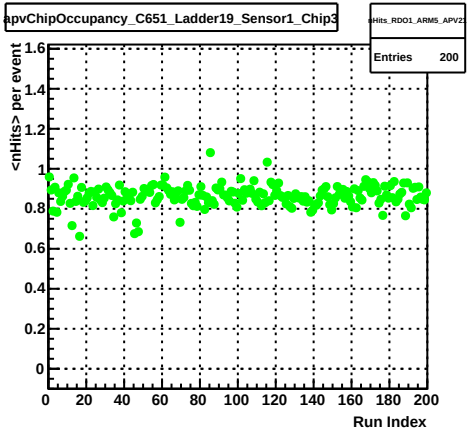
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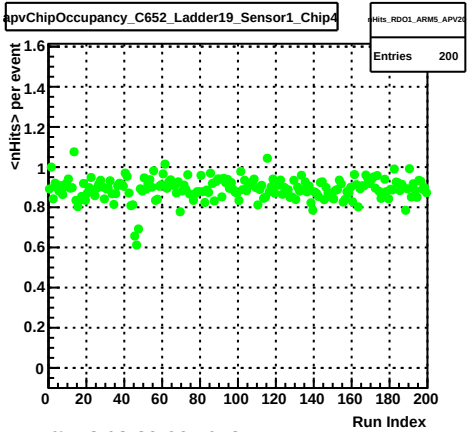
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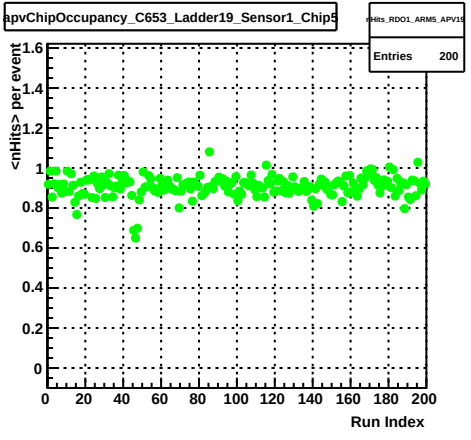
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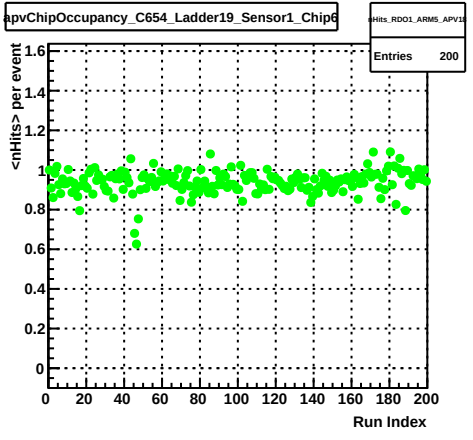
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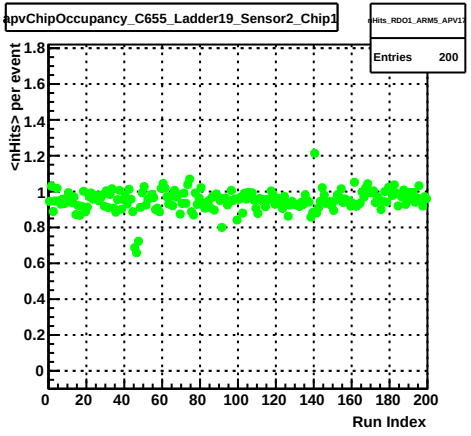
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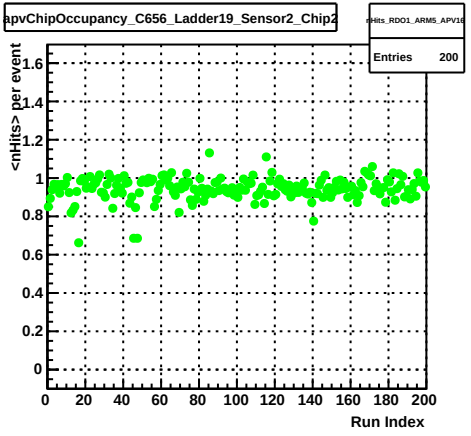
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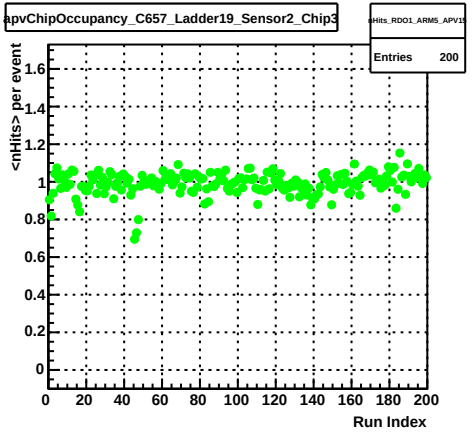
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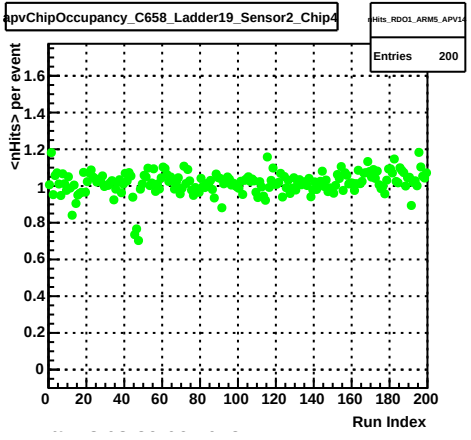
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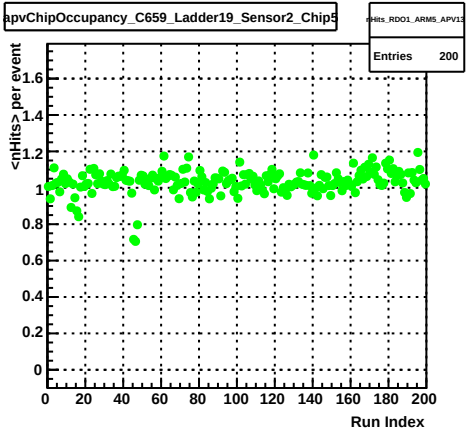
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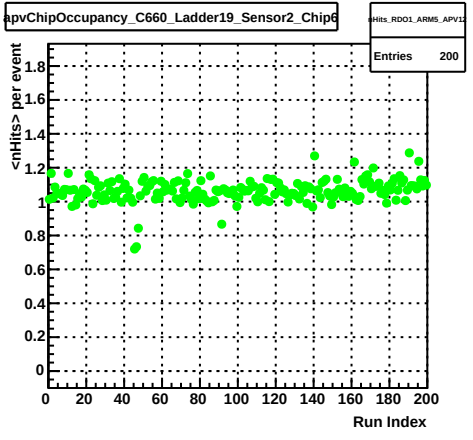
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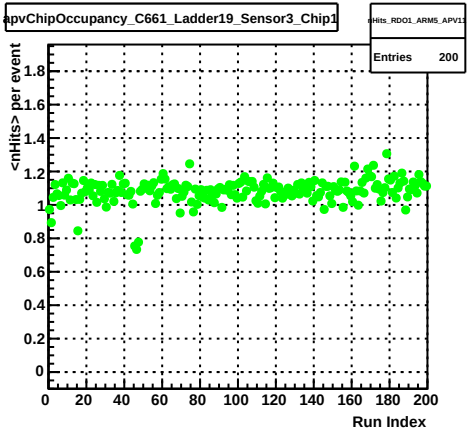
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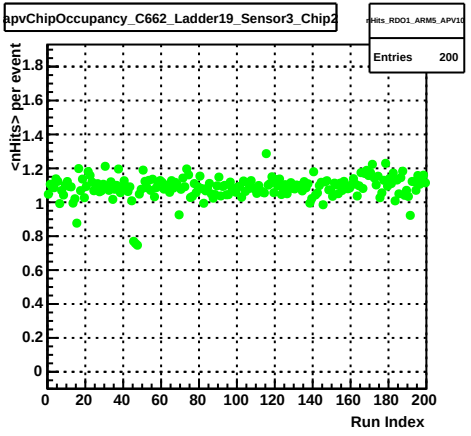
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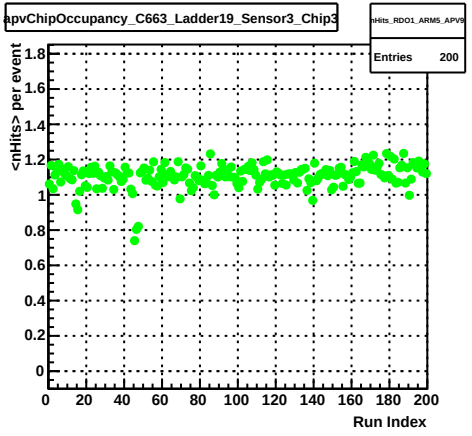
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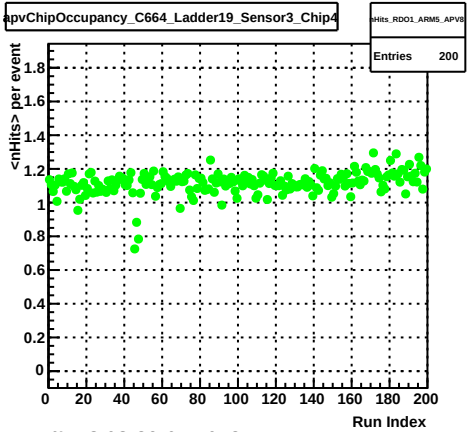
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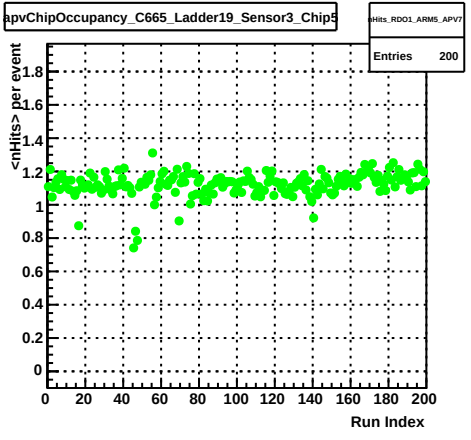
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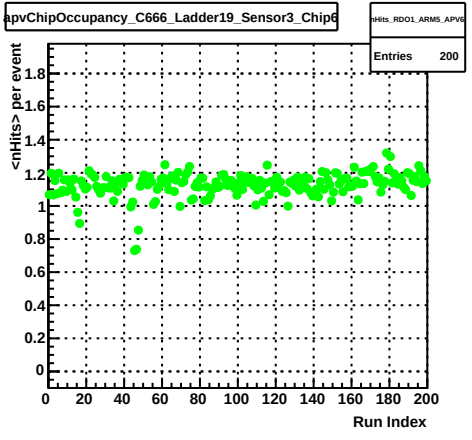
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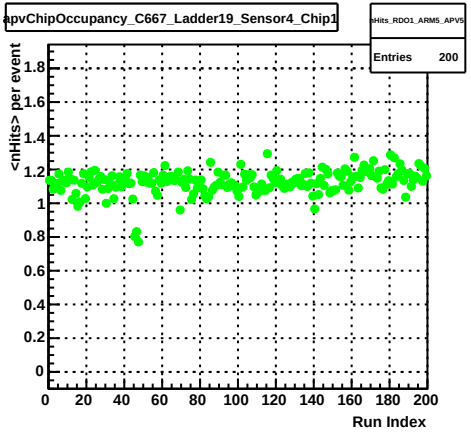
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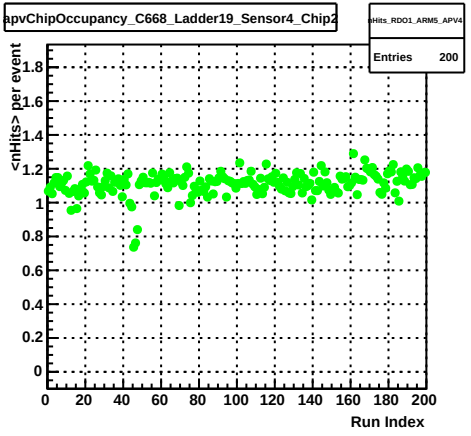
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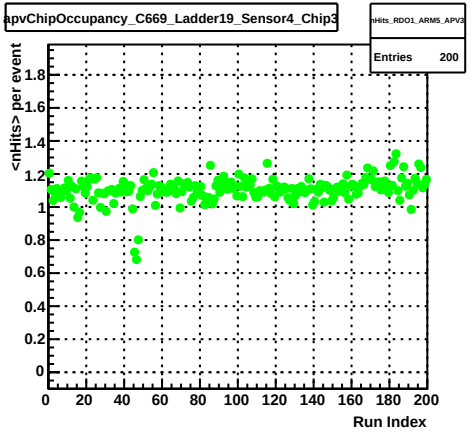
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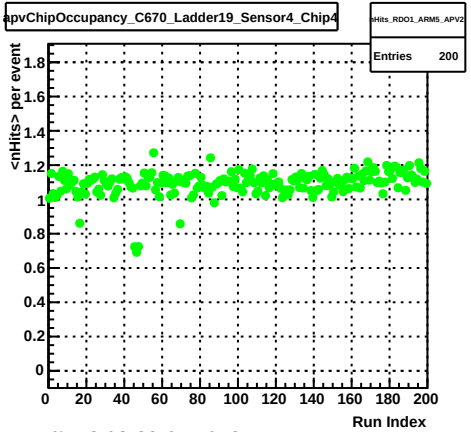
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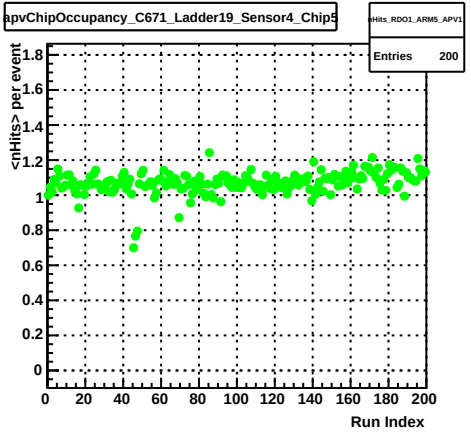
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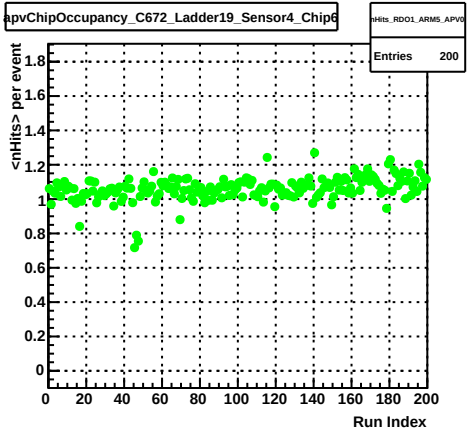
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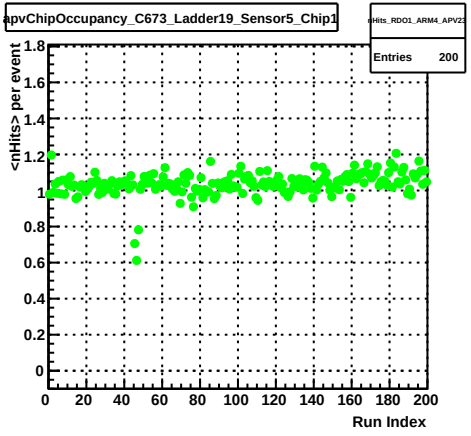
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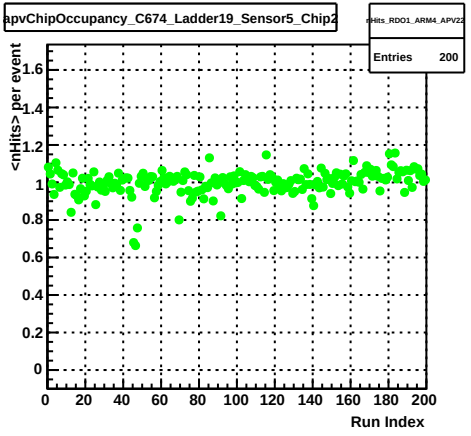
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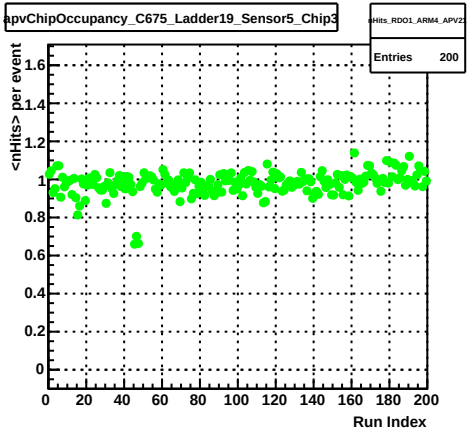
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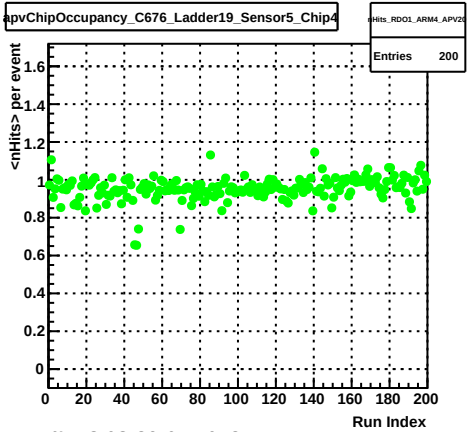
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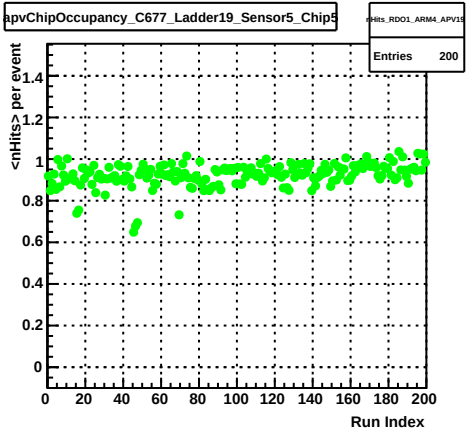
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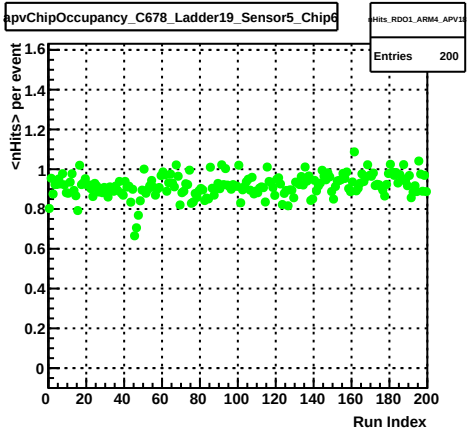
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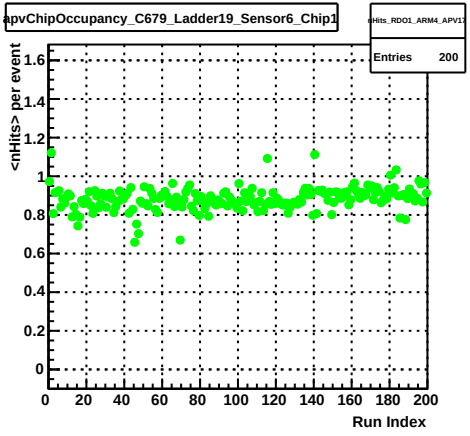
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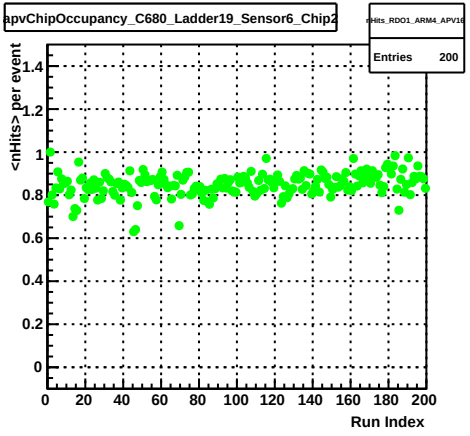
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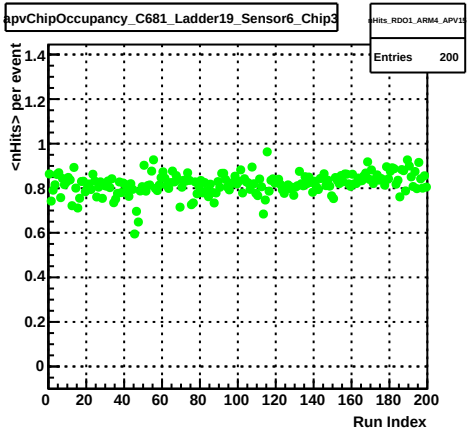
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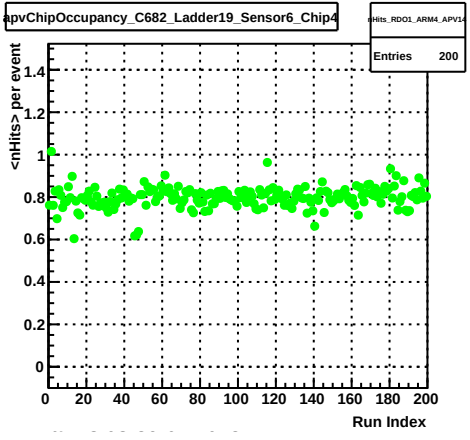
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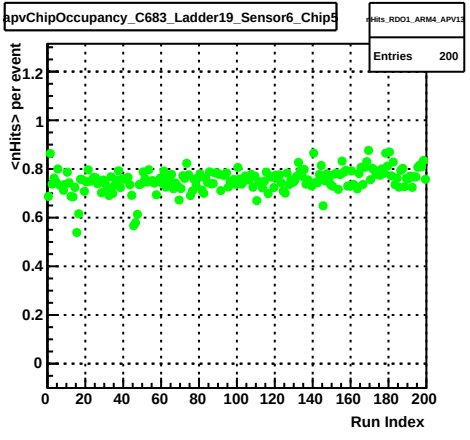
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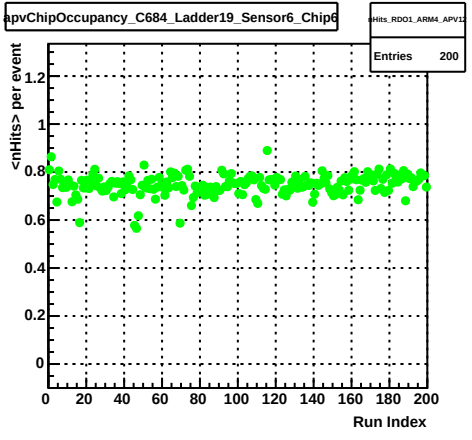
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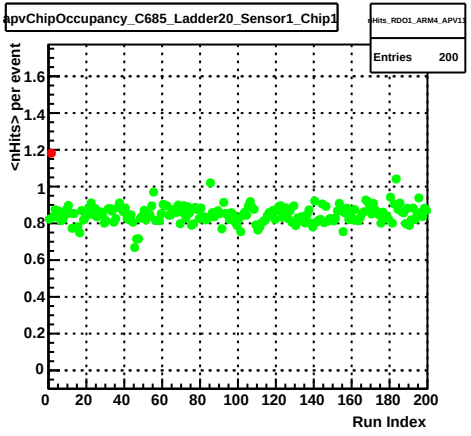
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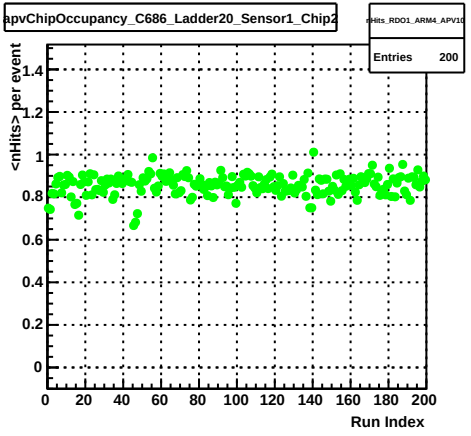
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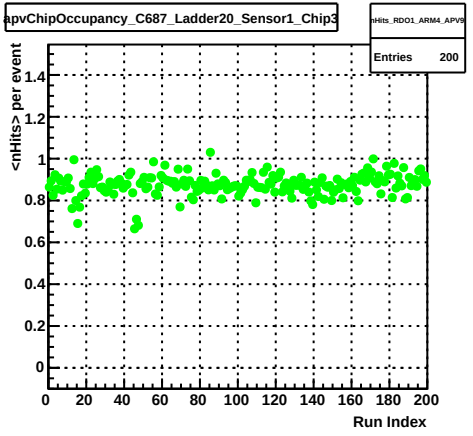
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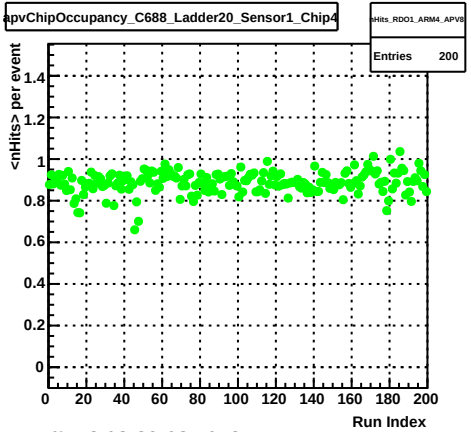
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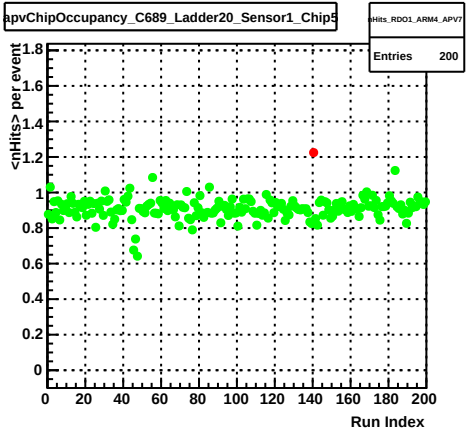
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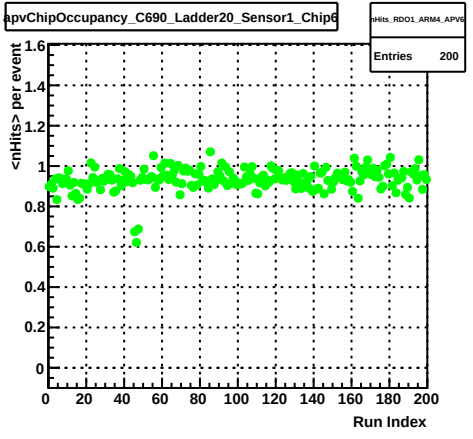
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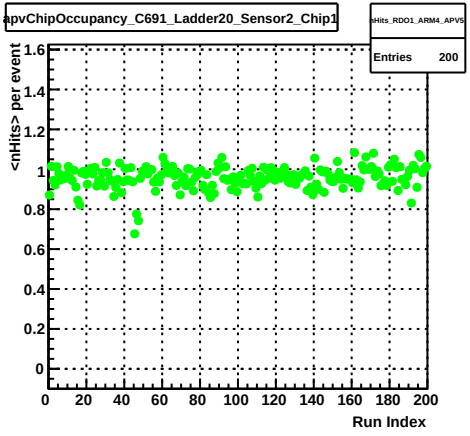
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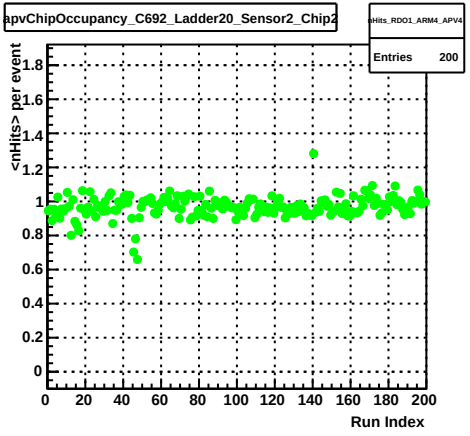
Run Index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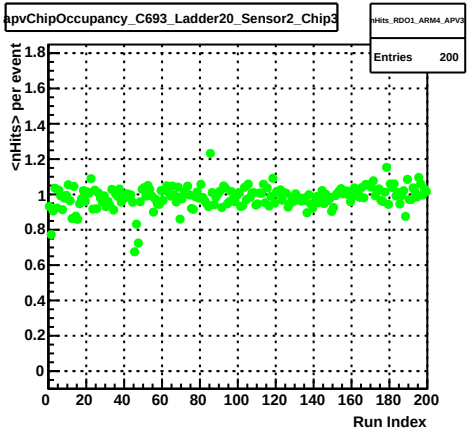
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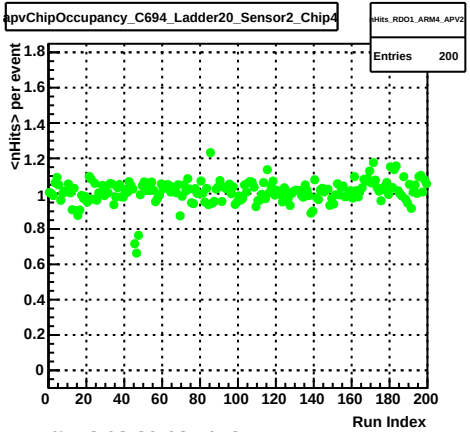
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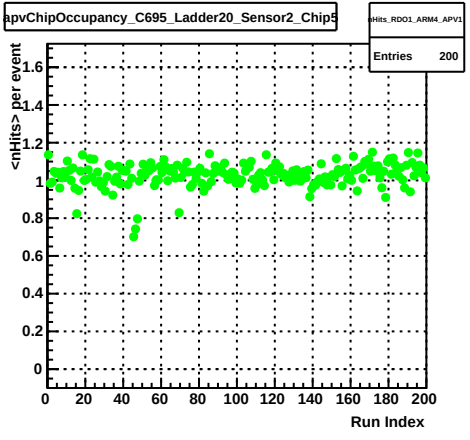
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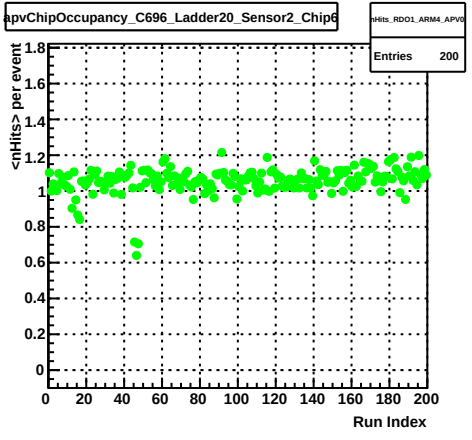
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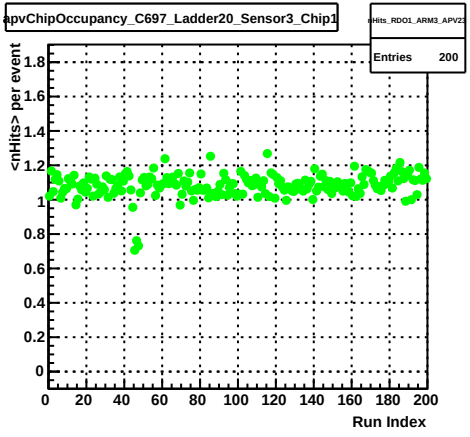
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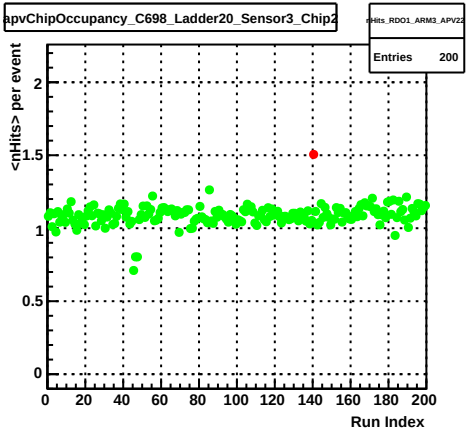
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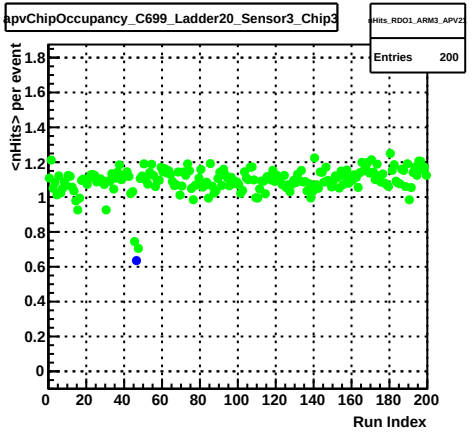
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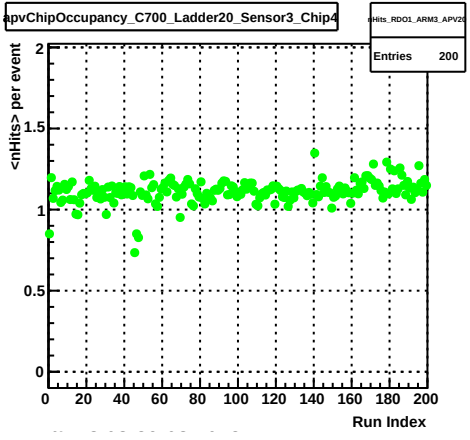
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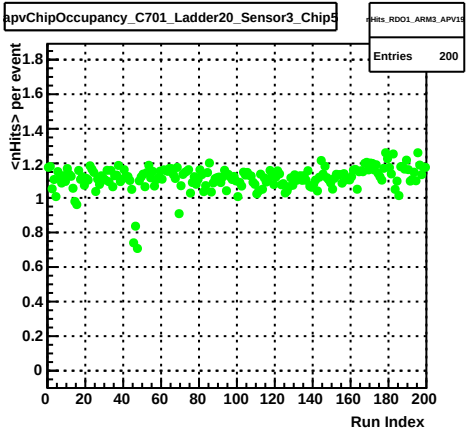
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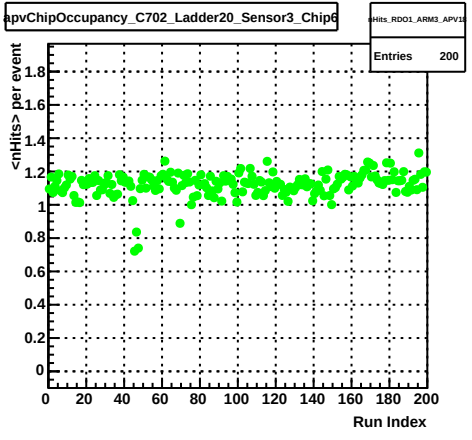
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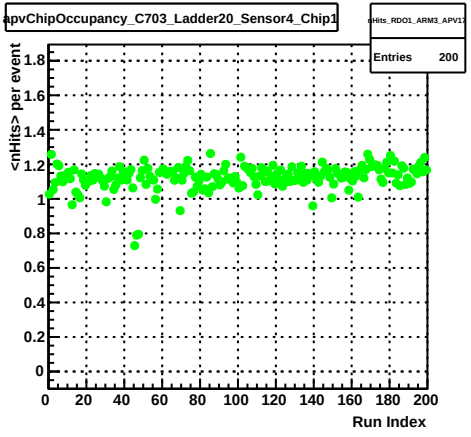
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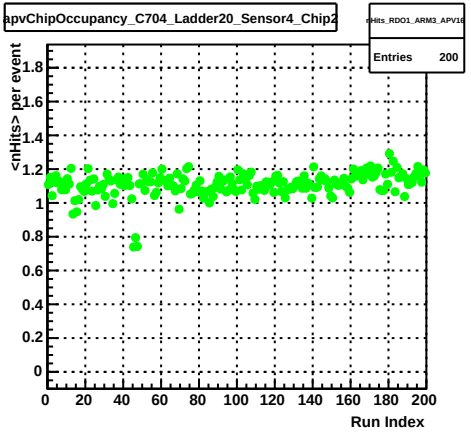
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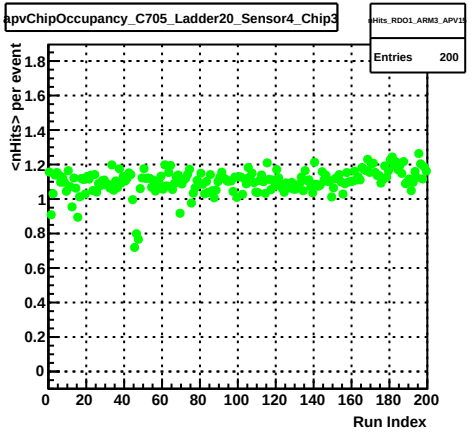
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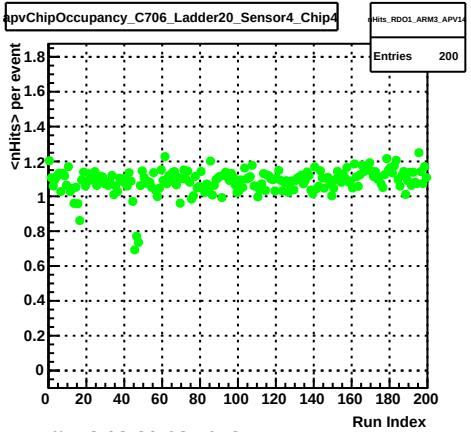
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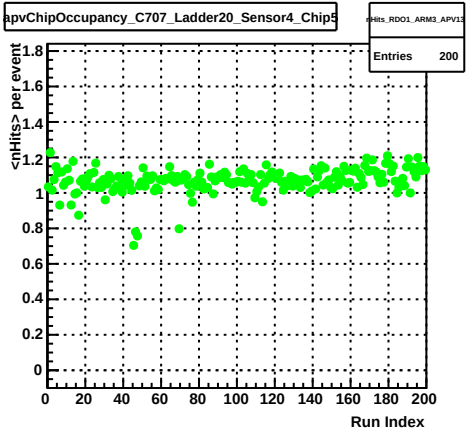
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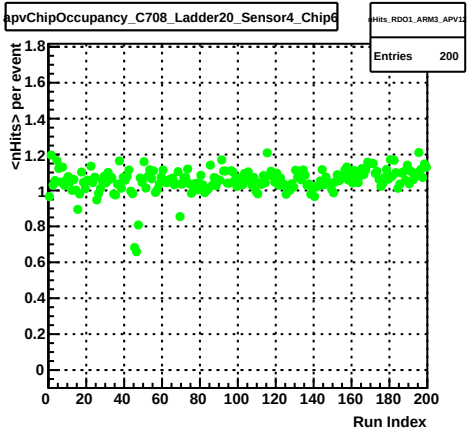
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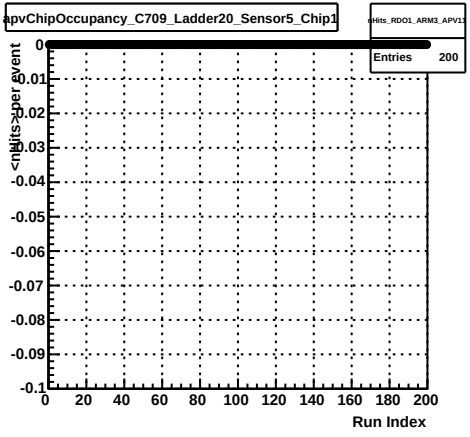
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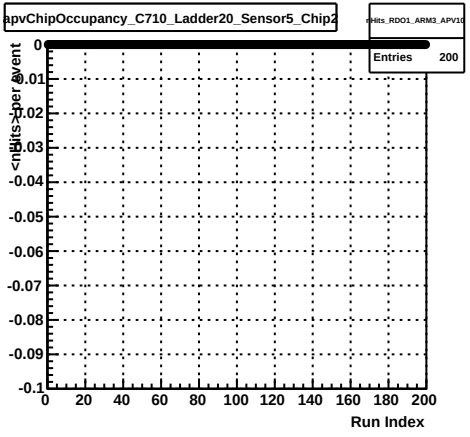
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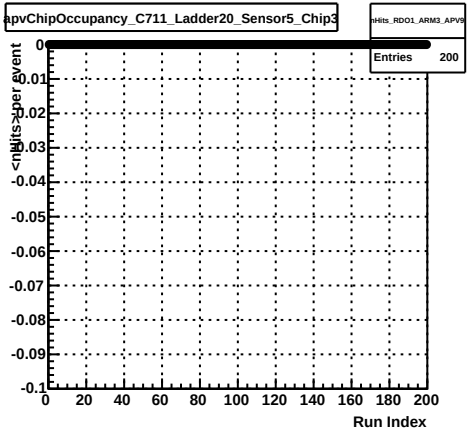
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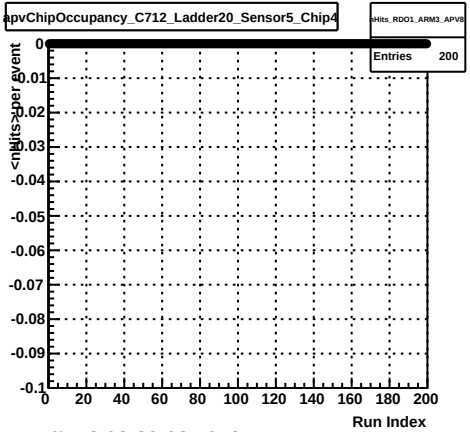
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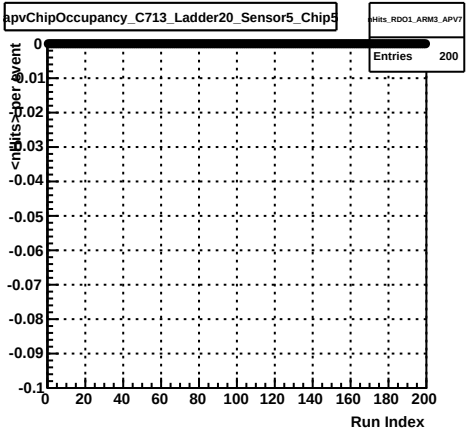
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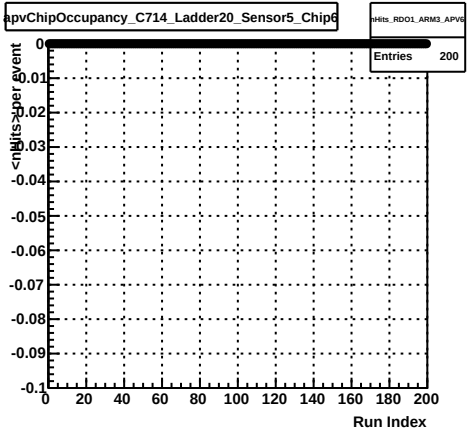
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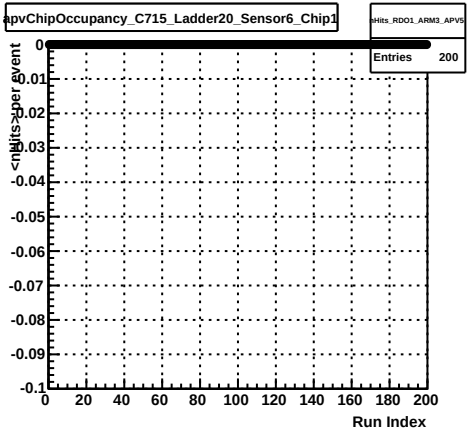
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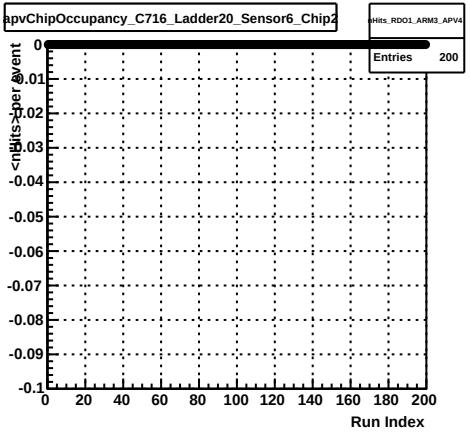
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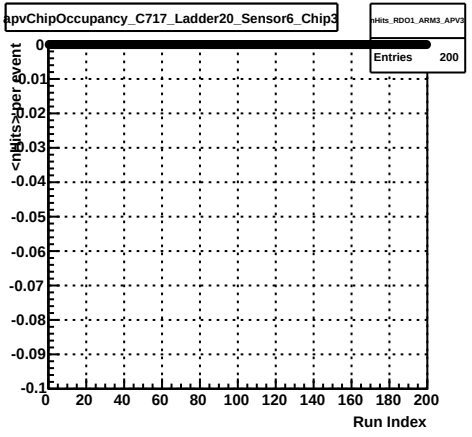
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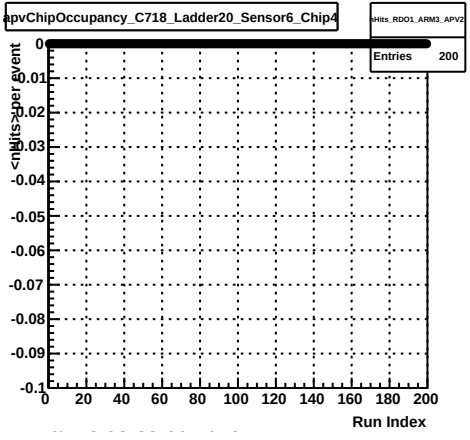
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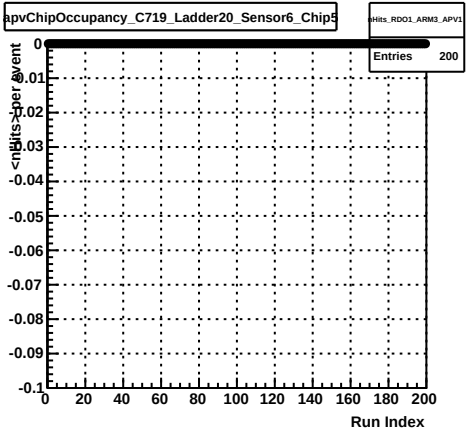
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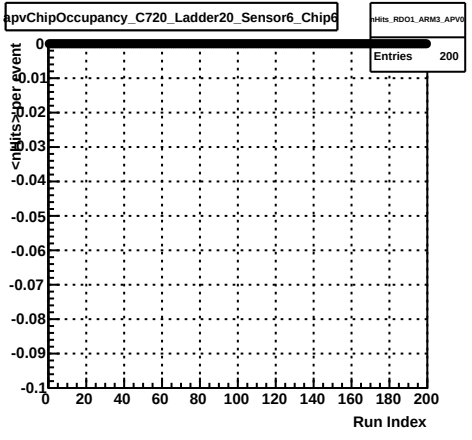
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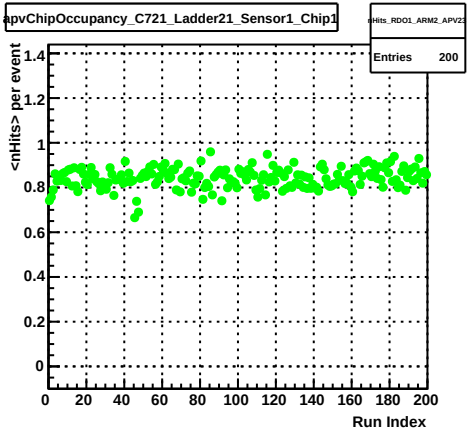
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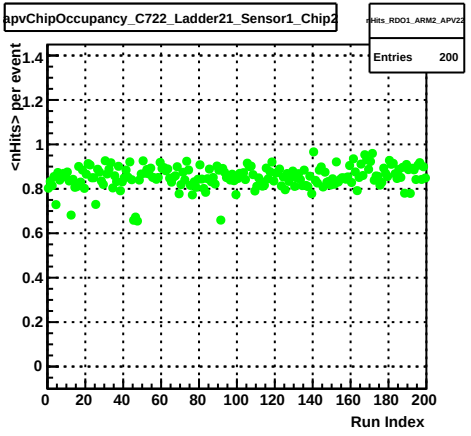
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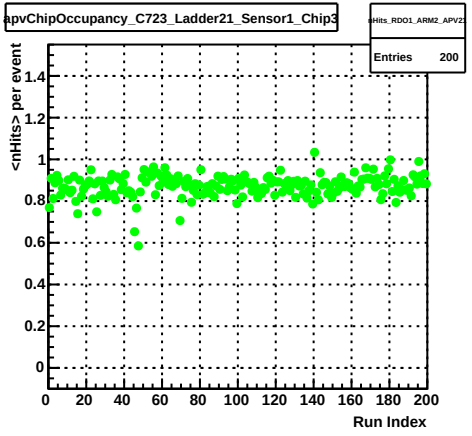
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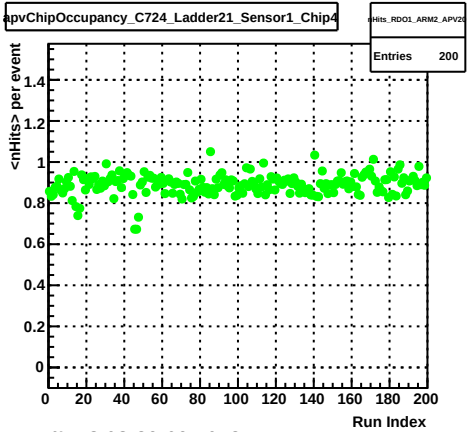
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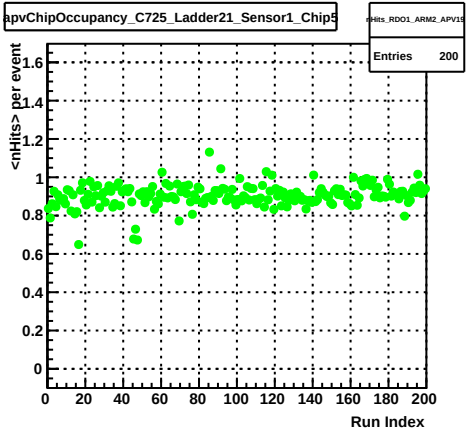
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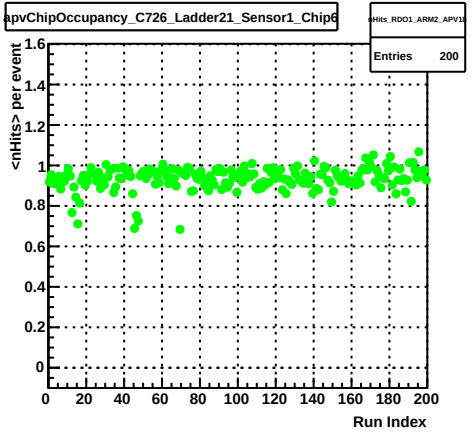
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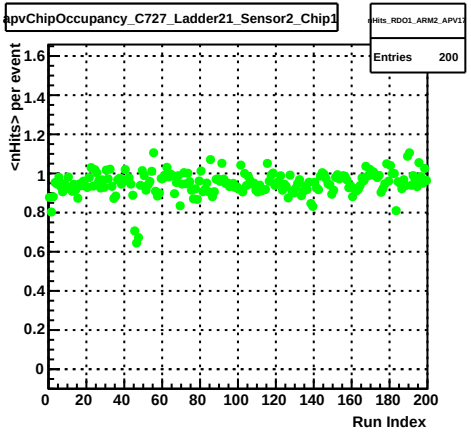
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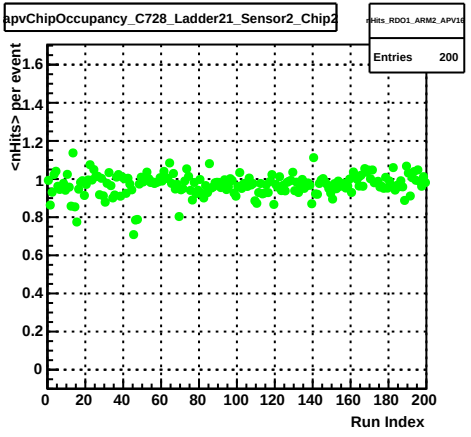
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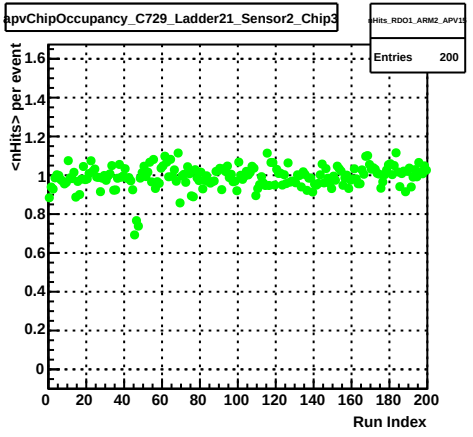
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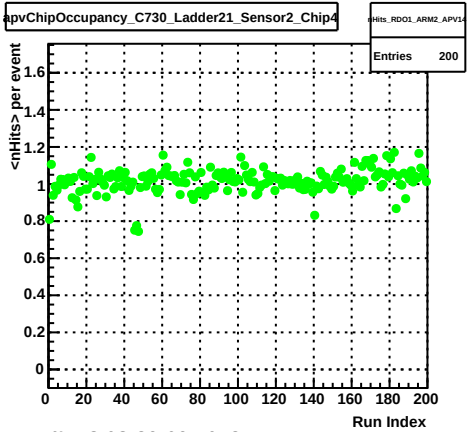
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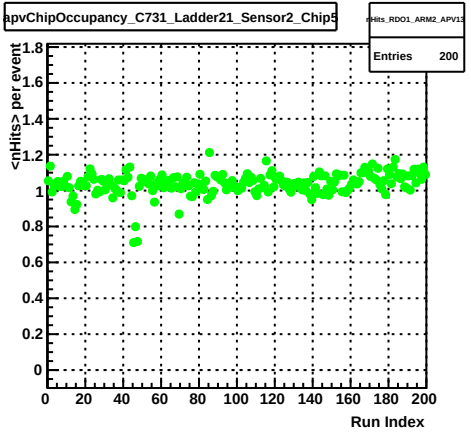
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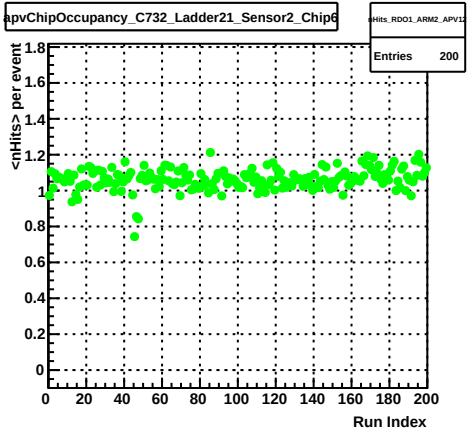
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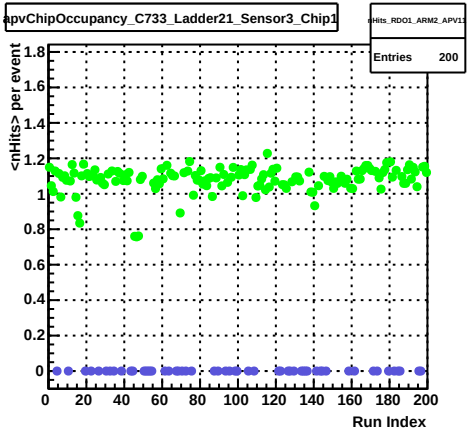
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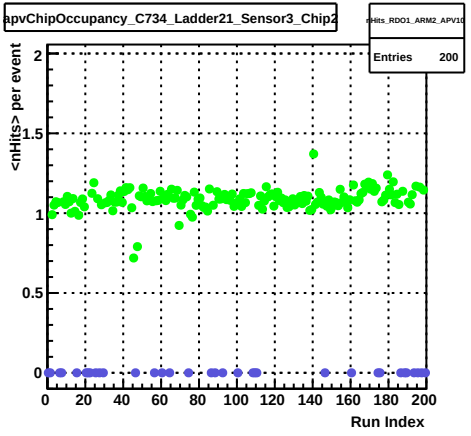
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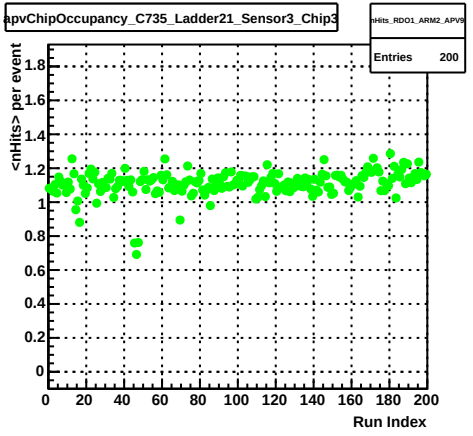
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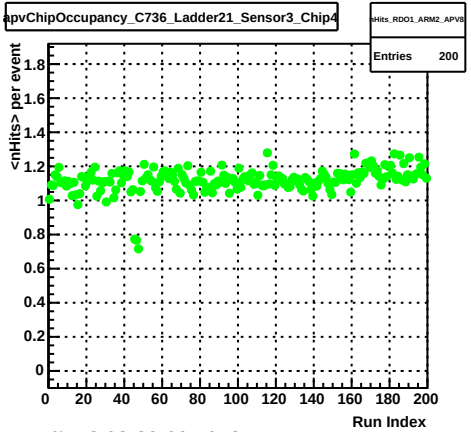
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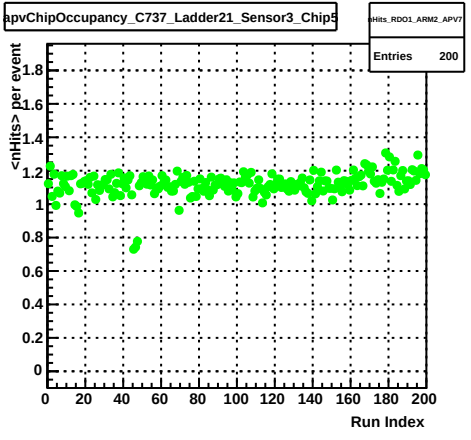
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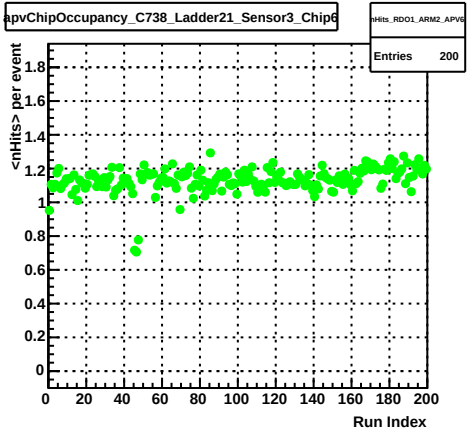
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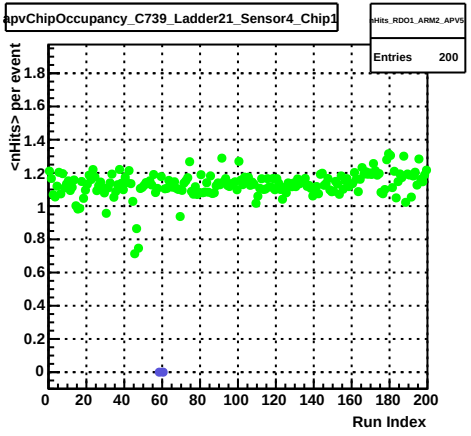
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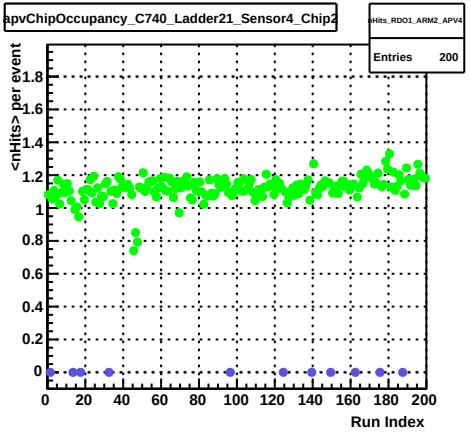
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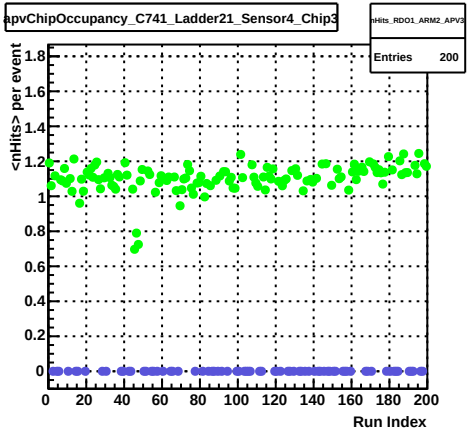
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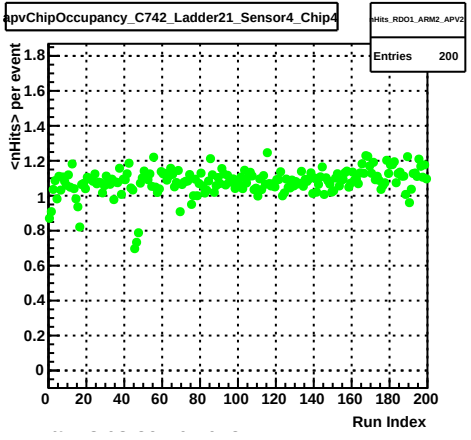
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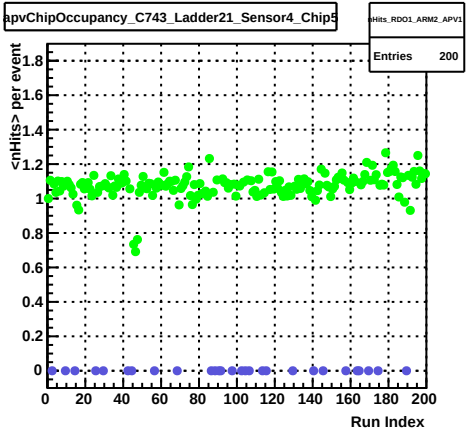
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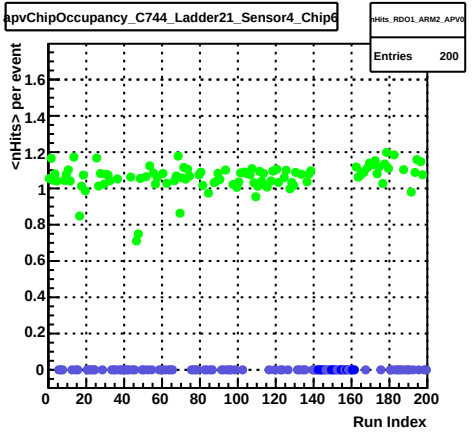
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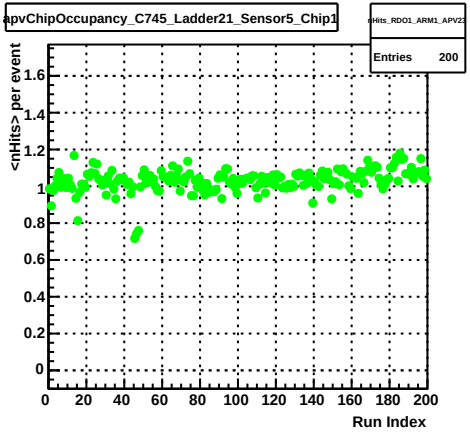
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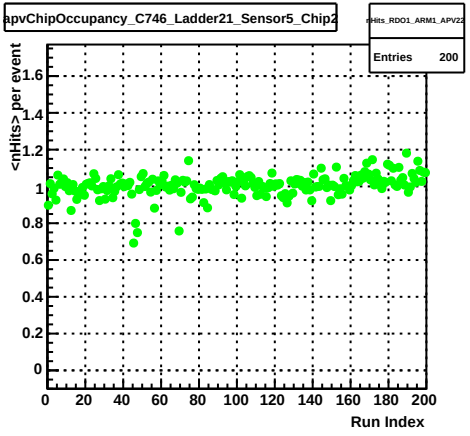
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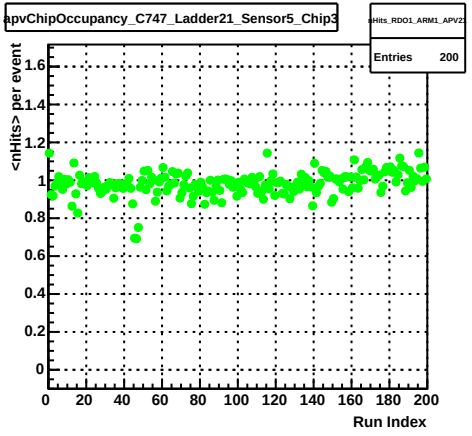
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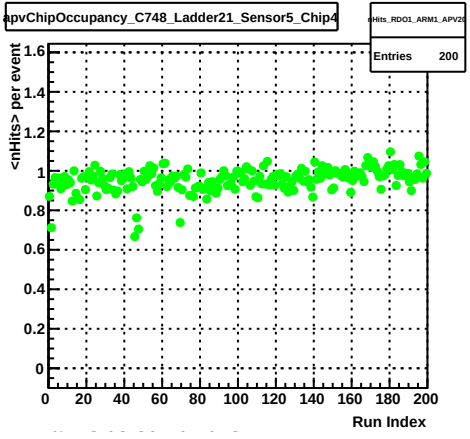
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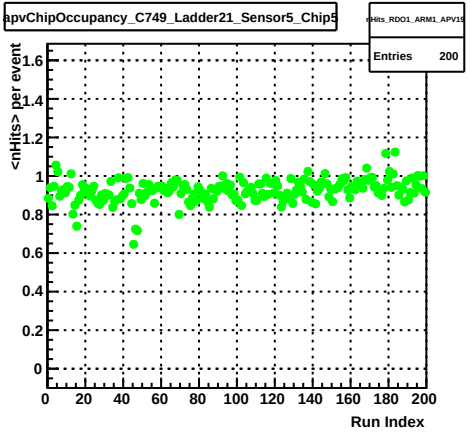
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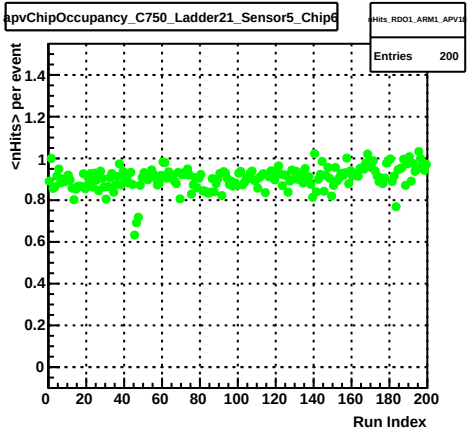
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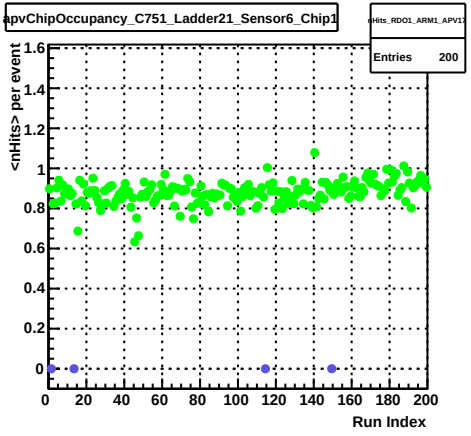
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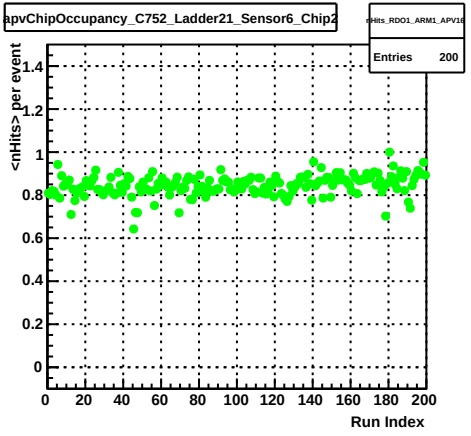
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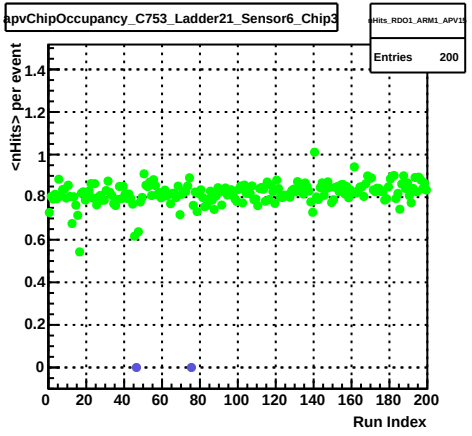
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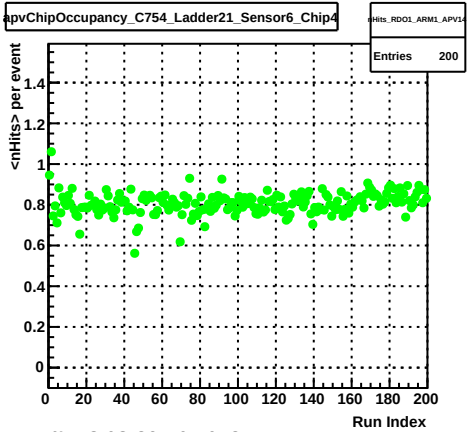
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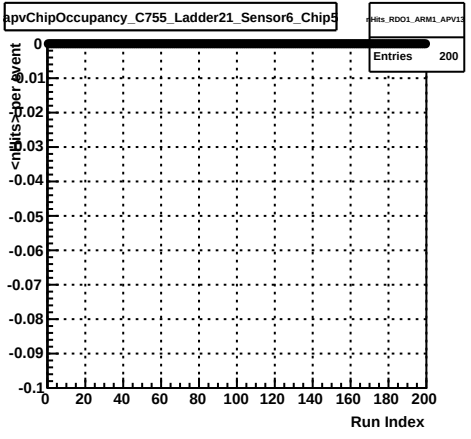
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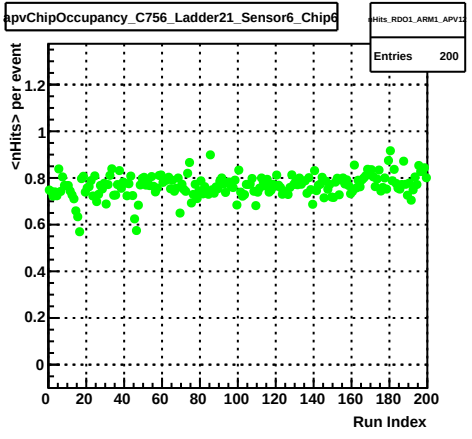
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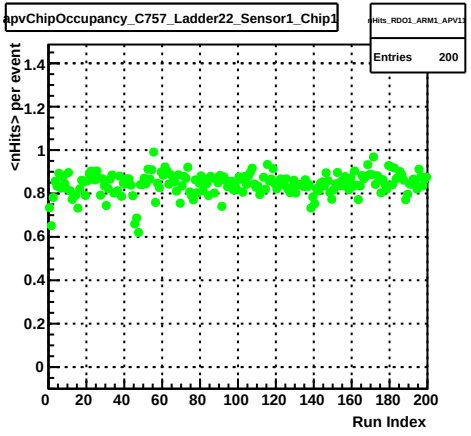
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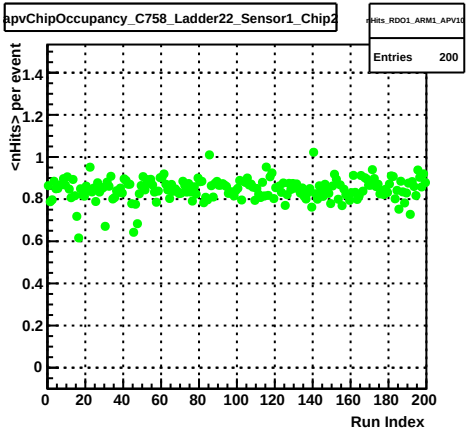
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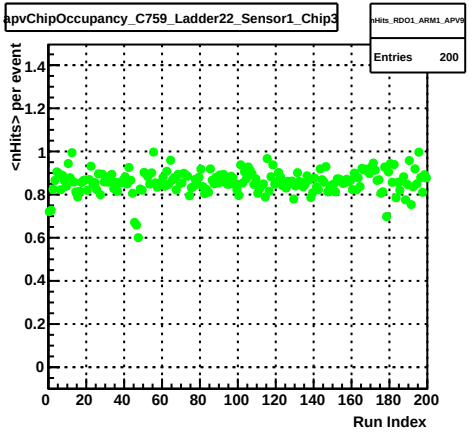
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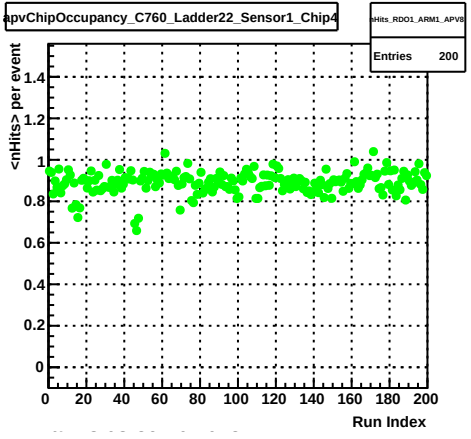
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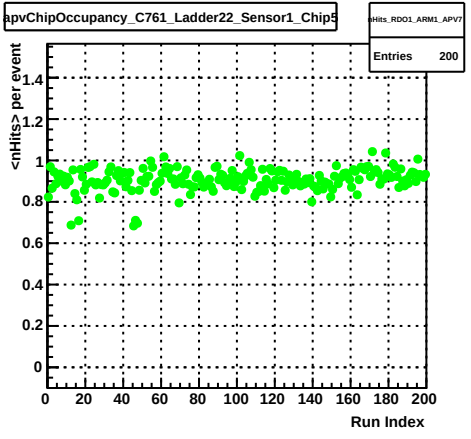
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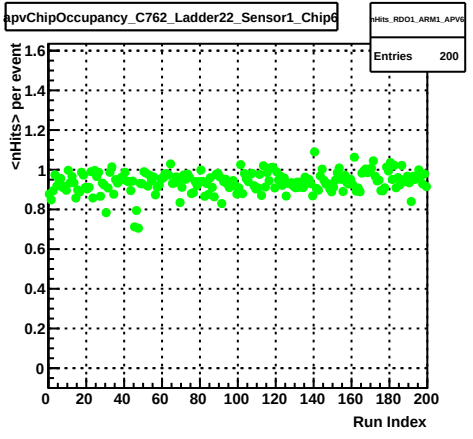
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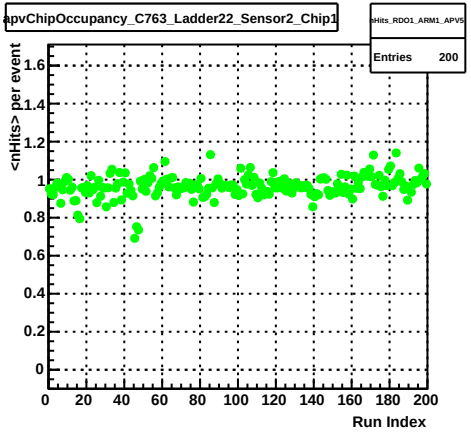
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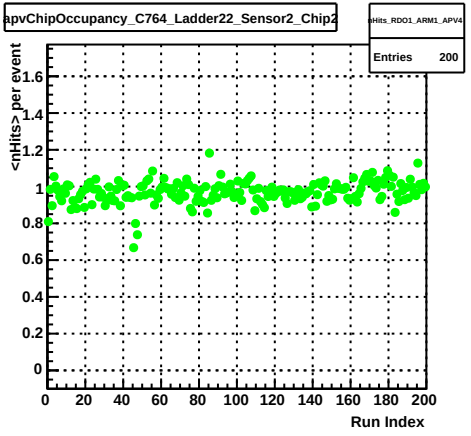
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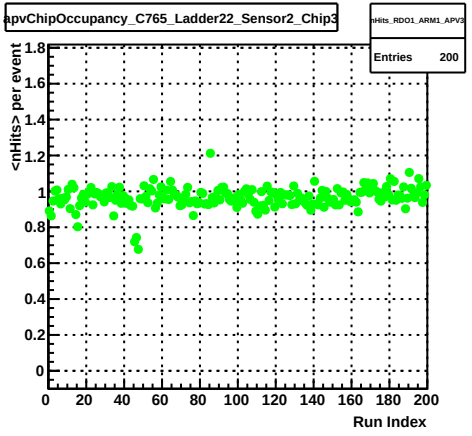
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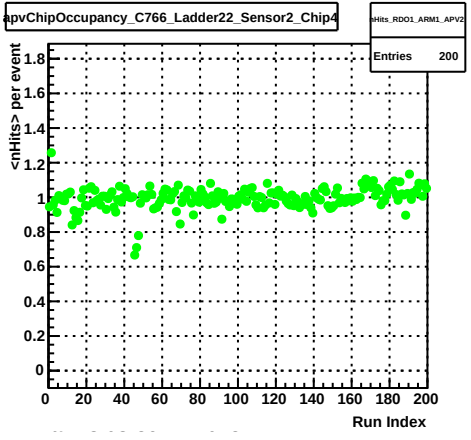
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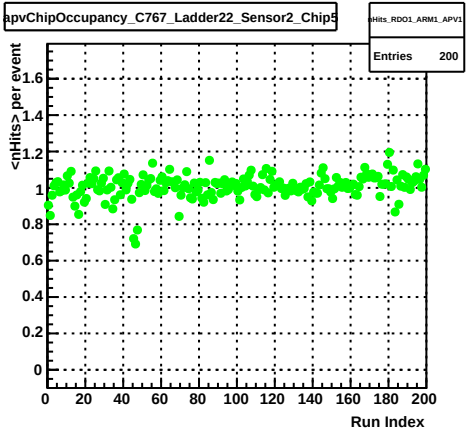
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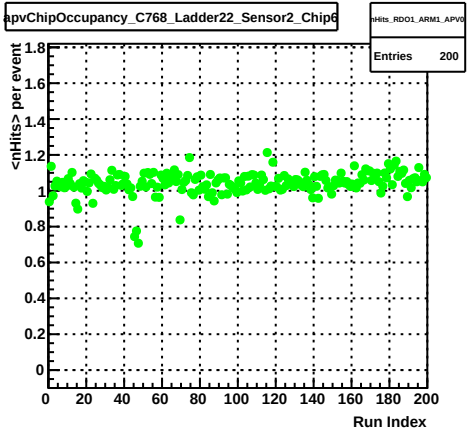
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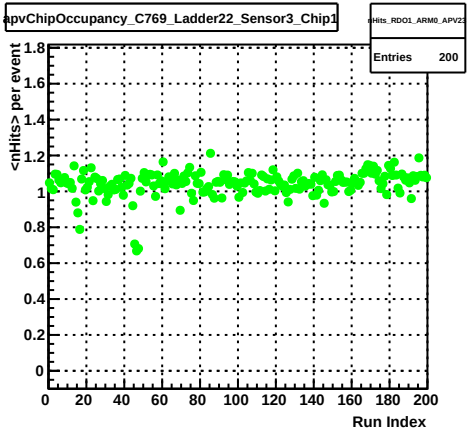
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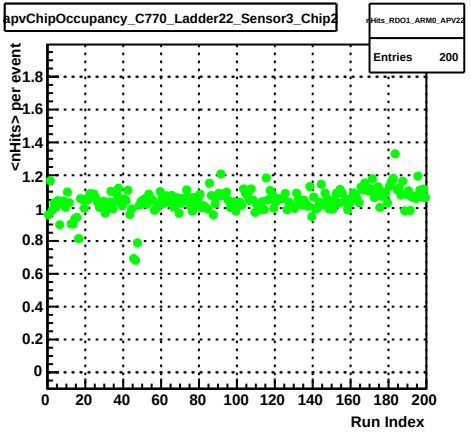
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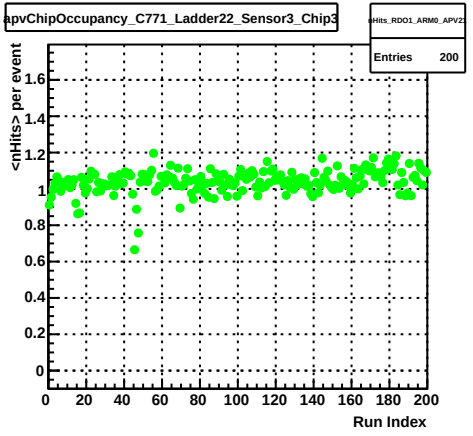
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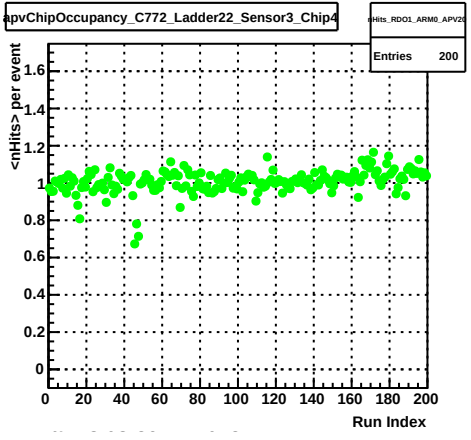
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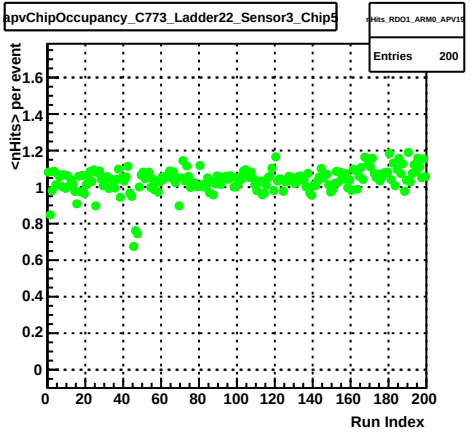
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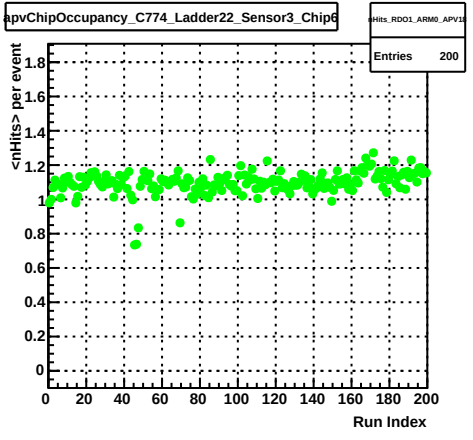
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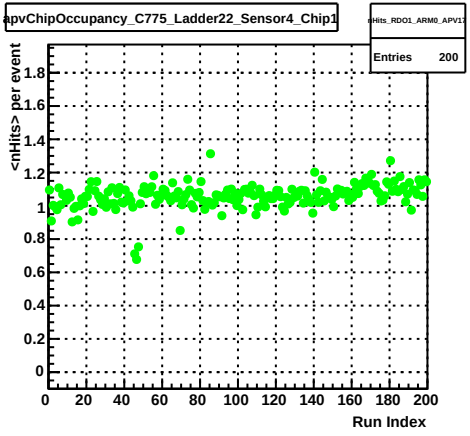
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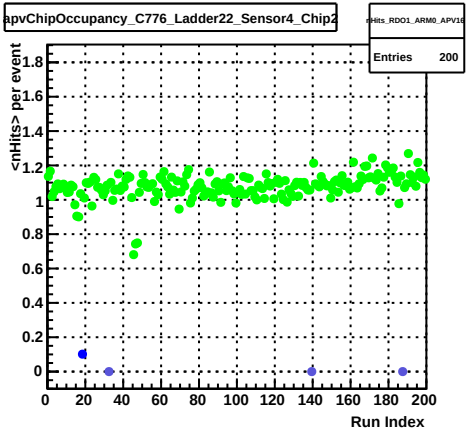
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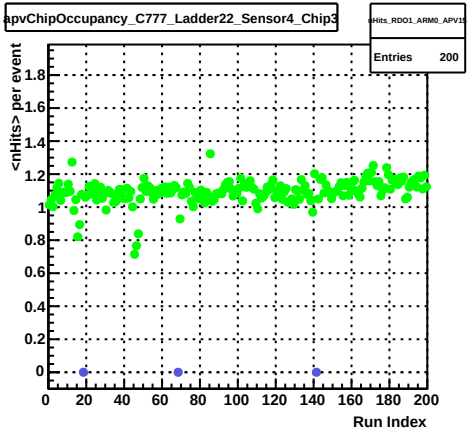
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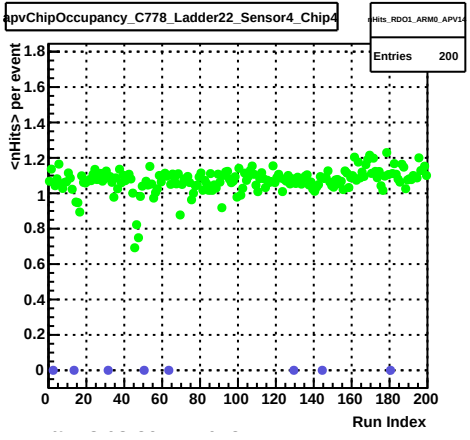
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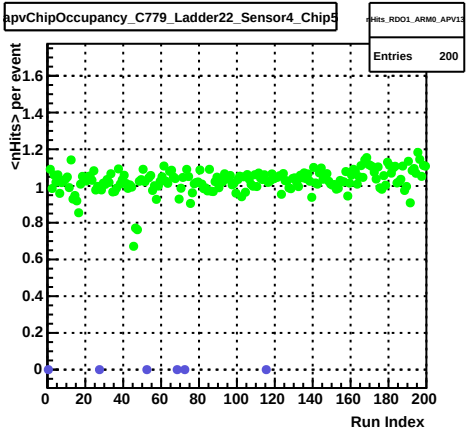
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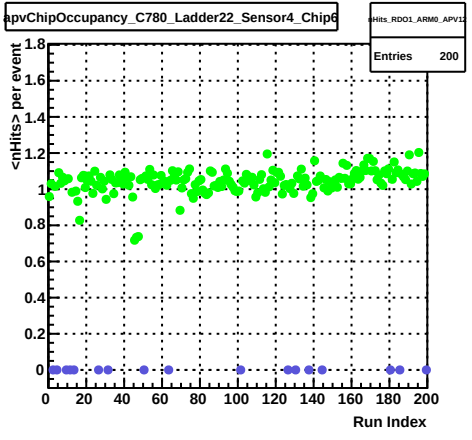
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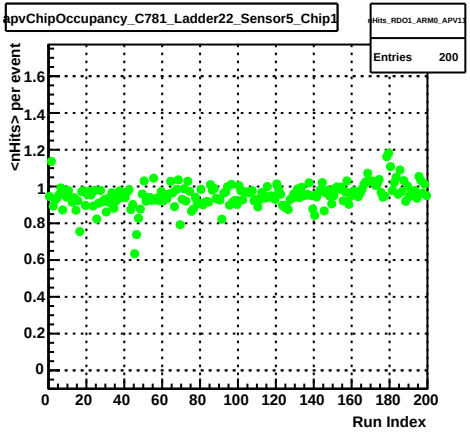
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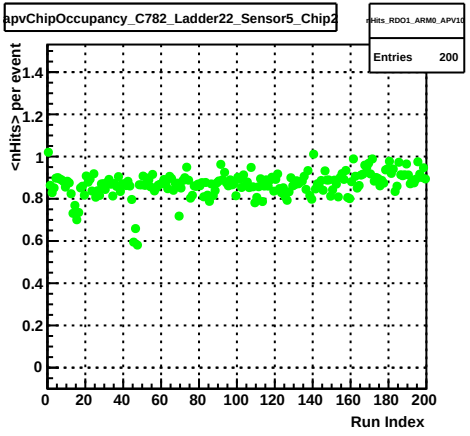
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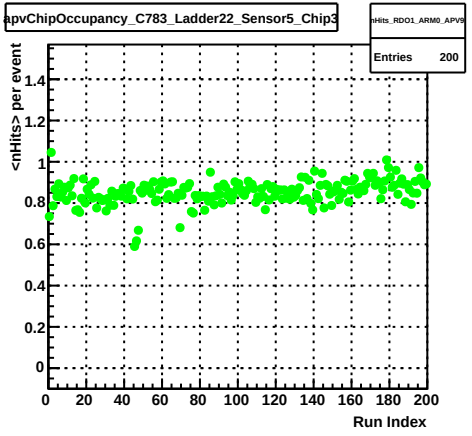
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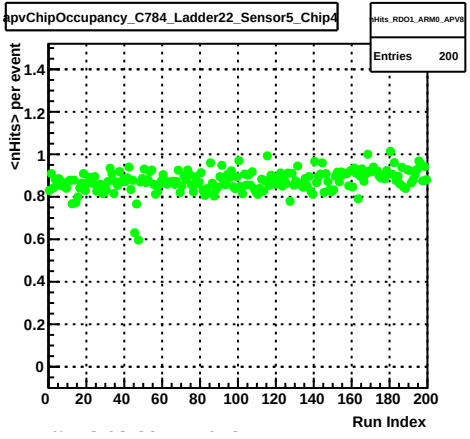
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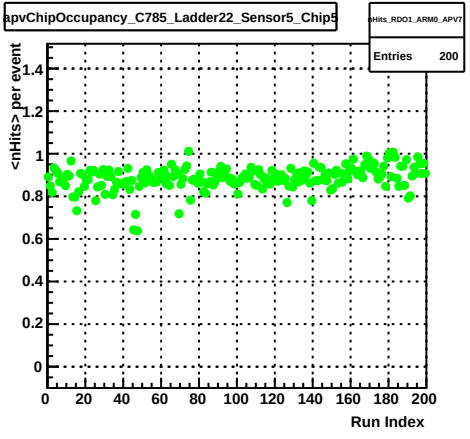
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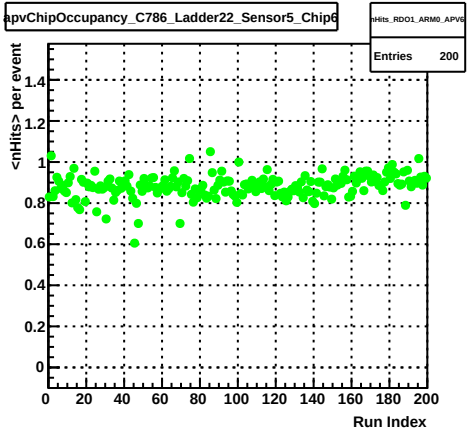
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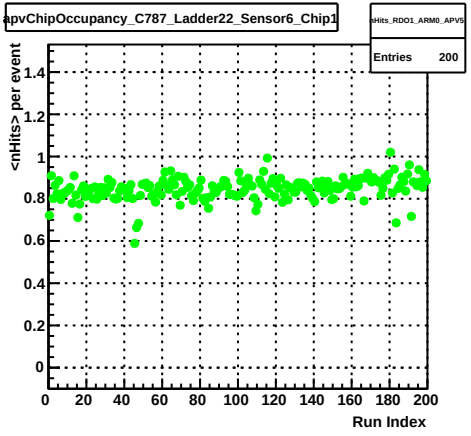
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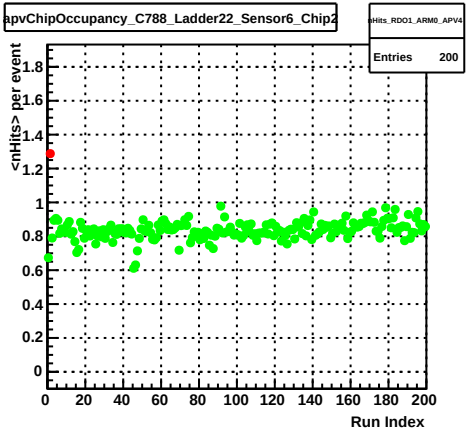
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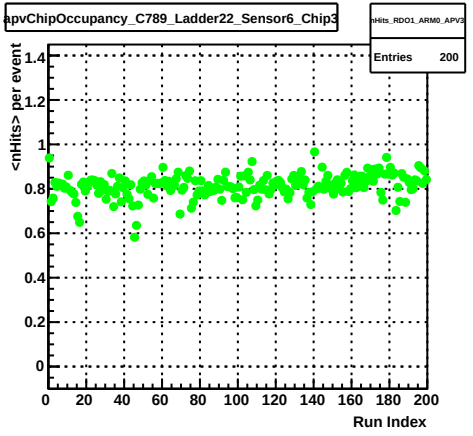
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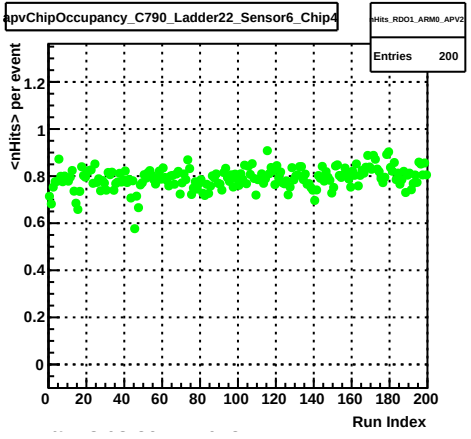
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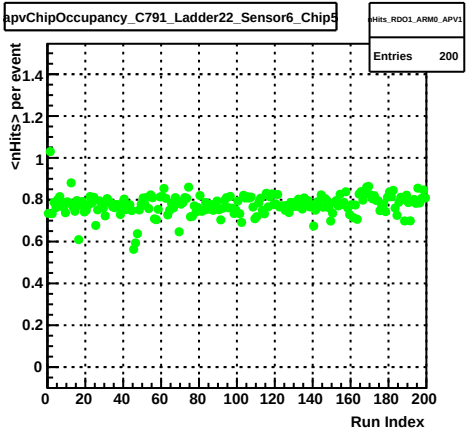
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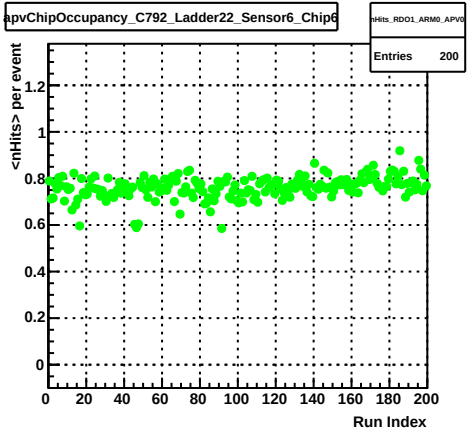
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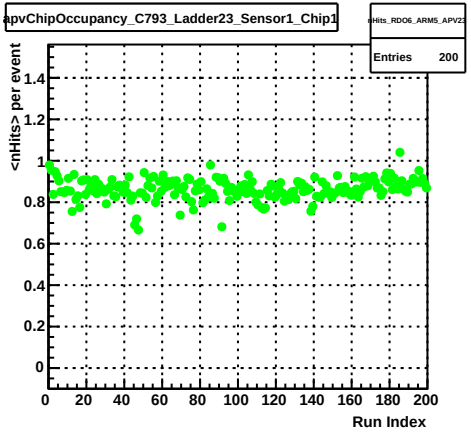
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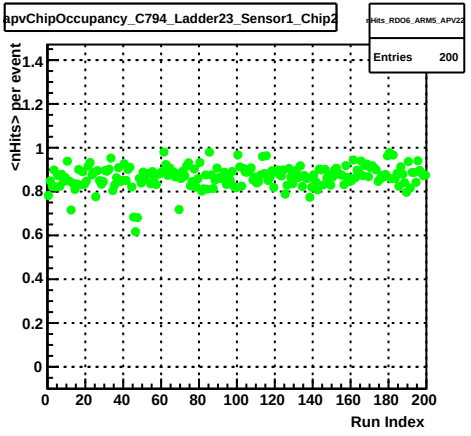
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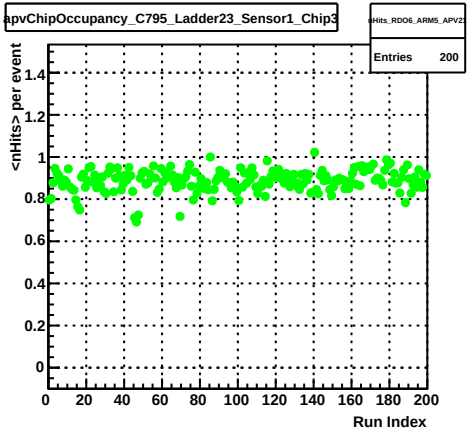
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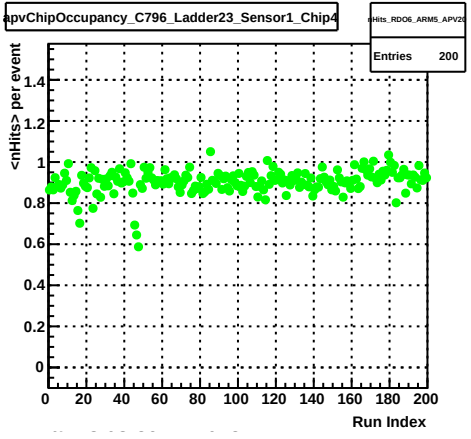
Run Index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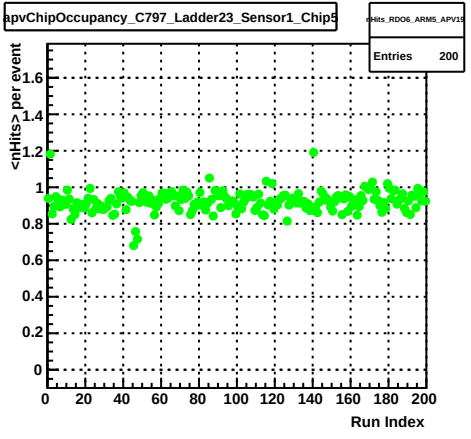
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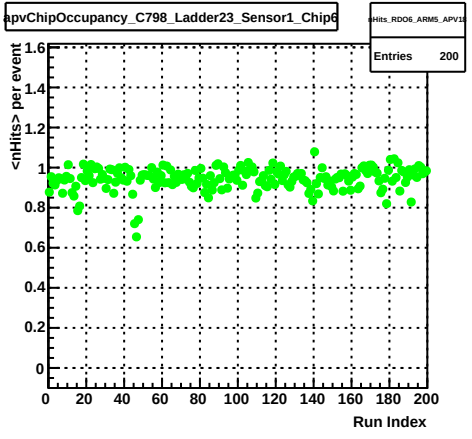
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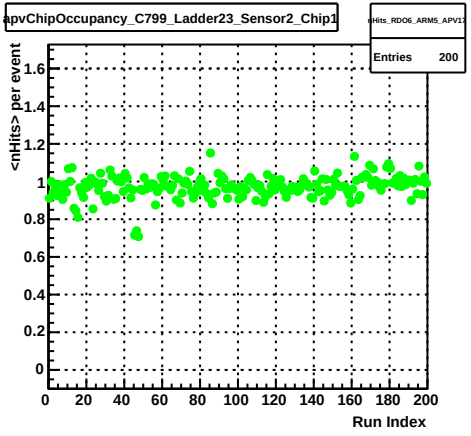
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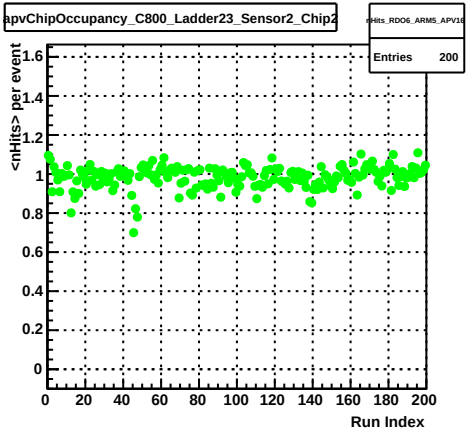
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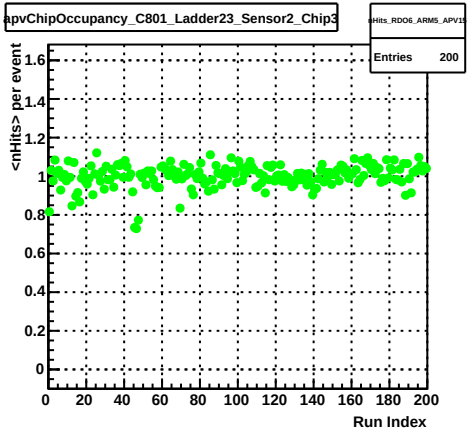
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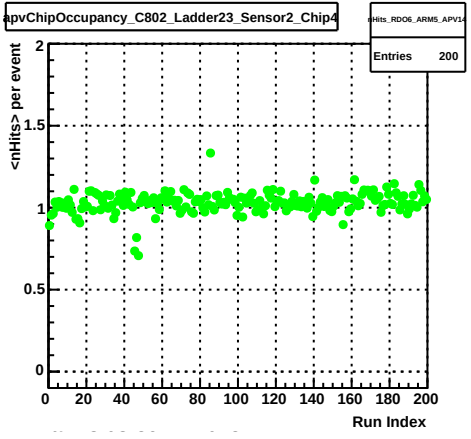
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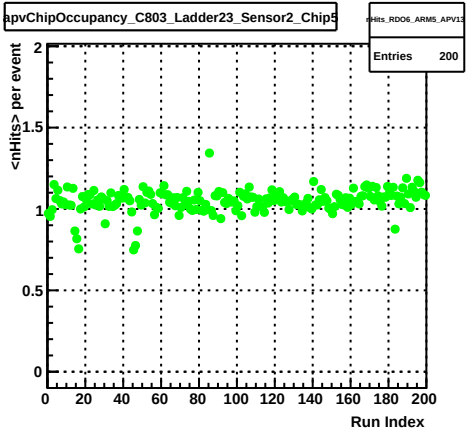
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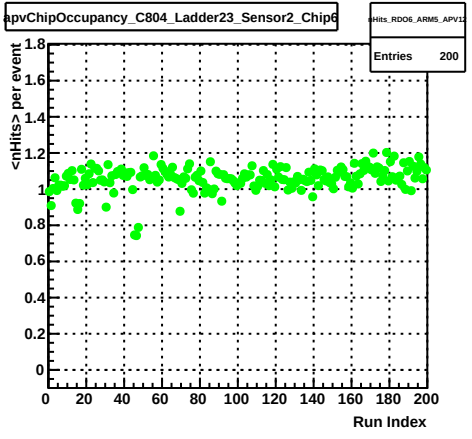
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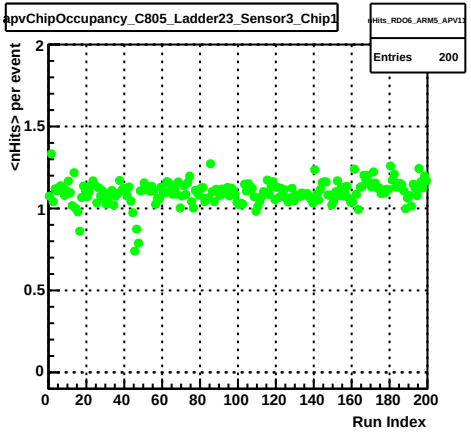
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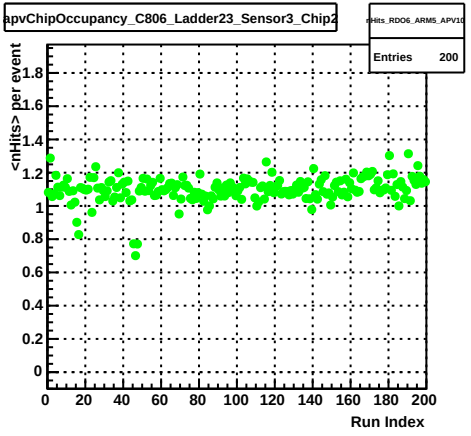
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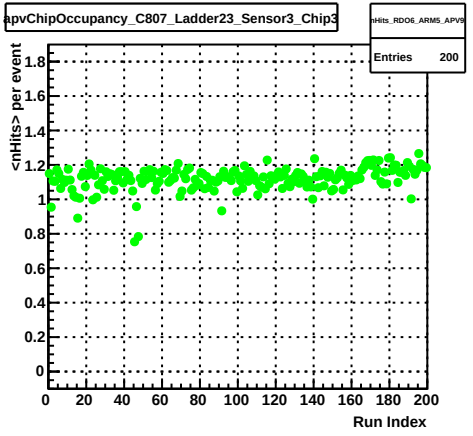
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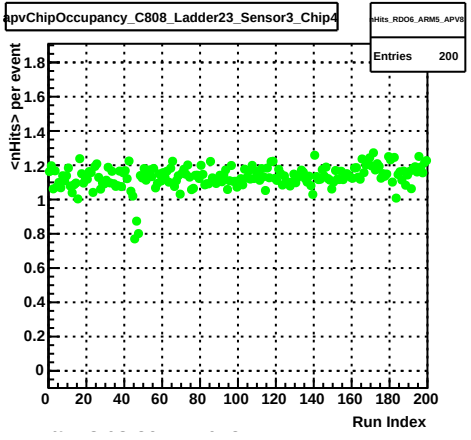
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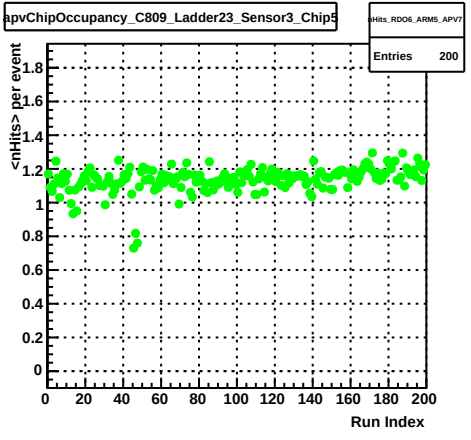
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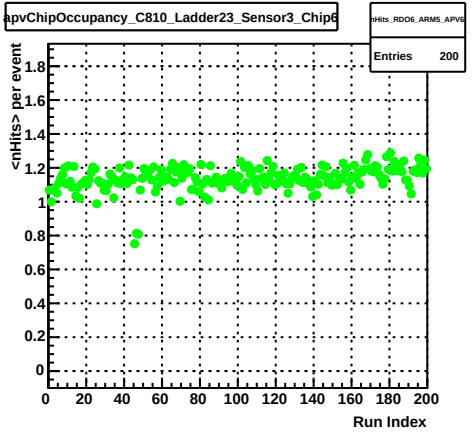
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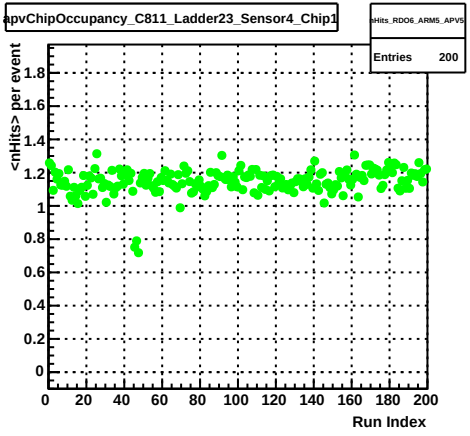
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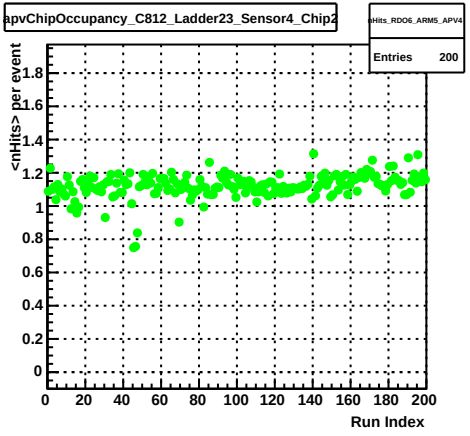
Run Index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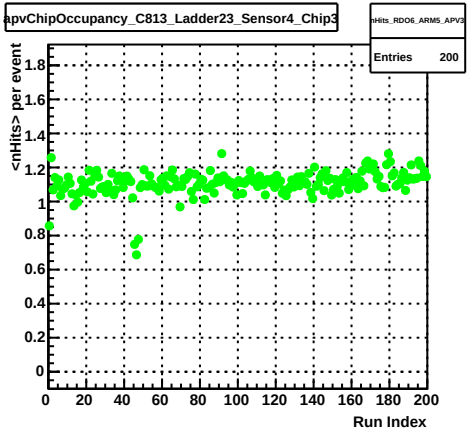
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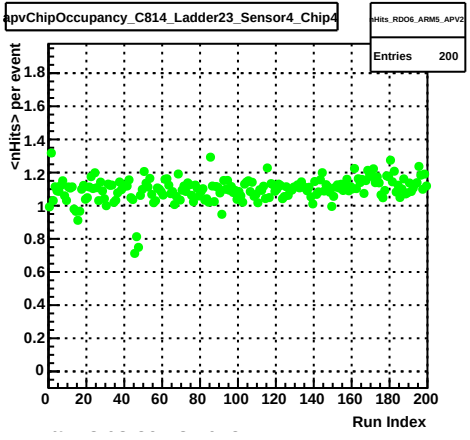
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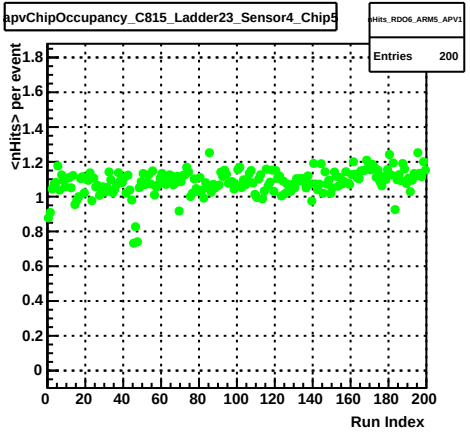
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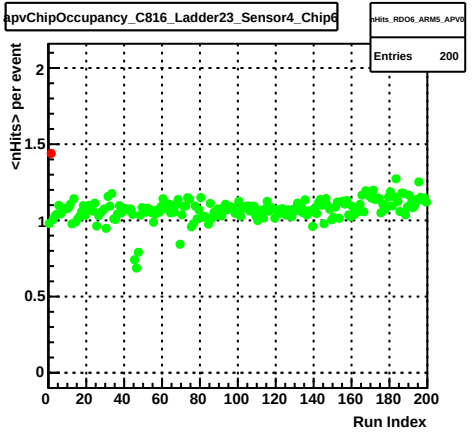
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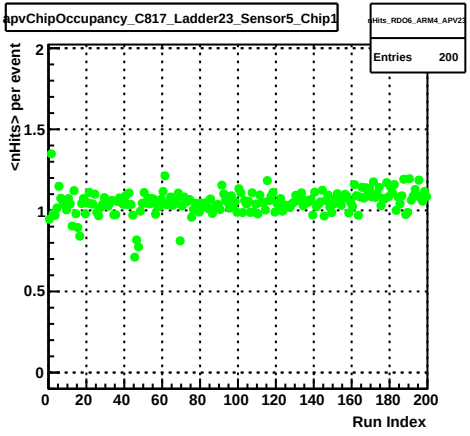
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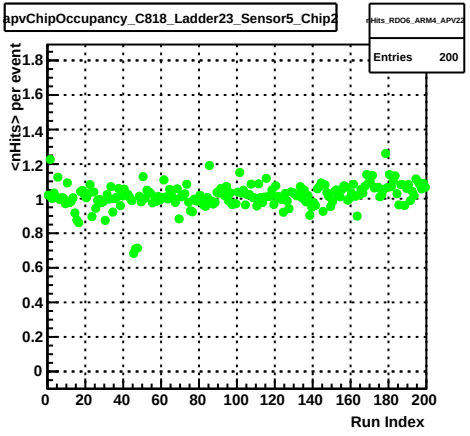
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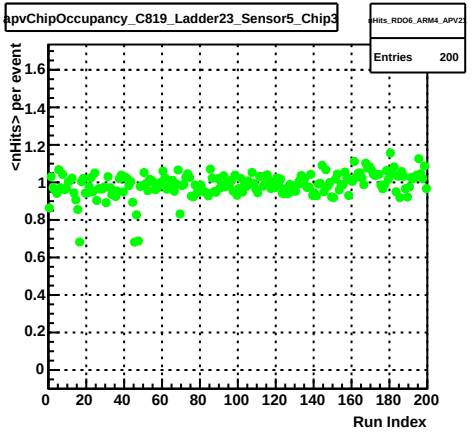
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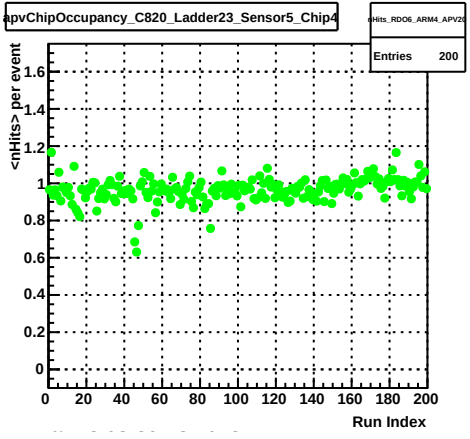
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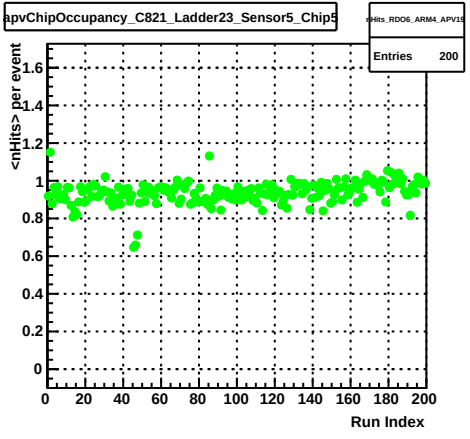
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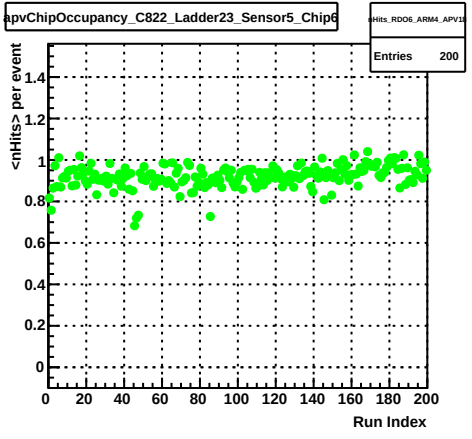
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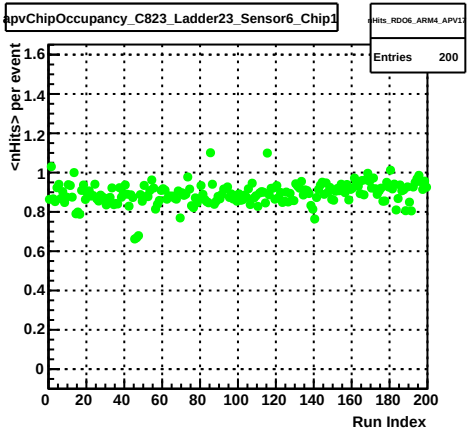
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Entries 200
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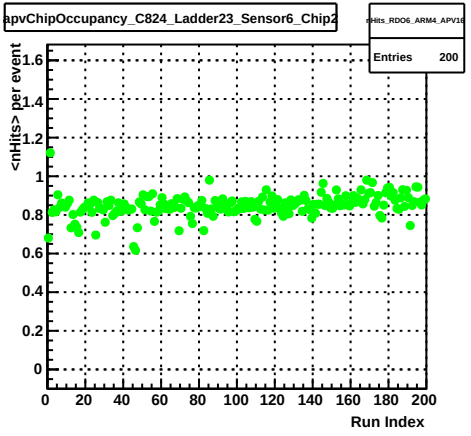
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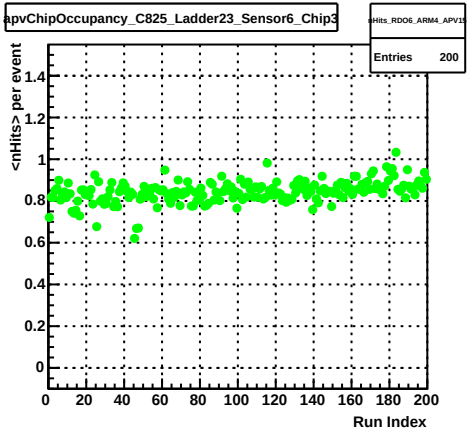
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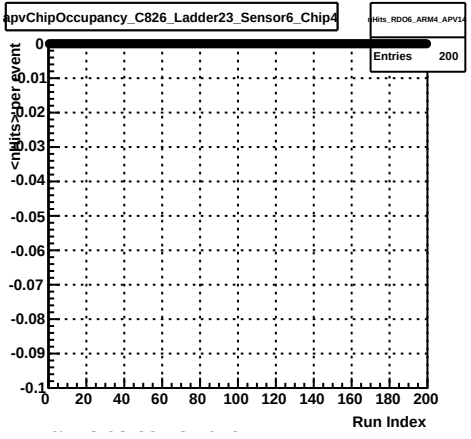
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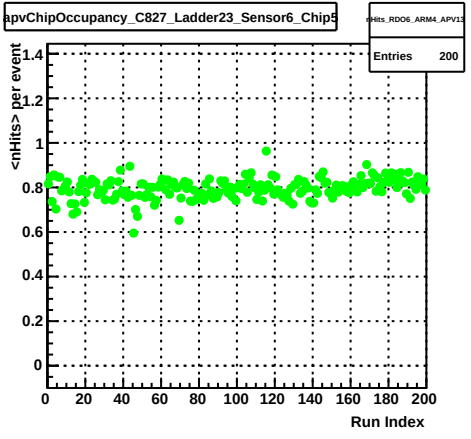
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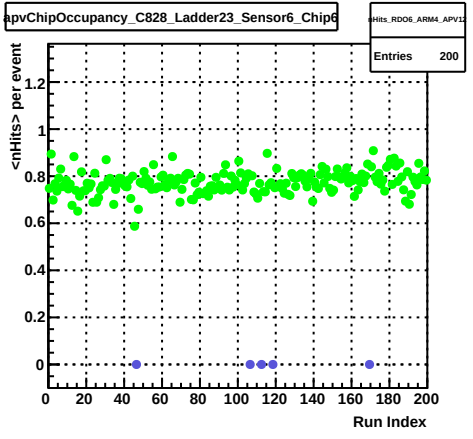
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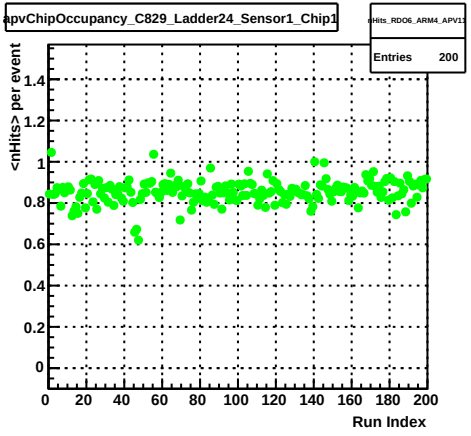
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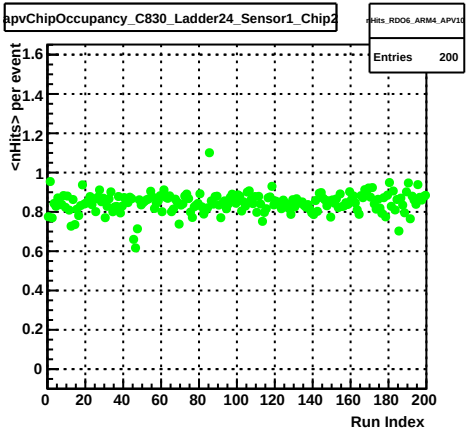
Run Index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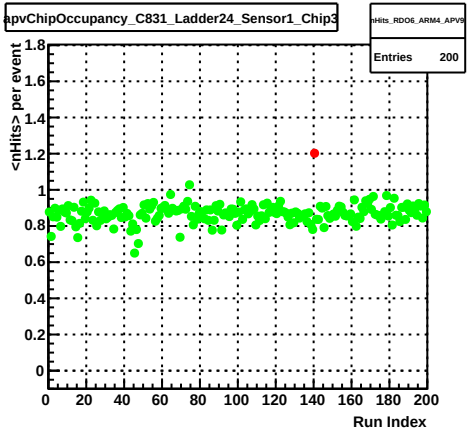
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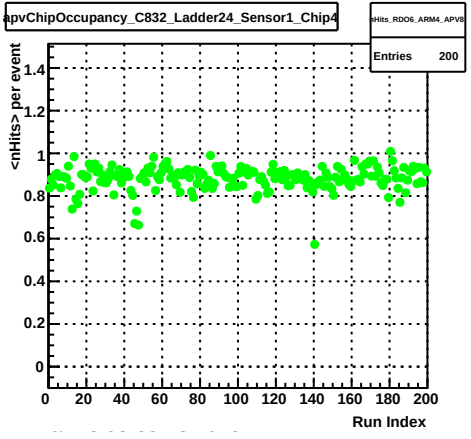
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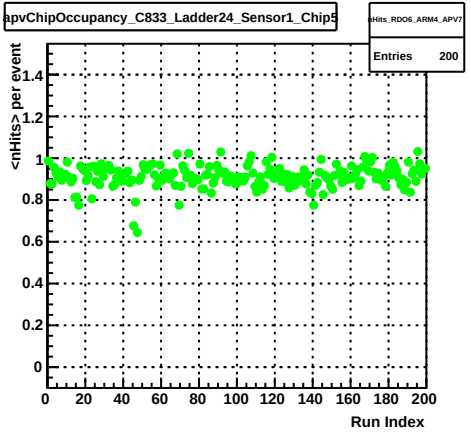
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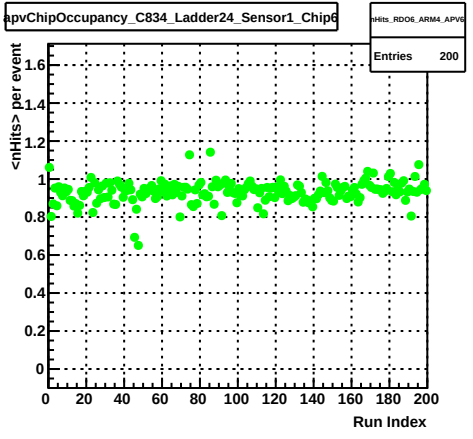
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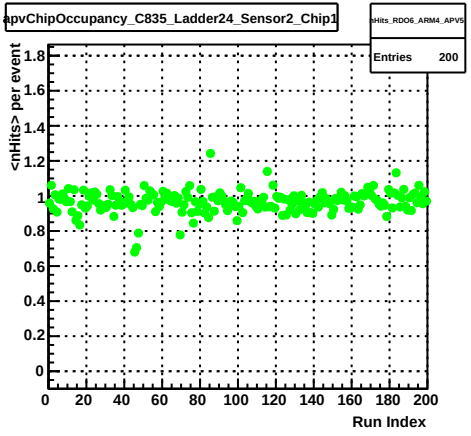
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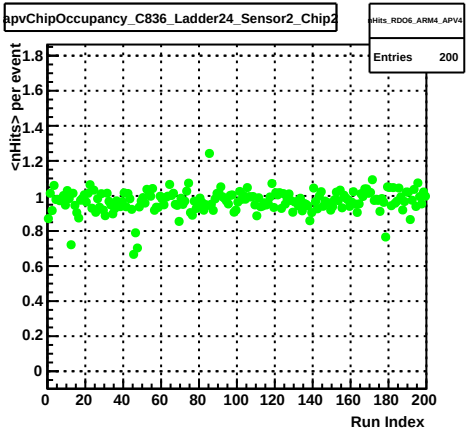
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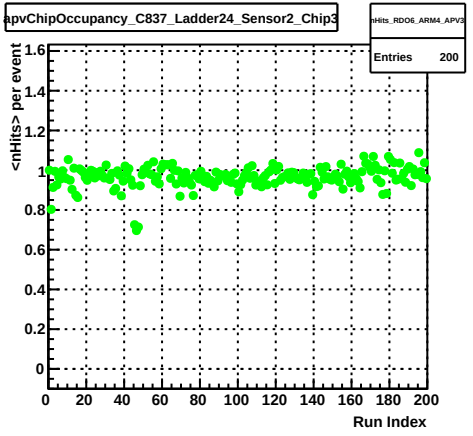
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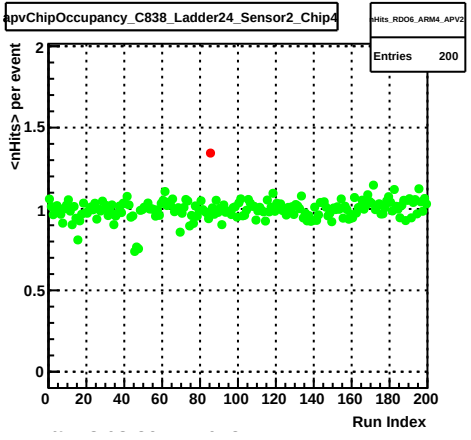
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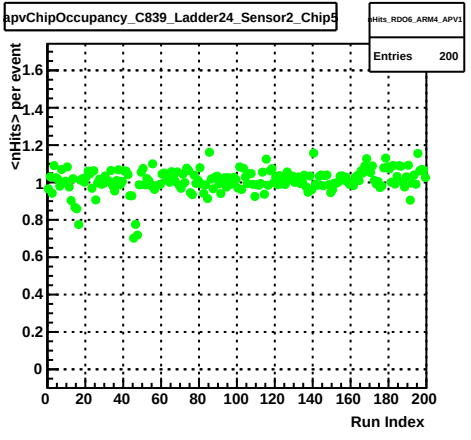
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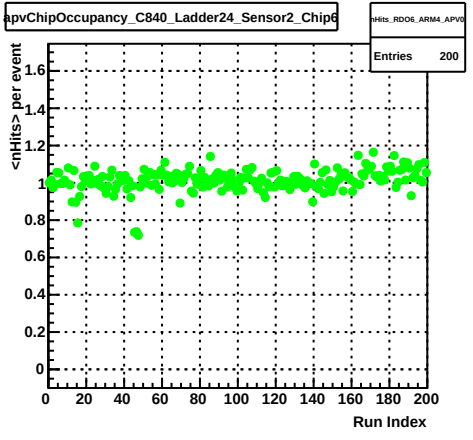
Run Index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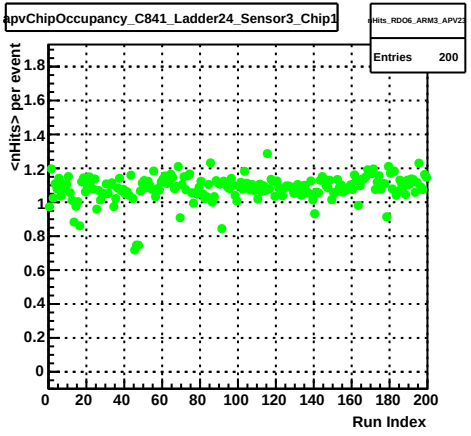
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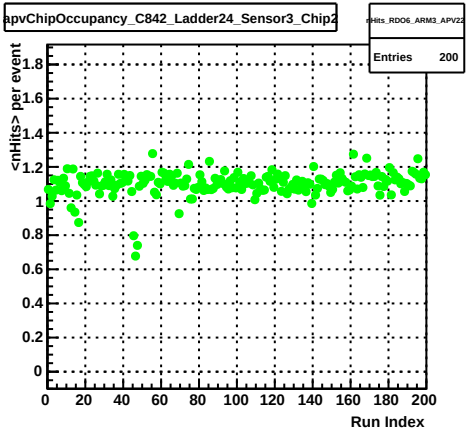
Run Index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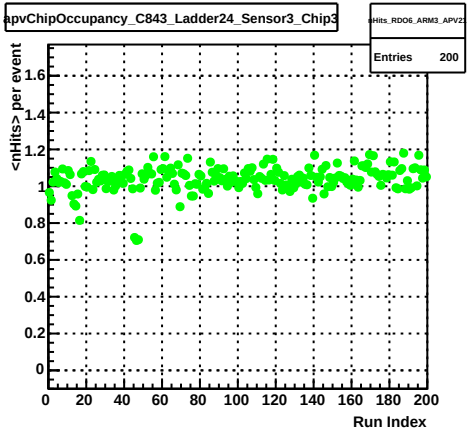
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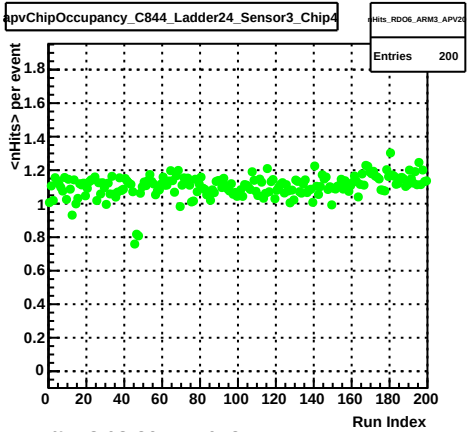
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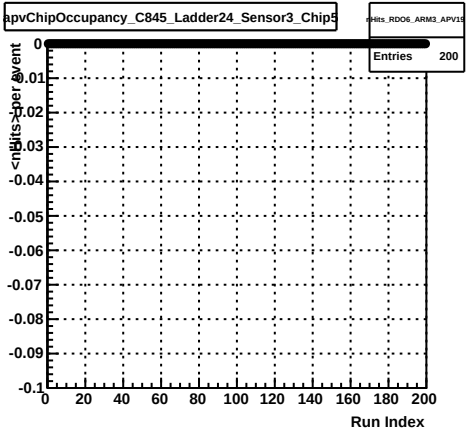
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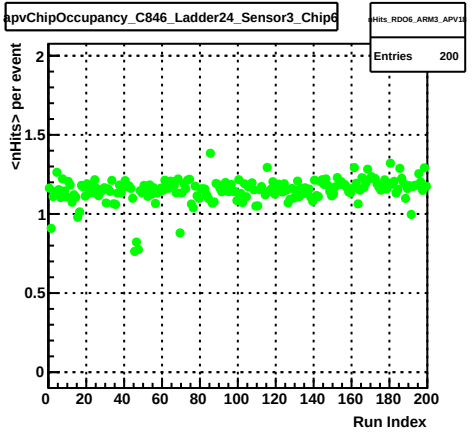
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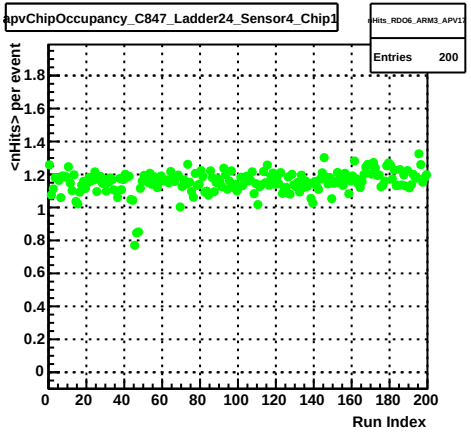
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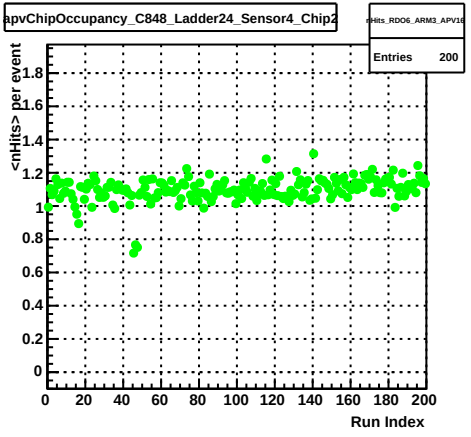
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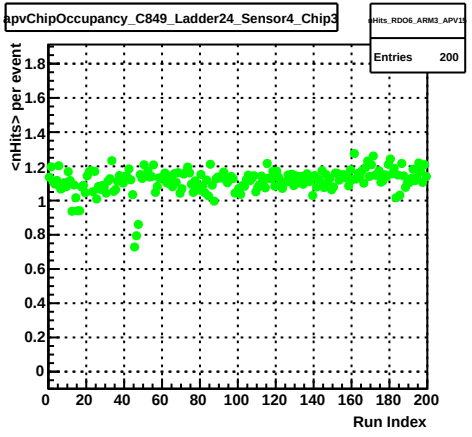
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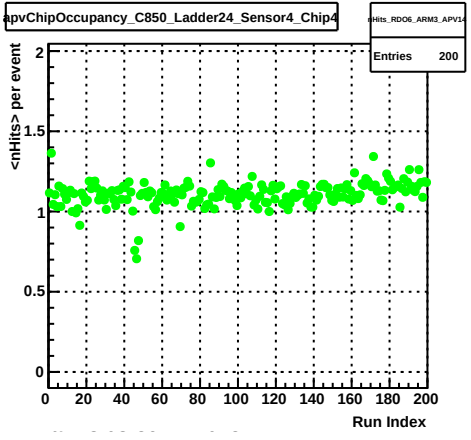
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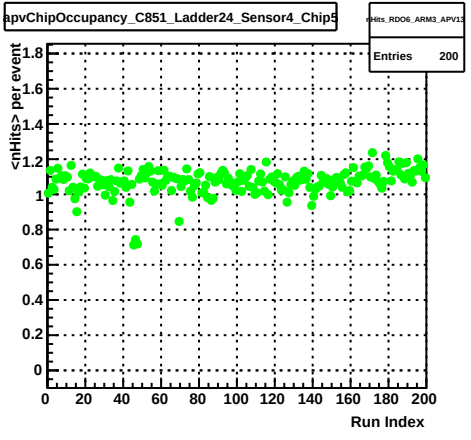
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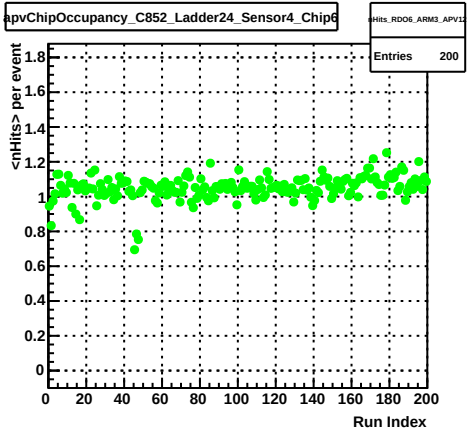
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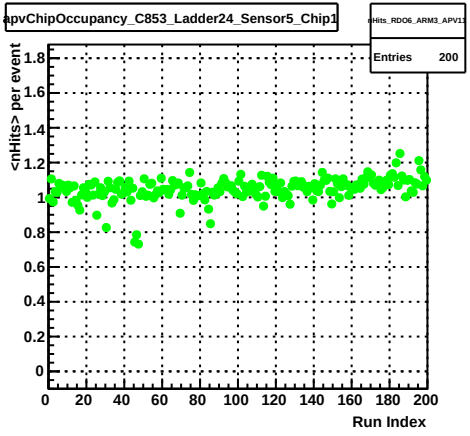
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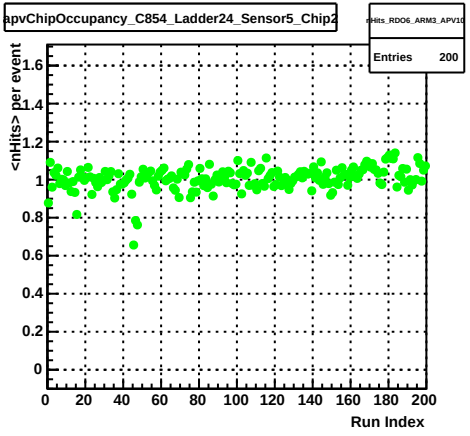
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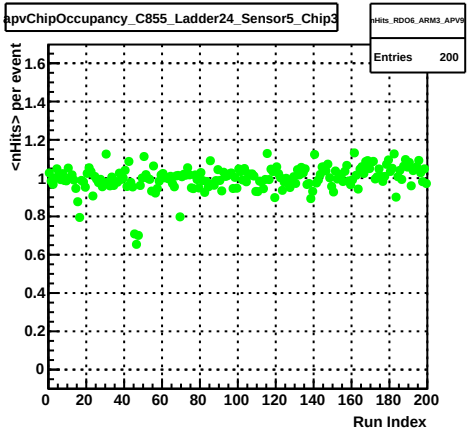
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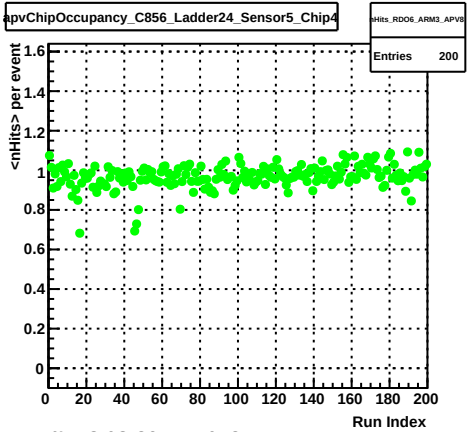
Run Index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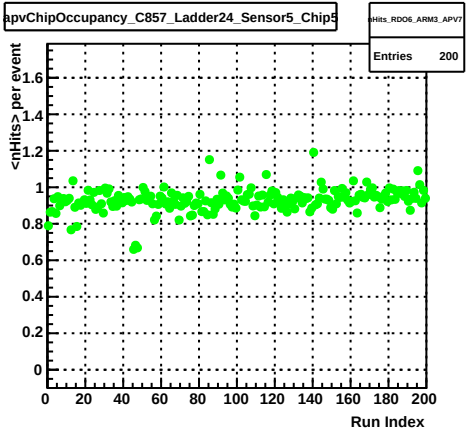
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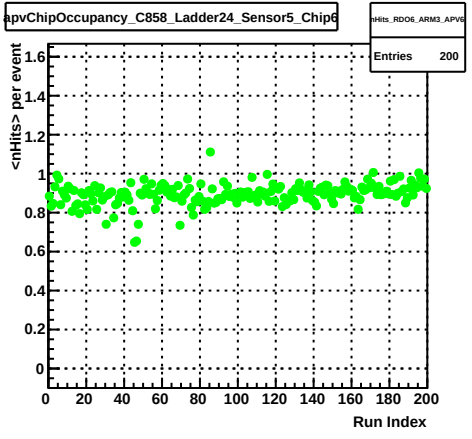
Run Index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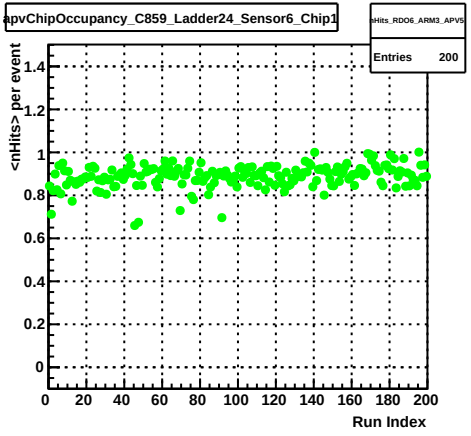
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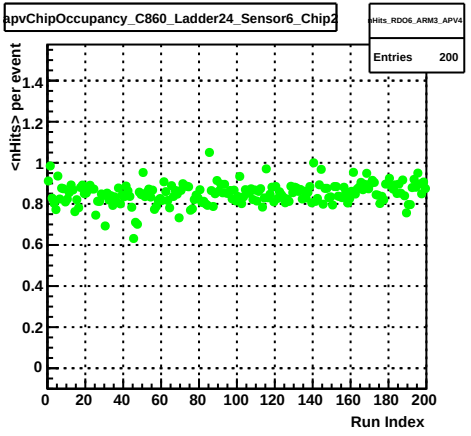
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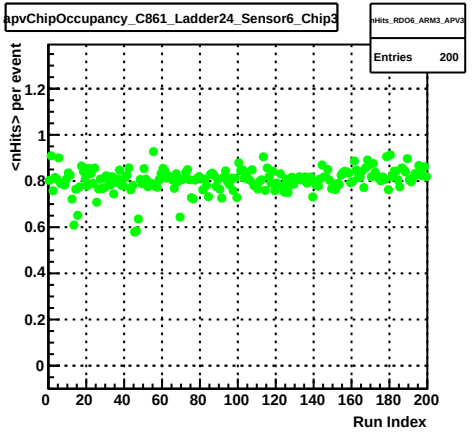
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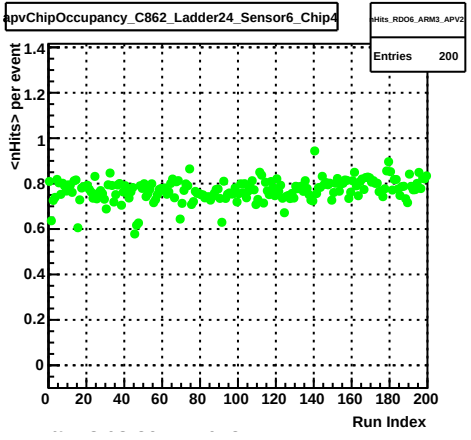
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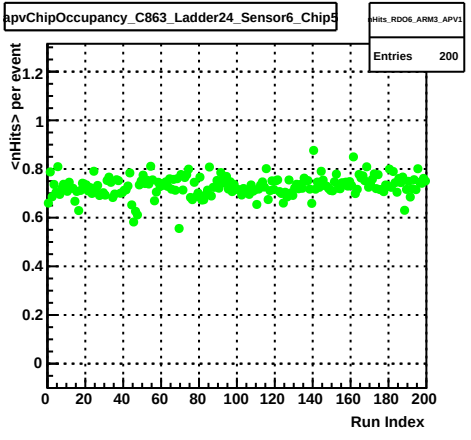
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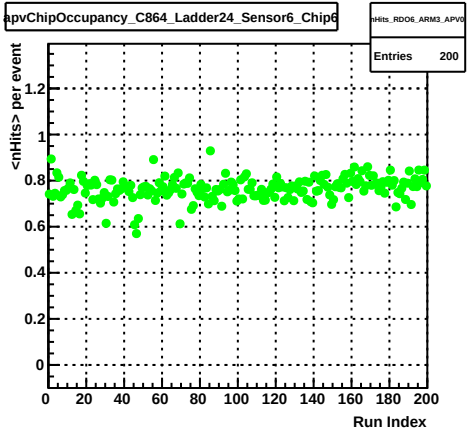
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