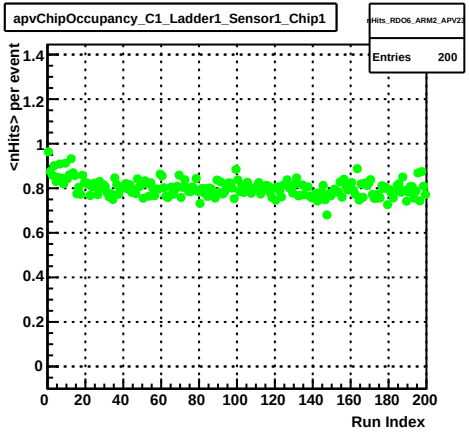
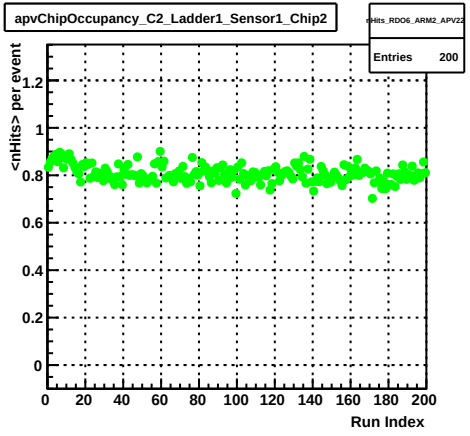
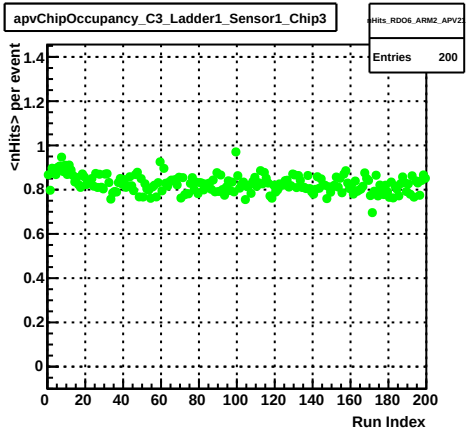
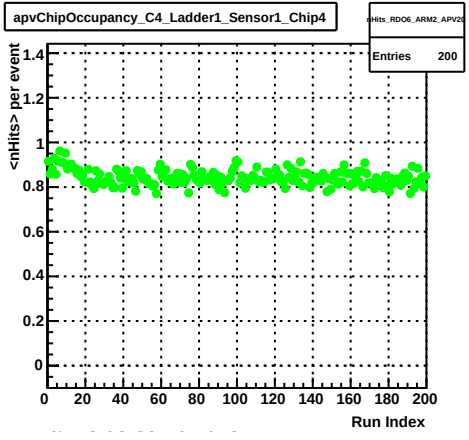
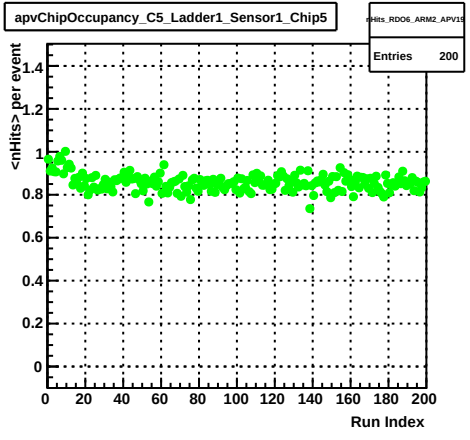
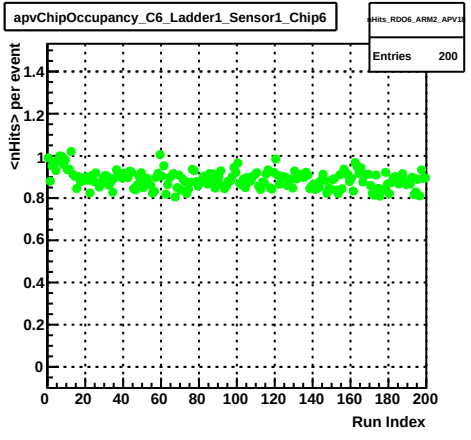

apvChipOccupancy_C1_Ladder1_Sensor1_Chip1
Entries 200
<nHits> per event
Run Index

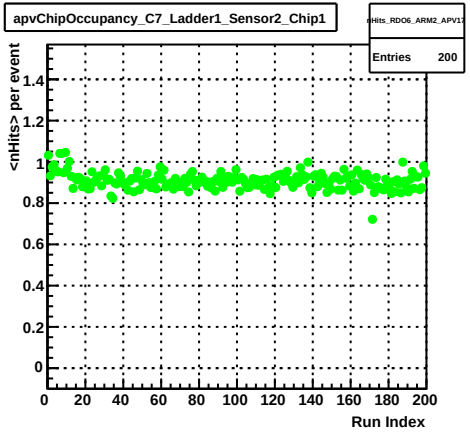

apvChipOccupancy_C2_Ladder1_Sensor1_Chip2
Entries 200
<nHits> per event
Run Index

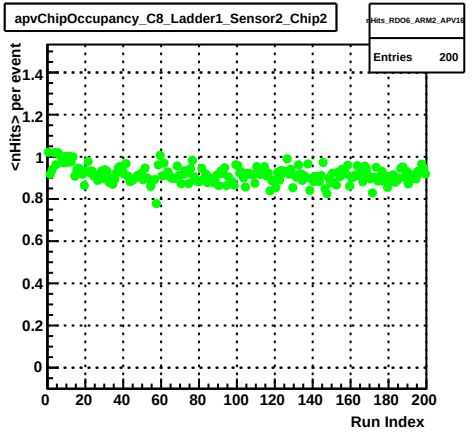

apvChipOccupancy_C3_Ladder1_Sensor1_Chip3
Entries 200
<nHits> per event
Run Index

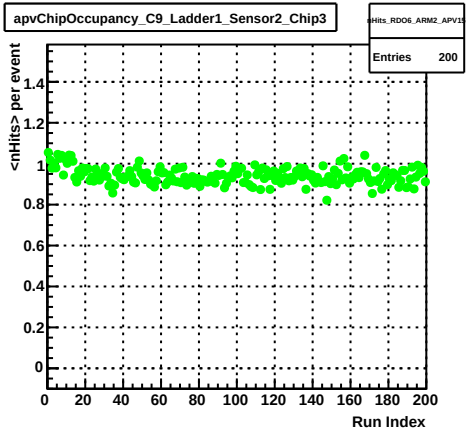

apvChipOccupancy_C4_Ladder1_Sensor1_Chip4
Entries 200
<nHits> per event
Run Index

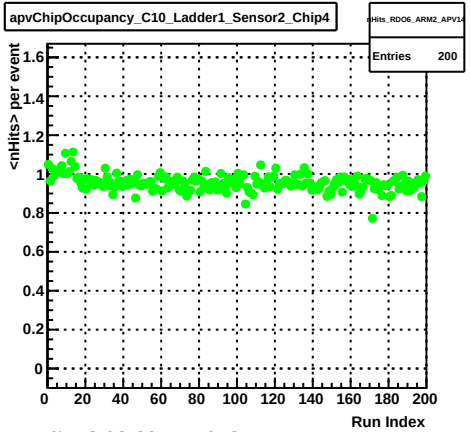

apvChipOccupancy_C5_Ladder1_Sensor1_Chip5
Entries 200
<nHits> per event
Run Index

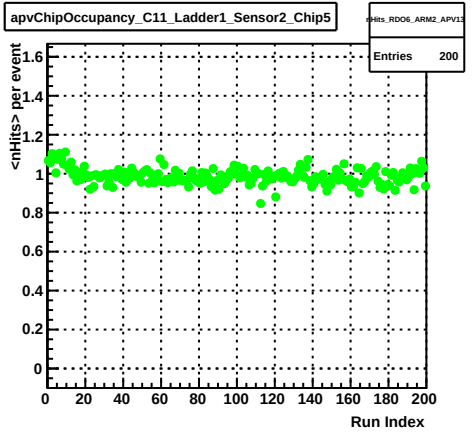

apvChipOccupancy_C6_Ladder1_Sensor1_Chip6
Entries 200
<nHits> per event
Run Index

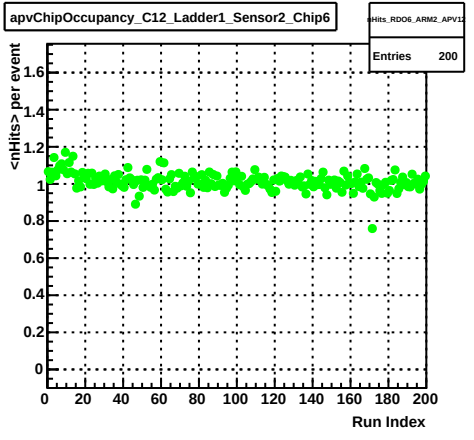

apvChipOccupancy_C7_Ladder1_Sensor2_Chip1
Entries 200
<nHits> per event
Run Index

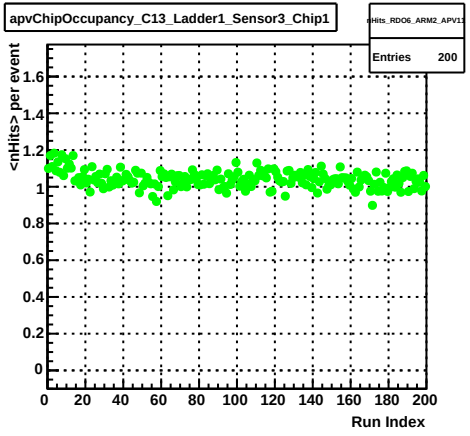

apvChipOccupancy_C8_Ladder1_Sensor2_Chip2
Entries 200
<nHits> per event
Run Index

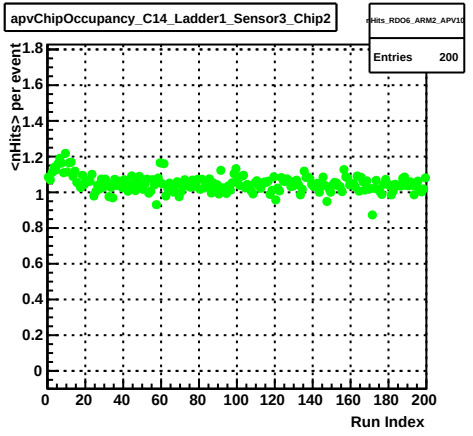

apvChipOccupancy_C9_Ladder1_Sensor2_Chip3
Entries 200
<nHits> per event
Run Index

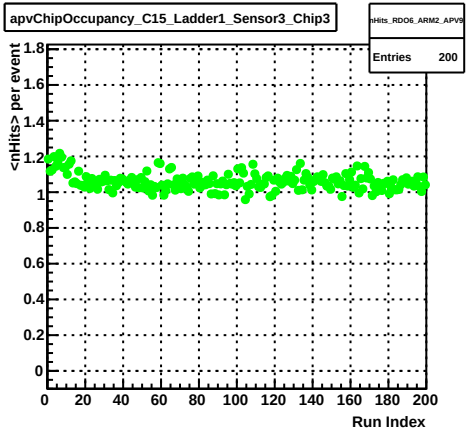

apvChipOccupancy_C10_Ladder1_Sensor2_Chip4
Entries 200
<nHits> per event
Run Index

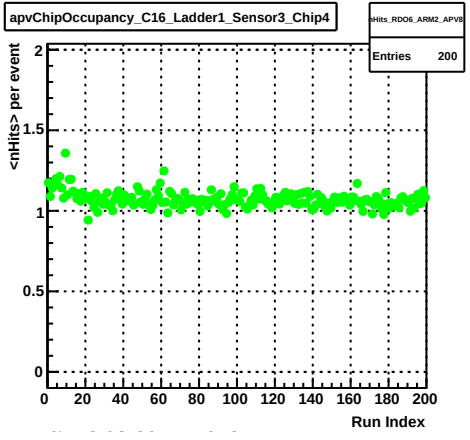

apvChipOccupancy_C11_Ladder1_Sensor2_Chip5
Entries 200
<nHits> per event
Run Index

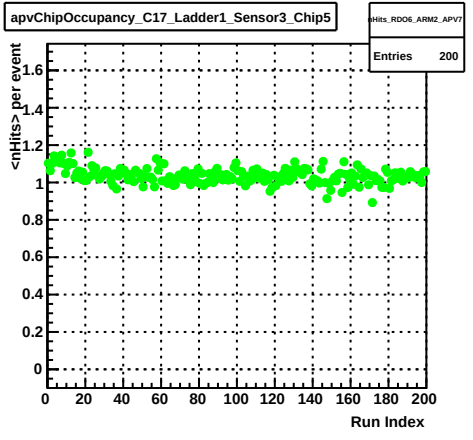

apvChipOccupancy_C12_Ladder1_Sensor2_Chip6
Entries 200
<nHits> per event
Run Index

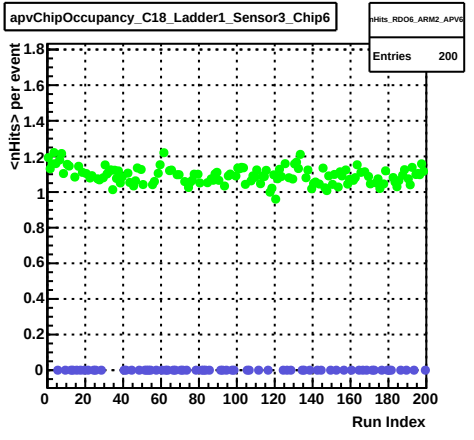

apvChipOccupancy_C13_Ladder1_Sensor3_Chip1
nHits_RDO6_ARM2_APV13
Entries 200
<nHits> per event
Run Index

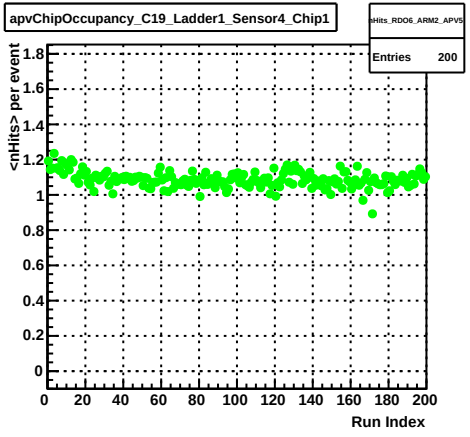

apvChipOccupancy_C14_Ladder1_Sensor3_Chip2
nHits_RDO6_ARM2_APV10
Entries 200
<nHits> per event
Run Index

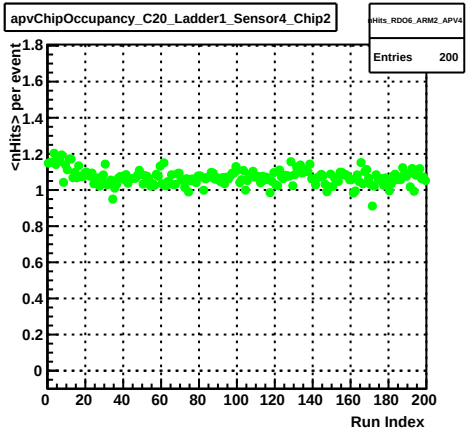

apvChipOccupancy_C15_Ladder1_Sensor3_Chip3
nHits_RDO6_ARM2_APV9
Entries 200
<nHits> per event
Run Index

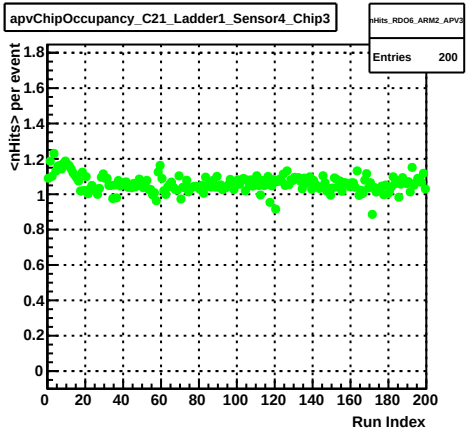

apvChipOccupancy_C16_Ladder1_Sensor3_Chip4
nHits_RDO6_ARM2_APV8
Entries 200
<nHits> per event
Run Index

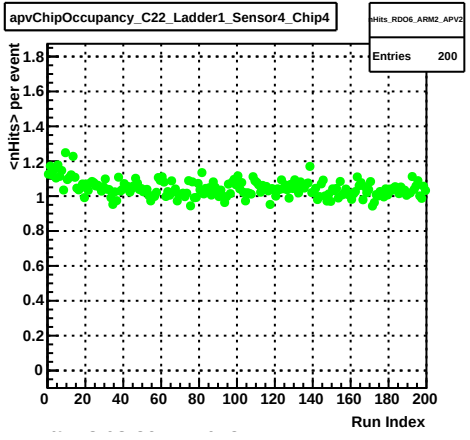

apvChipOccupancy_C17_Ladder1_Sensor3_Chip5
nHits_RDO6_ARM2_APV7
Entries 200
<nHits> per event
Run Index

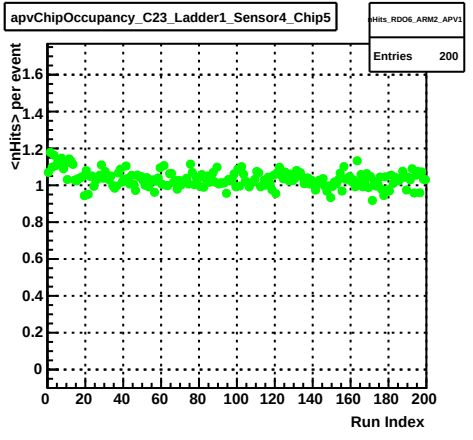

apvChipOccupancy_C18_Ladder1_Sensor3_Chip6
nHits_RDO6_ARM2_APV6
Entries 200
<nHits> per event
Run Index

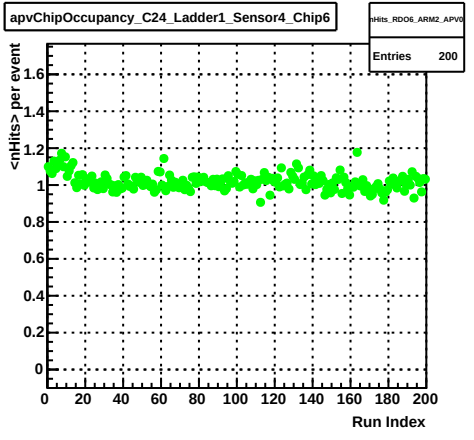

apvChipOccupancy_C19_Ladder1_Sensor4_Chip1
nHits_RDO6_ARM2_APV5
Entries 200
<nHits> per event
Run Index

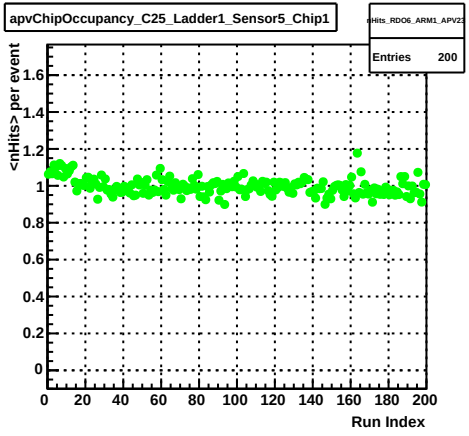

apvChipOccupancy_C20_Ladder1_Sensor4_Chip2
nHits_RDO6_ARM2_APV4
Entries 200
<nHits> per event
Run Index

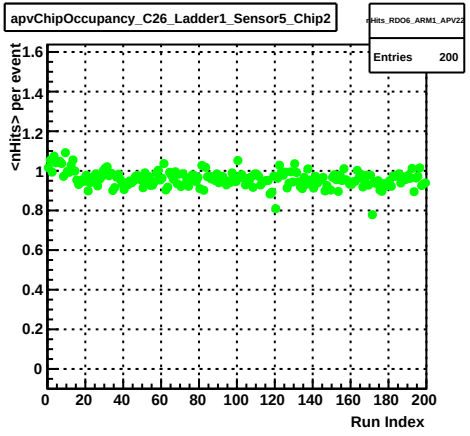

apvChipOccupancy_C21_Ladder1_Sensor4_Chip3
nHits_RDO6_ARM2_APV3
Entries 200
<nHits> per event
Run Index

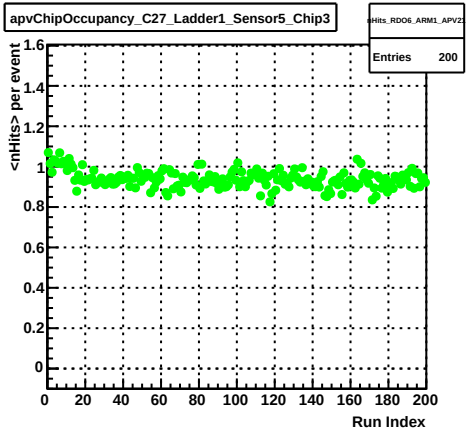

apvChipOccupancy_C22_Ladder1_Sensor4_Chip4
nHits_RDO6_ARM2_APV2
Entries 200
<nHits> per event
Run Index

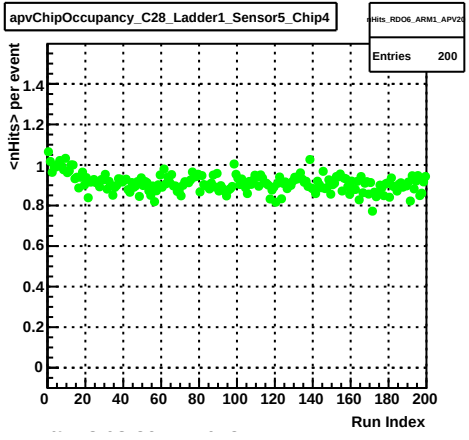

apvChipOccupancy_C23_Ladder1_Sensor4_Chip5
nHits_RDO6_ARM2_APV1
Entries 200
<nHits> per event
Run Index

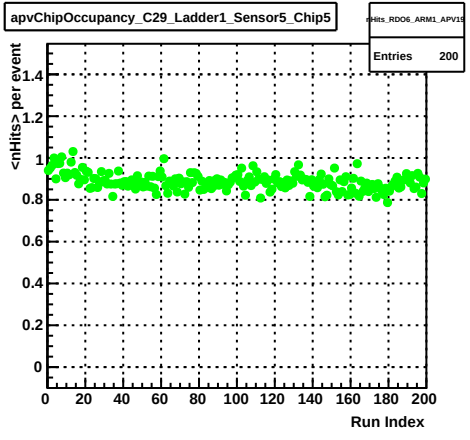

apvChipOccupancy_C24_Ladder1_Sensor4_Chip6
nHits_RDO6_ARM2_APV0
Entries 200
<nHits> per event
Run Index

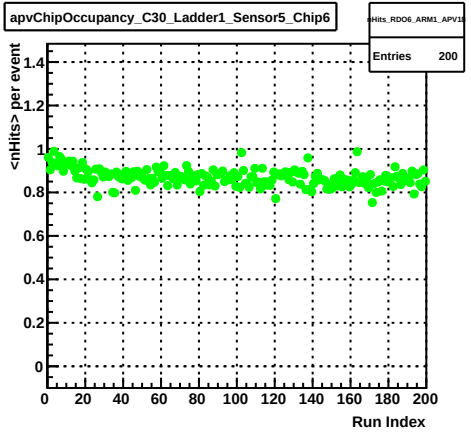

apvChipOccupancy_C25_Ladder1_Sensor5_Chip1
Entries 200
<nHits> per event
Run Index

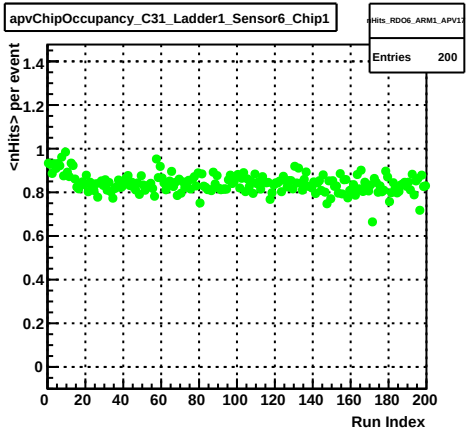

apvChipOccupancy_C26_Ladder1_Sensor5_Chip2
Entries 200
<nHits> per event
Run Index

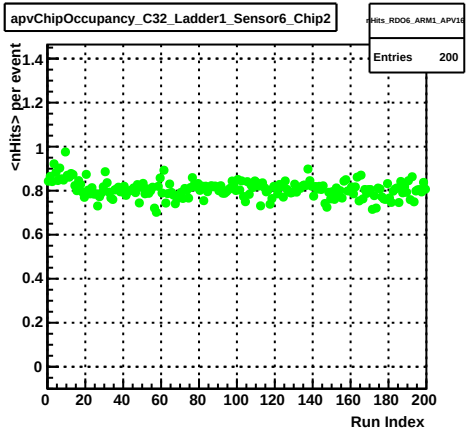

apvChipOccupancy_C27_Ladder1_Sensor5_Chip3
Entries 200
<nHits> per event
Run Index

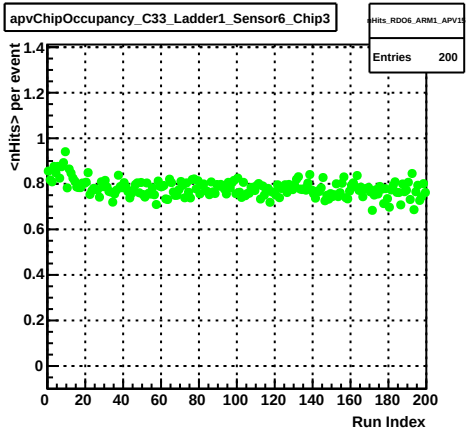

apvChipOccupancy_C28_Ladder1_Sensor5_Chip4
Entries 200
<nHits> per event
Run Index

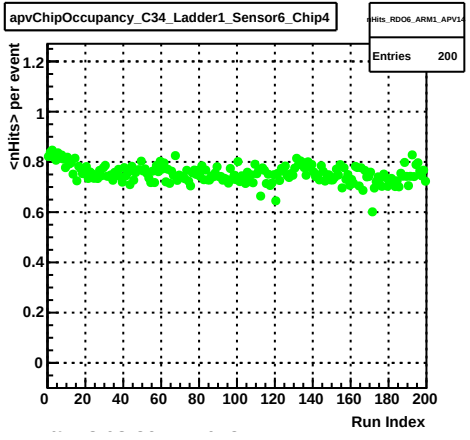

apvChipOccupancy_C29_Ladder1_Sensor5_Chip5
Entries 200
<nHits> per event
Run Index

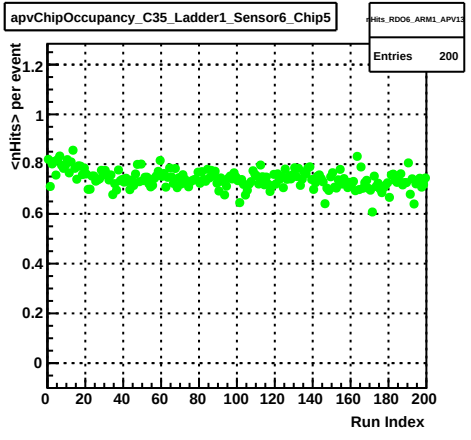

apvChipOccupancy_C30_Ladder1_Sensor5_Chip6
Entries 200
<nHits> per event
Run Index

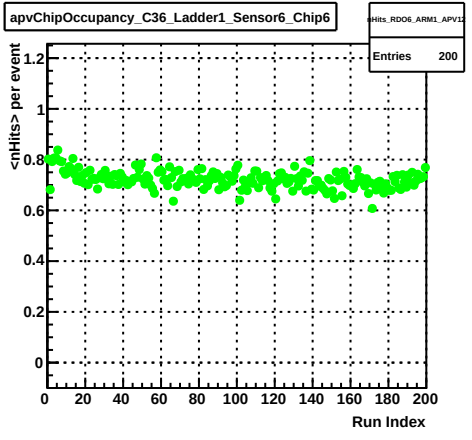

apvChipOccupancy_C31_Ladder1_Sensor6_Chip1
Entries 200
<nHits> per event
Run Index

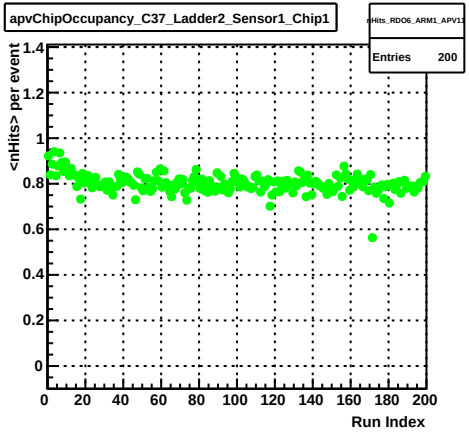

apvChipOccupancy_C32_Ladder1_Sensor6_Chip2
Entries 200
<nHits> per event
Run Index

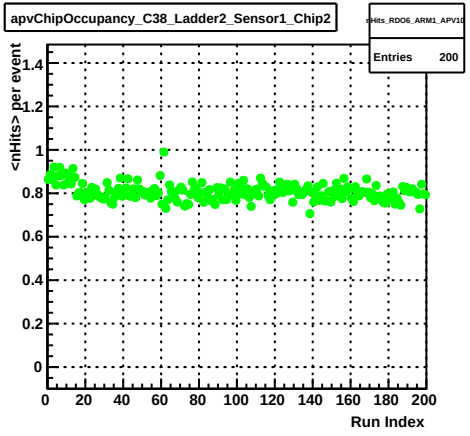

apvChipOccupancy_C33_Ladder1_Sensor6_Chip3
Entries 200
<nHits> per event
Run Index

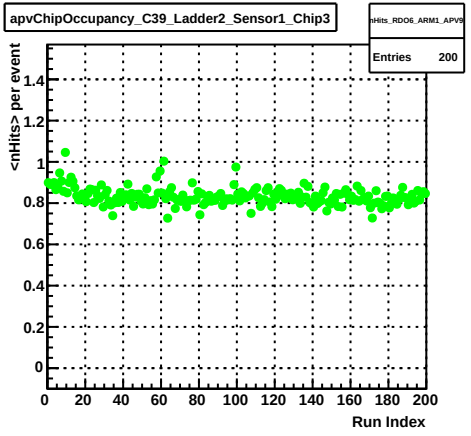

apvChipOccupancy_C34_Ladder1_Sensor6_Chip4
Entries 200
<nHits> per event
Run Index

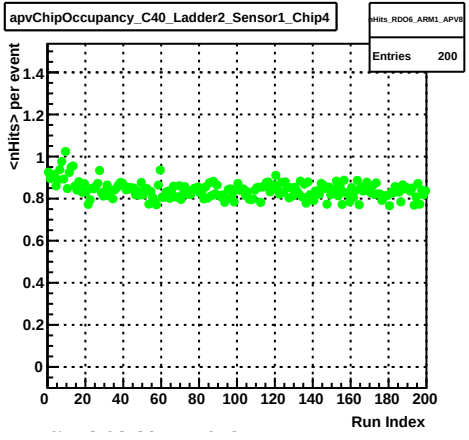

apvChipOccupancy_C35_Ladder1_Sensor6_Chip5
Entries 200
<nHits> per event
Run Index

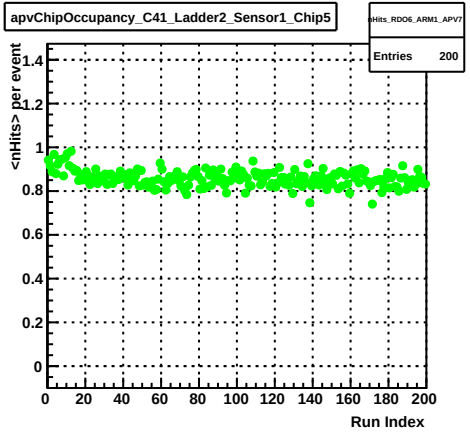

apvChipOccupancy_C36_Ladder1_Sensor6_Chip6
Entries 200
<nHits> per event
Run Index

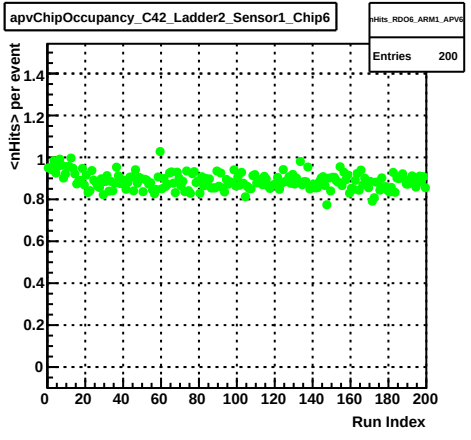

apvChipOccupancy_C37_Ladder2_Sensor1_Chip1
Entries 200
<nHits> per event
Run Index

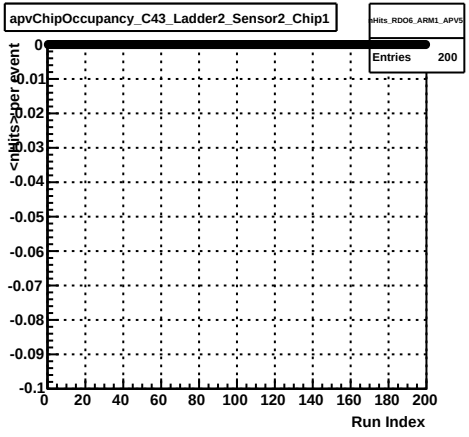

apvChipOccupancy_C38_Ladder2_Sensor1_Chip2
Entries 200
<nHits> per event
Run Index

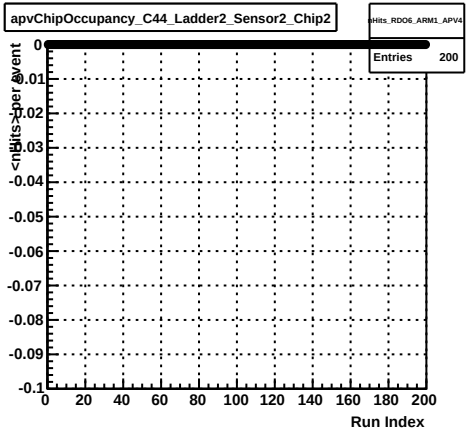

apvChipOccupancy_C39_Ladder2_Sensor1_Chip3
Entries 200
<nHits> per event
Run Index

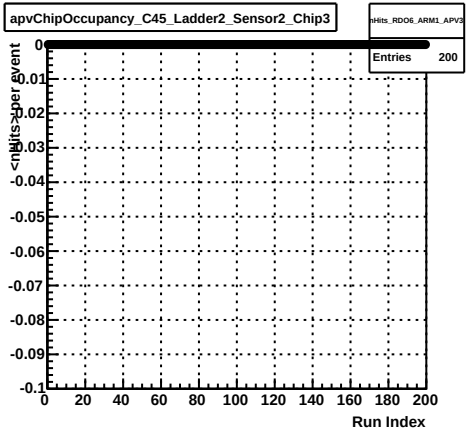

apvChipOccupancy_C40_Ladder2_Sensor1_Chip4
Entries 200
<nHits> per event
Run Index

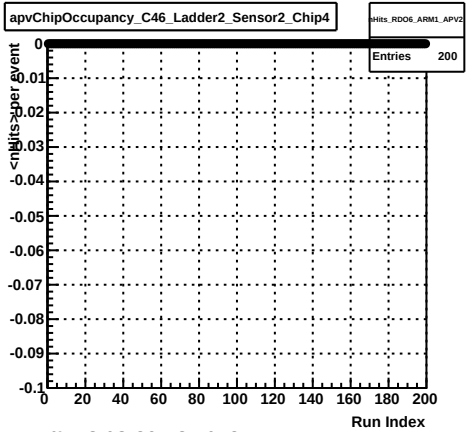

apvChipOccupancy_C41_Ladder2_Sensor1_Chip5
Entries 200
<nHits> per event
Run Index

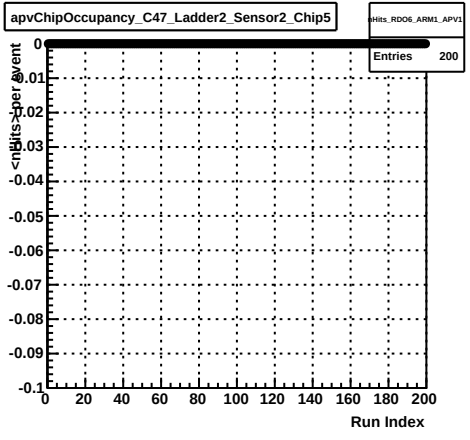

apvChipOccupancy_C42_Ladder2_Sensor1_Chip6
Entries 200
<nHits> per event
Run Index

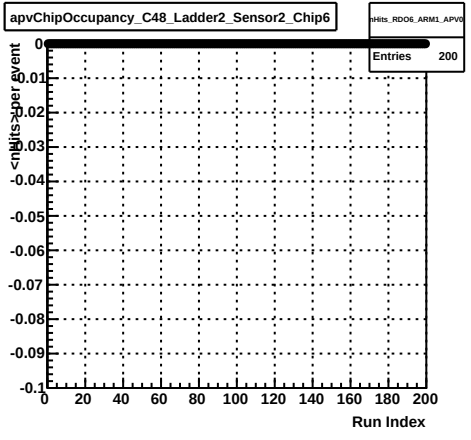

apvChipOccupancy_C43_Ladder2_Sensor2_Chip1
nHits_RDO6_ARM1_APV5
Entries 200
<nHits> per event
Run Index

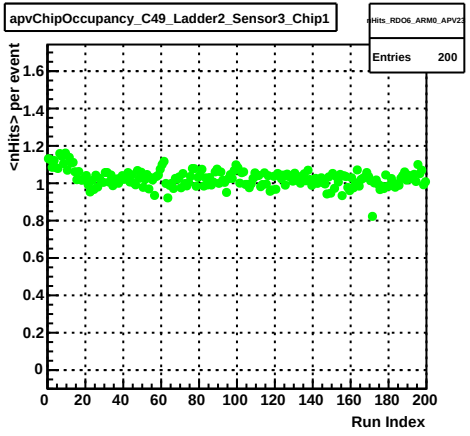

apvChipOccupancy_C44_Ladder2_Sensor2_Chip2
nHits_RDO6_ARM1_APV4
Entries 200
<nHits> per event
Run Index

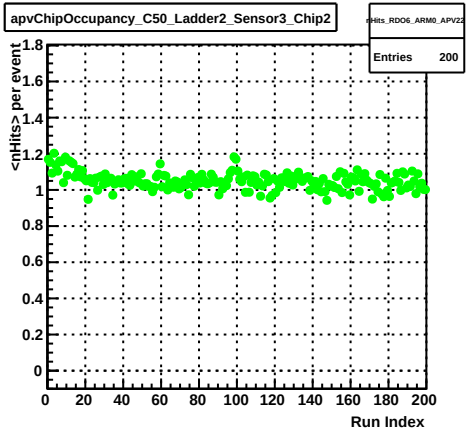

apvChipOccupancy_C45_Ladder2_Sensor2_Chip3
nHits_RDO6_ARM1_APV3
Entries 200
<nHits> per event
Run Index

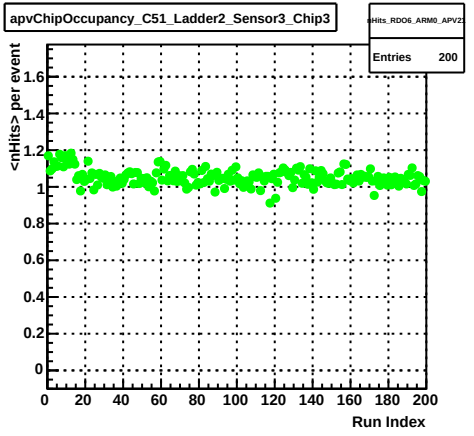

apvChipOccupancy_C46_Ladder2_Sensor2_Chip4
nHits_RDO6_ARM1_APV2
Entries 200
<nHits> per event
Run Index

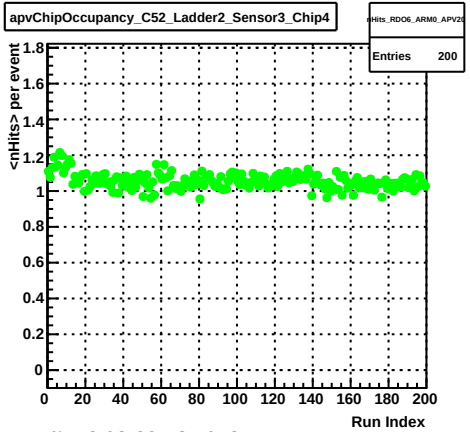

apvChipOccupancy_C47_Ladder2_Sensor2_Chip5
nHits_RDO6_ARM1_APV1
Entries 200
<nHits> per event
Run Index

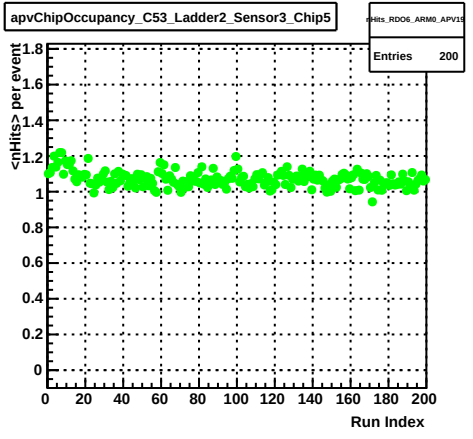

apvChipOccupancy_C48_Ladder2_Sensor2_Chip6
nHits_RDO6_ARM1_APV0
Entries 200
<nHits> per event
Run Index

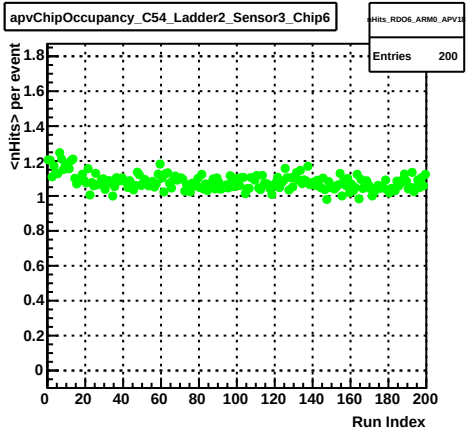

apvChipOccupancy_C49_Ladder2_Sensor3_Chip1
Entries 200
<nHits> per event
Run Index

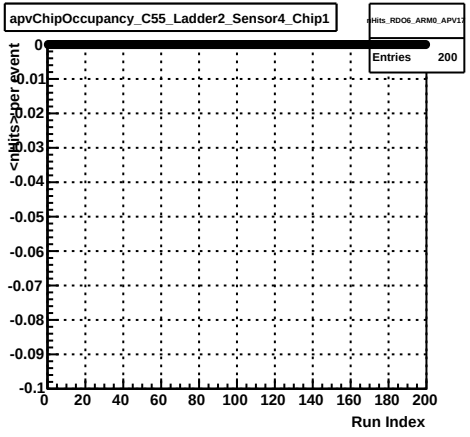

apvChipOccupancy_C50_Ladder2_Sensor3_Chip2
Entries 200
<nHits> per event
Run Index

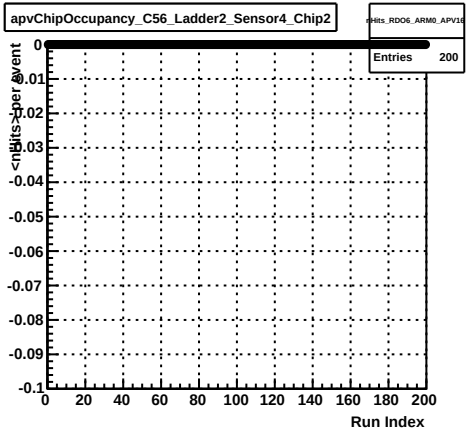

apvChipOccupancy_C51_Ladder2_Sensor3_Chip3
Entries 200
<nHits> per event
Run Index

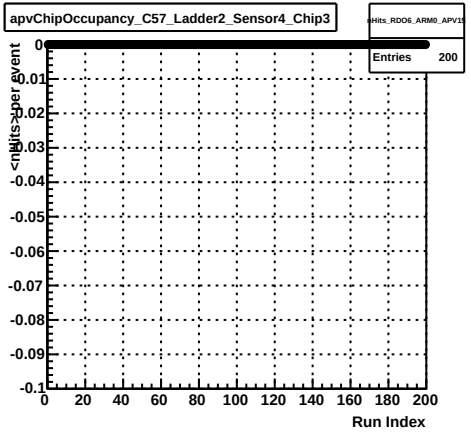

apvChipOccupancy_C52_Ladder2_Sensor3_Chip4
Entries 200
<nHits> per event
Run Index

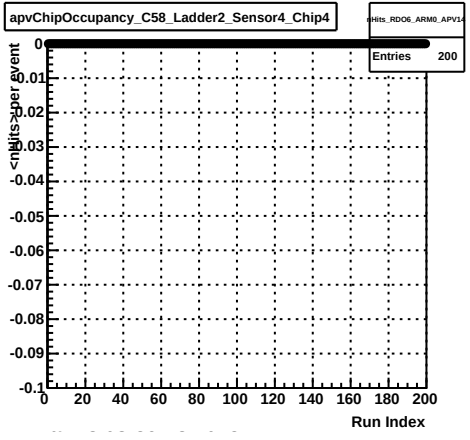

apvChipOccupancy_C53_Ladder2_Sensor3_Chip5
Entries 200
<nHits> per event
Run Index

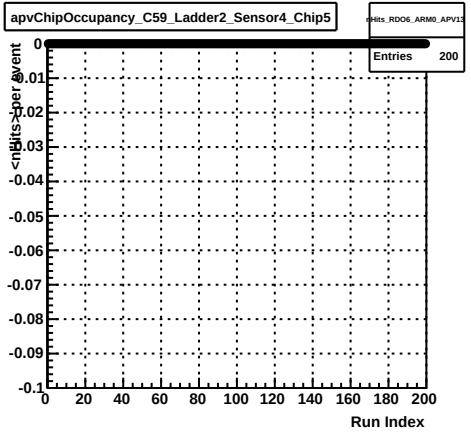

apvChipOccupancy_C54_Ladder2_Sensor3_Chip6
Entries 200
<nHits> per event
Run Index

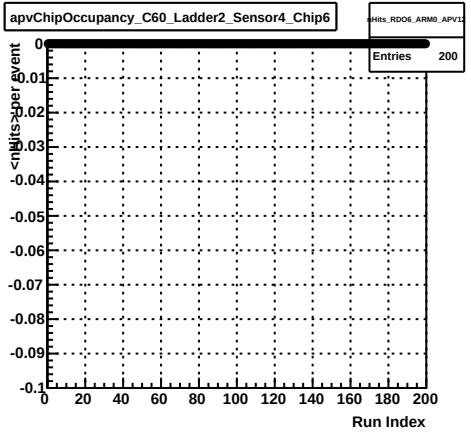

apvChipOccupancy_C55_Ladder2_Sensor4_Chip1
Entries 200
<nHits> per event
Run Index

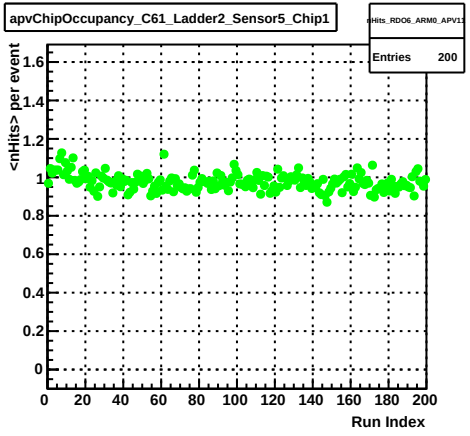

apvChipOccupancy_C56_Ladder2_Sensor4_Chip2
Entries 200
<nHits> per event
Run Index

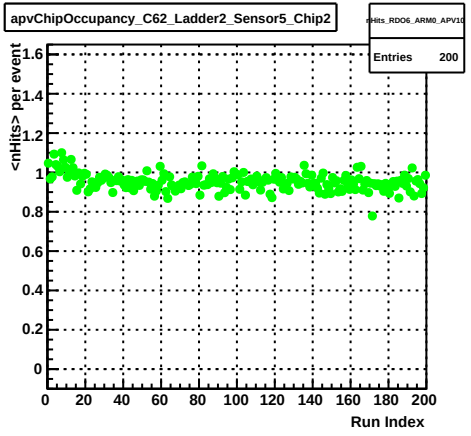

apvChipOccupancy_C57_Ladder2_Sensor4_Chip3
Entries 200
<nHits> per event
Run Index

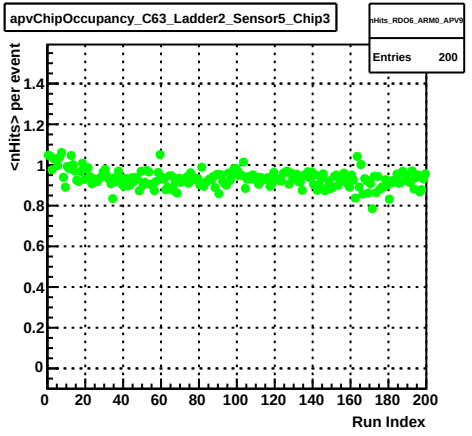

apvChipOccupancy_C58_Ladder2_Sensor4_Chip4
Entries 200
<nHits> per event
Run Index

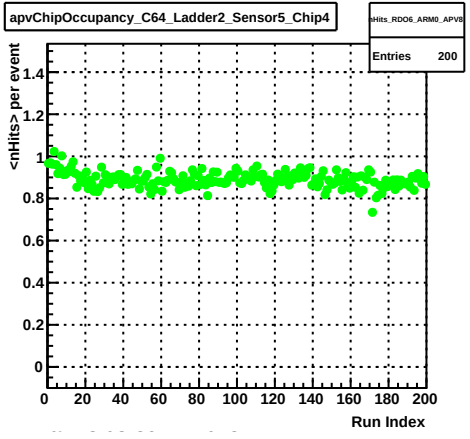

apvChipOccupancy_C59_Ladder2_Sensor4_Chip5
Entries 200
<nHits> per event
Run Index

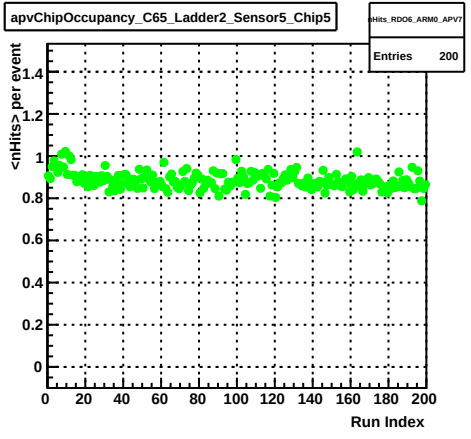

apvChipOccupancy_C60_Ladder2_Sensor4_Chip6
Entries 200
<nHits> per event
Run Index

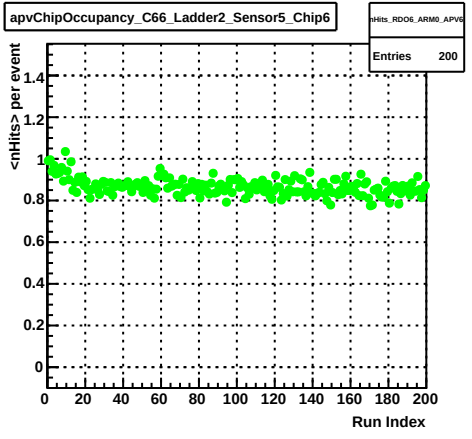

apvChipOccupancy_C61_Ladder2_Sensor5_Chip1
Entries 200
<nHits> per event
Run Index

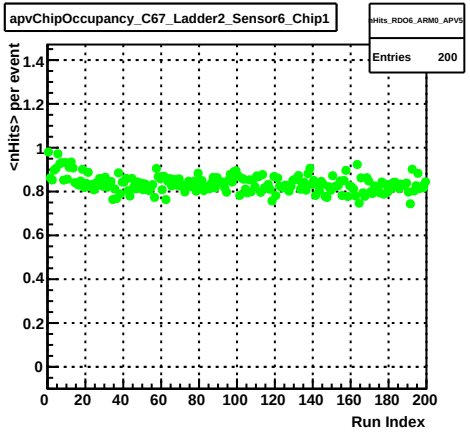

apvChipOccupancy_C62_Ladder2_Sensor5_Chip2
Entries 200
<nHits> per event
Run Index

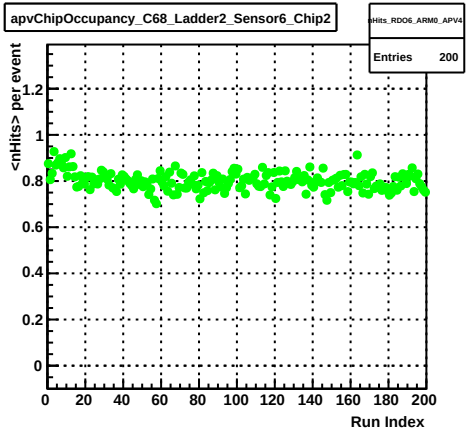

apvChipOccupancy_C63_Ladder2_Sensor5_Chip3
Entries 200
<nHits> per event
Run Index

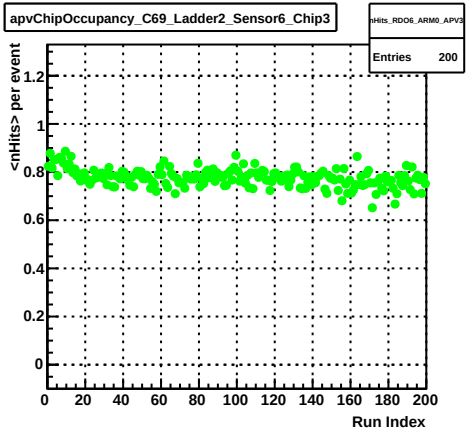

apvChipOccupancy_C64_Ladder2_Sensor5_Chip4
Entries 200
<nHits> per event
Run Index

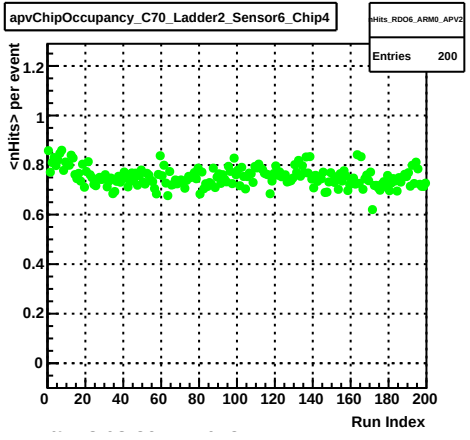

apvChipOccupancy_C65_Ladder2_Sensor5_Chip5
Entries 200
<nHits> per event
Run Index

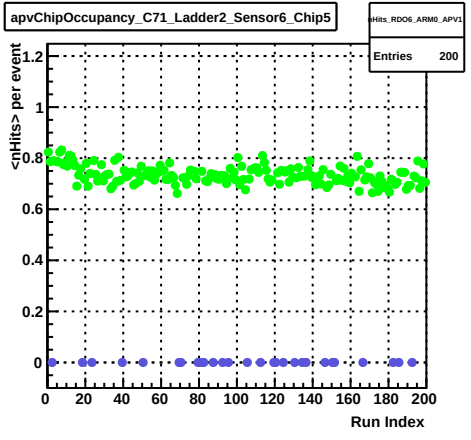

apvChipOccupancy_C66_Ladder2_Sensor5_Chip6
Entries 200
<nHits> per event
Run Index

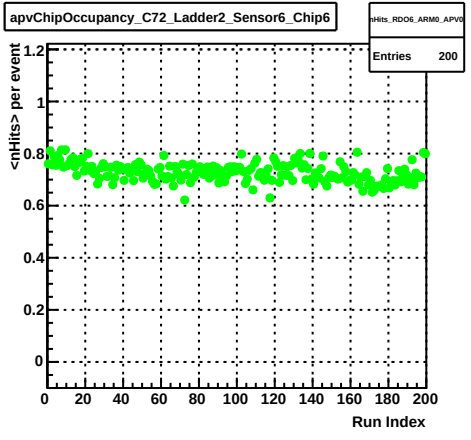

apvChipOccupancy_C67_Ladder2_Sensor6_Chip1
nHits_RDO6_ARM0_APV5
Entries 200
<nHits> per event
Run Index

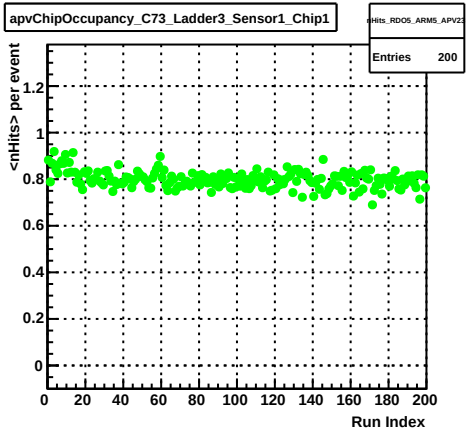

apvChipOccupancy_C68_Ladder2_Sensor6_Chip2
nHits_RDO6_ARM0_APV4
Entries 200
<nHits> per event
Run Index

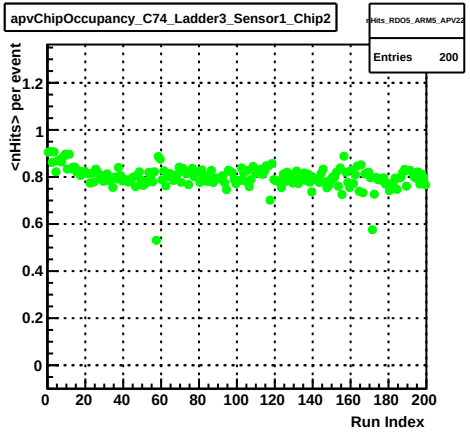

apvChipOccupancy_C69_Ladder2_Sensor6_Chip3
nHits_RDO6_ARM0_APV3
Entries 200
<nHits> per event
Run Index

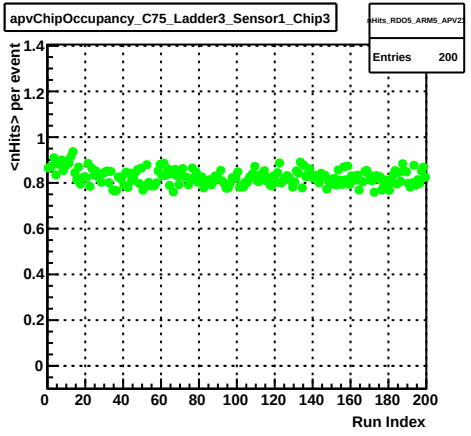

apvChipOccupancy_C70_Ladder2_Sensor6_Chip4
nHits_RDO6_ARM0_APV2
Entries 200
<nHits> per event
Run Index

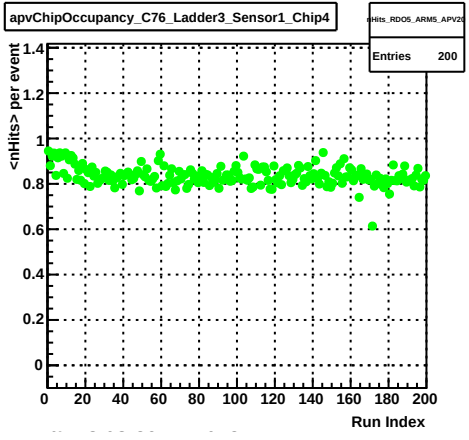

apvChipOccupancy_C71_Ladder2_Sensor6_Chip5
nHits_RDO6_ARM0_APV1
Entries 200
<nHits> per event
Run Index

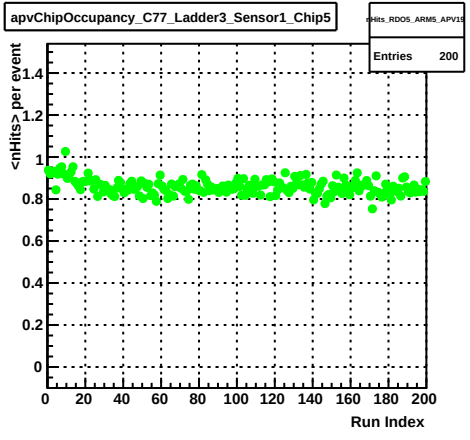

apvChipOccupancy_C72_Ladder2_Sensor6_Chip6
nHits_RDO6_ARM0_APV0
Entries 200
<nHits> per event
Run Index

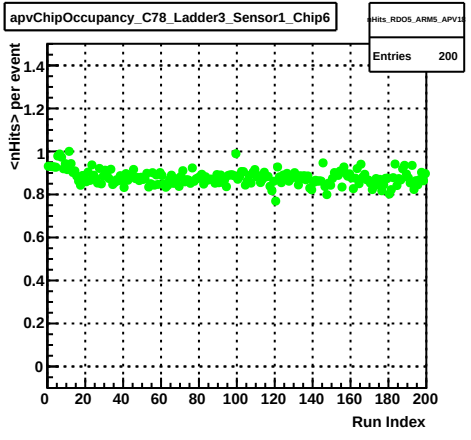

apvChipOccupancy_C73_Ladder3_Sensor1_Chip1
Entries 200
<nHits> per event
Run Index

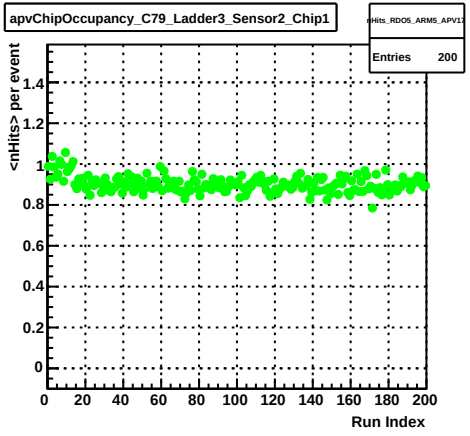

apvChipOccupancy_C74_Ladder3_Sensor1_Chip2
Entries 200
<nHits> per event
Run Index

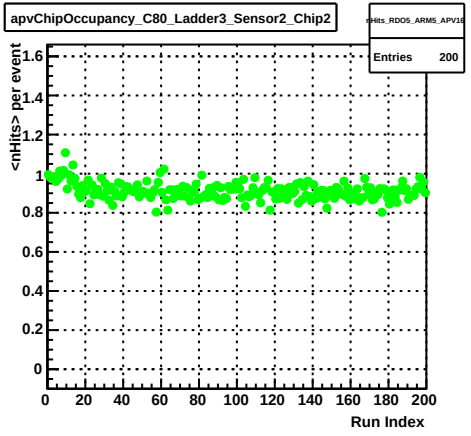

apvChipOccupancy_C75_Ladder3_Sensor1_Chip3
Entries 200
<nHits> per event
Run Index

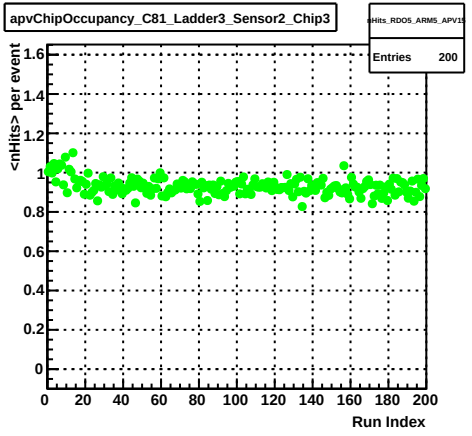

apvChipOccupancy_C76_Ladder3_Sensor1_Chip4
Entries 200
<nHits> per event
Run Index

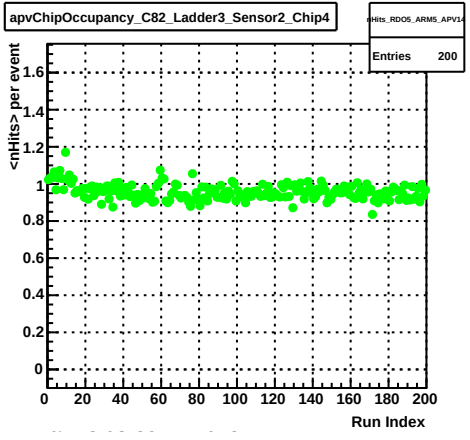

apvChipOccupancy_C77_Ladder3_Sensor1_Chip5
Entries 200
<nHits> per event
Run Index

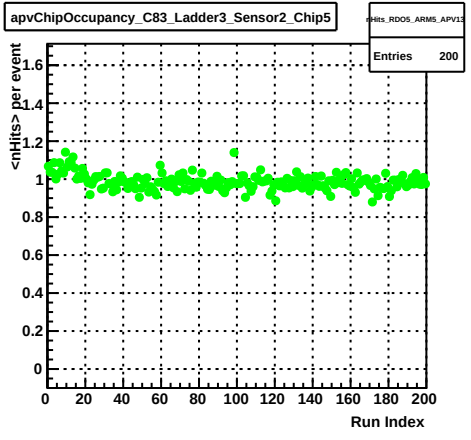

apvChipOccupancy_C78_Ladder3_Sensor1_Chip6
Entries 200
<nHits> per event
Run Index

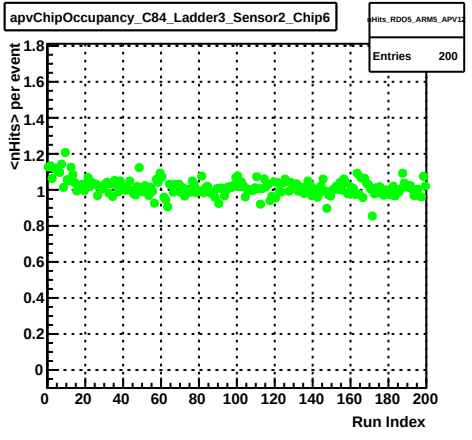

apvChipOccupancy_C79_Ladder3_Sensor2_Chip1
Entries 200
<nHits> per event
Run Index

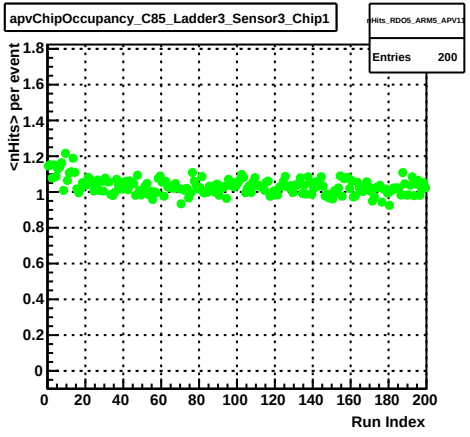

apvChipOccupancy_C80_Ladder3_Sensor2_Chip2
Entries 200
<nHits> per event
Run Index

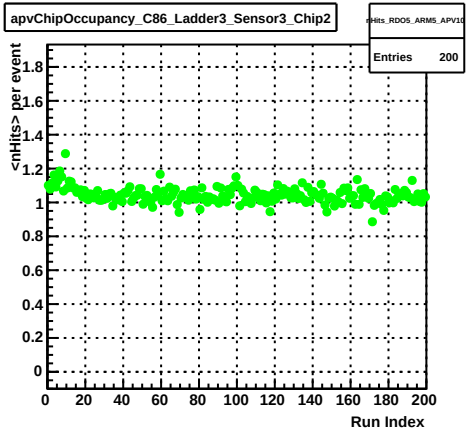

apvChipOccupancy_C81_Ladder3_Sensor2_Chip3
Entries 200
<nHits> per event
Run Index

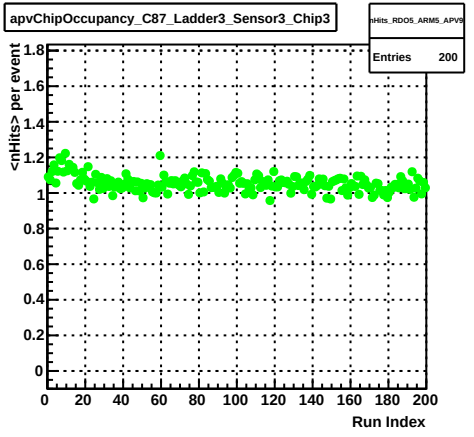

apvChipOccupancy_C82_Ladder3_Sensor2_Chip4
Entries 200
<nHits> per event
Run Index

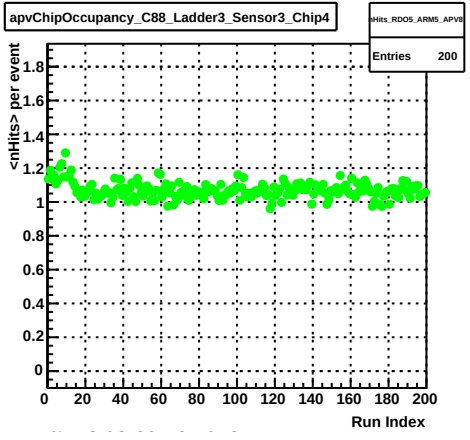

apvChipOccupancy_C83_Ladder3_Sensor2_Chip5
Entries 200
<nHits> per event
Run Index

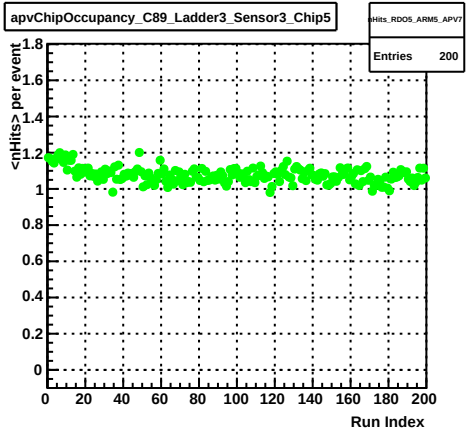

apvChipOccupancy_C84_Ladder3_Sensor2_Chip6
Entries 200
<nHits> per event
Run Index

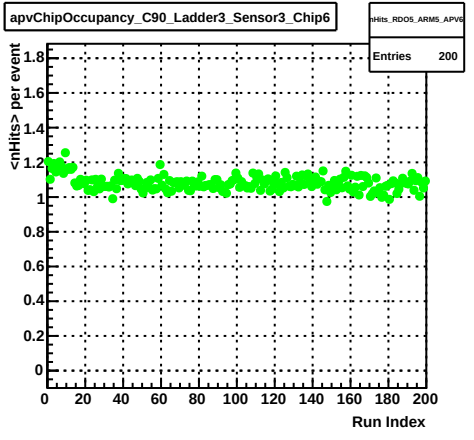

apvChipOccupancy_C85_Ladder3_Sensor3_Chip1
Entries 200
<nHits> per event
Run Index

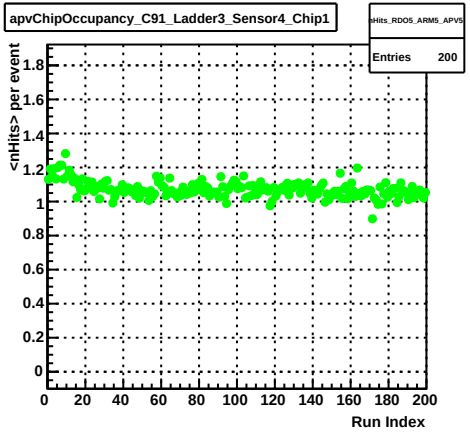

apvChipOccupancy_C86_Ladder3_Sensor3_Chip2
Entries 200
<nHits> per event
Run Index

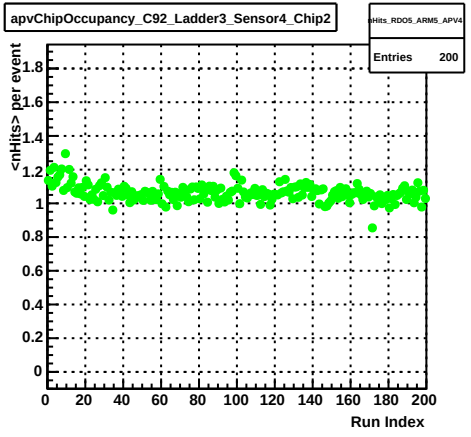

apvChipOccupancy_C87_Ladder3_Sensor3_Chip3
Entries 200
<nHits> per event
Run Index

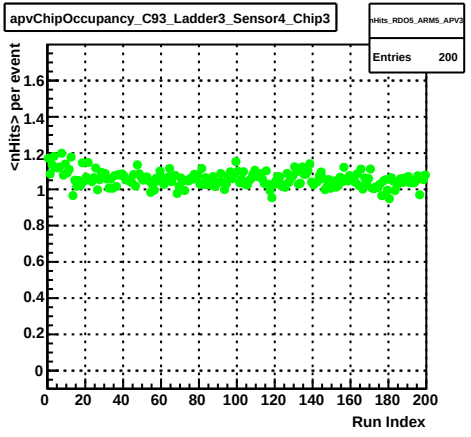

apvChipOccupancy_C88_Ladder3_Sensor3_Chip4
Entries 200
<nHits> per event
Run Index

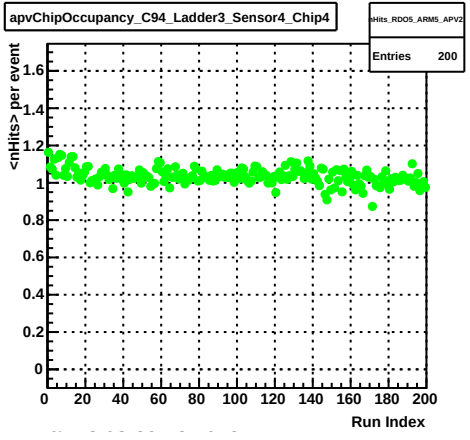

apvChipOccupancy_C89_Ladder3_Sensor3_Chip5
Entries 200
<nHits> per event
Run Index

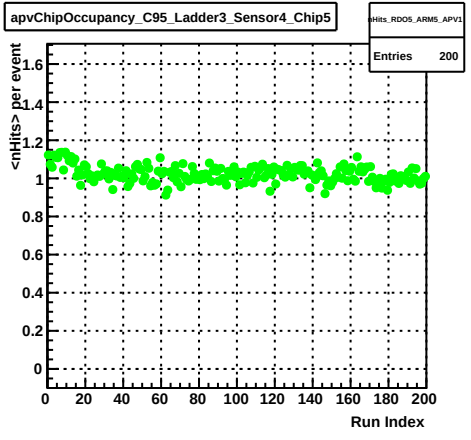

apvChipOccupancy_C90_Ladder3_Sensor3_Chip6
Entries 200
<nHits> per event
Run Index

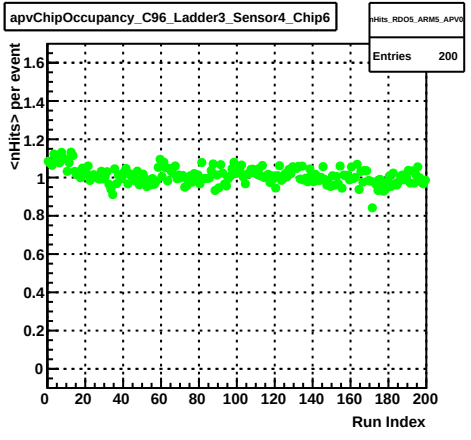

apvChipOccupancy_C91_Ladder3_Sensor4_Chip1
nHits_RDO5_ARM5_APV5
Entries 200
<nHits> per event
Run Index

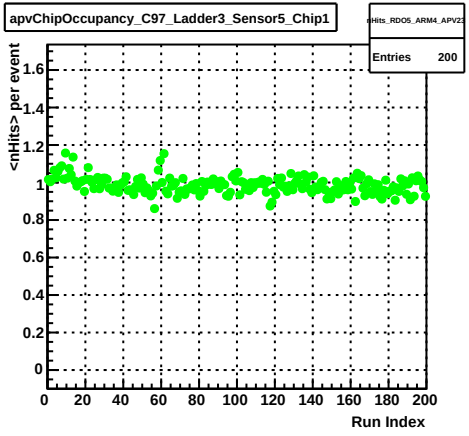

apvChipOccupancy_C92_Ladder3_Sensor4_Chip2
nHits_RDO5_ARM5_APV4
Entries 200
<nHits> per event
Run Index

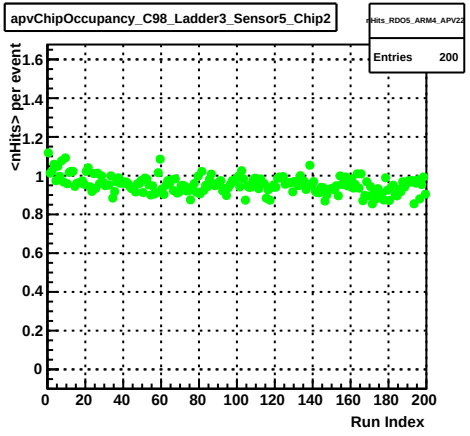

apvChipOccupancy_C93_Ladder3_Sensor4_Chip3
nHits_RDO5_ARM5_APV3
Entries 200
<nHits> per event
Run Index

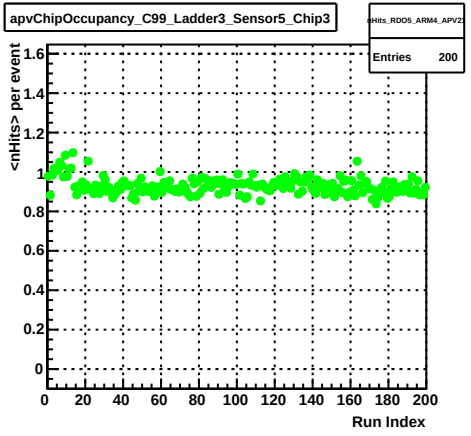

apvChipOccupancy_C94_Ladder3_Sensor4_Chip4
nHits_RDO5_ARM5_APV2
Entries 200
<nHits> per event
Run Index

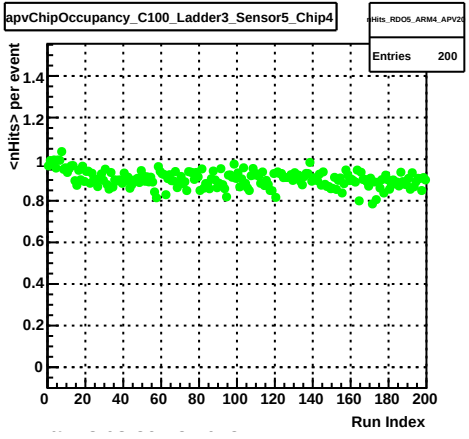

apvChipOccupancy_C95_Ladder3_Sensor4_Chip5
nHits_RDO5_ARM5_APV1
Entries 200
<nHits> per event
Run Index

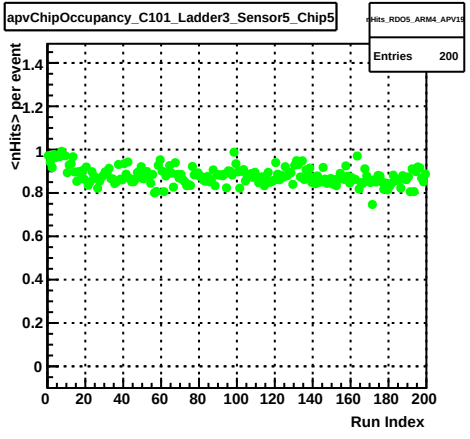

apvChipOccupancy_C96_Ladder3_Sensor4_Chip6
nHits_RDO5_ARM5_APV0
Entries 200
<nHits> per event
Run Index

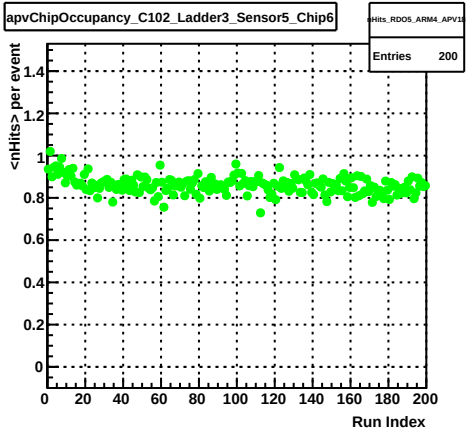

apvChipOccupancy_C97_Ladder3_Sensor5_Chip1
Entries 200
<nHits> per event
Run Index

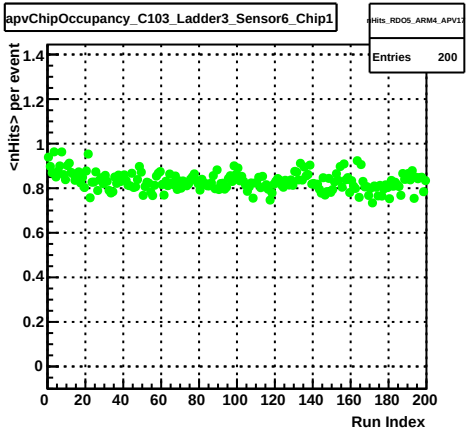

apvChipOccupancy_C98_Ladder3_Sensor5_Chip2
Entries 200
<nHits> per event
Run Index

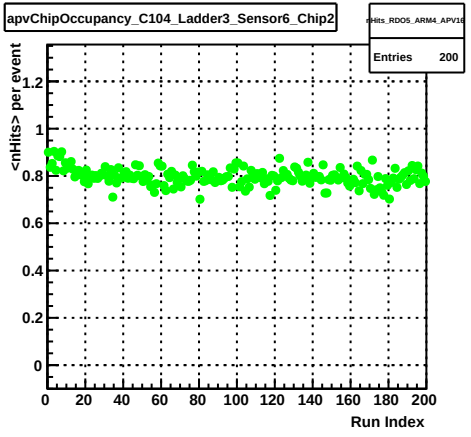

apvChipOccupancy_C99_Ladder3_Sensor5_Chip3
Entries 200
<nHits> per event
Run Index

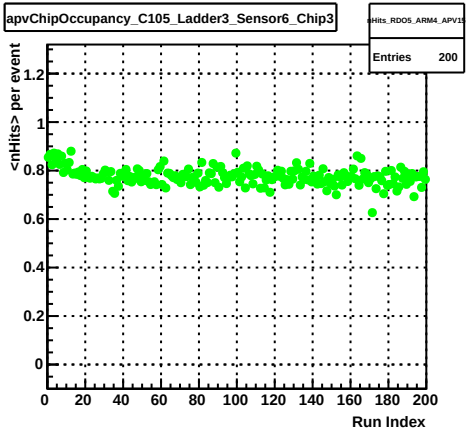

apvChipOccupancy_C100_Ladder3_Sensor5_Chip4
Entries 200
<nHits> per event
Run Index

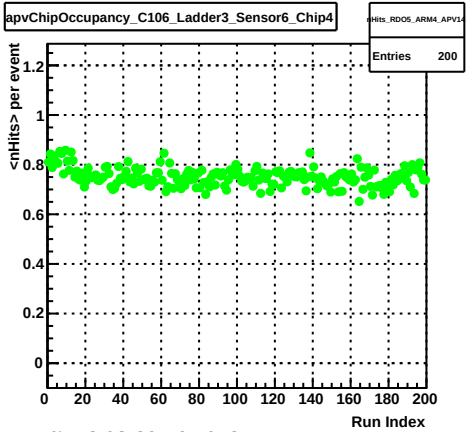

apvChipOccupancy_C101_Ladder3_Sensor5_Chip5
Entries 200
<nHits> per event
Run Index

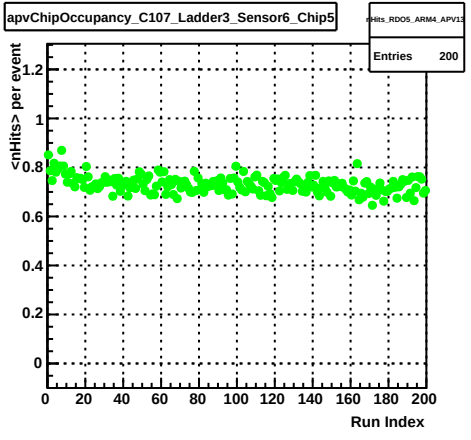

apvChipOccupancy_C102_Ladder3_Sensor5_Chip6
Entries 200
<nHits> per event
Run Index

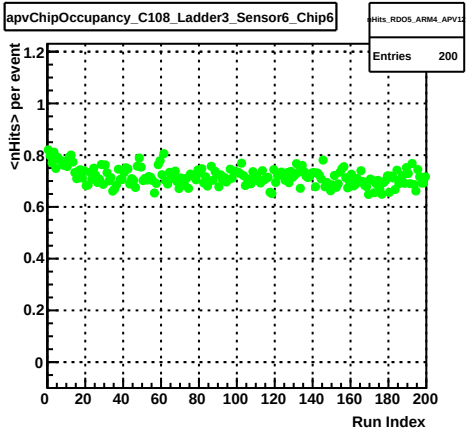

apvChipOccupancy_C103_Ladder3_Sensor6_Chip1
Entries 200
<nHits> per event
Run Index

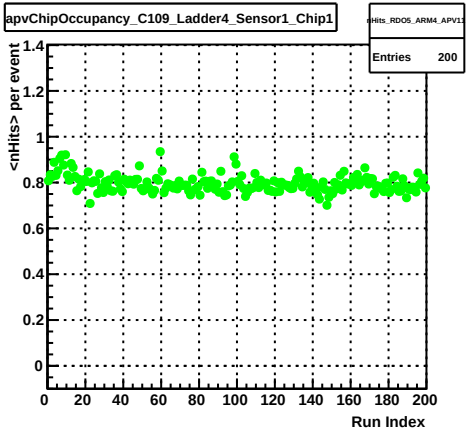

apvChipOccupancy_C104_Ladder3_Sensor6_Chip2
Entries 200
<nHits> per event
Run Index

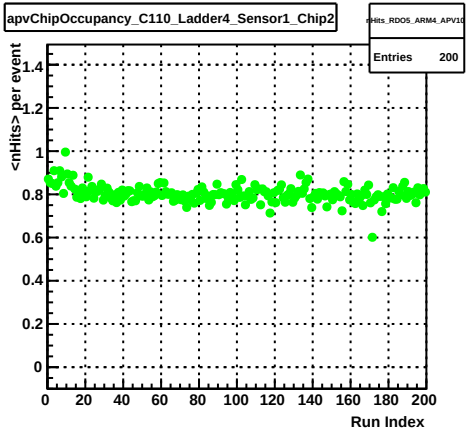

apvChipOccupancy_C105_Ladder3_Sensor6_Chip3
Entries 200
<nHits> per event
Run Index

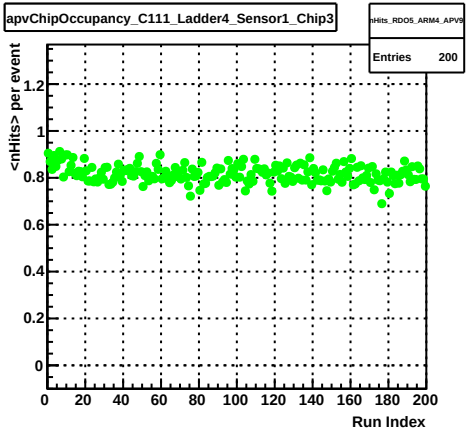

apvChipOccupancy_C106_Ladder3_Sensor6_Chip4
Entries 200
<nHits> per event
Run Index

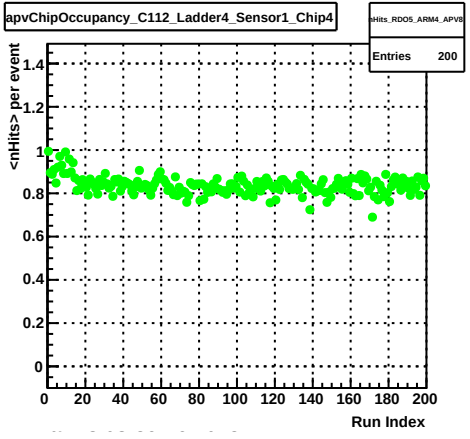

apvChipOccupancy_C107_Ladder3_Sensor6_Chip5
Entries 200
<nHits> per event
Run Index

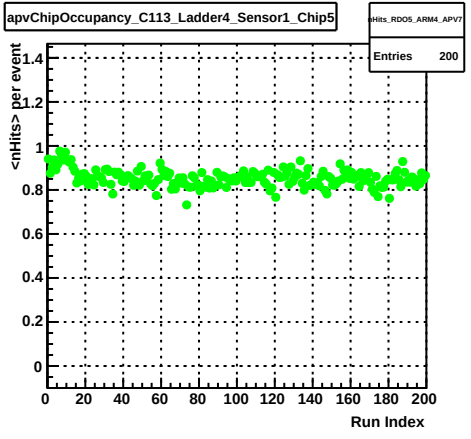

apvChipOccupancy_C108_Ladder3_Sensor6_Chip6
Entries 200
<nHits> per event
Run Index

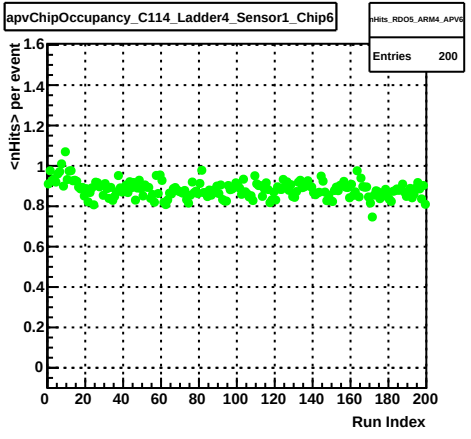

apvChipOccupancy_C109_Ladder4_Sensor1_Chip1
Entries 200
<nHits> per event
Run Index

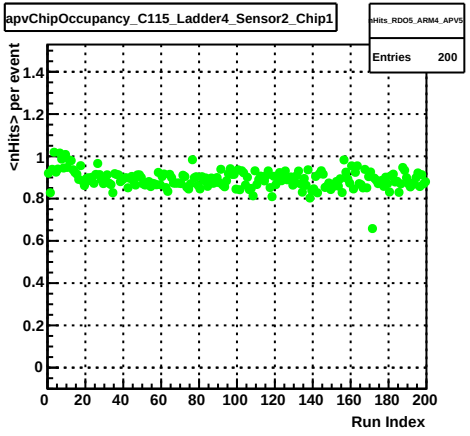

apvChipOccupancy_C110_Ladder4_Sensor1_Chip2
Entries 200
<nHits> per event
Run Index

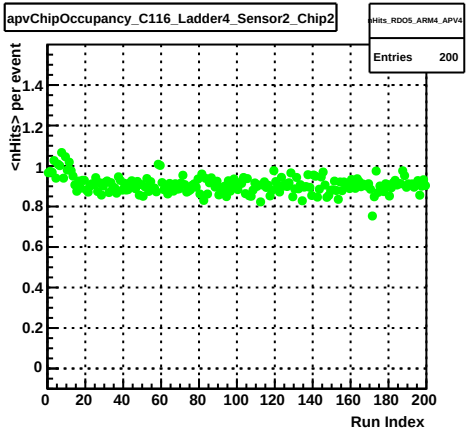

apvChipOccupancy_C111_Ladder4_Sensor1_Chip3
Entries 200
<nHits> per event
Run Index

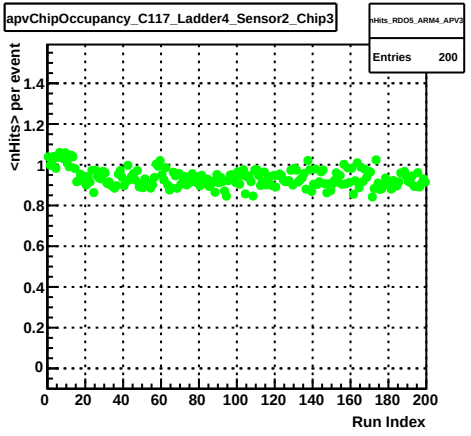

apvChipOccupancy_C112_Ladder4_Sensor1_Chip4
Entries 200
<nHits> per event
Run Index

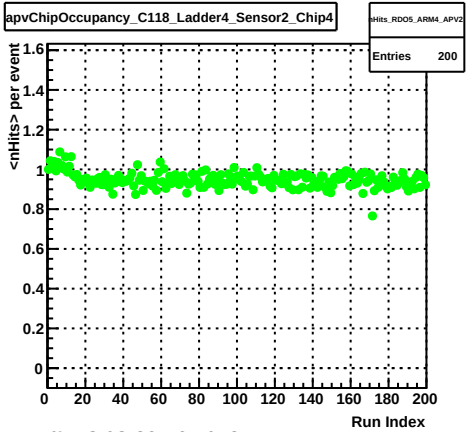

apvChipOccupancy_C113_Ladder4_Sensor1_Chip5
Entries 200
<nHits> per event
Run Index

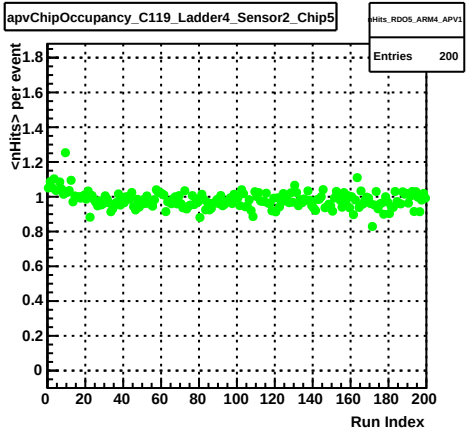

apvChipOccupancy_C114_Ladder4_Sensor1_Chip6
Entries 200
<nHits> per event
Run Index

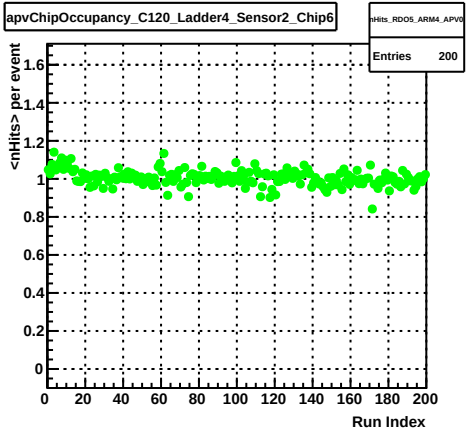

apvChipOccupancy_C115_Ladder4_Sensor2_Chip1
nHits_RD05_ARM4_APV5
Entries 200
<nHits> per event
Run Index

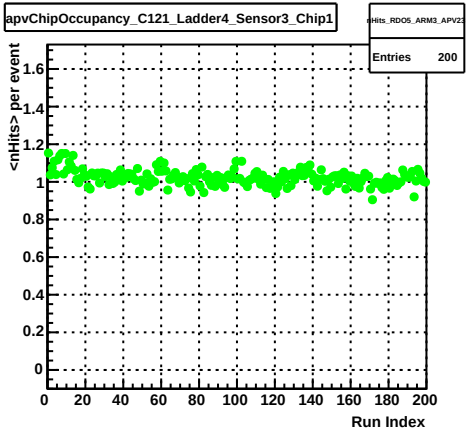

apvChipOccupancy_C116_Ladder4_Sensor2_Chip2
nHits_RD05_ARM4_APV4
Entries 200
<nHits> per event
Run Index

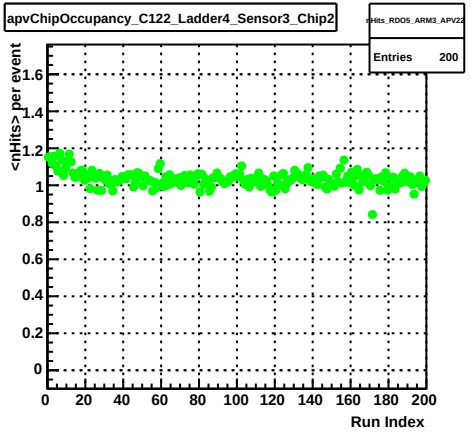

apvChipOccupancy_C117_Ladder4_Sensor2_Chip3
nHits_RD05_ARM4_APV3
Entries 200
<nHits> per event
Run Index

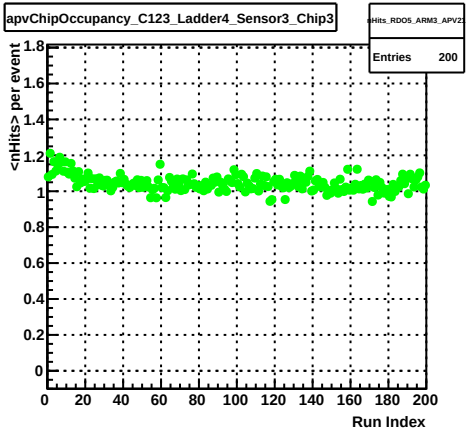

apvChipOccupancy_C118_Ladder4_Sensor2_Chip4
nHits_RD05_ARM4_APV2
Entries 200
<nHits> per event
Run Index

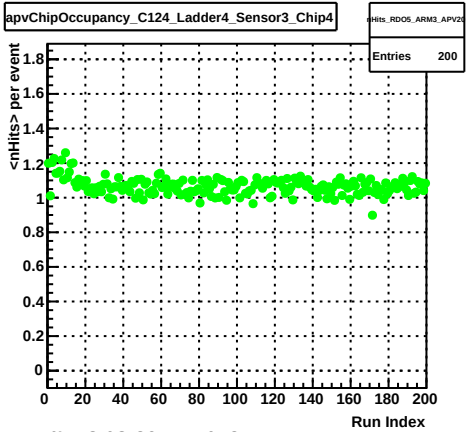

apvChipOccupancy_C119_Ladder4_Sensor2_Chip5
nHits_RD05_ARM4_APV1
Entries 200
<nHits> per event
Run Index

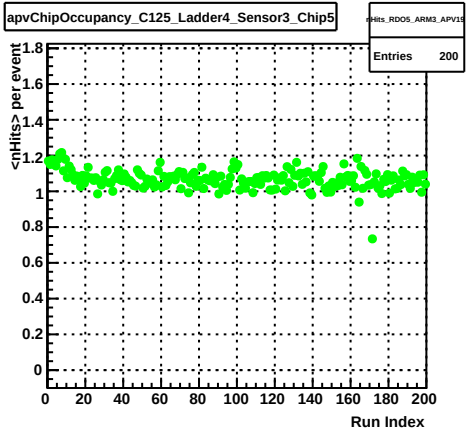

apvChipOccupancy_C120_Ladder4_Sensor2_Chip6
nHits_RD05_ARM4_APV0
Entries 200
<nHits> per event
Run Index

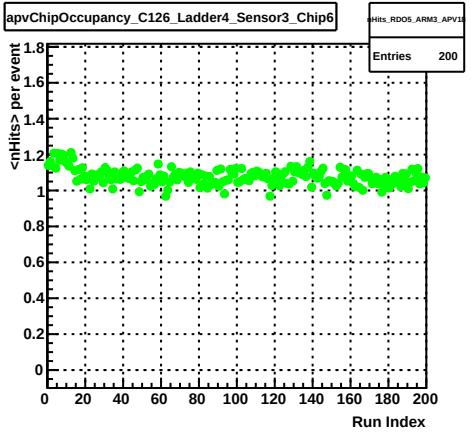

apvChipOccupancy_C121_Ladder4_Sensor3_Chip1
Entries 200
<nHits> per event
Run Index

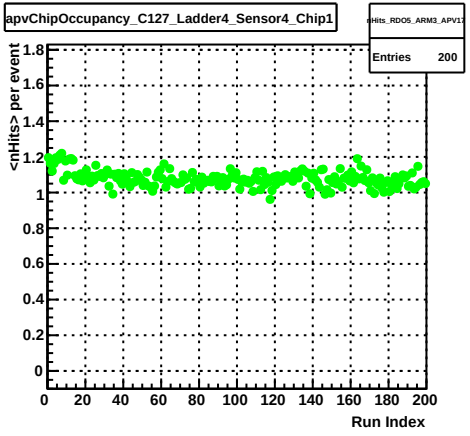

apvChipOccupancy_C122_Ladder4_Sensor3_Chip2
Entries 200
<nHits> per event
Run Index

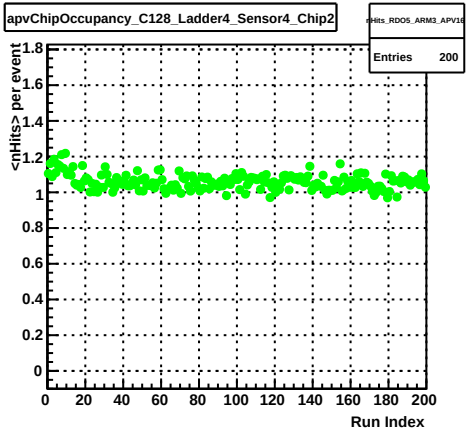

apvChipOccupancy_C123_Ladder4_Sensor3_Chip3
Entries 200
<nHits> per event
Run Index

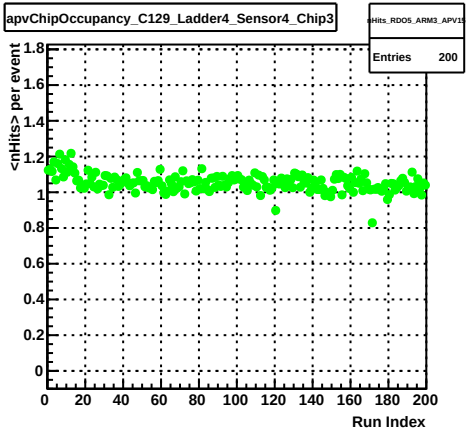

apvChipOccupancy_C124_Ladder4_Sensor3_Chip4
Entries 200
<nHits> per event
Run Index

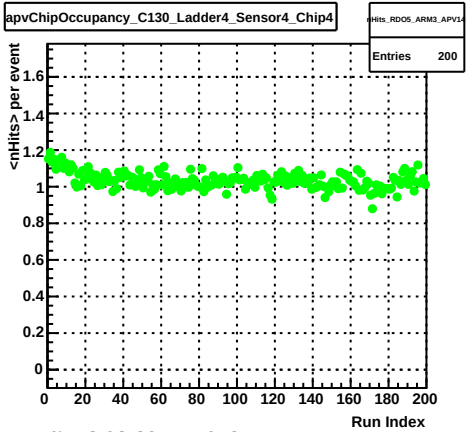

apvChipOccupancy_C125_Ladder4_Sensor3_Chip5
Entries 200
<nHits> per event
Run Index

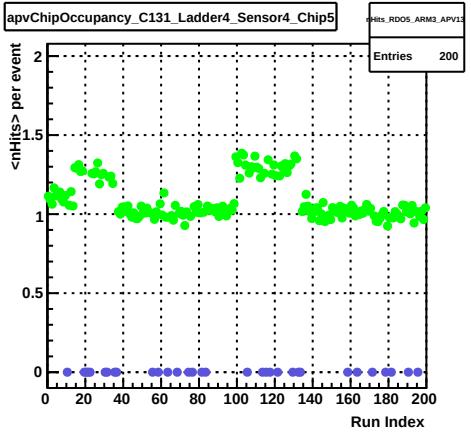

apvChipOccupancy_C126_Ladder4_Sensor3_Chip6
Entries 200
<nHits> per event
Run Index

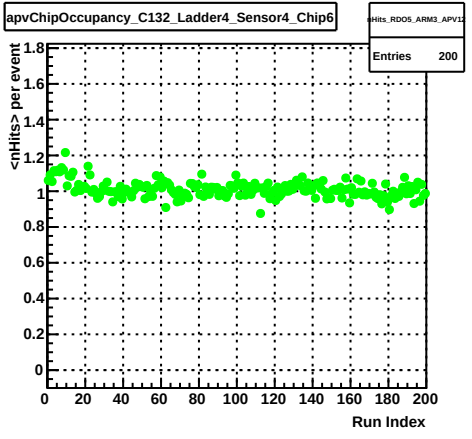

apvChipOccupancy_C127_Ladder4_Sensor4_Chip1
nHits_RDO5_ARM3_APV12
Entries 200
<nHits> per event
Run Index

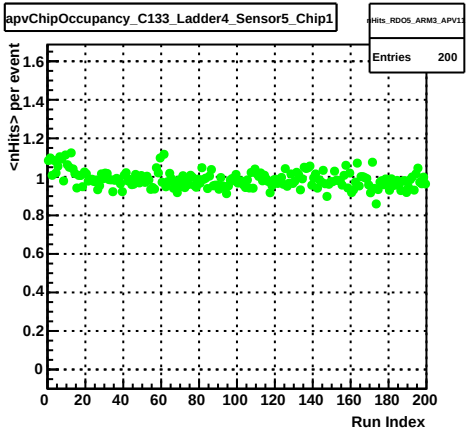

apvChipOccupancy_C128_Ladder4_Sensor4_Chip2
nHits_RDO5_ARM3_APV16
Entries 200
<nHits> per event
Run Index

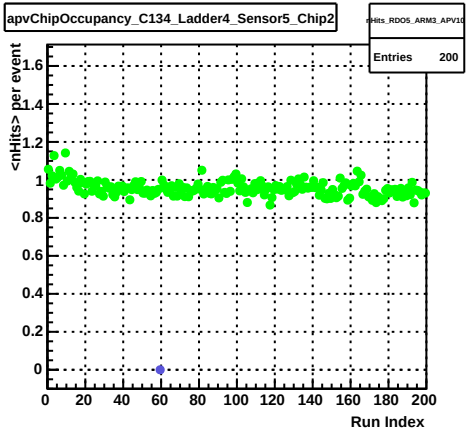

apvChipOccupancy_C129_Ladder4_Sensor4_Chip3
nHits_RDO5_ARM3_APV19
Entries 200
<nHits> per event
Run Index

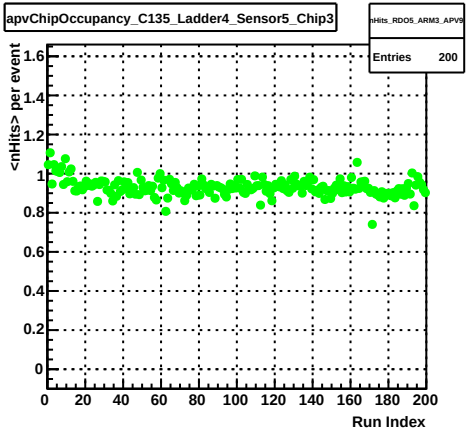

apvChipOccupancy_C130_Ladder4_Sensor4_Chip4
nHits_RDO5_ARM3_APV14
Entries 200
<nHits> per event
Run Index

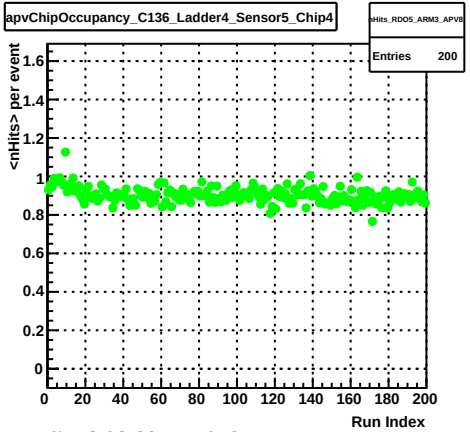

apvChipOccupancy_C131_Ladder4_Sensor4_Chip5
nHits_RDO5_ARM3_APV13
Entries 200
<nHits> per event
Run Index

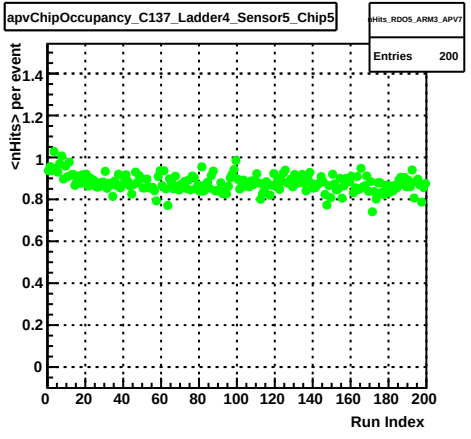

apvChipOccupancy_C132_Ladder4_Sensor4_Chip6
nHits_RDO5_ARM3_APV15
Entries 200
<nHits> per event
Run Index

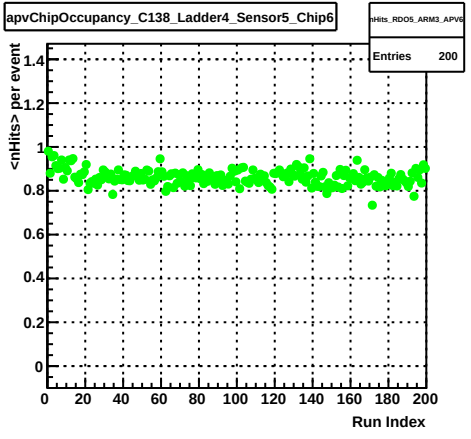

apvChipOccupancy_C133_Ladder4_Sensor5_Chip1
Entries 200
<nHits> per event
Run Index

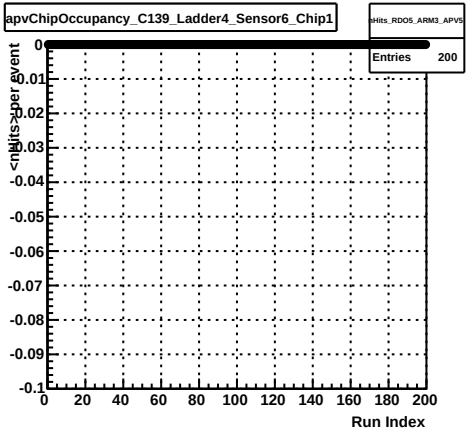

apvChipOccupancy_C134_Ladder4_Sensor5_Chip2
Entries 200
<nHits> per event
Run Index

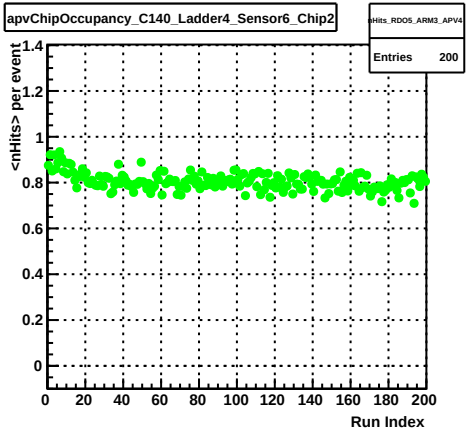

apvChipOccupancy_C135_Ladder4_Sensor5_Chip3
Entries 200
<nHits> per event
Run Index

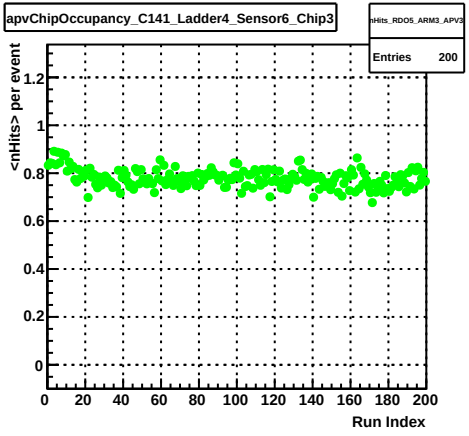

apvChipOccupancy_C136_Ladder4_Sensor5_Chip4
Entries 200
<nHits> per event
Run Index

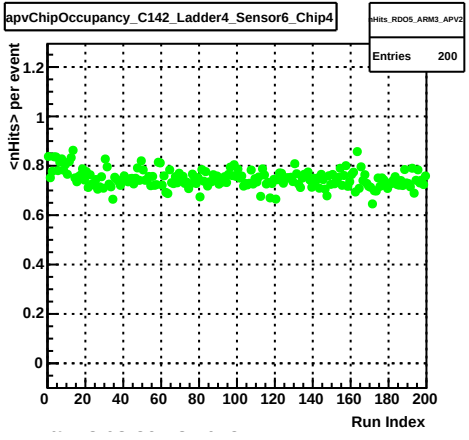

apvChipOccupancy_C137_Ladder4_Sensor5_Chip5
Entries 200
<nHits> per event
Run Index

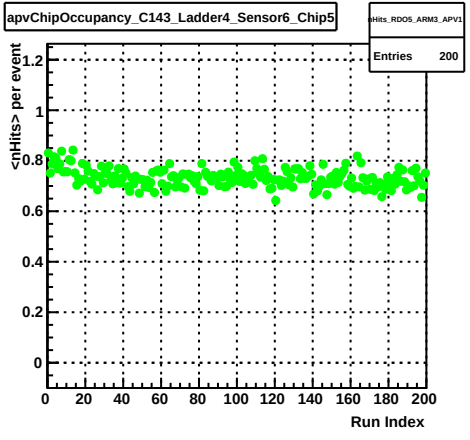

apvChipOccupancy_C138_Ladder4_Sensor5_Chip6
Entries 200
<nHits> per event
Run Index

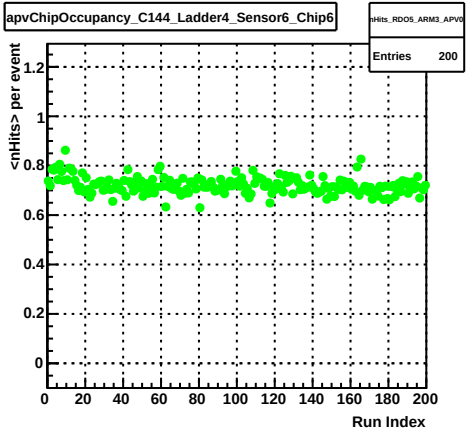

apvChipOccupancy_C139_Ladder4_Sensor6_Chip1
nHits_RD05_ARM3_APV5
Entries 200
<nHits> per event
Run Index

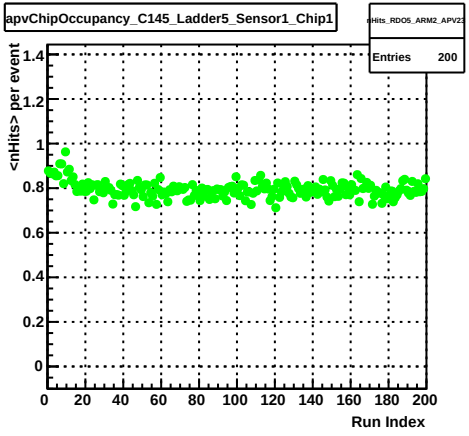

apvChipOccupancy_C140_Ladder4_Sensor6_Chip2
nHits_RD05_ARM3_APV4
Entries 200
<nHits> per event
Run Index

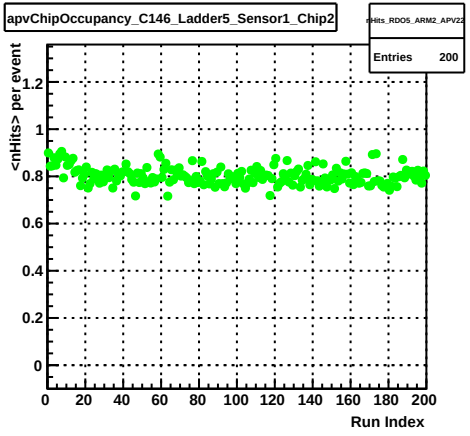

apvChipOccupancy_C141_Ladder4_Sensor6_Chip3
nHits_RD05_ARM3_APV3
Entries 200
<nHits> per event
Run Index

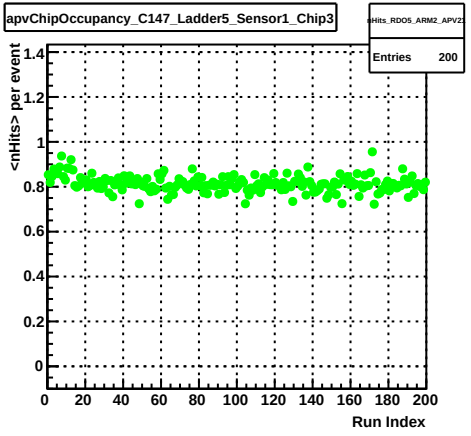

apvChipOccupancy_C142_Ladder4_Sensor6_Chip4
nHits_RD05_ARM3_APV2
Entries 200
<nHits> per event
Run Index

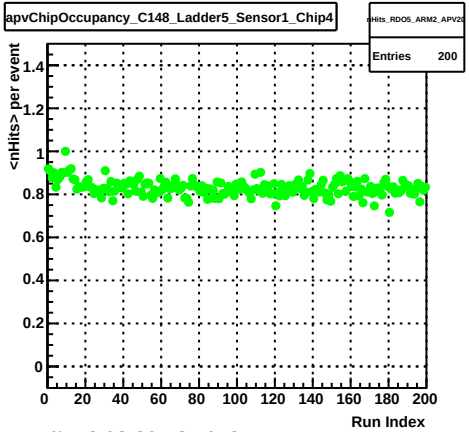

apvChipOccupancy_C143_Ladder4_Sensor6_Chip5
nHits_RD05_ARM3_APV1
Entries 200
<nHits> per event
Run Index

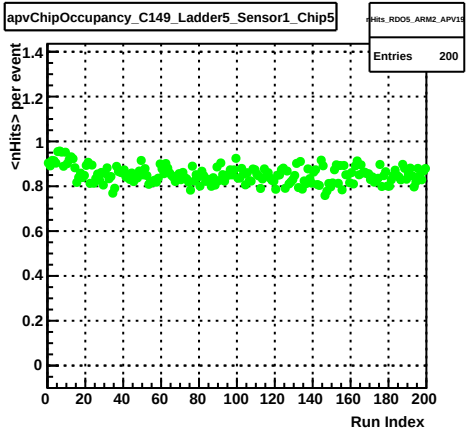

apvChipOccupancy_C144_Ladder4_Sensor6_Chip6
nHits_RD05_ARM3_APV0
Entries 200
<nHits> per event
Run Index

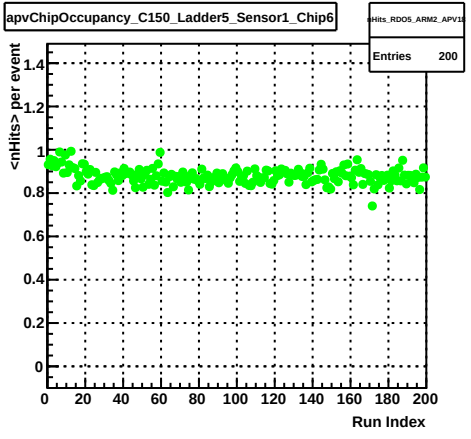

apvChipOccupancy_C145_Ladder5_Sensor1_Chip1
Entries 200
<nHits> per event
Run Index

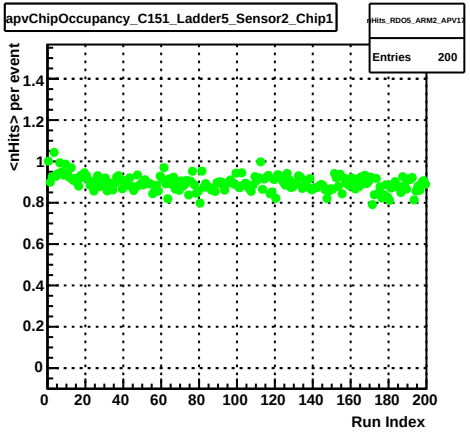

apvChipOccupancy_C146_Ladder5_Sensor1_Chip2
Entries 200
<nHits> per event
Run Index

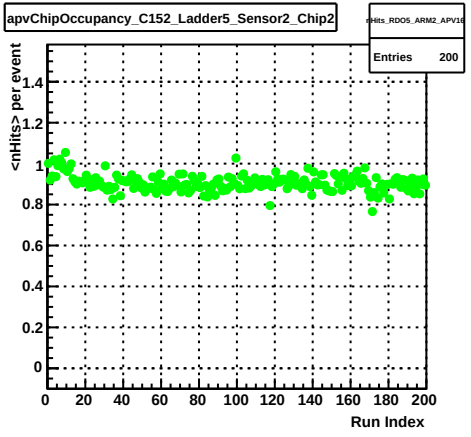

apvChipOccupancy_C147_Ladder5_Sensor1_Chip3
Entries 200
<nHits> per event
Run Index

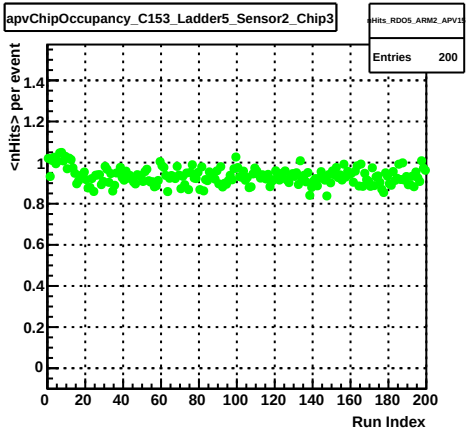

apvChipOccupancy_C148_Ladder5_Sensor1_Chip4
Entries 200
<nHits> per event
Run Index

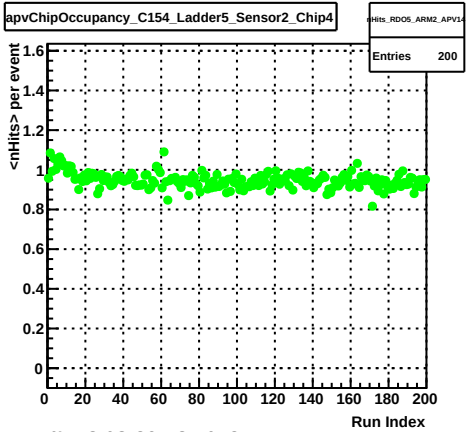

apvChipOccupancy_C149_Ladder5_Sensor1_Chip5
Entries 200
<nHits> per event
Run Index

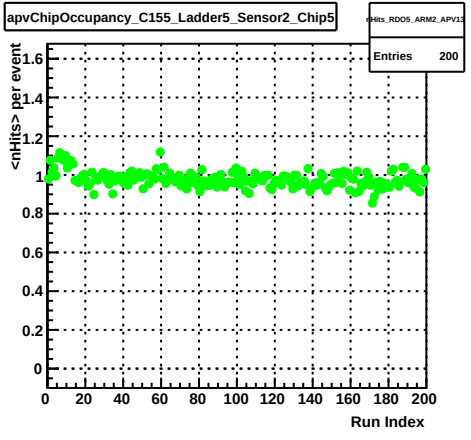

apvChipOccupancy_C150_Ladder5_Sensor1_Chip6
Entries 200
<nHits> per event
Run Index

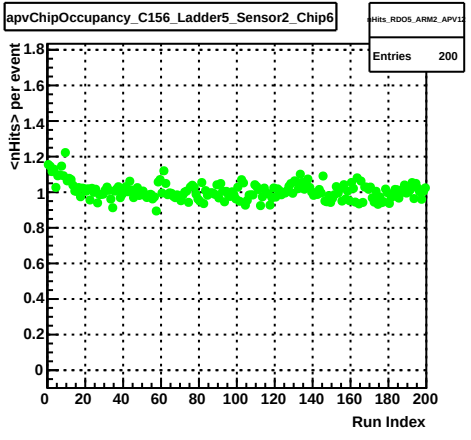

apvChipOccupancy_C151_Ladder5_Sensor2_Chip1
Entries 200
<nHits> per event
Run Index

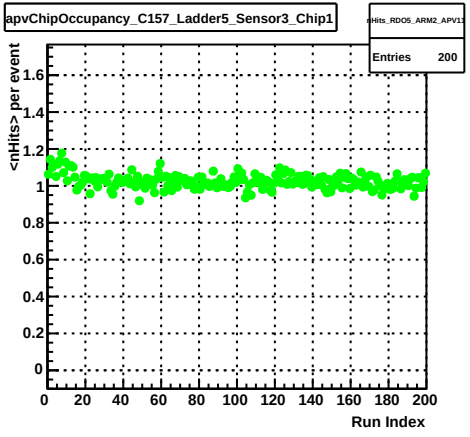

apvChipOccupancy_C152_Ladder5_Sensor2_Chip2
Entries 200
<nHits> per event
Run Index

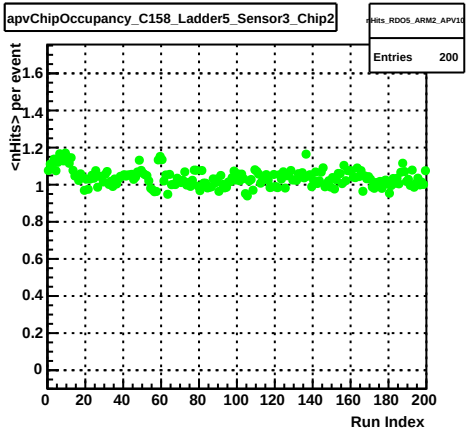

apvChipOccupancy_C153_Ladder5_Sensor2_Chip3
Entries 200
<nHits> per event
Run Index

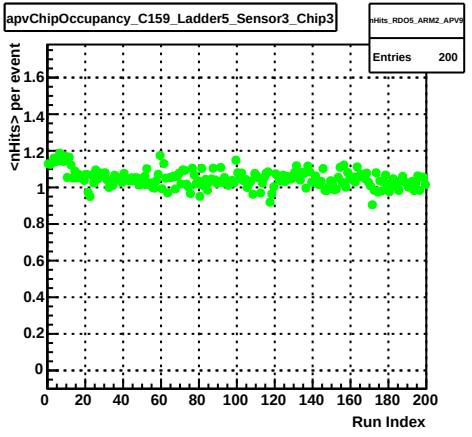

apvChipOccupancy_C154_Ladder5_Sensor2_Chip4
Entries 200
<nHits> per event
Run Index

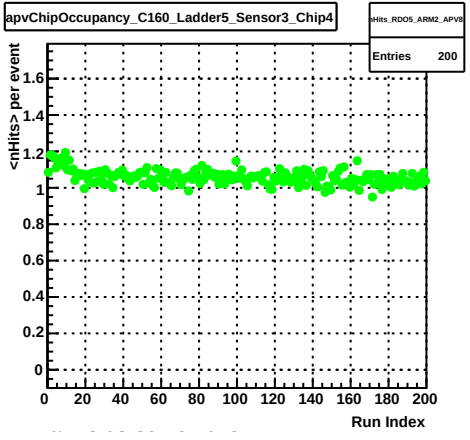

apvChipOccupancy_C155_Ladder5_Sensor2_Chip5
Entries 200
<nHits> per event
Run Index

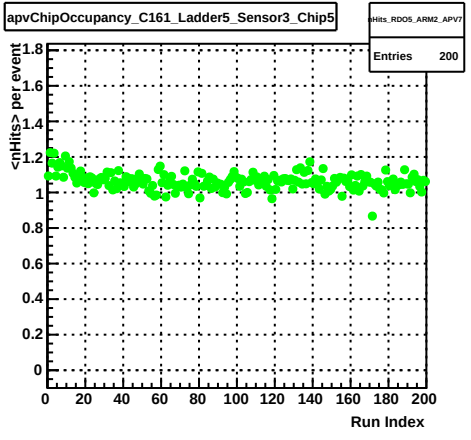

apvChipOccupancy_C156_Ladder5_Sensor2_Chip6
Entries 200
<nHits> per event
Run Index

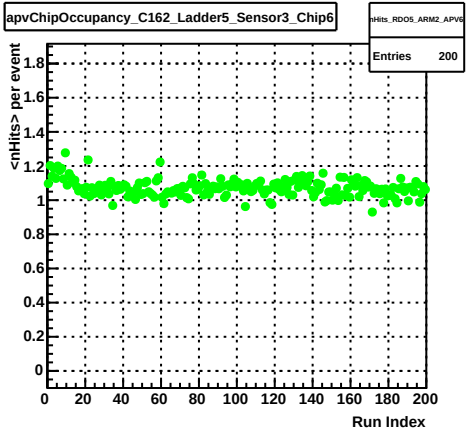

apvChipOccupancy_C157_Ladder5_Sensor3_Chip1
nHits_RDO5_ARM2_APV13
Entries 200
<nHits> per event
Run Index

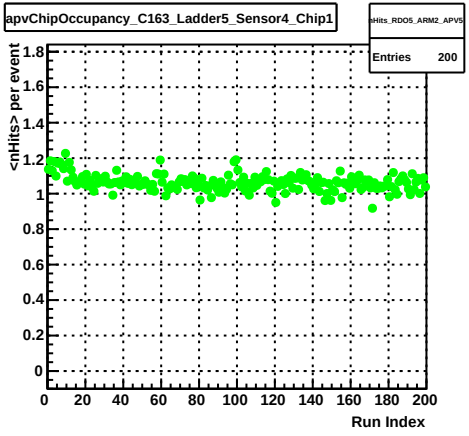

apvChipOccupancy_C158_Ladder5_Sensor3_Chip2
nHits_RDO5_ARM2_APV10
Entries 200
<nHits> per event
Run Index

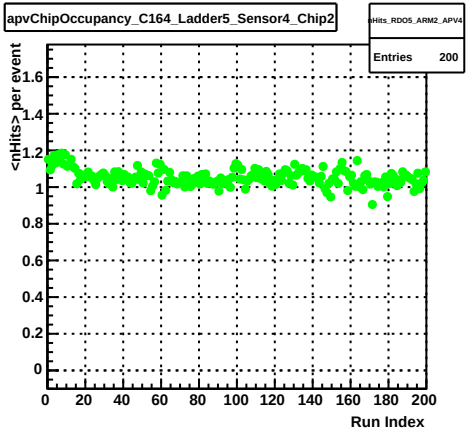

apvChipOccupancy_C159_Ladder5_Sensor3_Chip3
nHits_RDO5_ARM2_APV9
Entries 200
<nHits> per event
Run Index

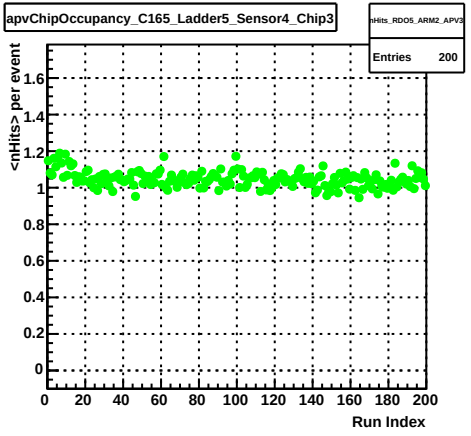

apvChipOccupancy_C160_Ladder5_Sensor3_Chip4
nHits_RDO5_ARM2_APV8
Entries 200
<nHits> per event
Run Index

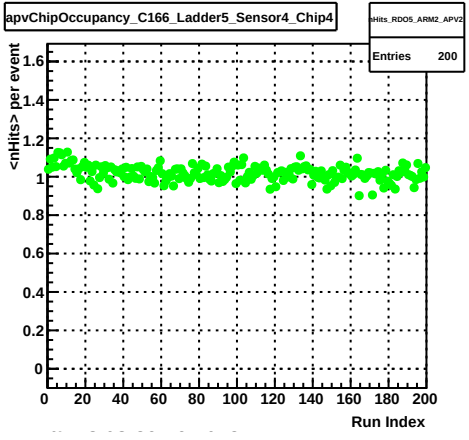

apvChipOccupancy_C161_Ladder5_Sensor3_Chip5
nHits_RDO5_ARM2_APV7
Entries 200
<nHits> per event
Run Index

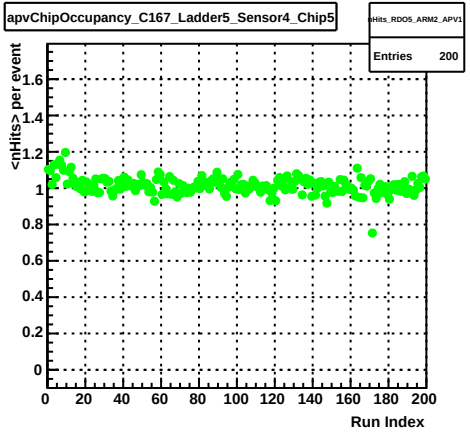

apvChipOccupancy_C162_Ladder5_Sensor3_Chip6
nHits_RDO5_ARM2_APV6
Entries 200
<nHits> per event
Run Index

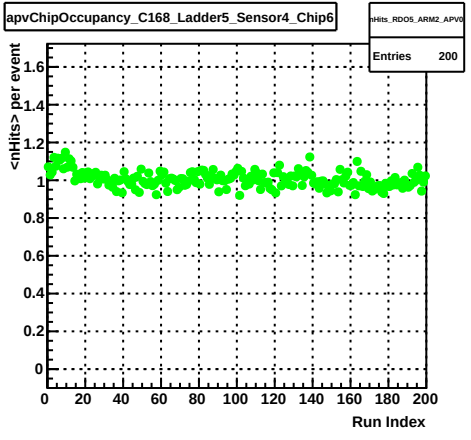

apvChipOccupancy_C163_Ladder5_Sensor4_Chip1
nHits_RD05_ARM2_APV5
Entries 200
<nHits> per event
Run Index

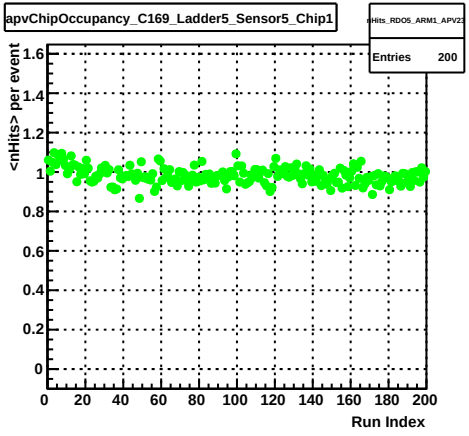

apvChipOccupancy_C164_Ladder5_Sensor4_Chip2
nHits_RD05_ARM2_APV4
Entries 200
<nHits> per event
Run Index

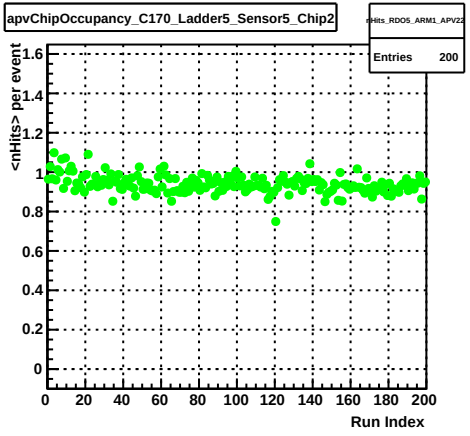

apvChipOccupancy_C165_Ladder5_Sensor4_Chip3
nHits_RD05_ARM2_APV3
Entries 200
<nHits> per event
Run Index

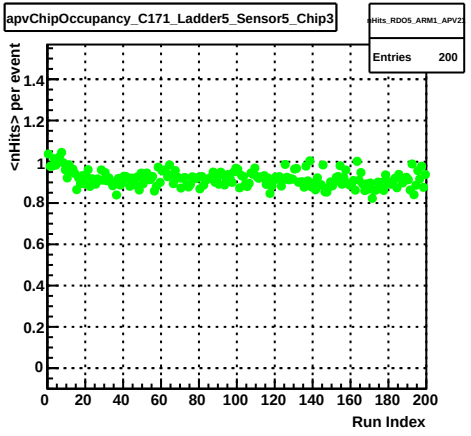

apvChipOccupancy_C166_Ladder5_Sensor4_Chip4
nHits_RD05_ARM2_APV2
Entries 200
<nHits> per event
Run Index

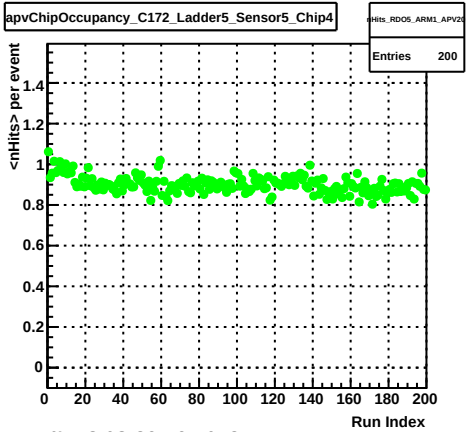

apvChipOccupancy_C167_Ladder5_Sensor4_Chip5
nHits_RD05_ARM2_APV1
Entries 200
<nHits> per event
Run Index

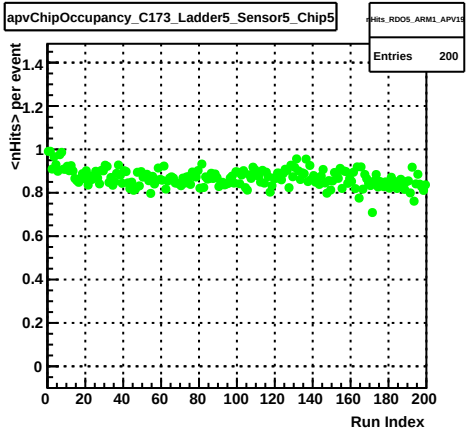

apvChipOccupancy_C168_Ladder5_Sensor4_Chip6
nHits_RD05_ARM2_APV0
Entries 200
<nHits> per event
Run Index

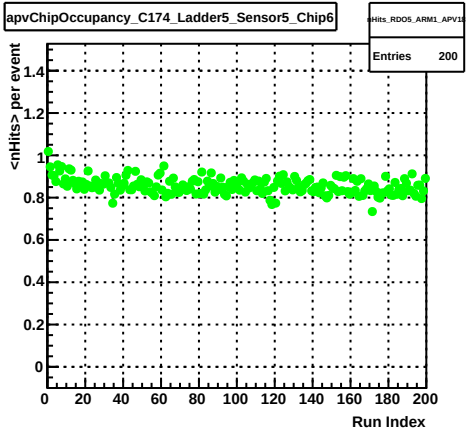

apvChipOccupancy_C169_Ladder5_Sensor5_Chip1
Entries 200
<nHits> per event
Run Index

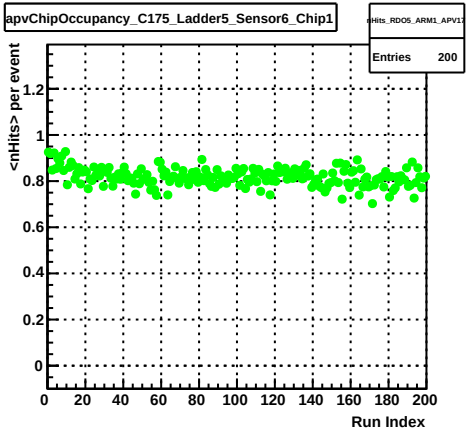

apvChipOccupancy_C170_Ladder5_Sensor5_Chip2
Entries 200
<nHits> per event
Run Index

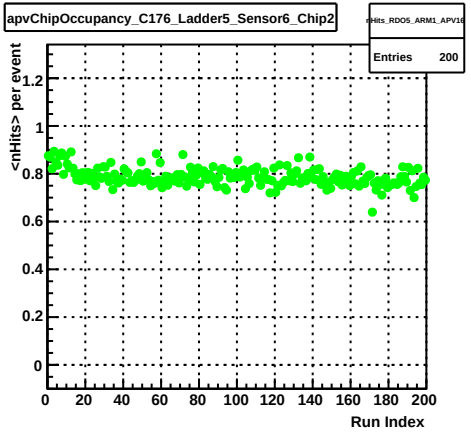

apvChipOccupancy_C171_Ladder5_Sensor5_Chip3
Entries 200
<nHits> per event
Run Index

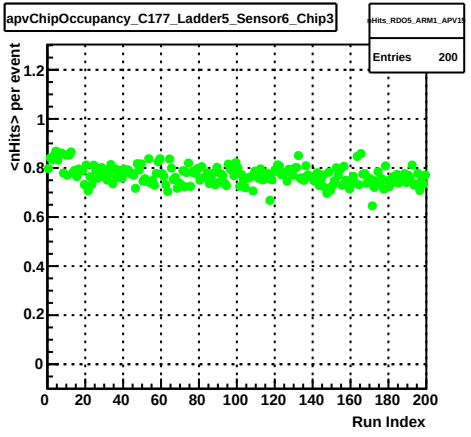

apvChipOccupancy_C172_Ladder5_Sensor5_Chip4
Entries 200
<nHits> per event
Run Index

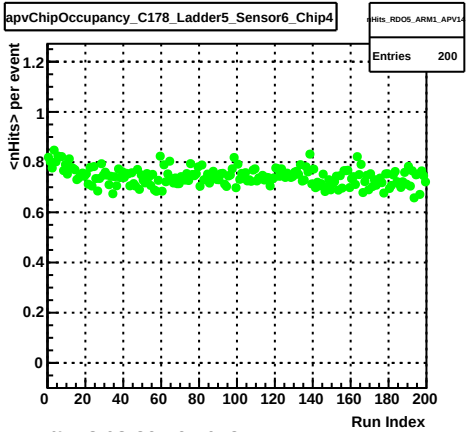

apvChipOccupancy_C173_Ladder5_Sensor5_Chip5
Entries 200
<nHits> per event
Run Index

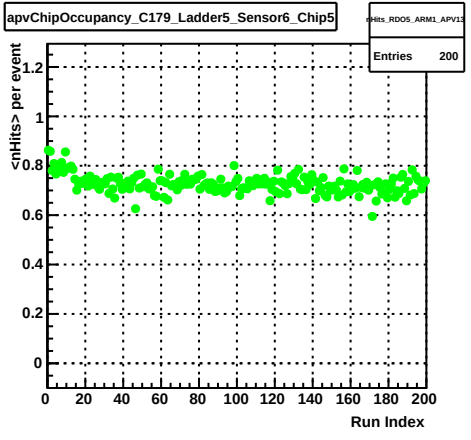

apvChipOccupancy_C174_Ladder5_Sensor5_Chip6
Entries 200
<nHits> per event
Run Index

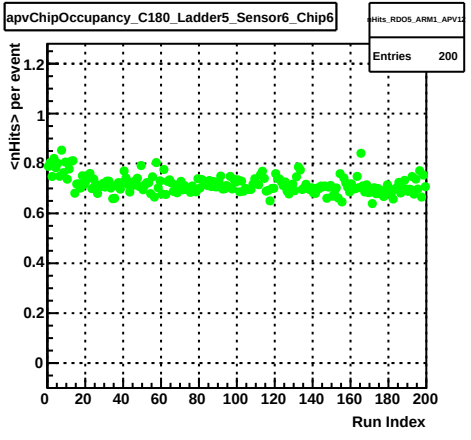

apvChipOccupancy_C175_Ladder5_Sensor6_Chip1
Entries 200
<nHits> per event
Run Index

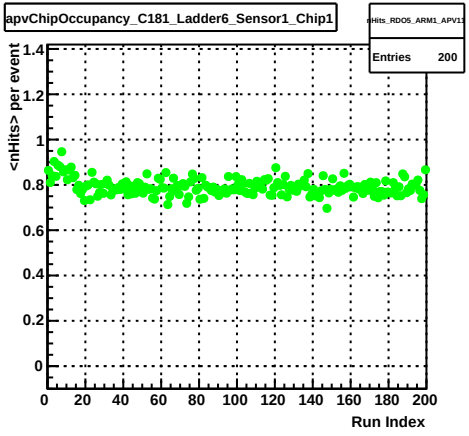

apvChipOccupancy_C176_Ladder5_Sensor6_Chip2
Entries 200
<nHits> per event
Run Index

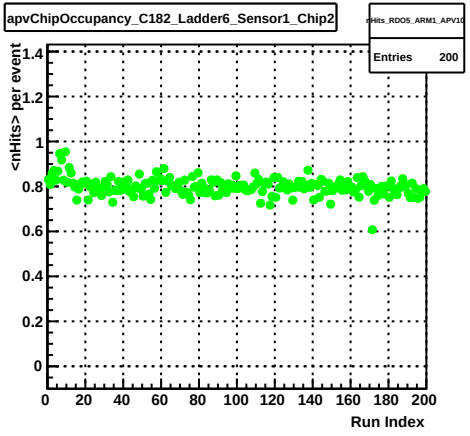

apvChipOccupancy_C177_Ladder5_Sensor6_Chip3
Entries 200
<nHits> per event
Run Index

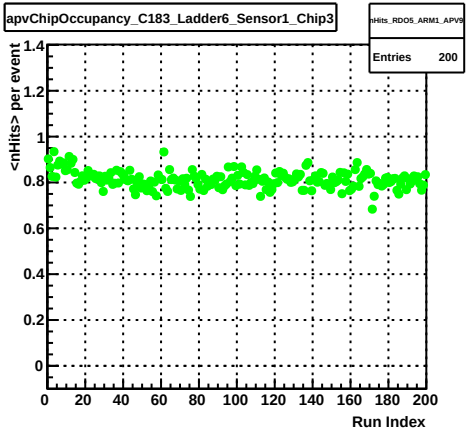

apvChipOccupancy_C178_Ladder5_Sensor6_Chip4
Entries 200
<nHits> per event
Run Index

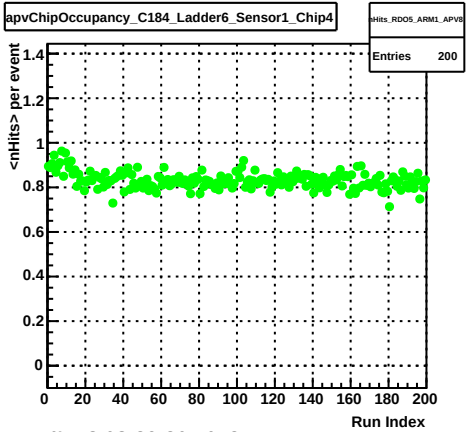

apvChipOccupancy_C179_Ladder5_Sensor6_Chip5
Entries 200
<nHits> per event
Run Index

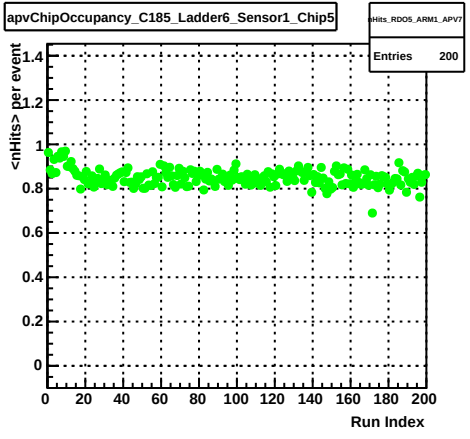

apvChipOccupancy_C180_Ladder5_Sensor6_Chip6
Entries 200
<nHits> per event
Run Index

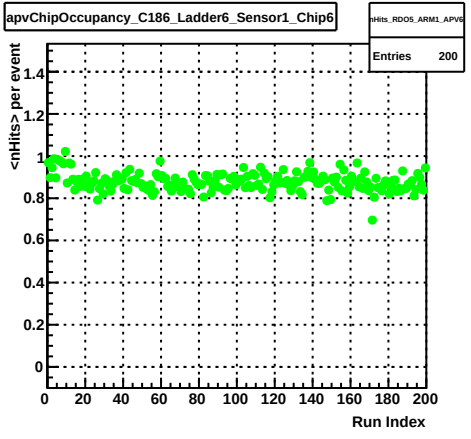

apvChipOccupancy_C181_Ladder6_Sensor1_Chip1
Entries 200
<nHits> per event
Run Index

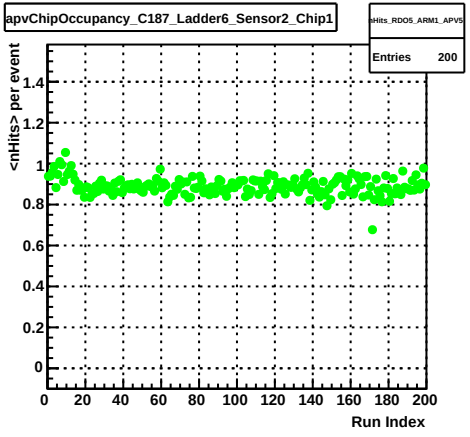

apvChipOccupancy_C182_Ladder6_Sensor1_Chip2
Entries 200
<nHits> per event
Run Index

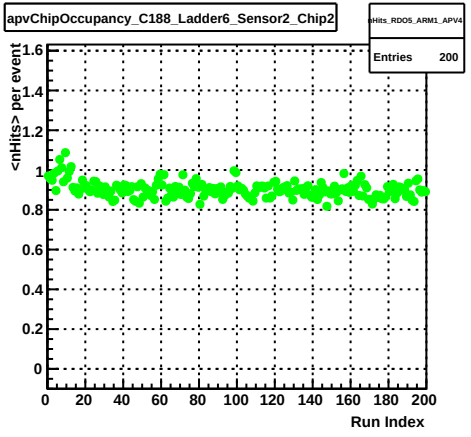

apvChipOccupancy_C183_Ladder6_Sensor1_Chip3
Entries 200
<nHits> per event
Run Index

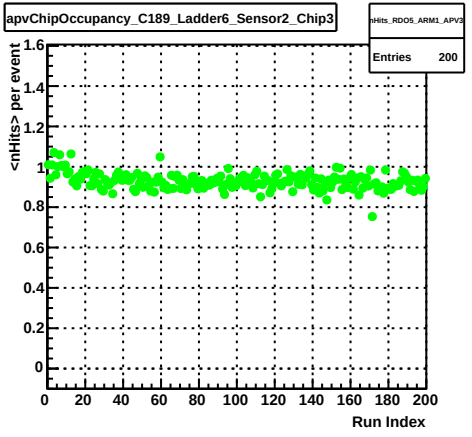

apvChipOccupancy_C184_Ladder6_Sensor1_Chip4
Entries 200
<nHits> per event
Run Index

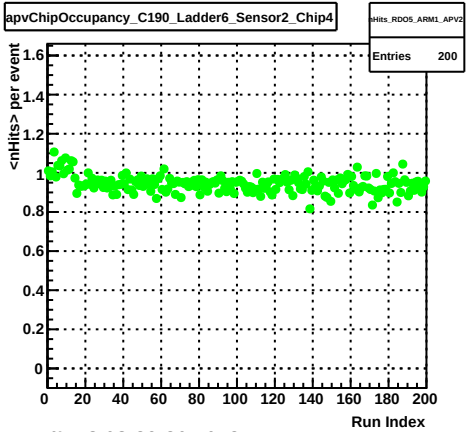

apvChipOccupancy_C185_Ladder6_Sensor1_Chip5
Entries 200
<nHits> per event
Run Index

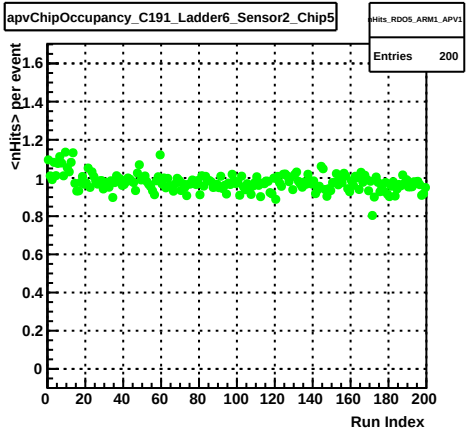

apvChipOccupancy_C186_Ladder6_Sensor1_Chip6
Entries 200
<nHits> per event
Run Index

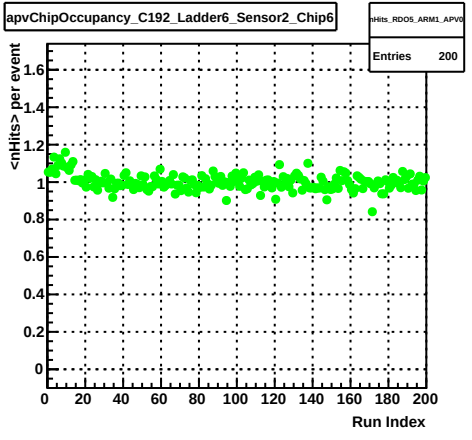

apvChipOccupancy_C187_Ladder6_Sensor2_Chip1
nHits_RD05_ARM1_APV5
Entries 200
<nHits> per event
Run Index

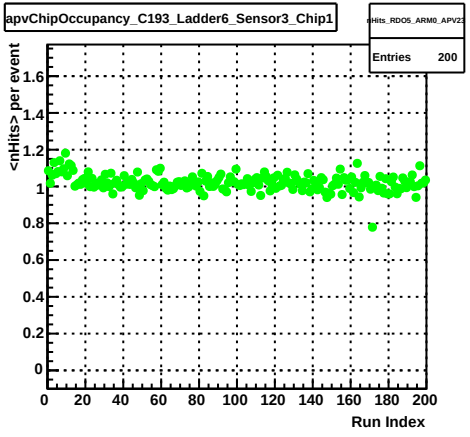

apvChipOccupancy_C188_Ladder6_Sensor2_Chip2
nHits_RD05_ARM1_APV4
Entries 200
<nHits> per event
Run Index

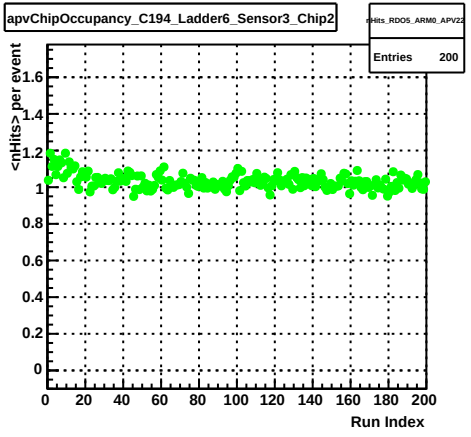

apvChipOccupancy_C189_Ladder6_Sensor2_Chip3
nHits_RD05_ARM1_APV3
Entries 200
<nHits> per event
Run Index

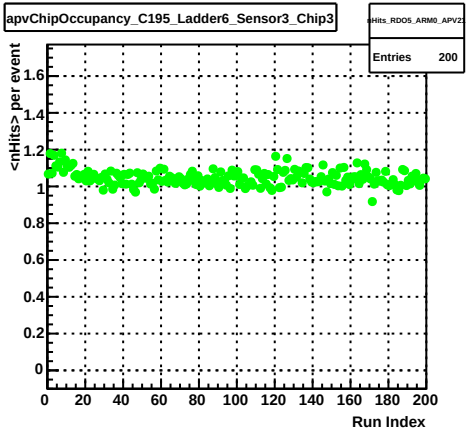

apvChipOccupancy_C190_Ladder6_Sensor2_Chip4
nHits_RD05_ARM1_APV2
Entries 200
<nHits> per event
Run Index

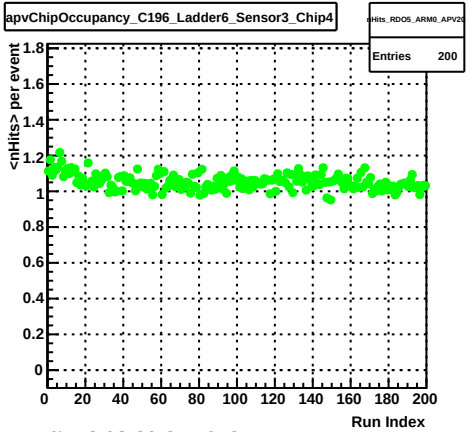

apvChipOccupancy_C191_Ladder6_Sensor2_Chip5
nHits_RD05_ARM1_APV1
Entries 200
<nHits> per event
Run Index

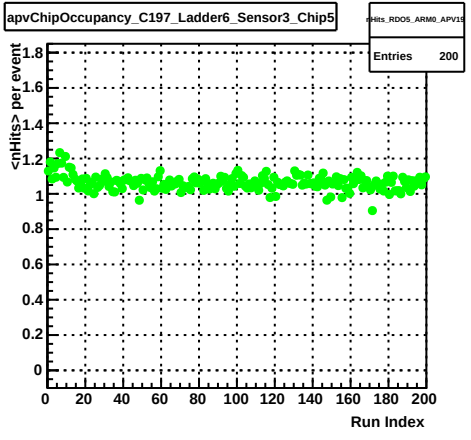

apvChipOccupancy_C192_Ladder6_Sensor2_Chip6
nHits_RD05_ARM1_APV0
Entries 200
<nHits> per event
Run Index

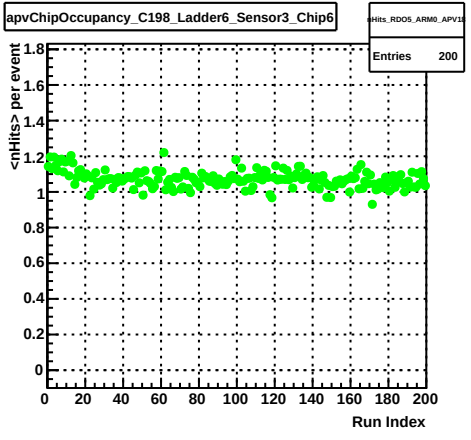

apvChipOccupancy_C193_Ladder6_Sensor3_Chip1
Entries 200
<nHits> per event
Run Index

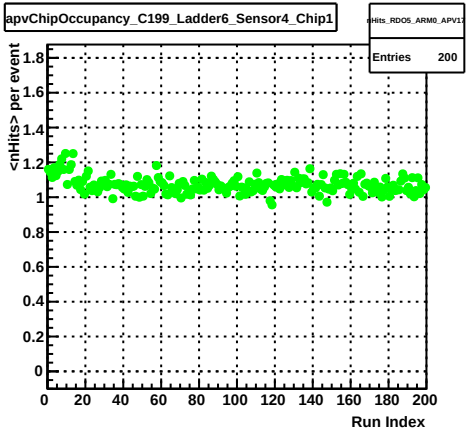

apvChipOccupancy_C194_Ladder6_Sensor3_Chip2
Entries 200
<nHits> per event
Run Index

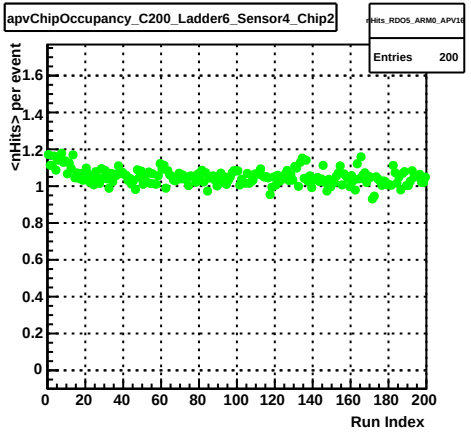

apvChipOccupancy_C195_Ladder6_Sensor3_Chip3
Entries 200
<nHits> per event
Run Index

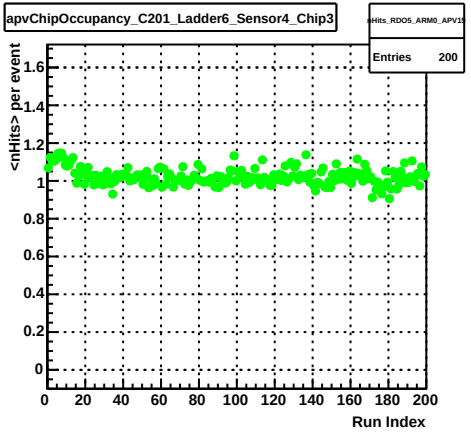

apvChipOccupancy_C196_Ladder6_Sensor3_Chip4
Entries 200
<nHits> per event
Run Index

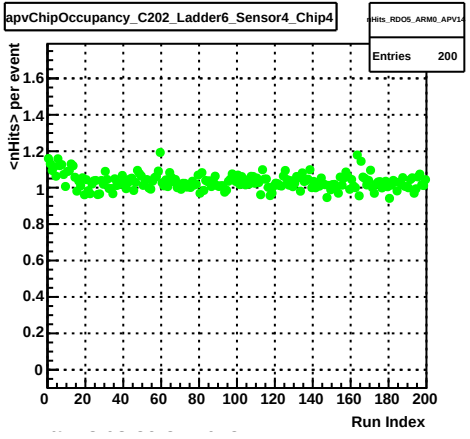

apvChipOccupancy_C197_Ladder6_Sensor3_Chip5
Entries 200
<nHits> per event
Run Index

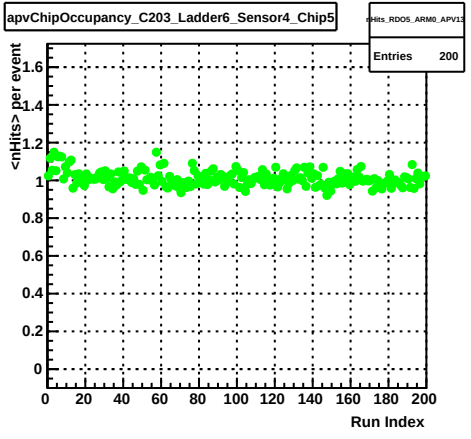

apvChipOccupancy_C198_Ladder6_Sensor3_Chip6
Entries 200
<nHits> per event
Run Index

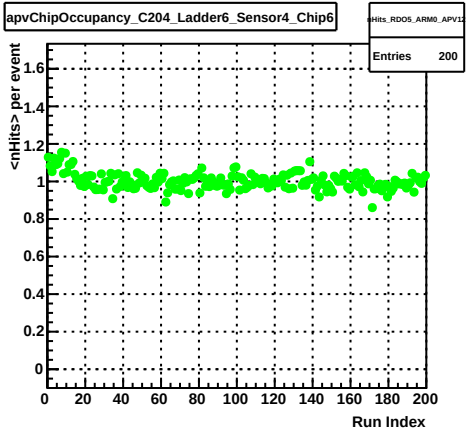

apvChipOccupancy_C199_Ladder6_Sensor4_Chip1
Entries 200
<nHits> per event
Run Index

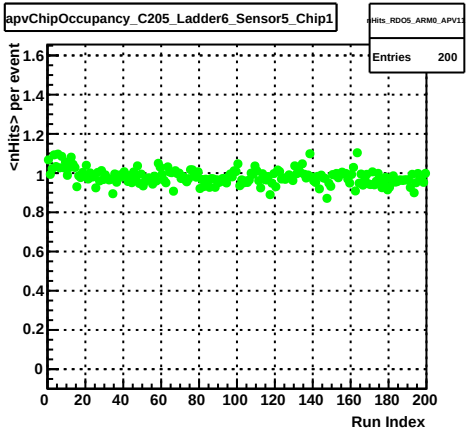

apvChipOccupancy_C200_Ladder6_Sensor4_Chip2
Entries 200
<nHits> per event
Run Index

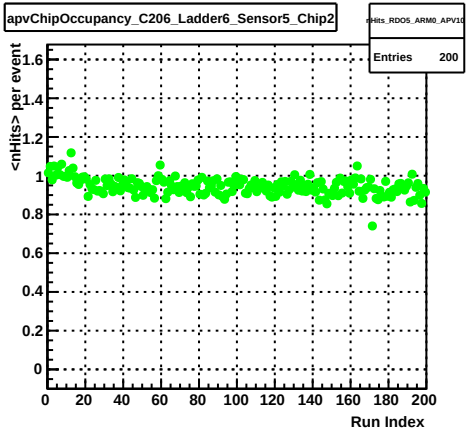

apvChipOccupancy_C201_Ladder6_Sensor4_Chip3
Entries 200
<nHits> per event
Run Index

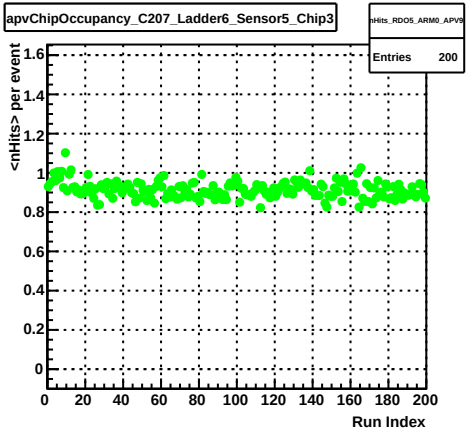

apvChipOccupancy_C202_Ladder6_Sensor4_Chip4
Entries 200
<nHits> per event
Run Index

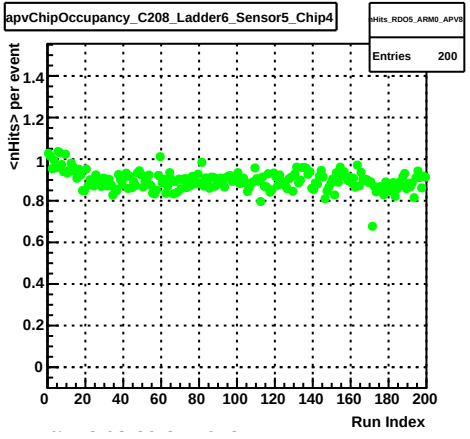

apvChipOccupancy_C203_Ladder6_Sensor4_Chip5
Entries 200
<nHits> per event
Run Index

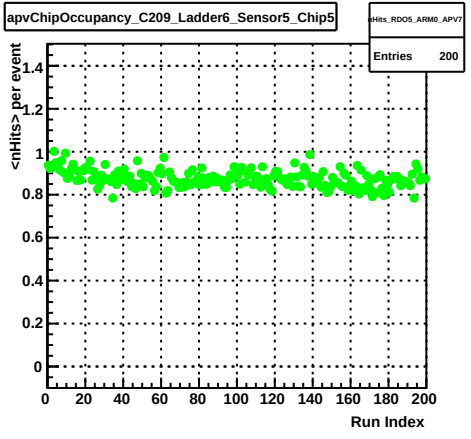

apvChipOccupancy_C204_Ladder6_Sensor4_Chip6
Entries 200
<nHits> per event
Run Index

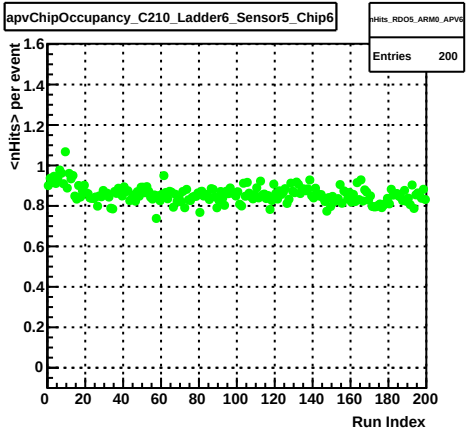

apvChipOccupancy_C205_Ladder6_Sensor5_Chip1
Entries 200
<nHits> per event
Run Index

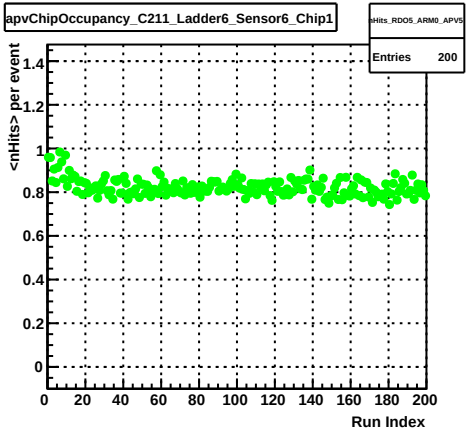

apvChipOccupancy_C206_Ladder6_Sensor5_Chip2
Entries 200
<nHits> per event
Run Index

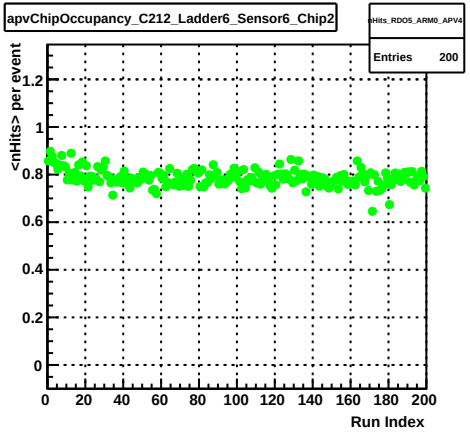

apvChipOccupancy_C207_Ladder6_Sensor5_Chip3
Entries 200
<nHits> per event
Run Index

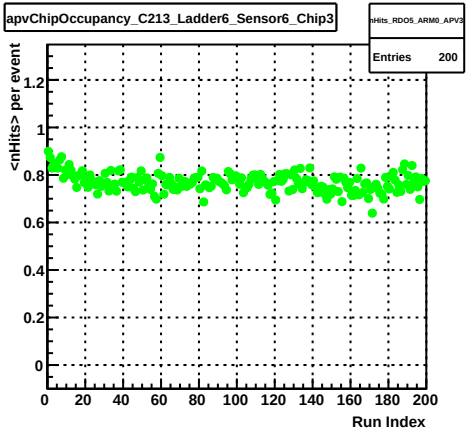

apvChipOccupancy_C208_Ladder6_Sensor5_Chip4
Entries 200
<nHits> per event
Run Index

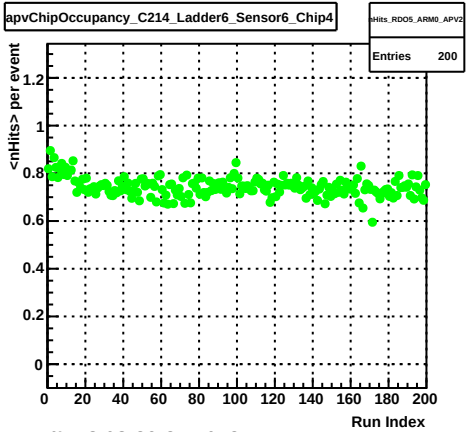

apvChipOccupancy_C209_Ladder6_Sensor5_Chip5
Entries 200
<nHits> per event
Run Index

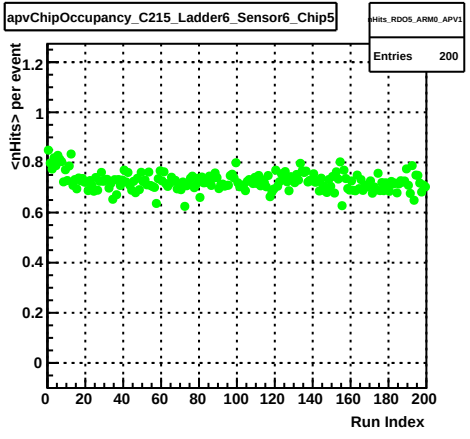

apvChipOccupancy_C210_Ladder6_Sensor5_Chip6
Entries 200
<nHits> per event
Run Index

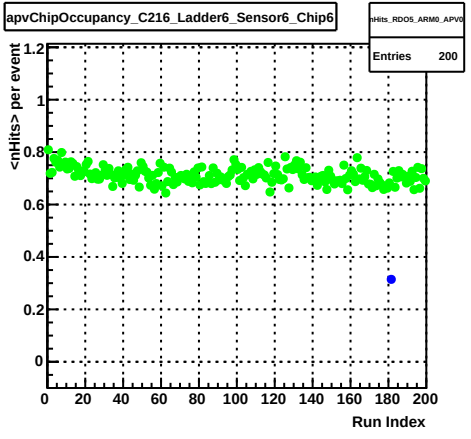

apvChipOccupancy_C211_Ladder6_Sensor6_Chip1
nHits_RDO5_ARM0_APV5
Entries 200
<nHits> per event
Run Index

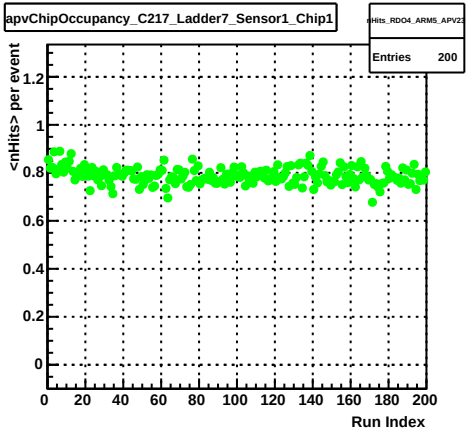

apvChipOccupancy_C212_Ladder6_Sensor6_Chip2
nHits_RDO5_ARM0_APV4
Entries 200
<nHits> per event
Run Index

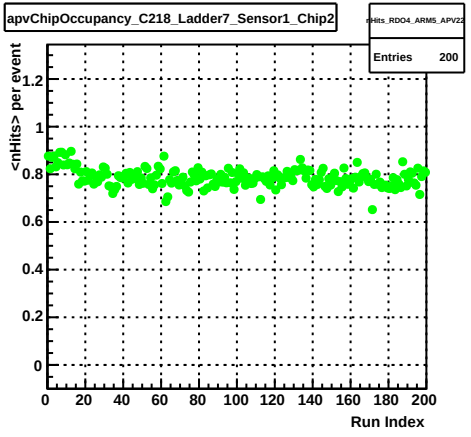

apvChipOccupancy_C213_Ladder6_Sensor6_Chip3
nHits_RDO5_ARM0_APV3
Entries 200
<nHits> per event
Run Index

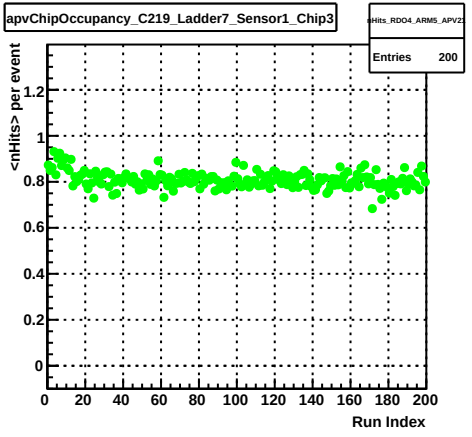

apvChipOccupancy_C214_Ladder6_Sensor6_Chip4
nHits_RDO5_ARM0_APV2
Entries 200
<nHits> per event
Run Index

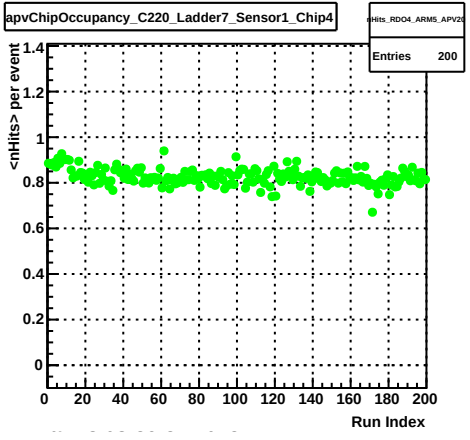

apvChipOccupancy_C215_Ladder6_Sensor6_Chip5
nHits_RDO5_ARM0_APV1
Entries 200
<nHits> per event
Run Index

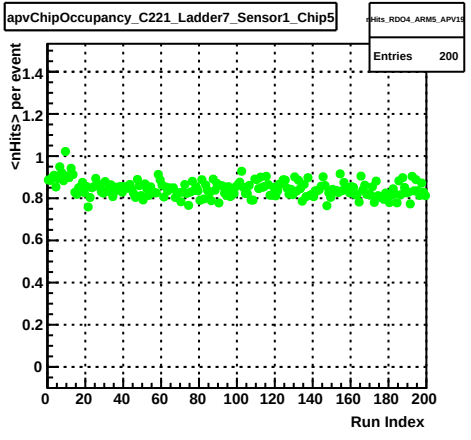

apvChipOccupancy_C216_Ladder6_Sensor6_Chip6
nHits_RDO5_ARM0_APV0
Entries 200
<nHits> per event
Run Index

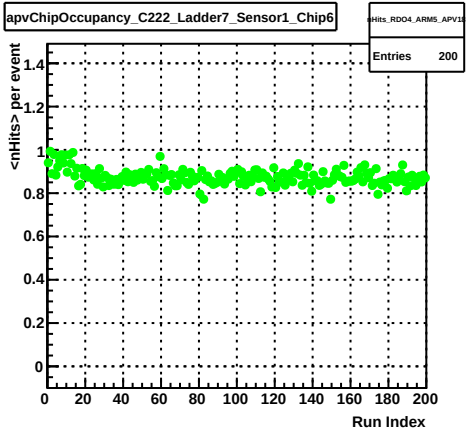

apvChipOccupancy_C217_Ladder7_Sensor1_Chip1
Entries 200
<nHits> per event
Run Index

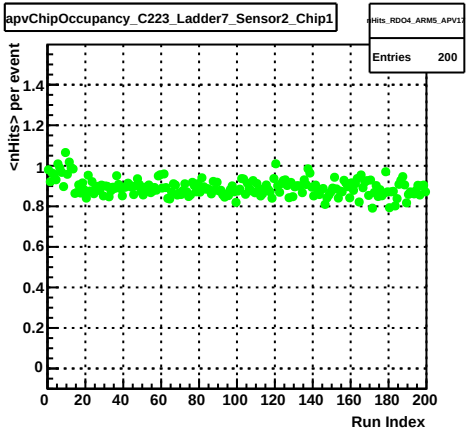

apvChipOccupancy_C218_Ladder7_Sensor1_Chip2
Entries 200
<nHits> per event
Run Index

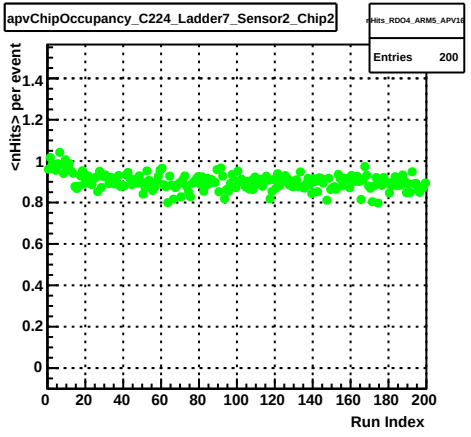

apvChipOccupancy_C219_Ladder7_Sensor1_Chip3
Entries 200
<nHits> per event
Run Index

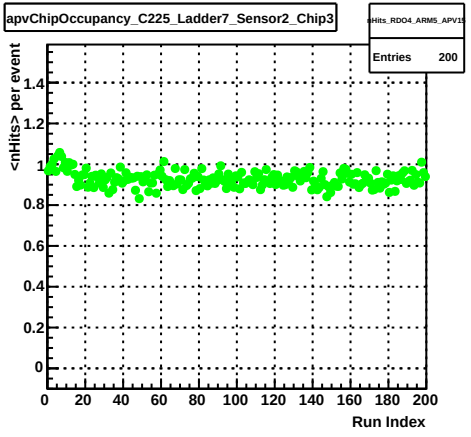

apvChipOccupancy_C220_Ladder7_Sensor1_Chip4
Entries 200
<nHits> per event
Run Index

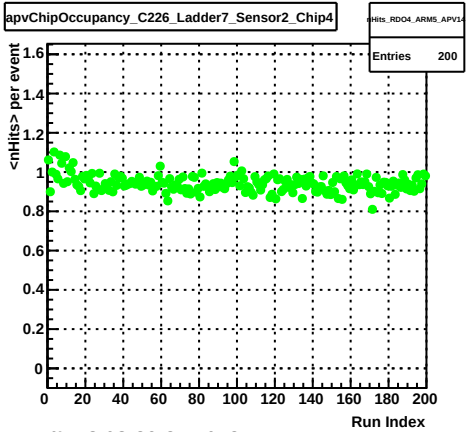

apvChipOccupancy_C221_Ladder7_Sensor1_Chip5
Entries 200
<nHits> per event
Run Index

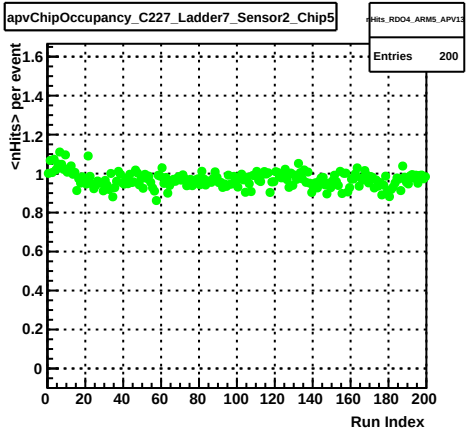

apvChipOccupancy_C222_Ladder7_Sensor1_Chip6
Entries 200
<nHits> per event
Run Index

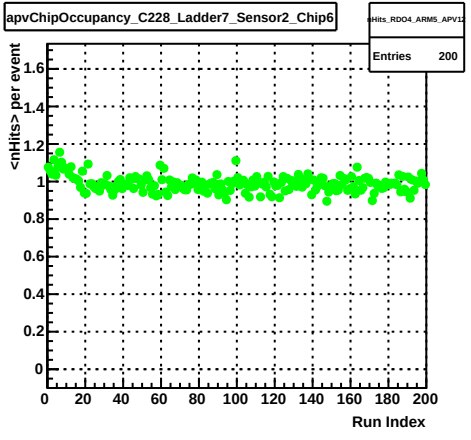

apvChipOccupancy_C223_Ladder7_Sensor2_Chip1
Entries 200
<nHits> per event
Run Index

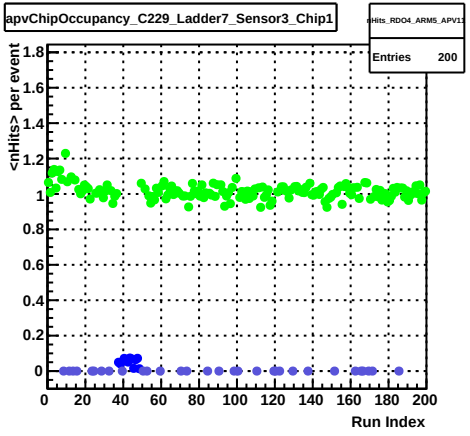

apvChipOccupancy_C224_Ladder7_Sensor2_Chip2
Entries 200
<nHits> per event
Run Index

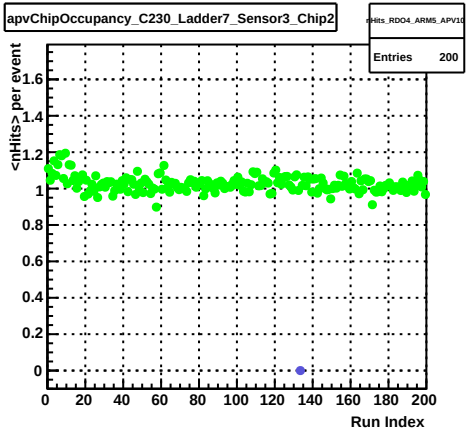

apvChipOccupancy_C225_Ladder7_Sensor2_Chip3
Entries 200
<nHits> per event
Run Index

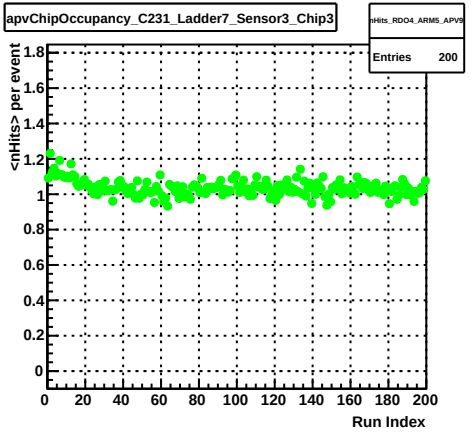

apvChipOccupancy_C226_Ladder7_Sensor2_Chip4
Entries 200
<nHits> per event
Run Index

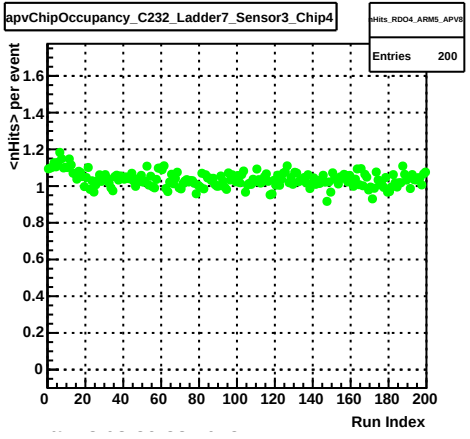

apvChipOccupancy_C227_Ladder7_Sensor2_Chip5
Entries 200
<nHits> per event
Run Index

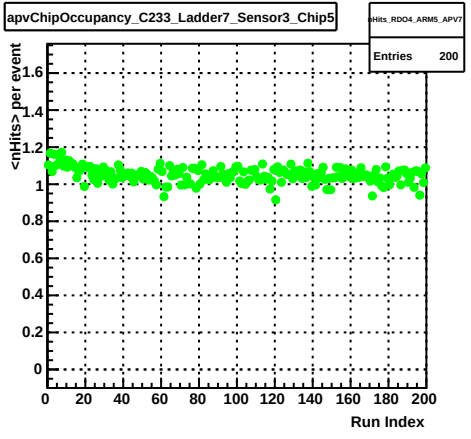

apvChipOccupancy_C228_Ladder7_Sensor2_Chip6
Entries 200
<nHits> per event
Run Index

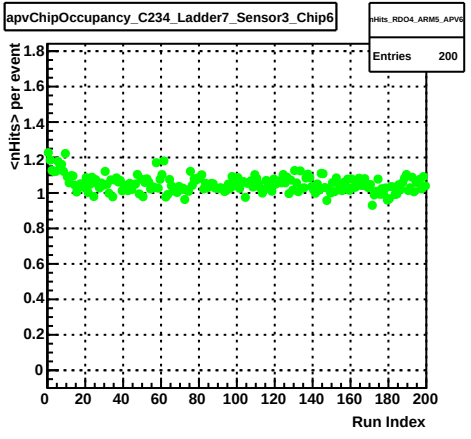

apvChipOccupancy_C229_Ladder7_Sensor3_Chip1
nHits_RD04_ARM5_APV13
Entries 200
<nHits> per event
Run Index

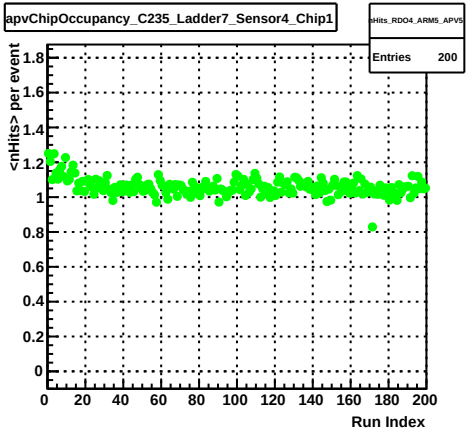

apvChipOccupancy_C230_Ladder7_Sensor3_Chip2
nHits_RD04_ARM5_APV10
Entries 200
<nHits> per event
Run Index

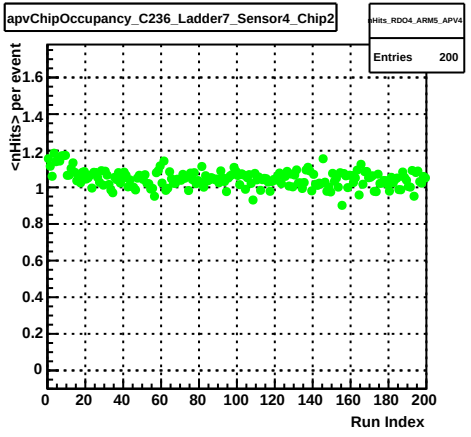

apvChipOccupancy_C231_Ladder7_Sensor3_Chip3
nHits_RD04_ARM5_APV9
Entries 200
<nHits> per event
Run Index

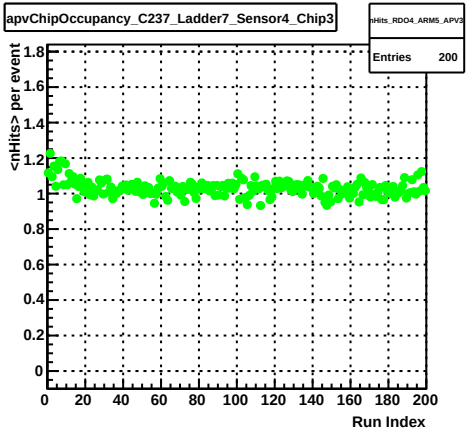

apvChipOccupancy_C232_Ladder7_Sensor3_Chip4
nHits_RD04_ARM5_APV8
Entries 200
<nHits> per event
Run Index

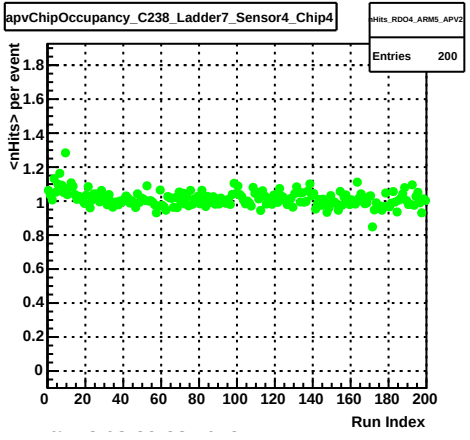

apvChipOccupancy_C233_Ladder7_Sensor3_Chip5
nHits_RD04_ARM5_APV7
Entries 200
<nHits> per event
Run Index

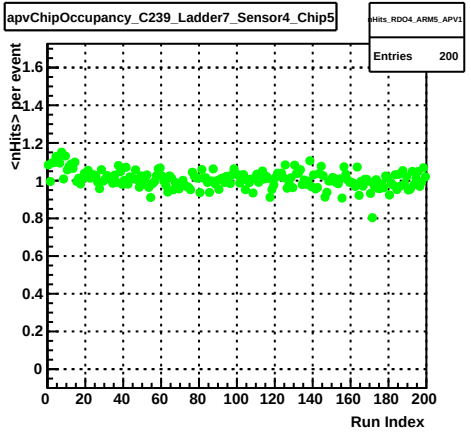

apvChipOccupancy_C234_Ladder7_Sensor3_Chip6
nHits_RD04_ARM5_APV6
Entries 200
<nHits> per event
Run Index

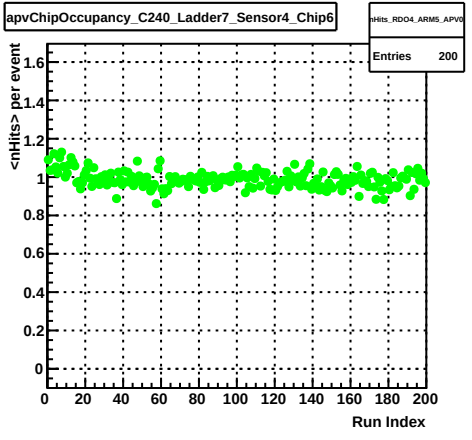

apvChipOccupancy_C235_Ladder7_Sensor4_Chip1
nHits_RDO4_ARM5_APV5
Entries 200
<nHits> per event
Run Index

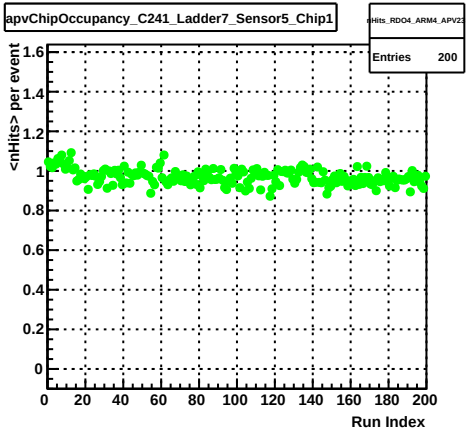

apvChipOccupancy_C236_Ladder7_Sensor4_Chip2
nHits_RDO4_ARM5_APV4
Entries 200
<nHits> per event
Run Index

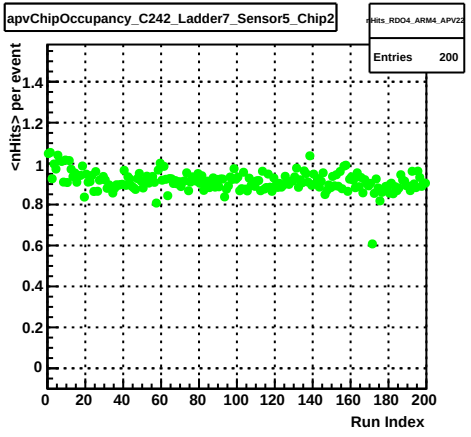

apvChipOccupancy_C237_Ladder7_Sensor4_Chip3
nHits_RDO4_ARM5_APV3
Entries 200
<nHits> per event
Run Index

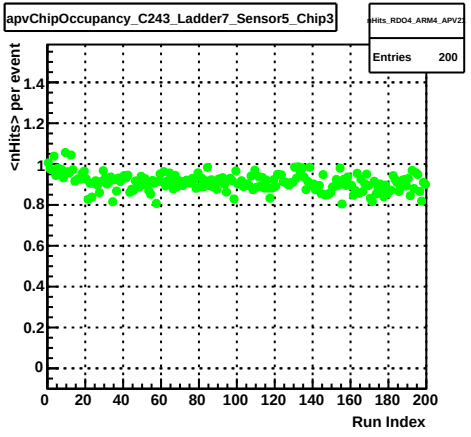

apvChipOccupancy_C238_Ladder7_Sensor4_Chip4
nHits_RDO4_ARM5_APV2
Entries 200
<nHits> per event
Run Index

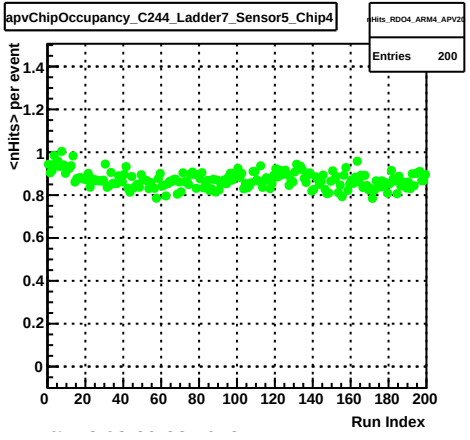

apvChipOccupancy_C239_Ladder7_Sensor4_Chip5
nHits_RDO4_ARM5_APV1
Entries 200
<nHits> per event
Run Index

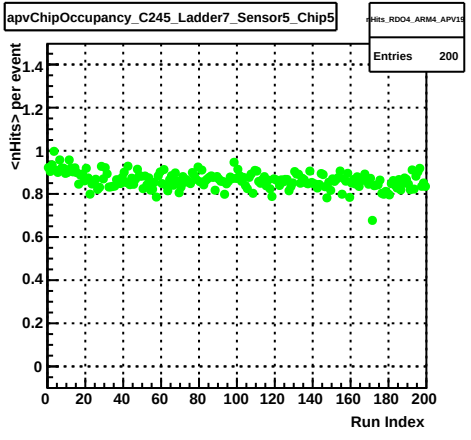

apvChipOccupancy_C240_Ladder7_Sensor4_Chip6
nHits_RDO4_ARM5_APV0
Entries 200
<nHits> per event
Run Index

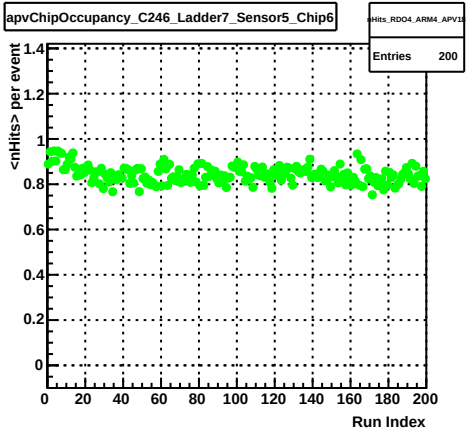

apvChipOccupancy_C241_Ladder7_Sensor5_Chip1
Entries 200
<nHits> per event
Run Index

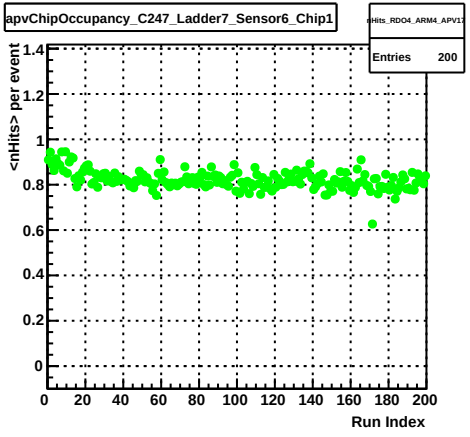

apvChipOccupancy_C242_Ladder7_Sensor5_Chip2
Entries 200
<nHits> per event
Run Index

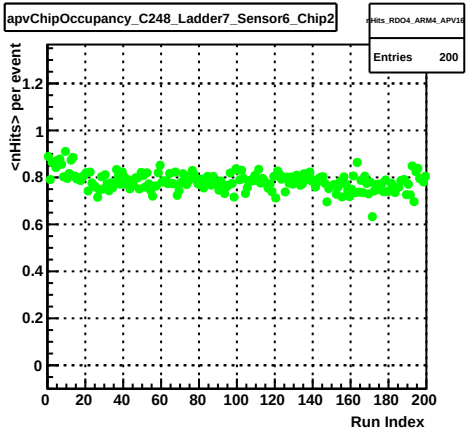

apvChipOccupancy_C243_Ladder7_Sensor5_Chip3
Entries 200
<nHits> per event
Run Index

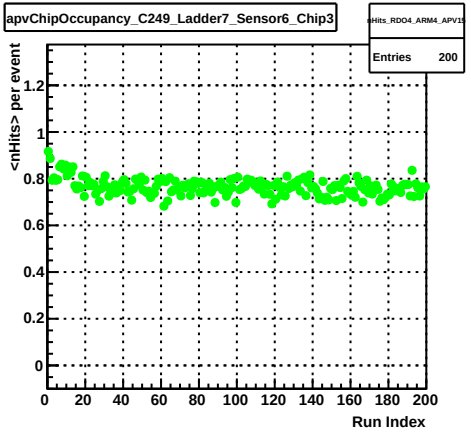

apvChipOccupancy_C244_Ladder7_Sensor5_Chip4
Entries 200
<nHits> per event
Run Index

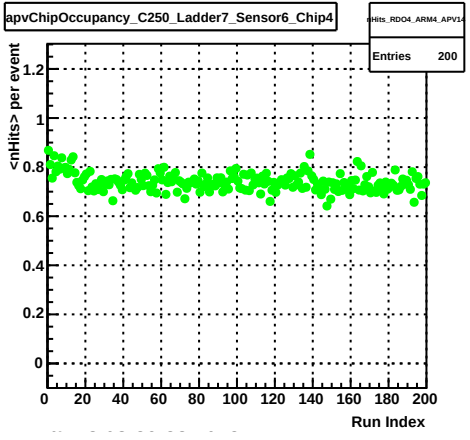

apvChipOccupancy_C245_Ladder7_Sensor5_Chip5
Entries 200
<nHits> per event
Run Index

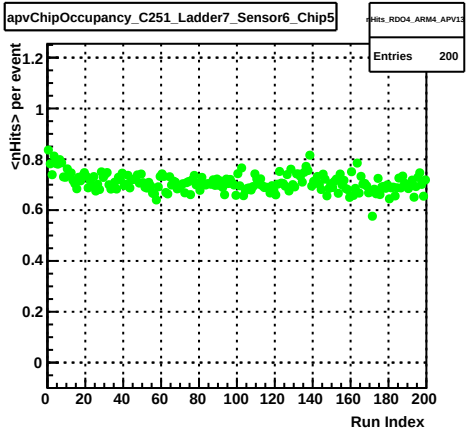

apvChipOccupancy_C246_Ladder7_Sensor5_Chip6
Entries 200
<nHits> per event
Run Index

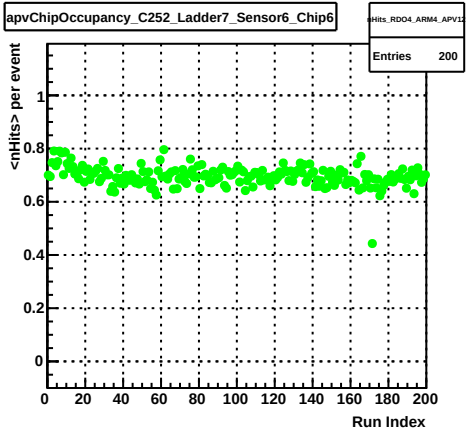

apvChipOccupancy_C247_Ladder7_Sensor6_Chip1
Entries 200
<nHits> per event
Run Index

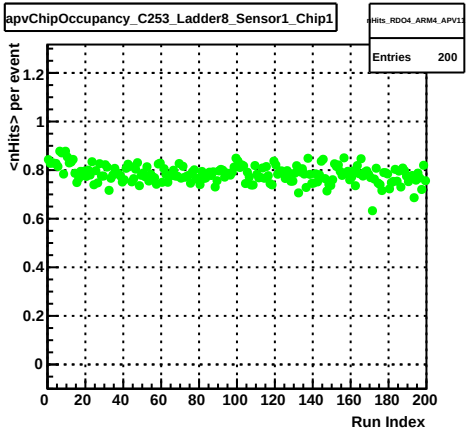

apvChipOccupancy_C248_Ladder7_Sensor6_Chip2
Entries 200
<nHits> per event
Run Index

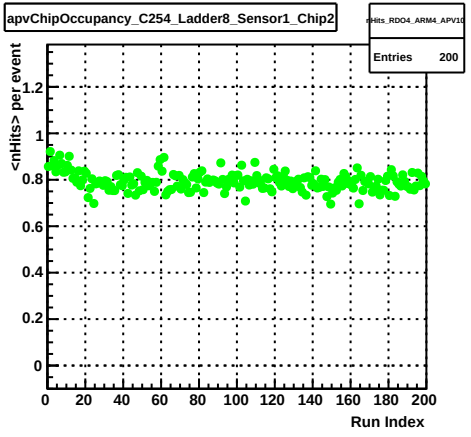

apvChipOccupancy_C249_Ladder7_Sensor6_Chip3
Entries 200
<nHits> per event
Run Index

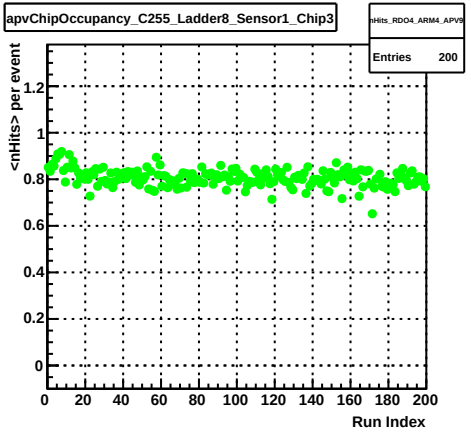

apvChipOccupancy_C250_Ladder7_Sensor6_Chip4
Entries 200
<nHits> per event
Run Index

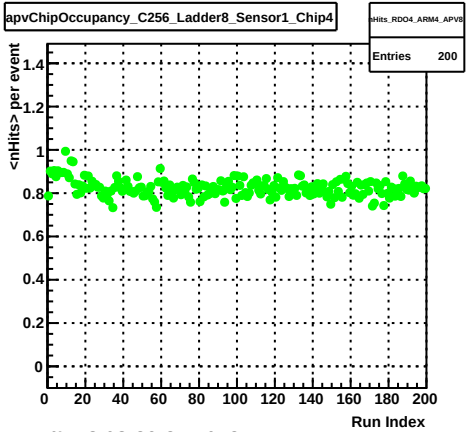

apvChipOccupancy_C251_Ladder7_Sensor6_Chip5
Entries 200
<nHits> per event
Run Index

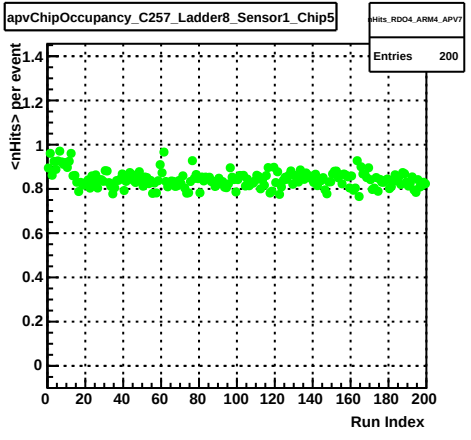

apvChipOccupancy_C252_Ladder7_Sensor6_Chip6
Entries 200
<nHits> per event
Run Index

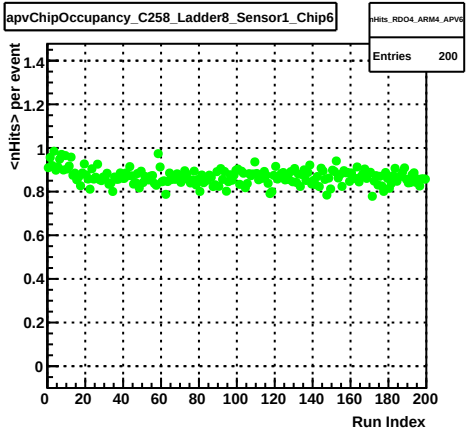

apvChipOccupancy_C253_Ladder8_Sensor1_Chip1
Entries 200
<nHits> per event
Run Index

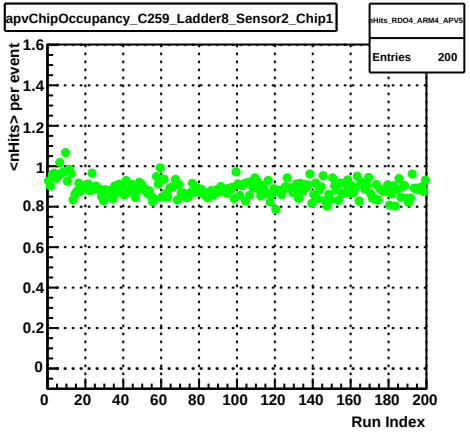

apvChipOccupancy_C254_Ladder8_Sensor1_Chip2
Entries 200
<nHits> per event
Run Index

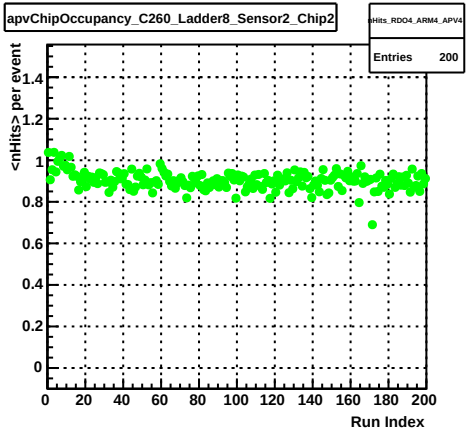

apvChipOccupancy_C255_Ladder8_Sensor1_Chip3
Entries 200
<nHits> per event
Run Index

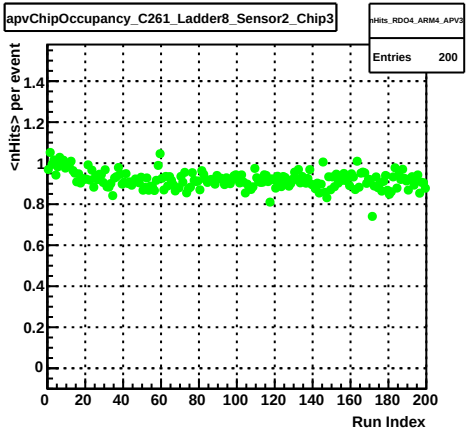

apvChipOccupancy_C256_Ladder8_Sensor1_Chip4
Entries 200
<nHits> per event
Run Index

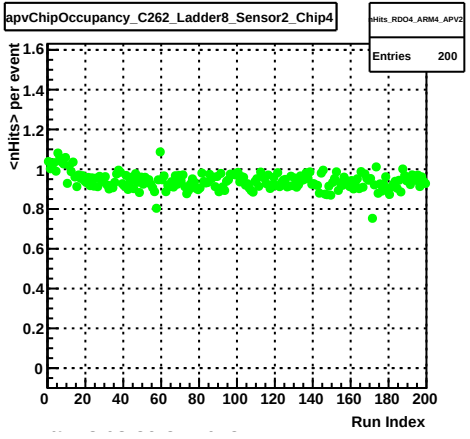

apvChipOccupancy_C257_Ladder8_Sensor1_Chip5
Entries 200
<nHits> per event
Run Index

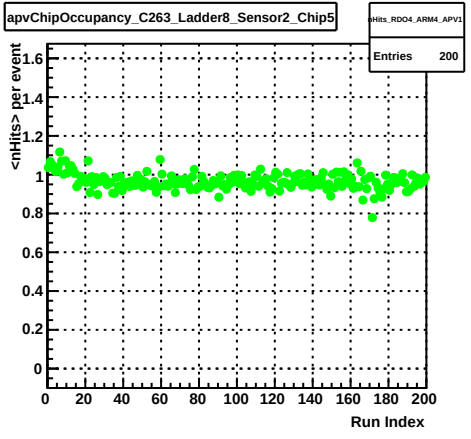

apvChipOccupancy_C258_Ladder8_Sensor1_Chip6
Entries 200
<nHits> per event
Run Index

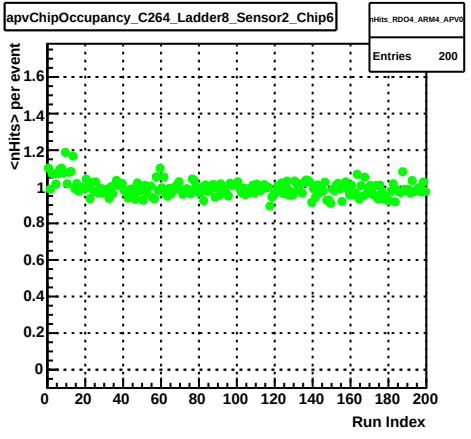

apvChipOccupancy_C259_Ladder8_Sensor2_Chip1
nHits_RDO4_ARM4_APV5
Entries 200
<nHits> per event
Run Index

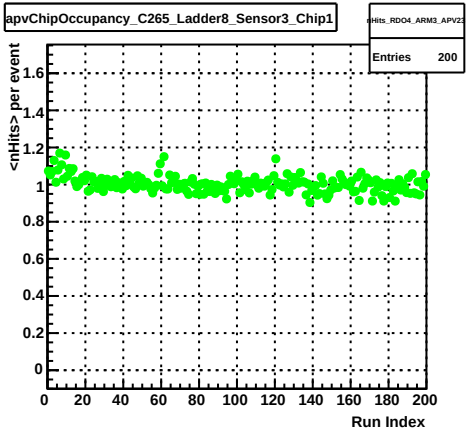

apvChipOccupancy_C260_Ladder8_Sensor2_Chip2
nHits_RDO4_ARM4_APV4
Entries 200
<nHits> per event
Run Index

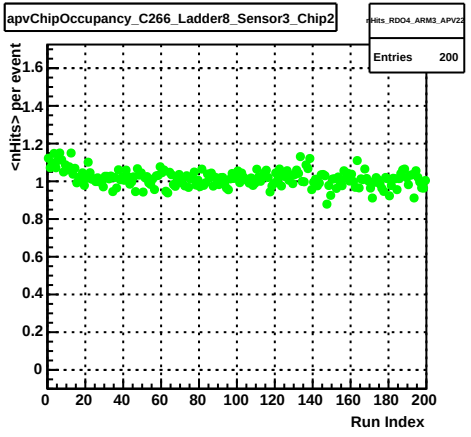

apvChipOccupancy_C261_Ladder8_Sensor2_Chip3
nHits_RDO4_ARM4_APV3
Entries 200
<nHits> per event
Run Index

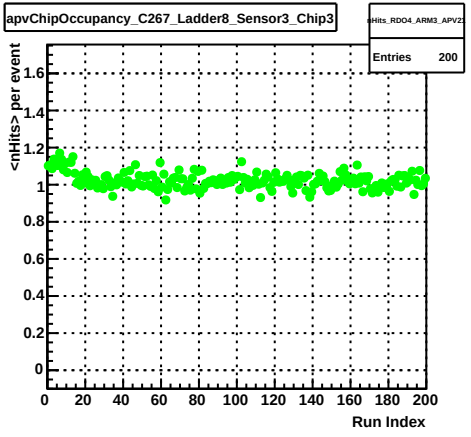

apvChipOccupancy_C262_Ladder8_Sensor2_Chip4
nHits_RDO4_ARM4_APV2
Entries 200
<nHits> per event
Run Index

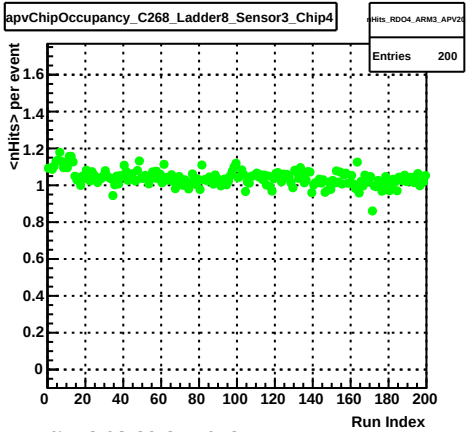

apvChipOccupancy_C263_Ladder8_Sensor2_Chip5
nHits_RDO4_ARM4_APV1
Entries 200
<nHits> per event
Run Index

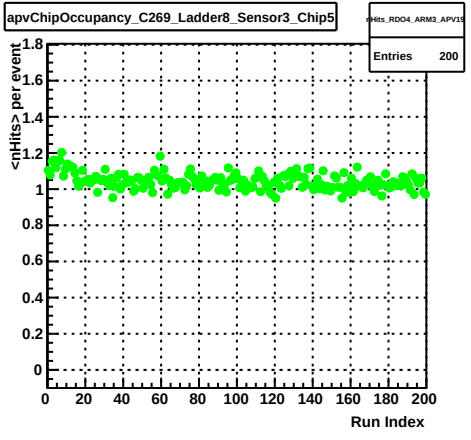

apvChipOccupancy_C264_Ladder8_Sensor2_Chip6
nHits_RDO4_ARM4_APV0
Entries 200
<nHits> per event
Run Index

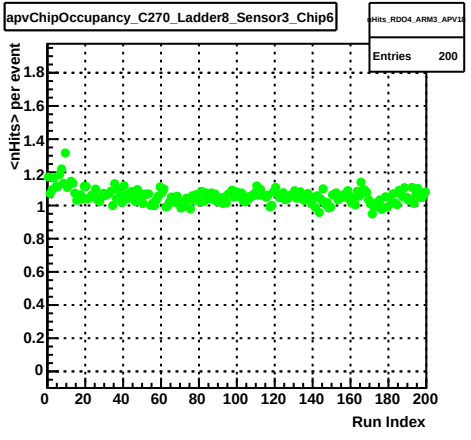

apvChipOccupancy_C265_Ladder8_Sensor3_Chip1
Entries 200
<nHits> per event
Run Index

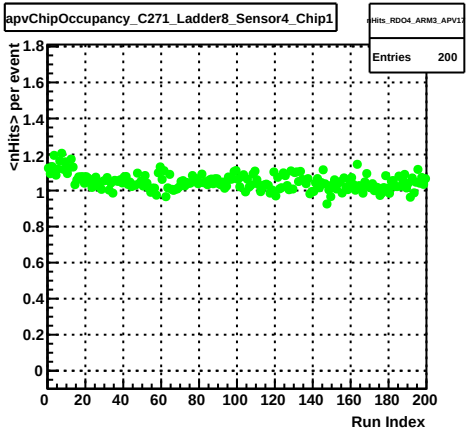

apvChipOccupancy_C266_Ladder8_Sensor3_Chip2
Entries 200
<nHits> per event
Run Index

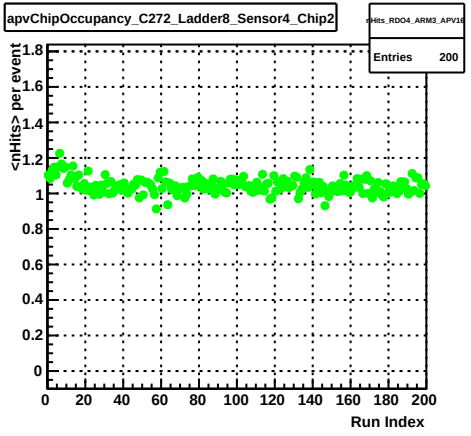

apvChipOccupancy_C267_Ladder8_Sensor3_Chip3
Entries 200
<nHits> per event
Run Index

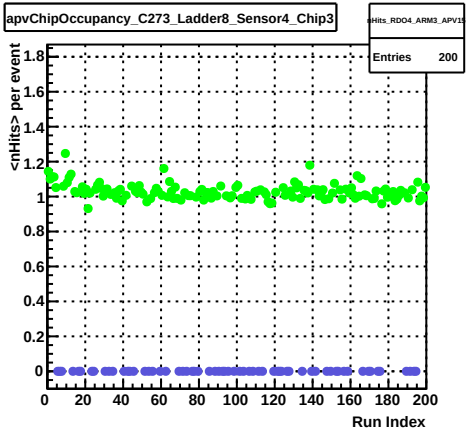

apvChipOccupancy_C268_Ladder8_Sensor3_Chip4
Entries 200
<nHits> per event
Run Index

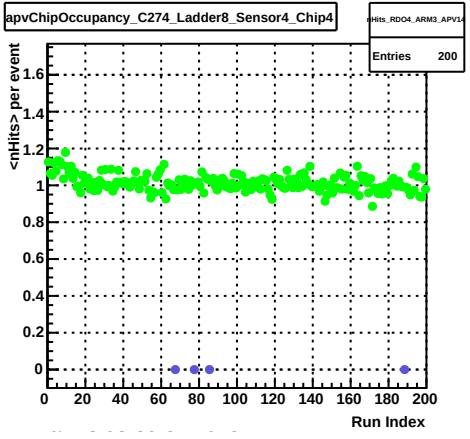

apvChipOccupancy_C269_Ladder8_Sensor3_Chip5
Entries 200
<nHits> per event
Run Index

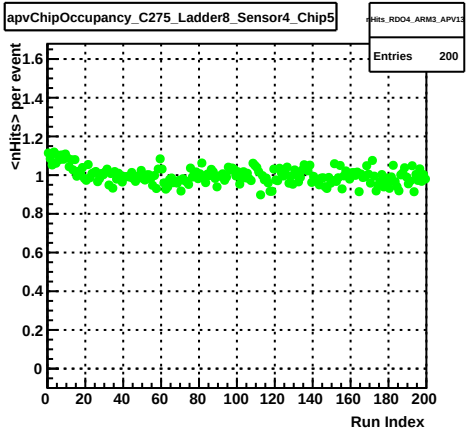

apvChipOccupancy_C270_Ladder8_Sensor3_Chip6
Entries 200
<nHits> per event
Run Index

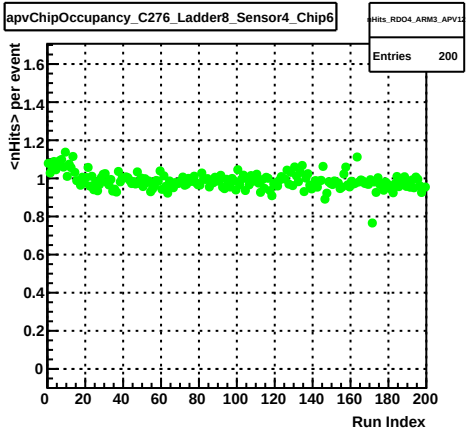

apvChipOccupancy_C271_Ladder8_Sensor4_Chip1
nHits_RDO4_ARM3_APV13
Entries 200
<nHits> per event
Run Index

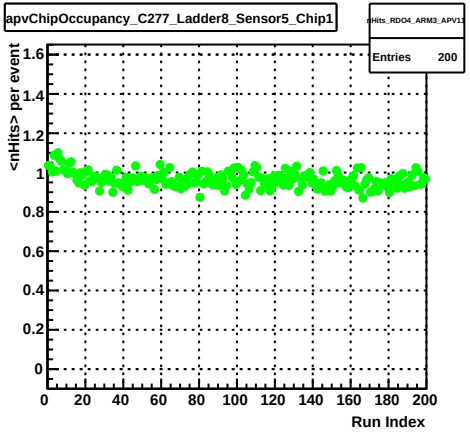

apvChipOccupancy_C272_Ladder8_Sensor4_Chip2
nHits_RDO4_ARM3_APV14
Entries 200
<nHits> per event
Run Index

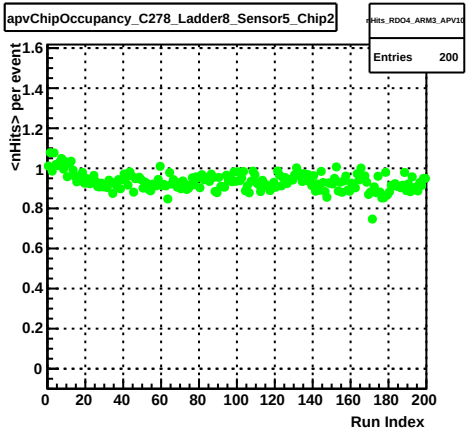

apvChipOccupancy_C273_Ladder8_Sensor4_Chip3
nHits_RDO4_ARM3_APV15
Entries 200
<nHits> per event
Run Index

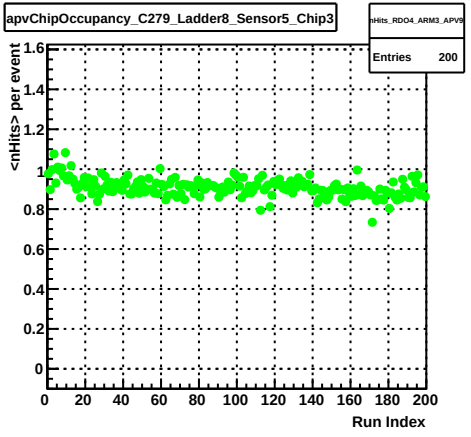

apvChipOccupancy_C274_Ladder8_Sensor4_Chip4
nHits_RDO4_ARM3_APV14
Entries 200
<nHits> per event
Run Index

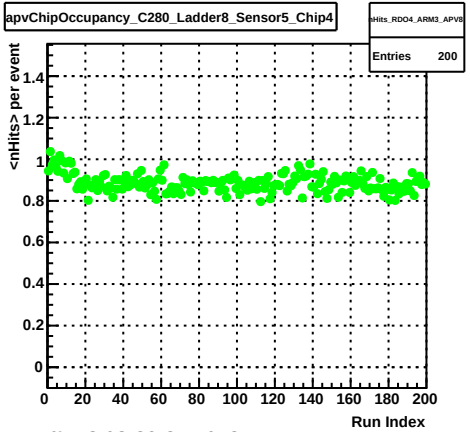

apvChipOccupancy_C275_Ladder8_Sensor4_Chip5
nHits_RDO4_ARM3_APV13
Entries 200
<nHits> per event
Run Index

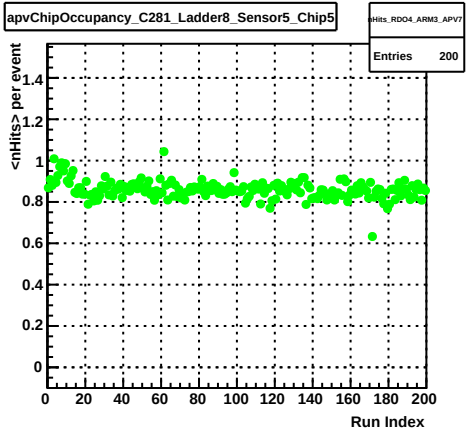

apvChipOccupancy_C276_Ladder8_Sensor4_Chip6
nHits_RDO4_ARM3_APV12
Entries 200
<nHits> per event
Run Index

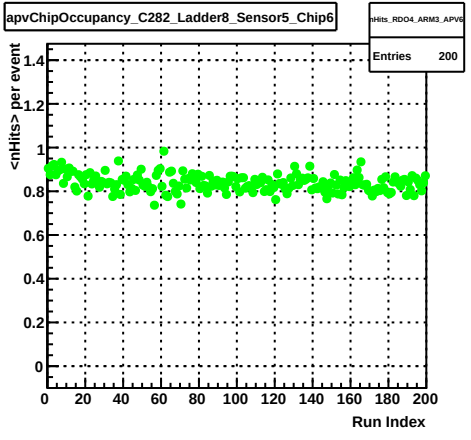

apvChipOccupancy_C277_Ladder8_Sensor5_Chip1
Entries 200
<nHits> per event
Run Index

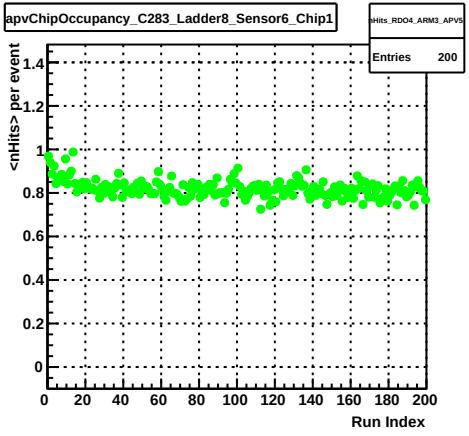

apvChipOccupancy_C278_Ladder8_Sensor5_Chip2
Entries 200
<nHits> per event
Run Index

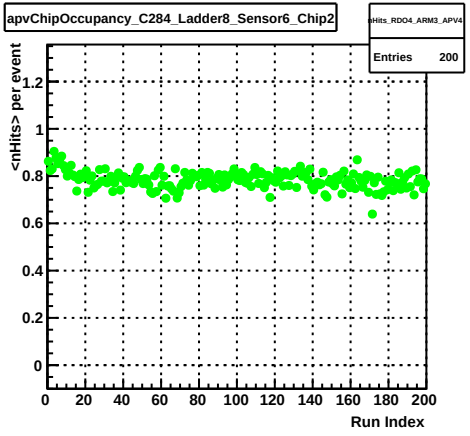

apvChipOccupancy_C279_Ladder8_Sensor5_Chip3
Entries 200
<nHits> per event
Run Index

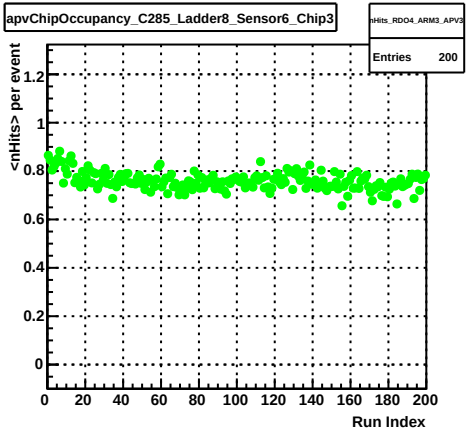

apvChipOccupancy_C280_Ladder8_Sensor5_Chip4
Entries 200
<nHits> per event
Run Index

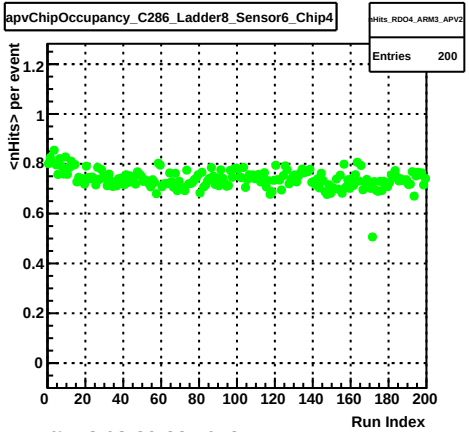

apvChipOccupancy_C281_Ladder8_Sensor5_Chip5
Entries 200
<nHits> per event
Run Index

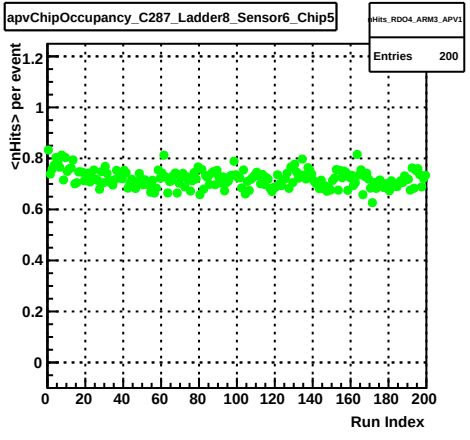

apvChipOccupancy_C282_Ladder8_Sensor5_Chip6
Entries 200
<nHits> per event
Run Index

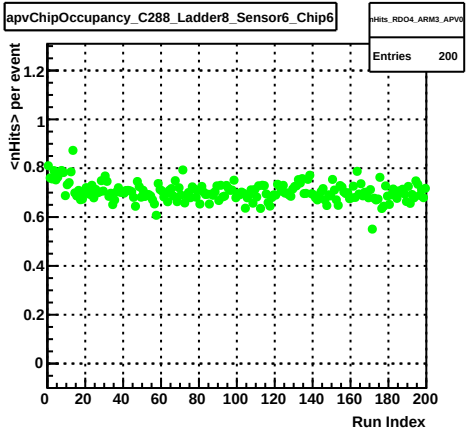

apvChipOccupancy_C283_Ladder8_Sensor6_Chip1
nHits_RDO4_ARM3_APV5
Entries 200
<nHits> per event
Run Index

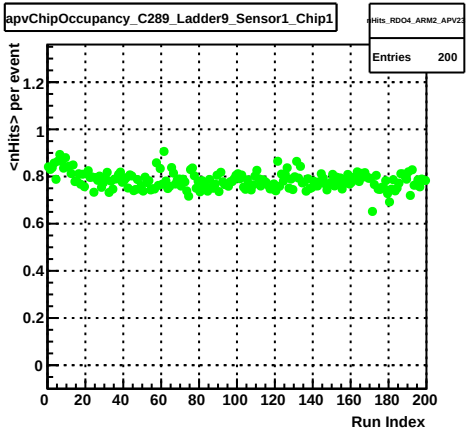

apvChipOccupancy_C284_Ladder8_Sensor6_Chip2
nHits_RDO4_ARM3_APV4
Entries 200
<nHits> per event
Run Index

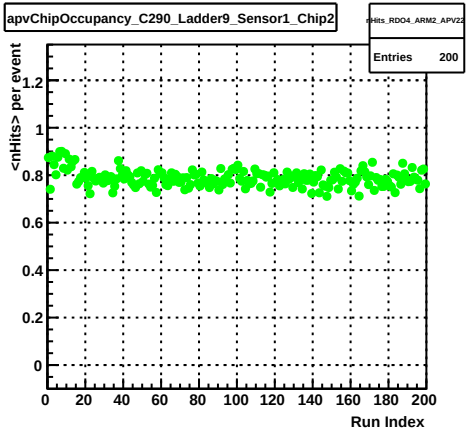

apvChipOccupancy_C285_Ladder8_Sensor6_Chip3
nHits_RDO4_ARM3_APV3
Entries 200
<nHits> per event
Run Index

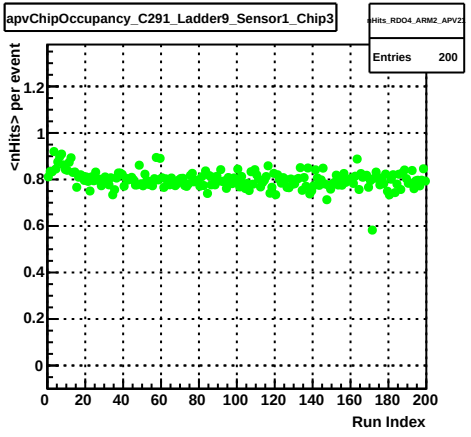

apvChipOccupancy_C286_Ladder8_Sensor6_Chip4
nHits_RDO4_ARM3_APV2
Entries 200
<nHits> per event
Run Index

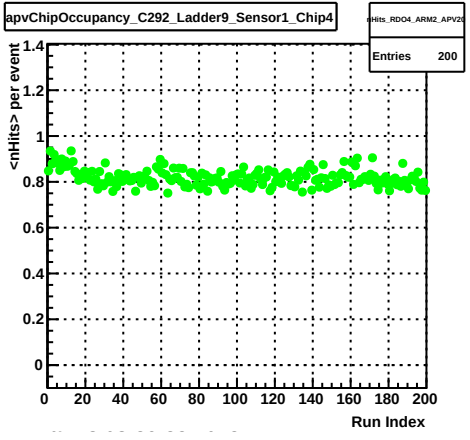

apvChipOccupancy_C287_Ladder8_Sensor6_Chip5
nHits_RDO4_ARM3_APV1
Entries 200
<nHits> per event
Run Index

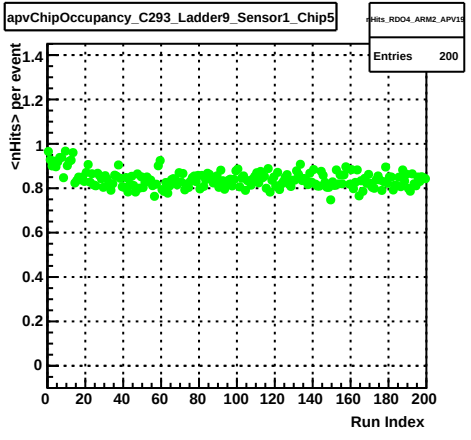

apvChipOccupancy_C288_Ladder8_Sensor6_Chip6
nHits_RDO4_ARM3_APV0
Entries 200
<nHits> per event
Run Index

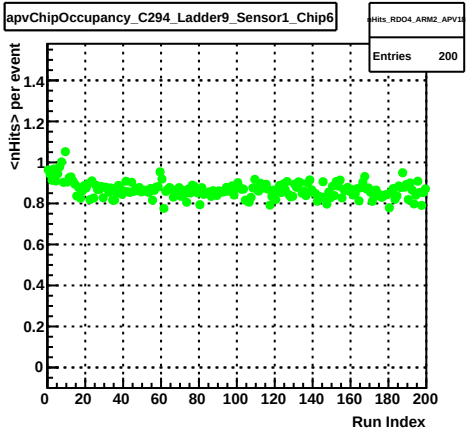

apvChipOccupancy_C289_Ladder9_Sensor1_Chip1
Entries 200
<nHits> per event
Run Index

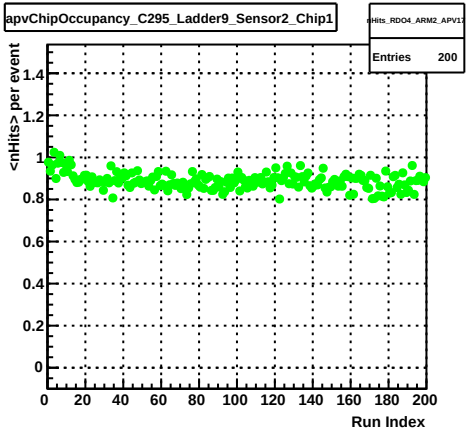

apvChipOccupancy_C290_Ladder9_Sensor1_Chip2
Entries 200
<nHits> per event
Run Index

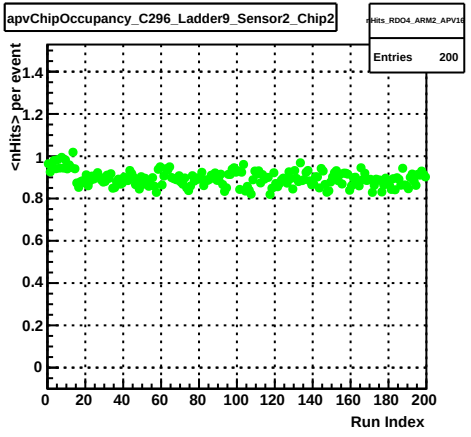

apvChipOccupancy_C291_Ladder9_Sensor1_Chip3
Entries 200
<nHits> per event
Run Index

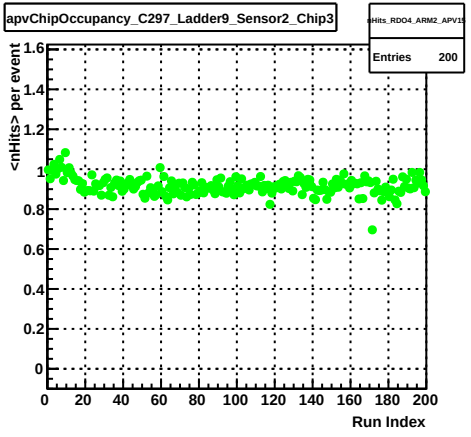

apvChipOccupancy_C292_Ladder9_Sensor1_Chip4
Entries 200
<nHits> per event
Run Index

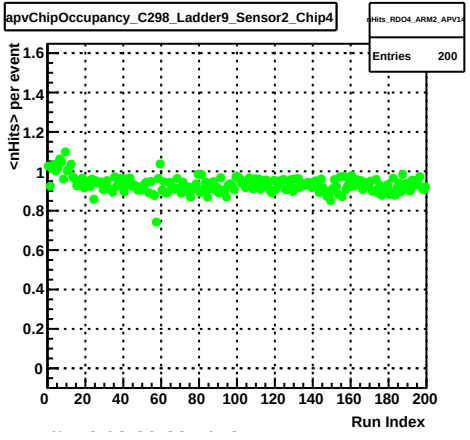

apvChipOccupancy_C293_Ladder9_Sensor1_Chip5
Entries 200
<nHits> per event
Run Index

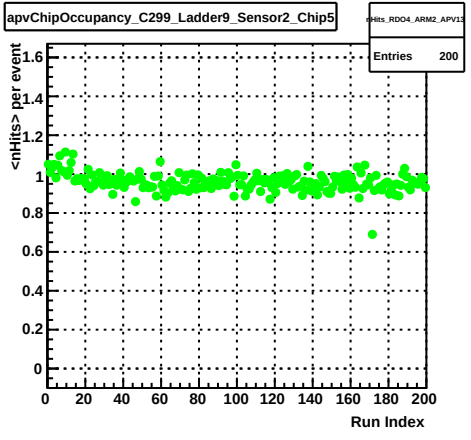

apvChipOccupancy_C294_Ladder9_Sensor1_Chip6
Entries 200
<nHits> per event
Run Index

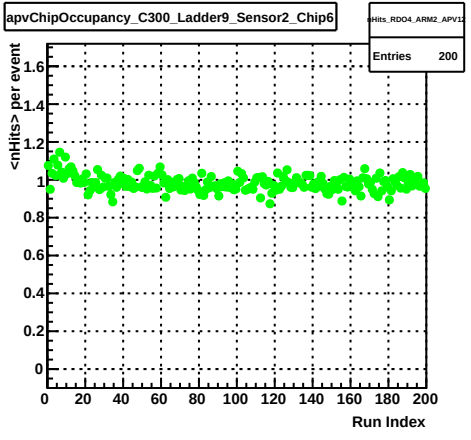

apvChipOccupancy_C295_Ladder9_Sensor2_Chip1
Entries 200
<nHits> per event
Run Index

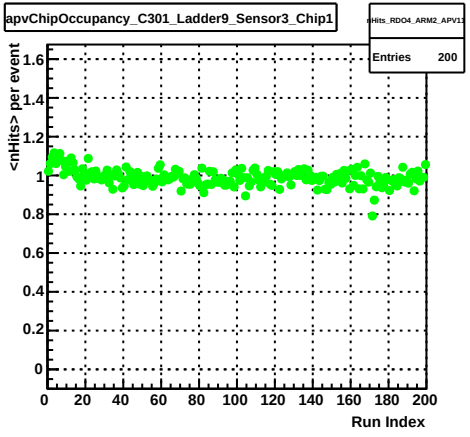

apvChipOccupancy_C296_Ladder9_Sensor2_Chip2
Entries 200
<nHits> per event
Run Index

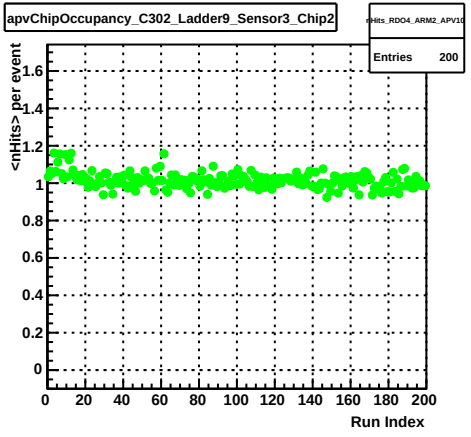

apvChipOccupancy_C297_Ladder9_Sensor2_Chip3
Entries 200
<nHits> per event
Run Index

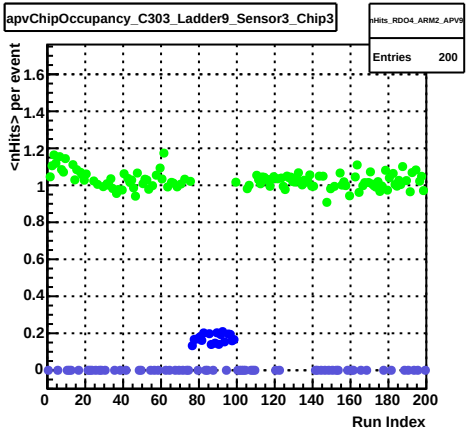

apvChipOccupancy_C298_Ladder9_Sensor2_Chip4
Entries 200
<nHits> per event
Run Index

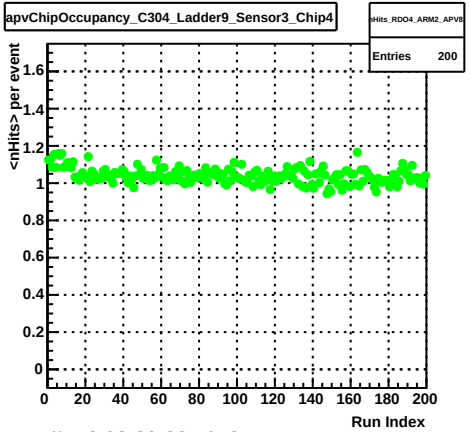

apvChipOccupancy_C299_Ladder9_Sensor2_Chip5
Entries 200
<nHits> per event
Run Index

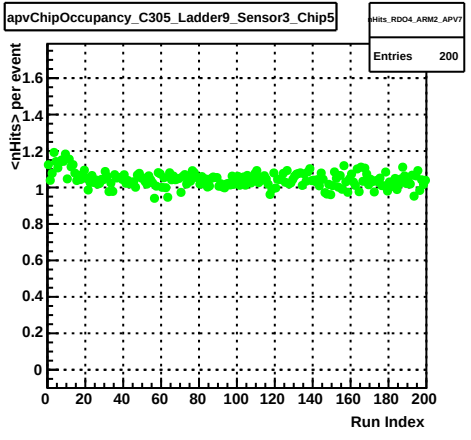

apvChipOccupancy_C300_Ladder9_Sensor2_Chip6
Entries 200
<nHits> per event
Run Index

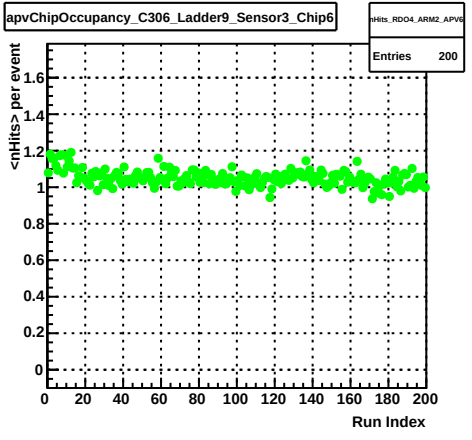

apvChipOccupancy_C301_Ladder9_Sensor3_Chip1
Entries 200
<nHits> per event
Run Index

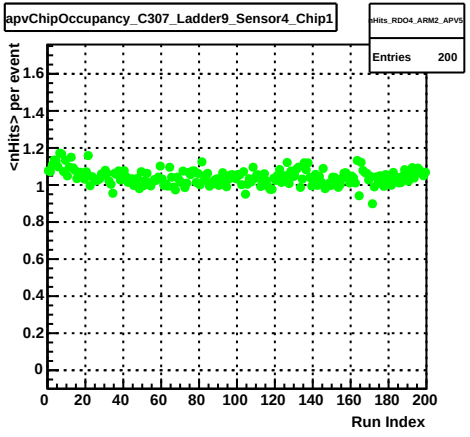

apvChipOccupancy_C302_Ladder9_Sensor3_Chip2
Entries 200
<nHits> per event
Run Index

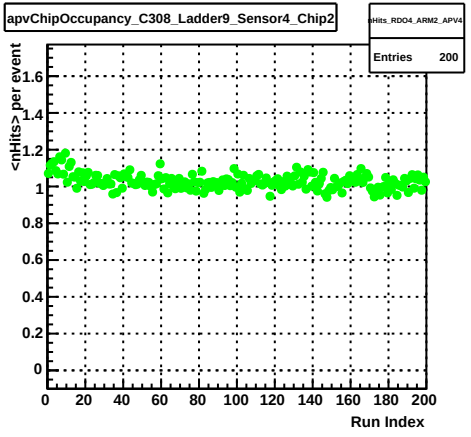

apvChipOccupancy_C303_Ladder9_Sensor3_Chip3
Entries 200
<nHits> per event
Run Index

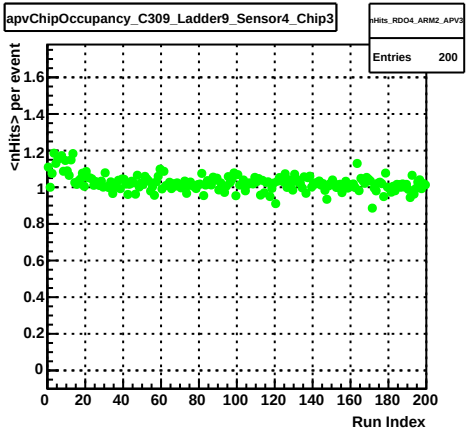

apvChipOccupancy_C304_Ladder9_Sensor3_Chip4
Entries 200
<nHits> per event
Run Index

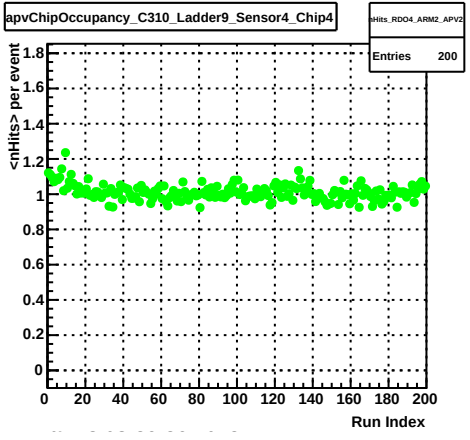

apvChipOccupancy_C305_Ladder9_Sensor3_Chip5
Entries 200
<nHits> per event
Run Index

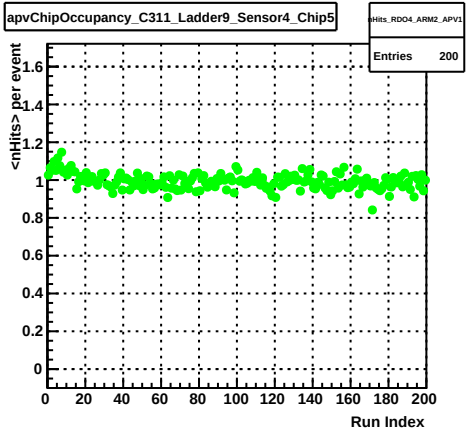

apvChipOccupancy_C306_Ladder9_Sensor3_Chip6
Entries 200
<nHits> per event
Run Index

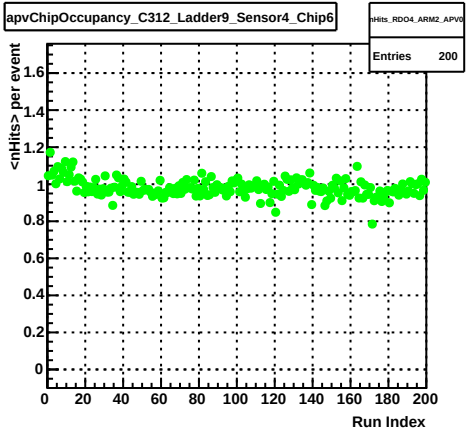

apvChipOccupancy_C307_Ladder9_Sensor4_Chip1
nHits_RD04_ARM2_APV5
Entries 200
<nHits> per event
Run Index

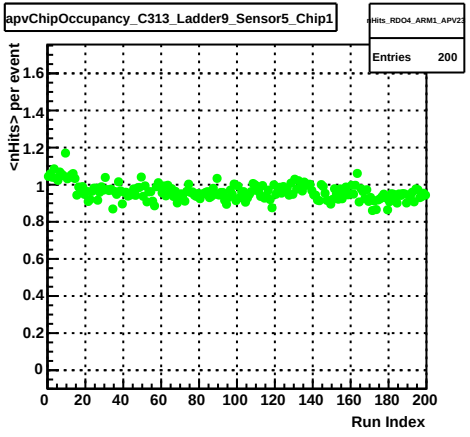

apvChipOccupancy_C308_Ladder9_Sensor4_Chip2
nHits_RD04_ARM2_APV4
Entries 200
<nHits> per event
Run Index

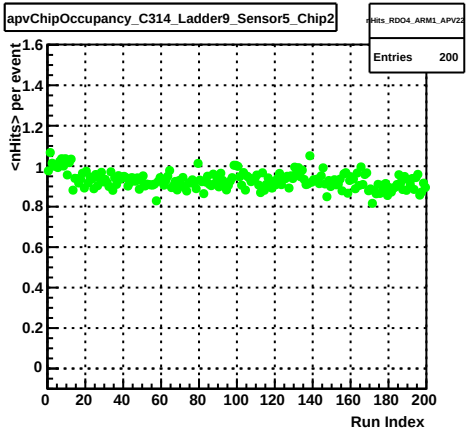

apvChipOccupancy_C309_Ladder9_Sensor4_Chip3
nHits_RD04_ARM2_APV3
Entries 200
<nHits> per event
Run Index

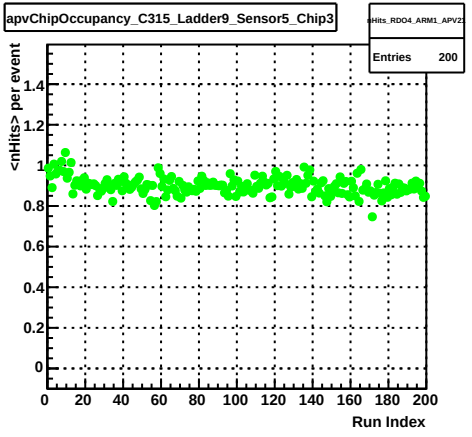

apvChipOccupancy_C310_Ladder9_Sensor4_Chip4
nHits_RD04_ARM2_APV2
Entries 200
<nHits> per event
Run Index

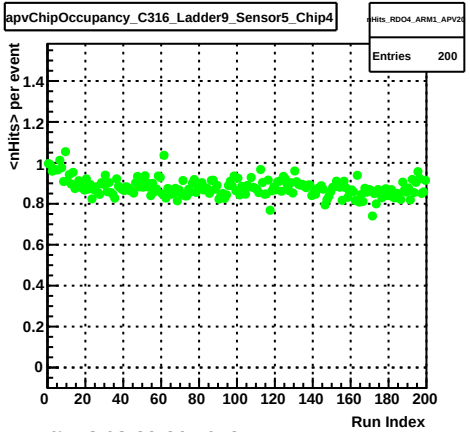

apvChipOccupancy_C311_Ladder9_Sensor4_Chip5
nHits_RD04_ARM2_APV1
Entries 200
<nHits> per event
Run Index

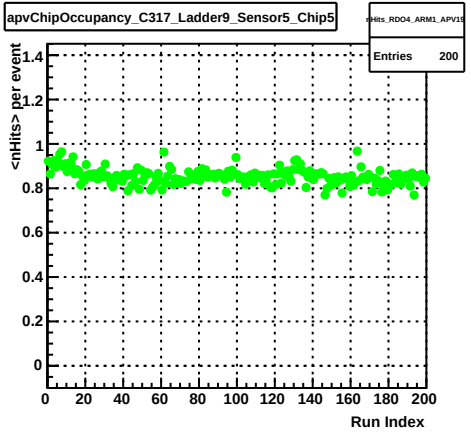

apvChipOccupancy_C312_Ladder9_Sensor4_Chip6
nHits_RD04_ARM2_APV0
Entries 200
<nHits> per event
Run Index

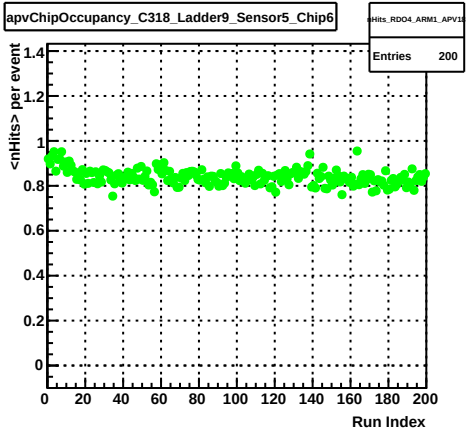

apvChipOccupancy_C313_Ladder9_Sensor5_Chip1
nHits_RD04_ARM1_APV23
Entries 200
<nHits> per event
Run Index

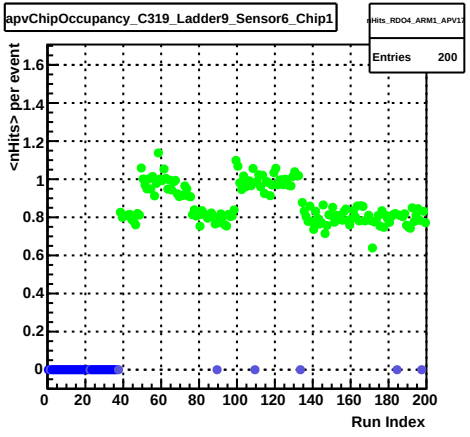

apvChipOccupancy_C314_Ladder9_Sensor5_Chip2
nHits_RD04_ARM1_APV22
Entries 200
<nHits> per event
Run Index

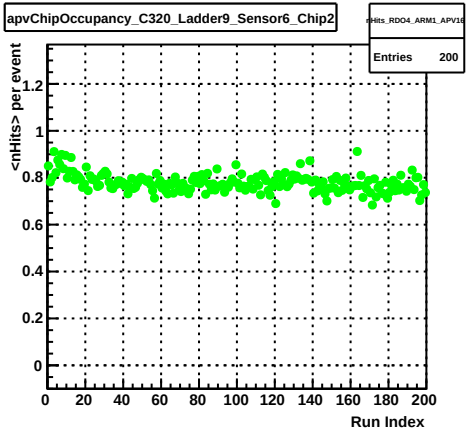

apvChipOccupancy_C315_Ladder9_Sensor5_Chip3
nHits_RD04_ARM1_APV21
Entries 200
<nHits> per event
Run Index

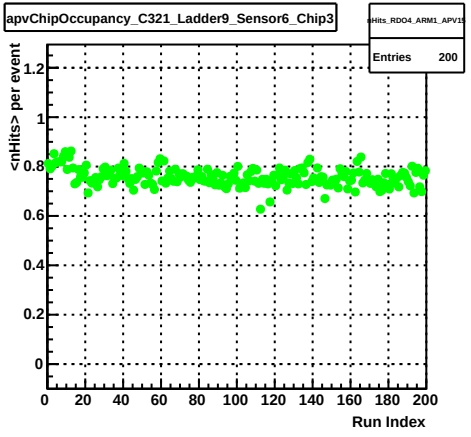

apvChipOccupancy_C316_Ladder9_Sensor5_Chip4
nHits_RD04_ARM1_APV20
Entries 200
<nHits> per event
Run Index

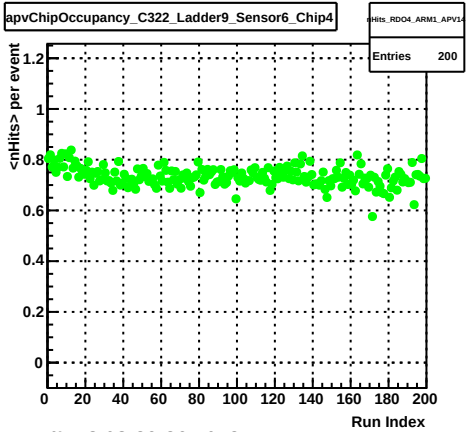

apvChipOccupancy_C317_Ladder9_Sensor5_Chip5
nHits_RD04_ARM1_APV19
Entries 200
<nHits> per event
Run Index

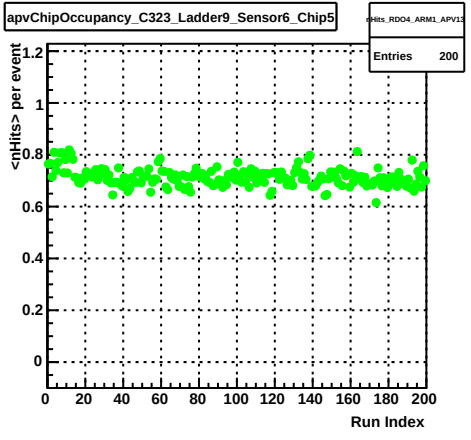

apvChipOccupancy_C318_Ladder9_Sensor5_Chip6
nHits_RD04_ARM1_APV18
Entries 200
<nHits> per event
Run Index

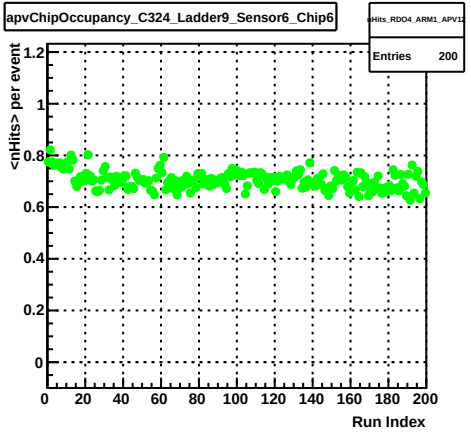

apvChipOccupancy_C319_Ladder9_Sensor6_Chip1
Entries 200
<nHits> per event
Run Index

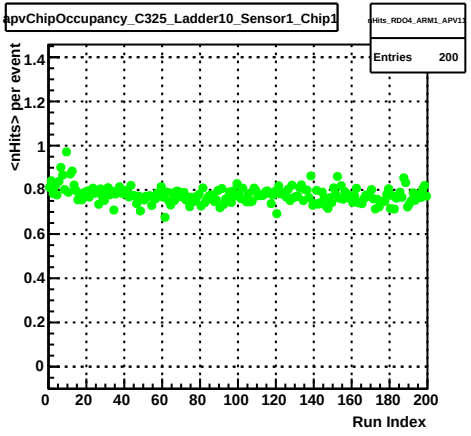

apvChipOccupancy_C320_Ladder9_Sensor6_Chip2
Entries 200
<nHits> per event
Run Index

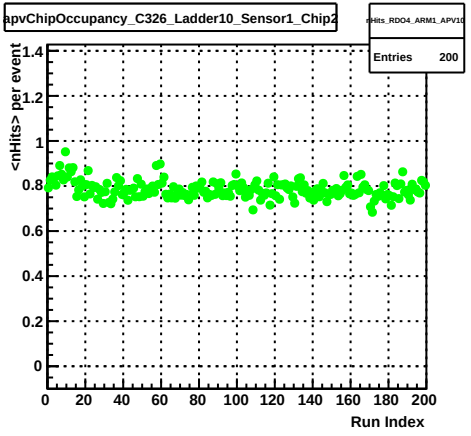

apvChipOccupancy_C321_Ladder9_Sensor6_Chip3
Entries 200
<nHits> per event
Run Index

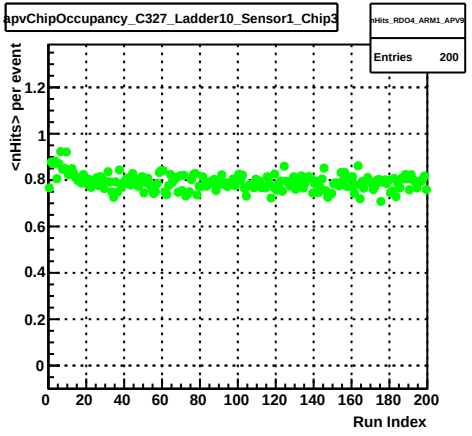

apvChipOccupancy_C322_Ladder9_Sensor6_Chip4
Entries 200
<nHits> per event
Run Index

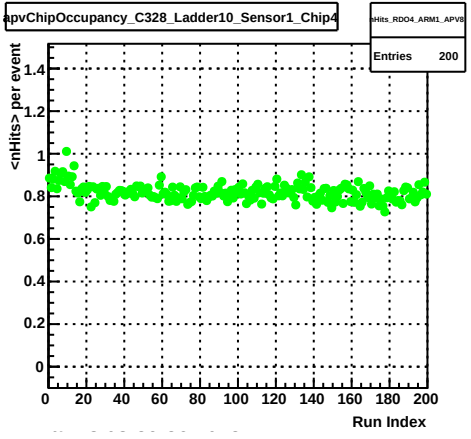

apvChipOccupancy_C323_Ladder9_Sensor6_Chip5
Entries 200
<nHits> per event
Run Index

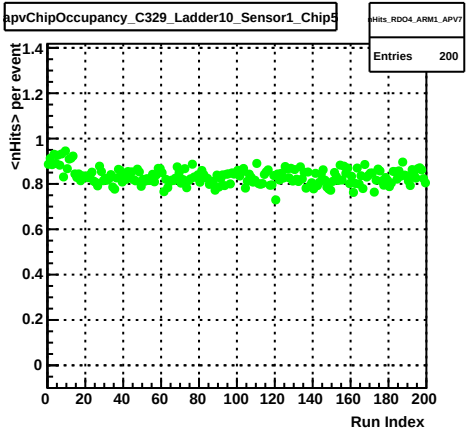

apvChipOccupancy_C324_Ladder9_Sensor6_Chip6
Entries 200
<nHits> per event
Run Index

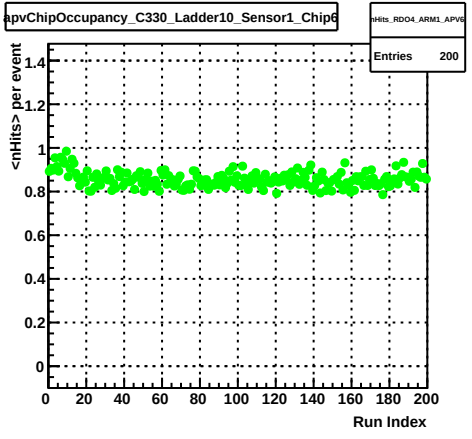

apvChipOccupancy_C325_Ladder10_Sensor1_Chip1
Entries 200
<nHits> per event
Run Index

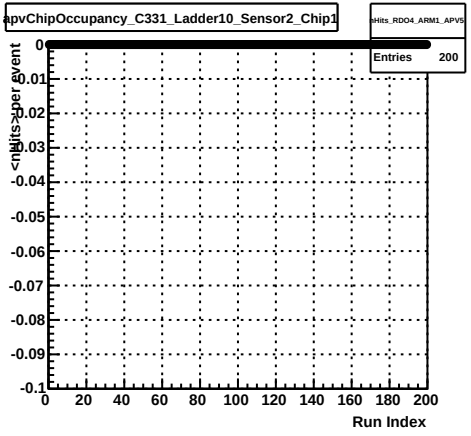

apvChipOccupancy_C326_Ladder10_Sensor1_Chip2
Entries 200
<nHits> per event
Run Index

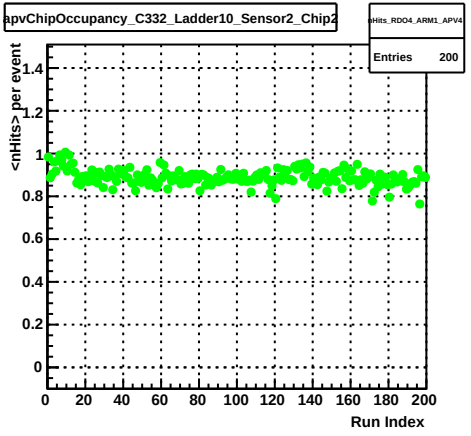

apvChipOccupancy_C327_Ladder10_Sensor1_Chip3
Entries 200
<nHits> per event
Run Index

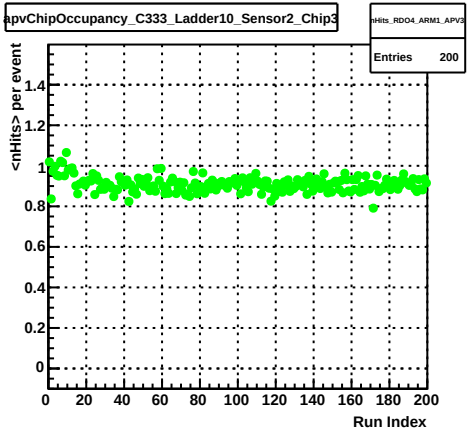

apvChipOccupancy_C328_Ladder10_Sensor1_Chip4
Entries 200
<nHits> per event
Run Index

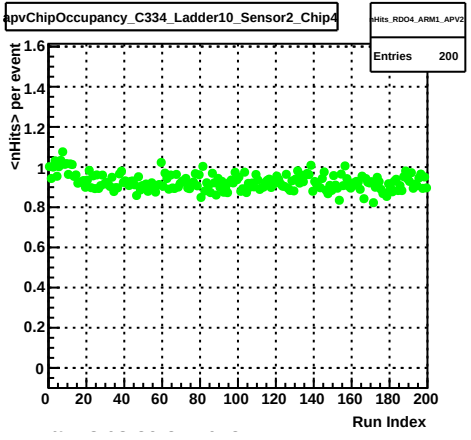

apvChipOccupancy_C329_Ladder10_Sensor1_Chip5
Entries 200
<nHits> per event
Run Index

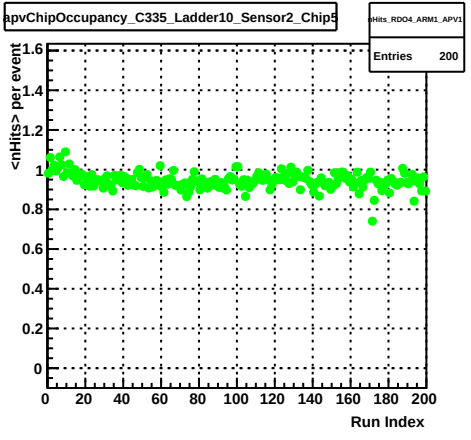

apvChipOccupancy_C330_Ladder10_Sensor1_Chip6
Entries 200
<nHits> per event
Run Index

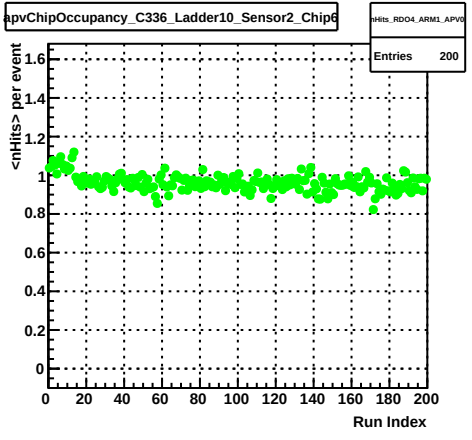

apvChipOccupancy_C331_Ladder10_Sensor2_Chip1
nHits_RD04_ARM1_APV5
Entries 200
<nHits> per event
Run Index

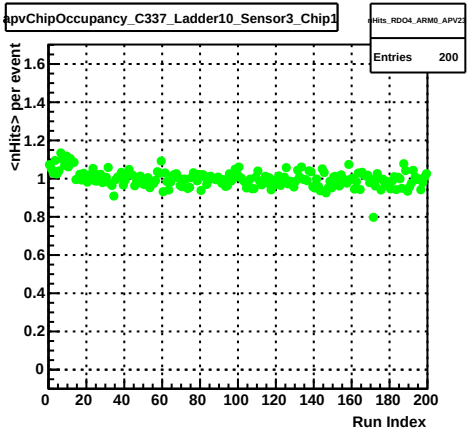

apvChipOccupancy_C332_Ladder10_Sensor2_Chip2
nHits_RD04_ARM1_APV4
Entries 200
<nHits> per event
Run Index

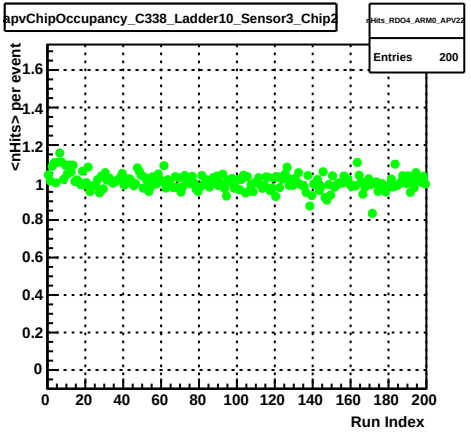

apvChipOccupancy_C333_Ladder10_Sensor2_Chip3
nHits_RD04_ARM1_APV3
Entries 200
<nHits> per event
Run Index

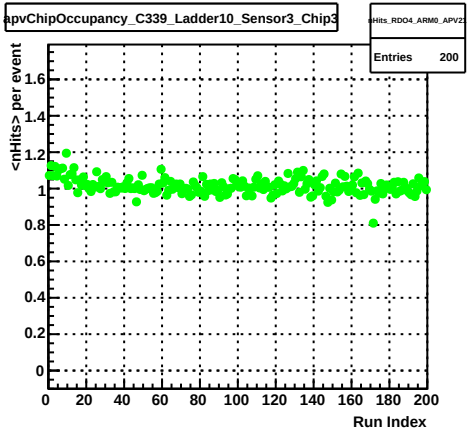

apvChipOccupancy_C334_Ladder10_Sensor2_Chip4
nHits_RD04_ARM1_APV2
Entries 200
<nHits> per event
Run Index

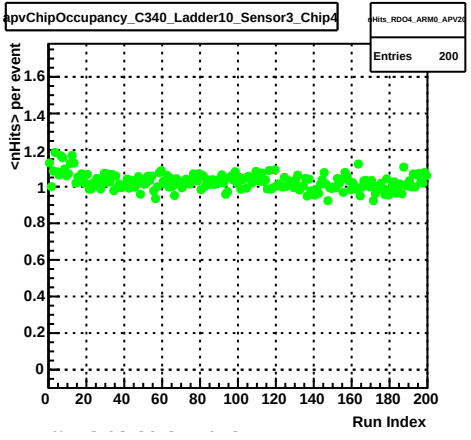

apvChipOccupancy_C335_Ladder10_Sensor2_Chip5
nHits_RD04_ARM1_APV1
Entries 200
<nHits> per event
Run Index

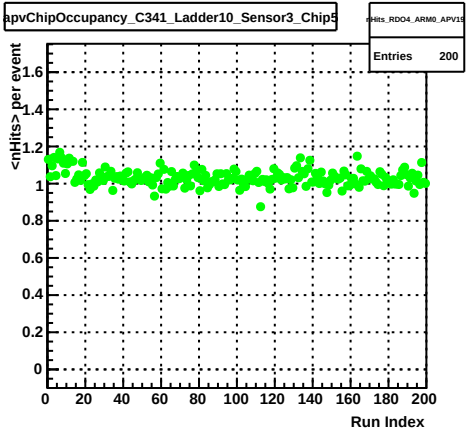

apvChipOccupancy_C336_Ladder10_Sensor2_Chip6
nHits_RD04_ARM1_APV0
Entries 200
<nHits> per event
Run Index

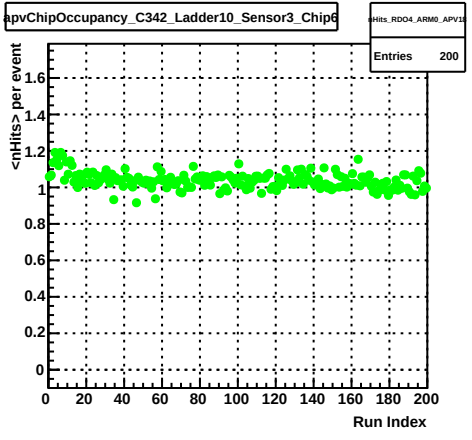

apvChipOccupancy_C337_Ladder10_Sensor3_Chip1
nHits_RD04_ARM0_APV25
Entries 200
<nHits> per event
Run Index

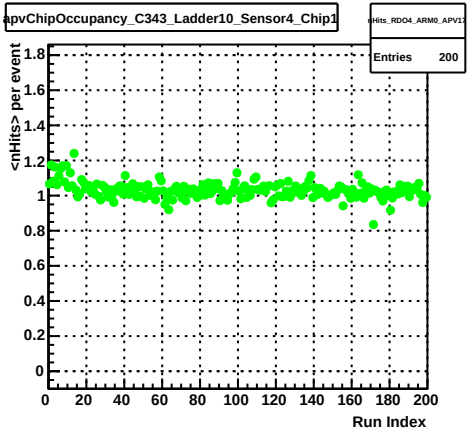

apvChipOccupancy_C338_Ladder10_Sensor3_Chip2
nHits_RD04_ARM0_APV25
Entries 200
<nHits> per event
Run Index

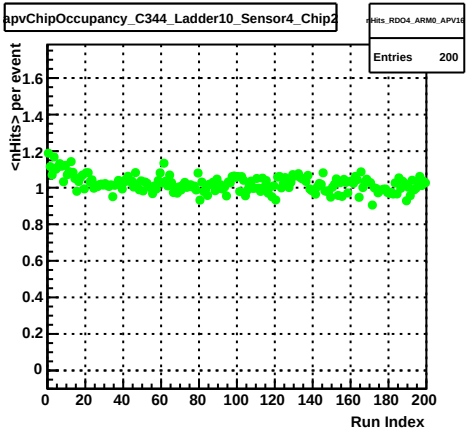

apvChipOccupancy_C339_Ladder10_Sensor3_Chip3
nHits_RD04_ARM0_APV25
Entries 200
<nHits> per event
Run Index

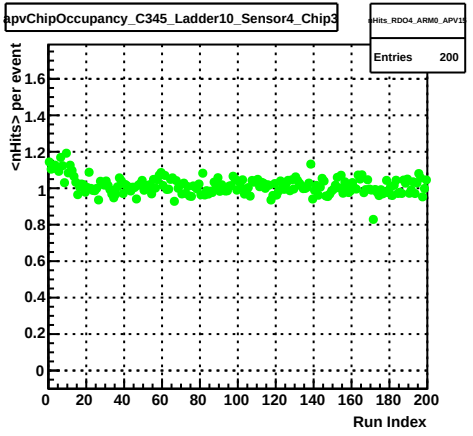

apvChipOccupancy_C340_Ladder10_Sensor3_Chip4
nHits_RD04_ARM0_APV25
Entries 200
<nHits> per event
Run Index

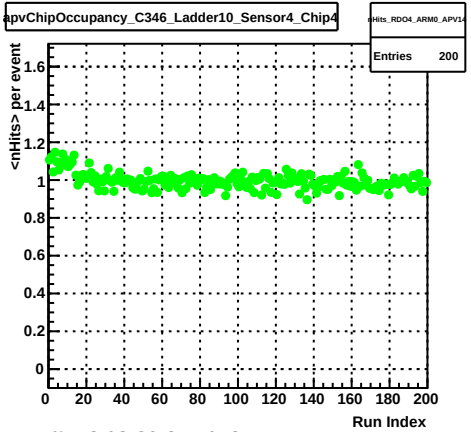

apvChipOccupancy_C341_Ladder10_Sensor3_Chip5
nHits_RD04_ARM0_APV18
Entries 200
<nHits> per event
Run Index

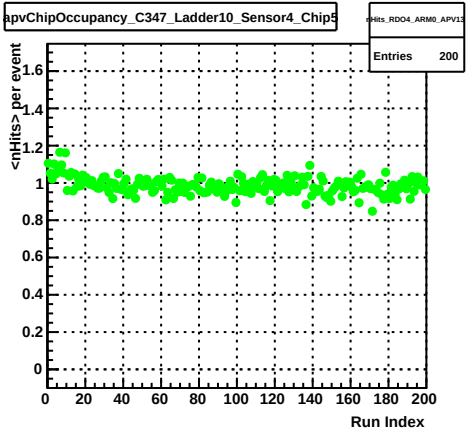

apvChipOccupancy_C342_Ladder10_Sensor3_Chip6
nHits_RD04_ARM0_APV18
Entries 200
<nHits> per event
Run Index

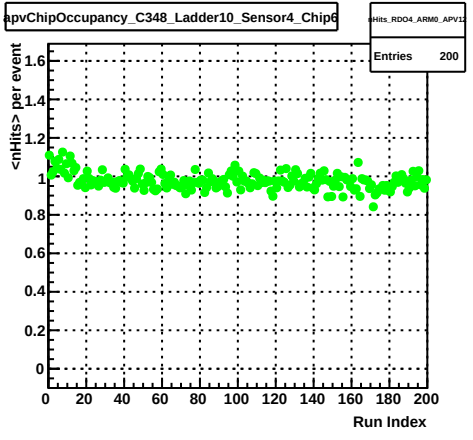

apvChipOccupancy_C343_Ladder10_Sensor4_Chip1
nHits_RDO4_ARM0_APV13
Entries 200
<nHits> per event
Run Index

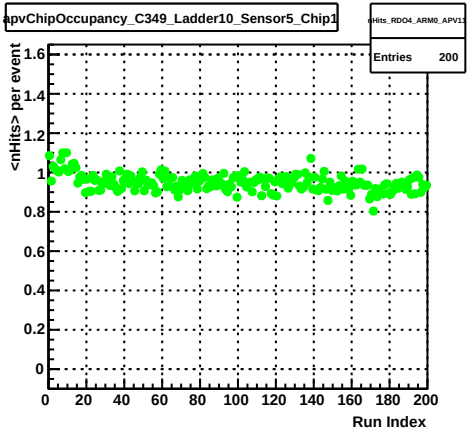

apvChipOccupancy_C344_Ladder10_Sensor4_Chip2
nHits_RDO4_ARM0_APV14
Entries 200
<nHits> per event
Run Index

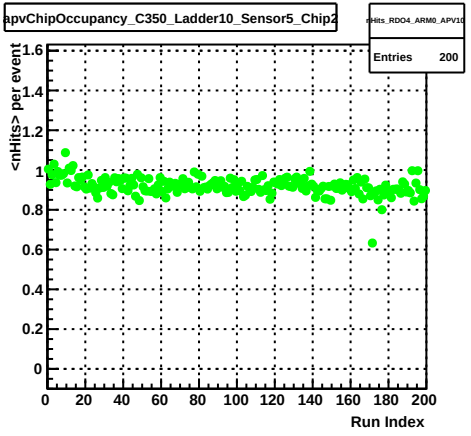

apvChipOccupancy_C345_Ladder10_Sensor4_Chip3
nHits_RDO4_ARM0_APV15
Entries 200
<nHits> per event
Run Index

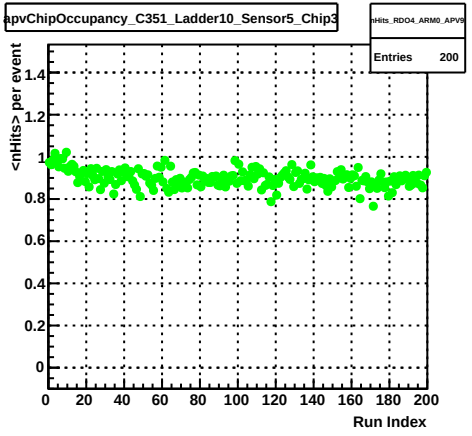

apvChipOccupancy_C346_Ladder10_Sensor4_Chip4
nHits_RDO4_ARM0_APV16
Entries 200
<nHits> per event
Run Index

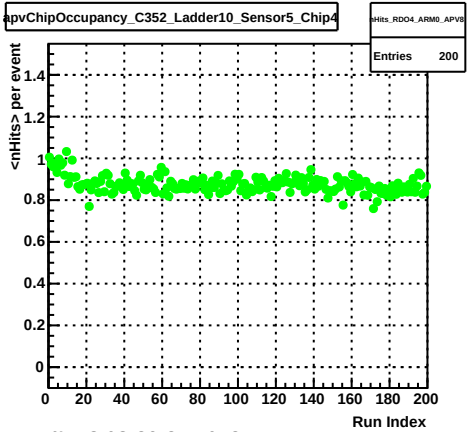

apvChipOccupancy_C347_Ladder10_Sensor4_Chip5
nHits_RDO4_ARM0_APV17
Entries 200
<nHits> per event
Run Index

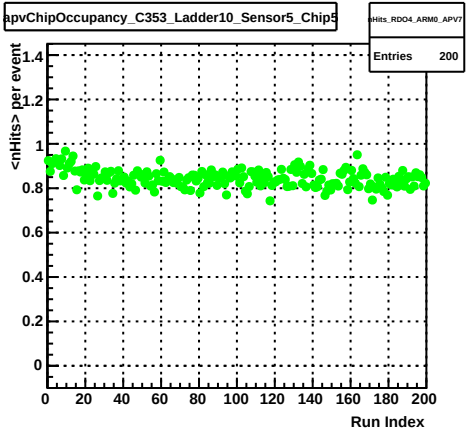

apvChipOccupancy_C348_Ladder10_Sensor4_Chip6
nHits_RDO4_ARM0_APV18
Entries 200
<nHits> per event
Run Index

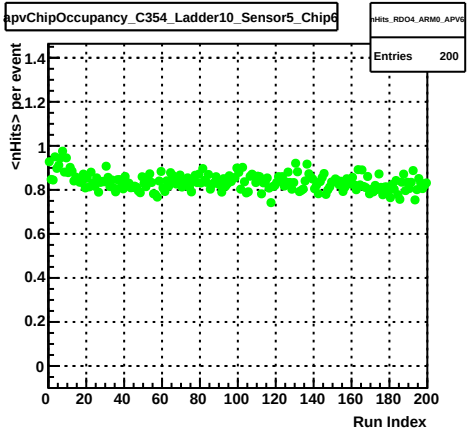

apvChipOccupancy_C349_Ladder10_Sensor5_Chip1
nHits_RDO4_ARM0_APV13
Entries 200
<nHits> per event
Run Index

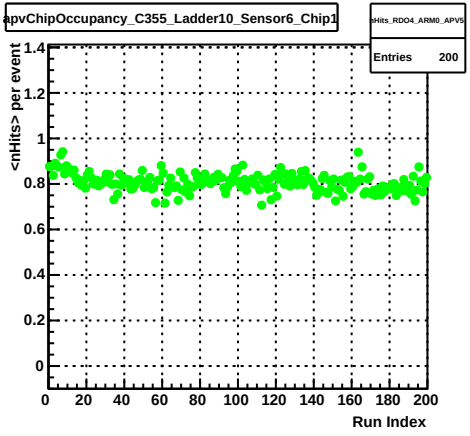

apvChipOccupancy_C350_Ladder10_Sensor5_Chip2
nHits_RDO4_ARM0_APV10
Entries 200
<nHits> per event
Run Index

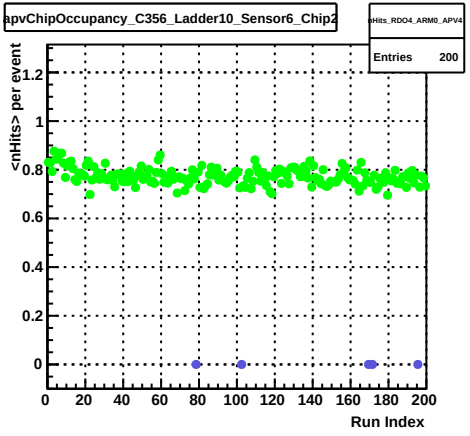

apvChipOccupancy_C351_Ladder10_Sensor5_Chip3
nHits_RDO4_ARM0_APV9
Entries 200
<nHits> per event
Run Index

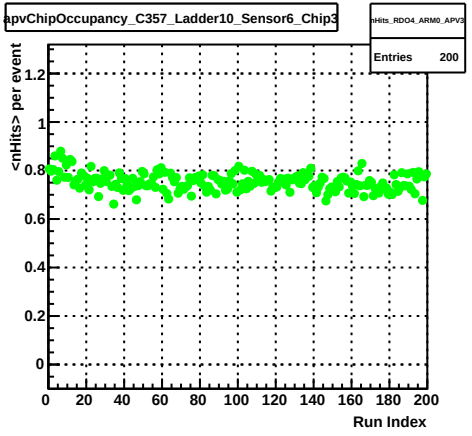

apvChipOccupancy_C352_Ladder10_Sensor5_Chip4
nHits_RDO4_ARM0_APV8
Entries 200
<nHits> per event
Run Index

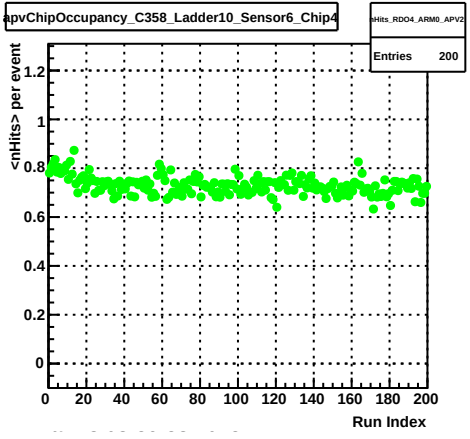

apvChipOccupancy_C353_Ladder10_Sensor5_Chip5
nHits_RDO4_ARM0_APV7
Entries 200
<nHits> per event
Run Index

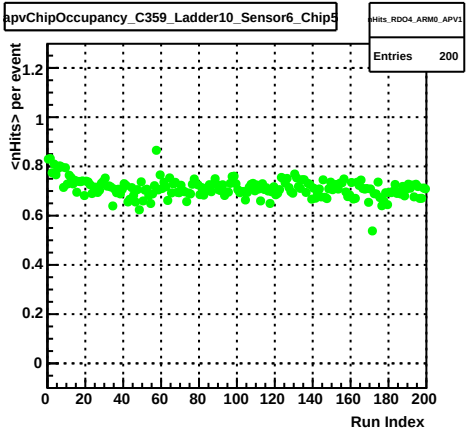

apvChipOccupancy_C354_Ladder10_Sensor5_Chip6
nHits_RDO4_ARM0_APV6
Entries 200
<nHits> per event
Run Index

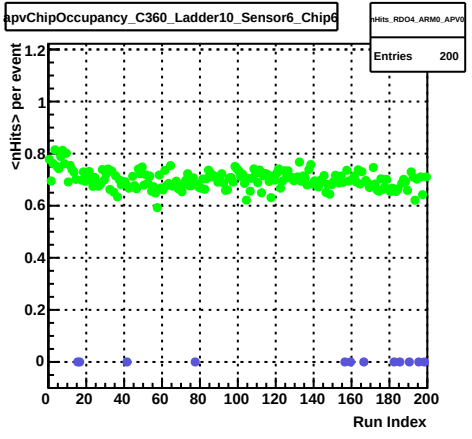

apvChipOccupancy_C355_Ladder10_Sensor6_Chip1
nHits_RDO4_ARM0_APV5
Entries 200
<nHits> per event
Run Index

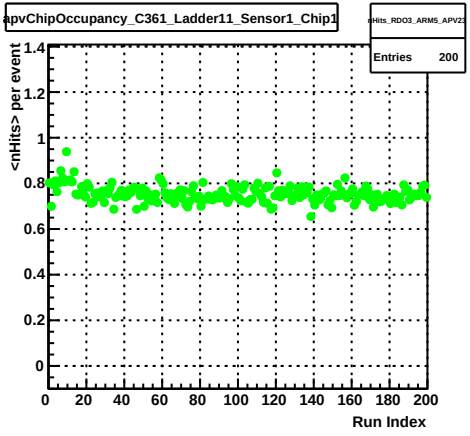

apvChipOccupancy_C356_Ladder10_Sensor6_Chip2
nHits_RDO4_ARM0_APV4
Entries 200
<nHits> per event
Run Index

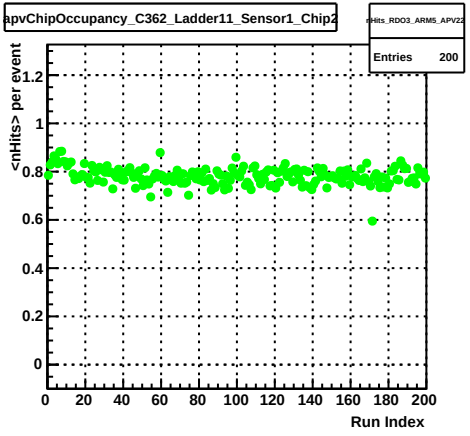

apvChipOccupancy_C357_Ladder10_Sensor6_Chip3
nHits_RDO4_ARM0_APV3
Entries 200
<nHits> per event
Run Index

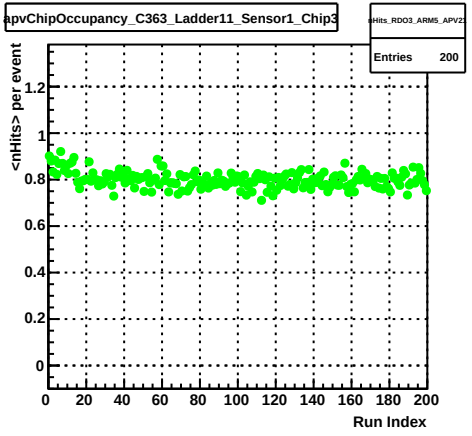

apvChipOccupancy_C358_Ladder10_Sensor6_Chip4
nHits_RDO4_ARM0_APV2
Entries 200
<nHits> per event
Run Index

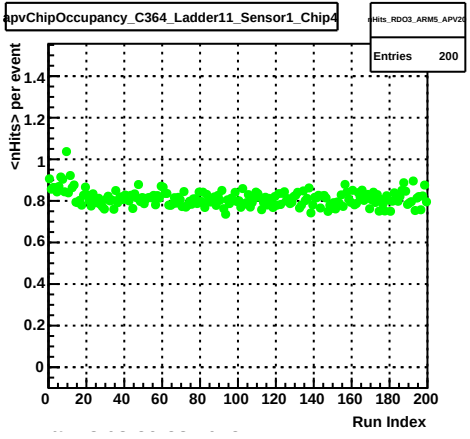

apvChipOccupancy_C359_Ladder10_Sensor6_Chip5
nHits_RDO4_ARM0_APV1
Entries 200
<nHits> per event
Run Index

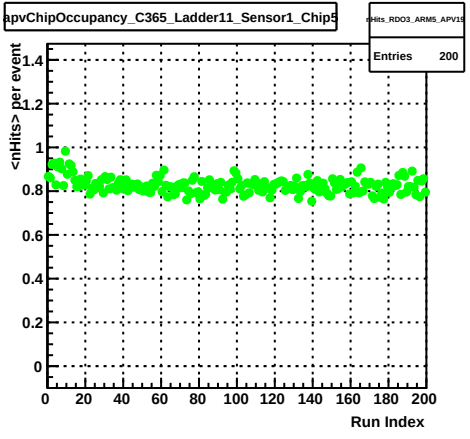

apvChipOccupancy_C360_Ladder10_Sensor6_Chip6
nHits_RDO4_ARM0_APV0
Entries 200
<nHits> per event
Run Index

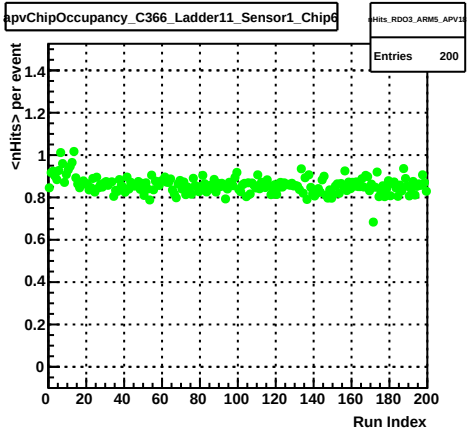

apvChipOccupancy_C361_Ladder11_Sensor1_Chip1
Entries 200
<nHits> per event
Run Index

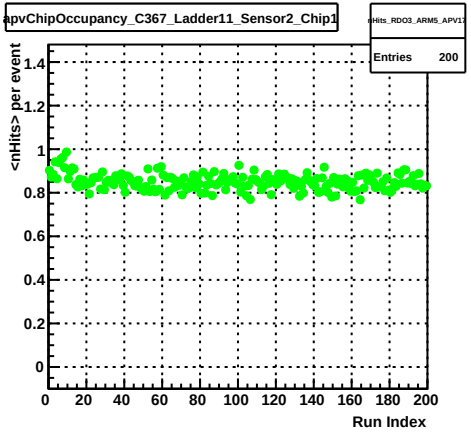

apvChipOccupancy_C362_Ladder11_Sensor1_Chip2
Entries 200
<nHits> per event
Run Index

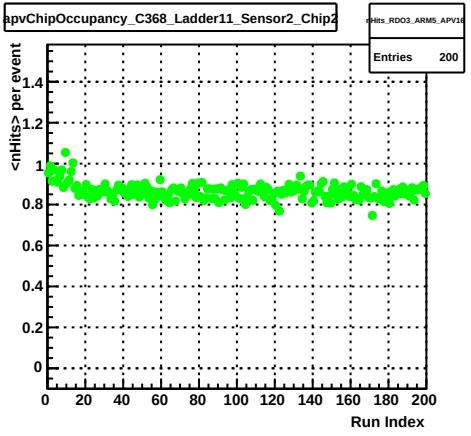

apvChipOccupancy_C363_Ladder11_Sensor1_Chip3
Entries 200
<nHits> per event
Run Index

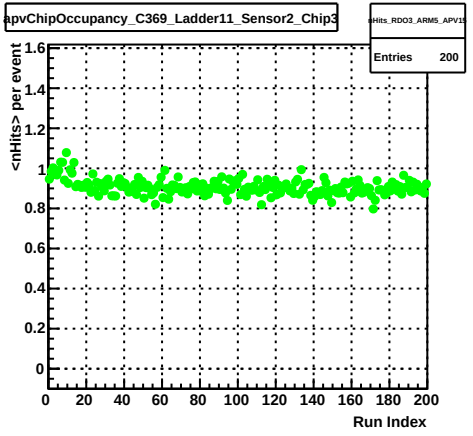

apvChipOccupancy_C364_Ladder11_Sensor1_Chip4
Entries 200
<nHits> per event
Run Index

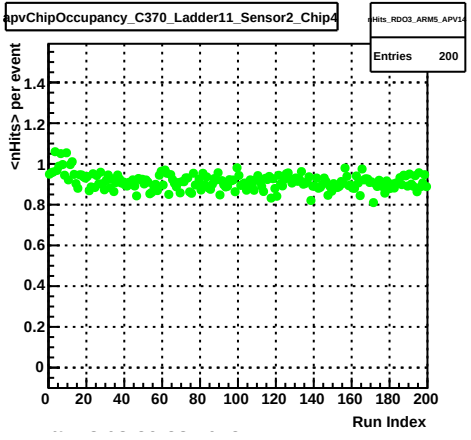

apvChipOccupancy_C365_Ladder11_Sensor1_Chip5
Entries 200
<nHits> per event
Run Index

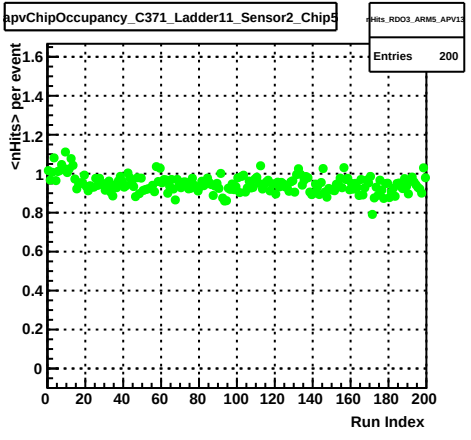

apvChipOccupancy_C366_Ladder11_Sensor1_Chip6
Entries 200
<nHits> per event
Run Index

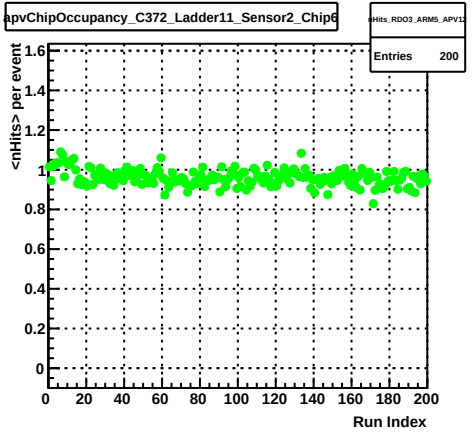

apvChipOccupancy_C367_Ladder11_Sensor2_Chip1
nHits_RDO3_ARM5_APV13
Entries 200
<nHits> per event
Run Index

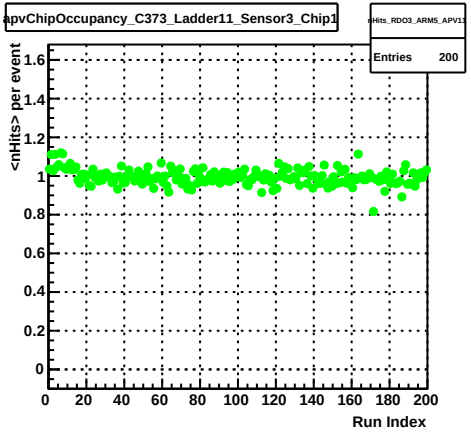

apvChipOccupancy_C368_Ladder11_Sensor2_Chip2
nHits_RDO3_ARM5_APV14
Entries 200
<nHits> per event
Run Index

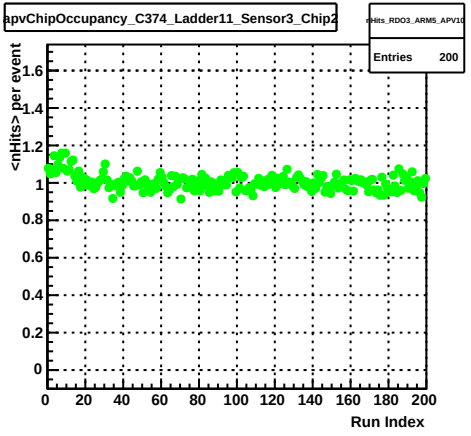

apvChipOccupancy_C369_Ladder11_Sensor2_Chip3
nHits_RDO3_ARM5_APV15
Entries 200
<nHits> per event
Run Index

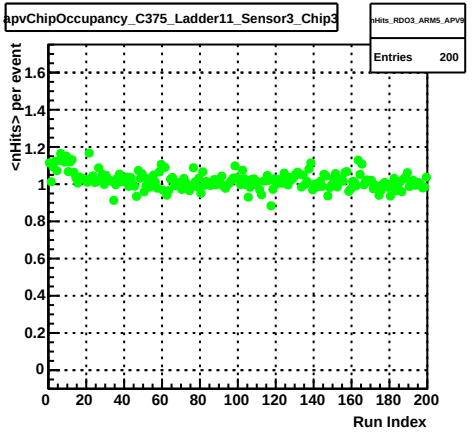

apvChipOccupancy_C370_Ladder11_Sensor2_Chip4
nHits_RDO3_ARM5_APV16
Entries 200
<nHits> per event
Run Index

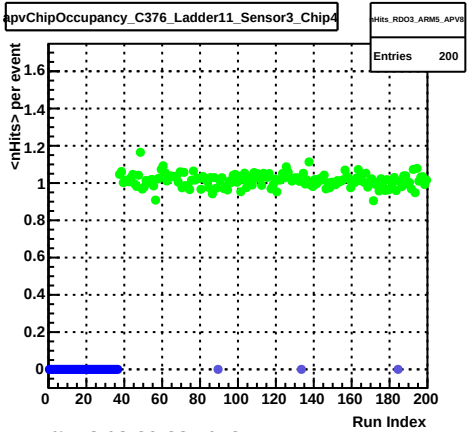

apvChipOccupancy_C371_Ladder11_Sensor2_Chip5
nHits_RDO3_ARM5_APV17
Entries 200
<nHits> per event
Run Index

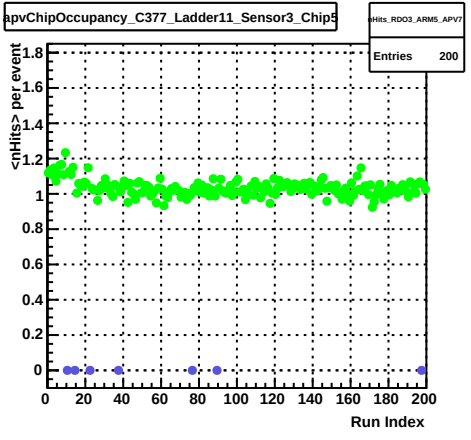

apvChipOccupancy_C372_Ladder11_Sensor2_Chip6
nHits_RDO3_ARM5_APV18
Entries 200
<nHits> per event
Run Index

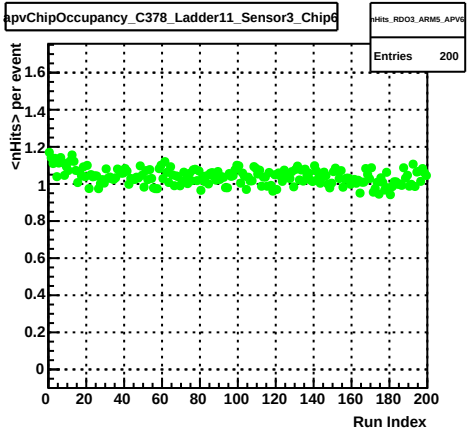

apvChipOccupancy_C373_Ladder11_Sensor3_Chip1
Entries 200
<nHits> per event
Run Index

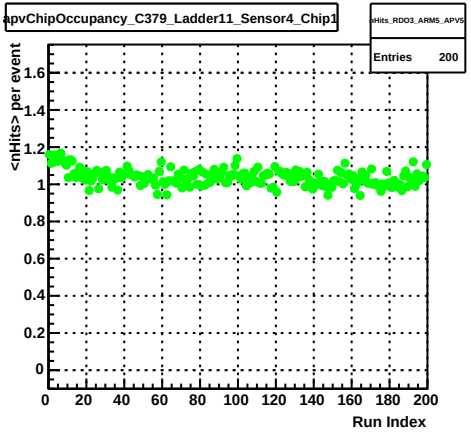

apvChipOccupancy_C374_Ladder11_Sensor3_Chip2
Entries 200
<nHits> per event
Run Index

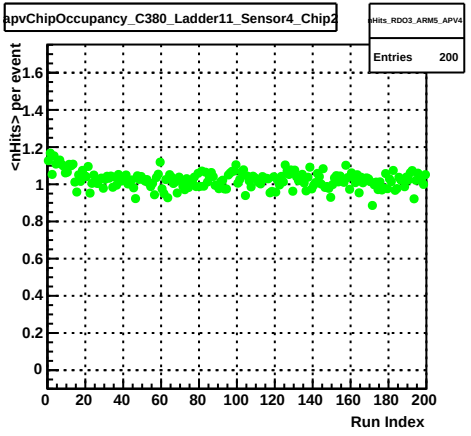

apvChipOccupancy_C375_Ladder11_Sensor3_Chip3
Entries 200
<nHits> per event
Run Index

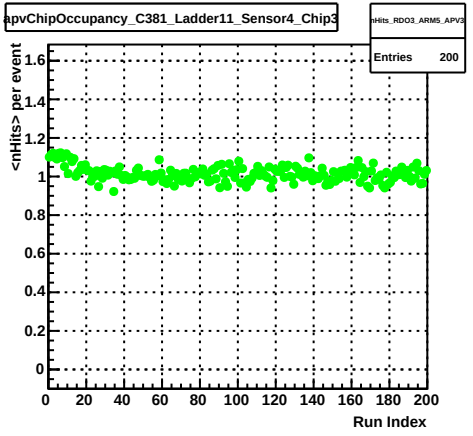

apvChipOccupancy_C376_Ladder11_Sensor3_Chip4
Entries 200
<nHits> per event
Run Index

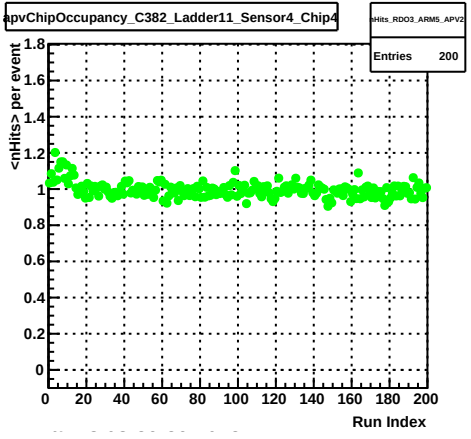

apvChipOccupancy_C377_Ladder11_Sensor3_Chip5
Entries 200
<nHits> per event
Run Index

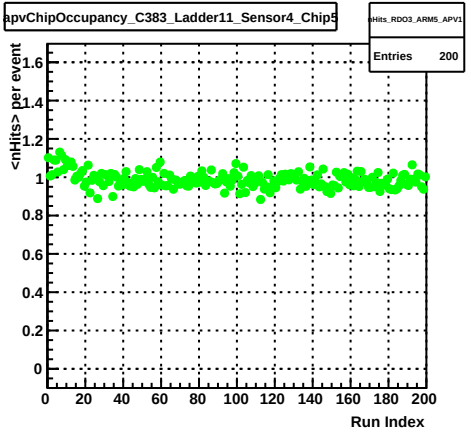

apvChipOccupancy_C378_Ladder11_Sensor3_Chip6
Entries 200
<nHits> per event
Run Index

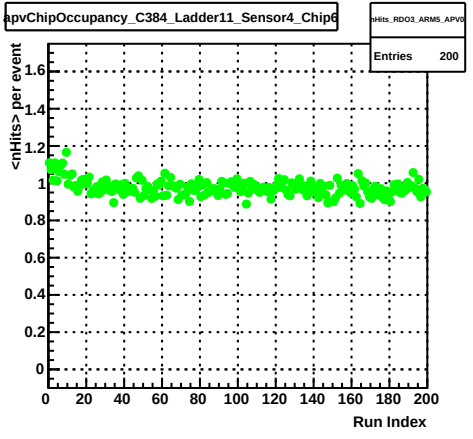

apvChipOccupancy_C379_Ladder11_Sensor4_Chip1
nHits_RD03_ARM5_APV5
Entries 200
<nHits> per event
Run Index

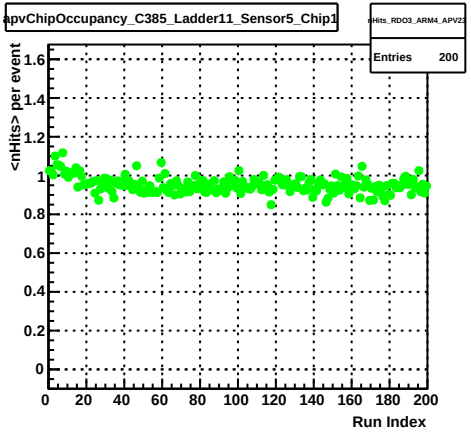

apvChipOccupancy_C380_Ladder11_Sensor4_Chip2
nHits_RD03_ARM5_APV4
Entries 200
<nHits> per event
Run Index

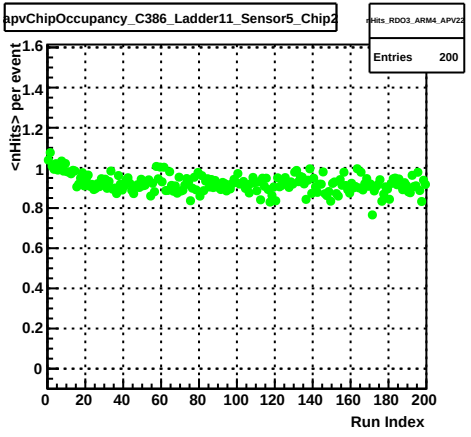

apvChipOccupancy_C381_Ladder11_Sensor4_Chip3
nHits_RD03_ARM5_APV3
Entries 200
<nHits> per event
Run Index

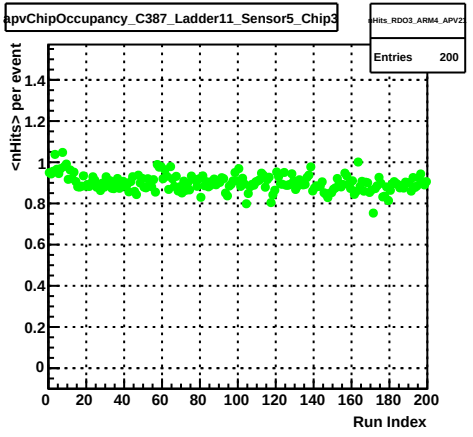

apvChipOccupancy_C382_Ladder11_Sensor4_Chip4
nHits_RD03_ARM5_APV2
Entries 200
<nHits> per event
Run Index

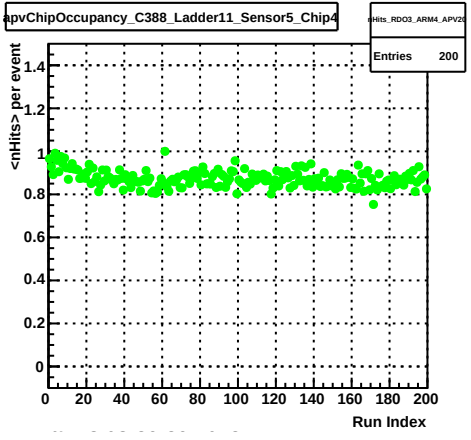

apvChipOccupancy_C383_Ladder11_Sensor4_Chip5
nHits_RD03_ARM5_APV1
Entries 200
<nHits> per event
Run Index

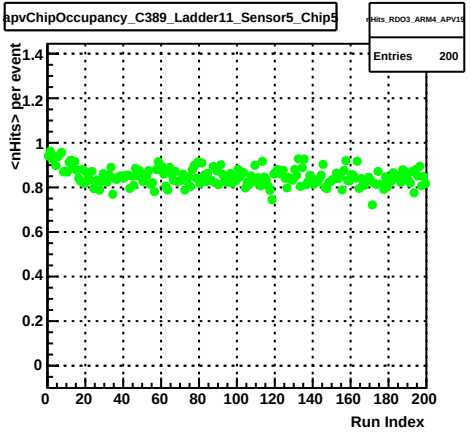

apvChipOccupancy_C384_Ladder11_Sensor4_Chip6
nHits_RD03_ARM5_APV0
Entries 200
<nHits> per event
Run Index

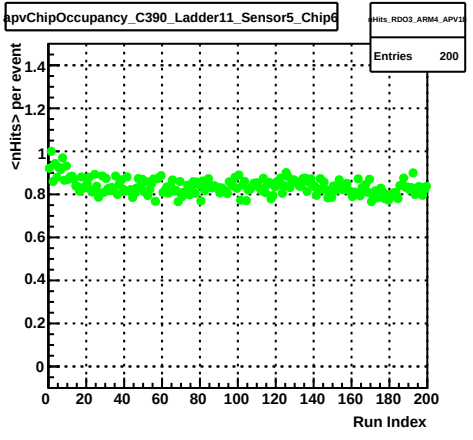

apvChipOccupancy_C385_Ladder11_Sensor5_Chip1
nHits_RDO3_ARM4_APV25
Entries 200
<nHits> per event
Run Index

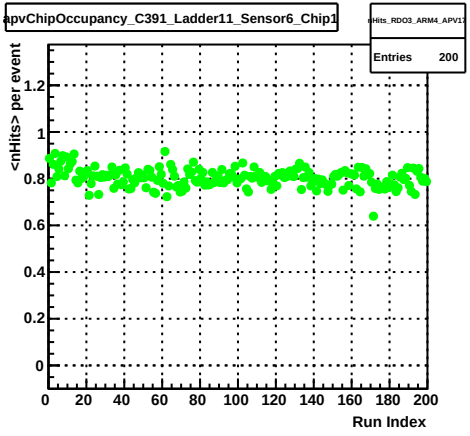

apvChipOccupancy_C386_Ladder11_Sensor5_Chip2
nHits_RDO3_ARM4_APV25
Entries 200
<nHits> per event
Run Index

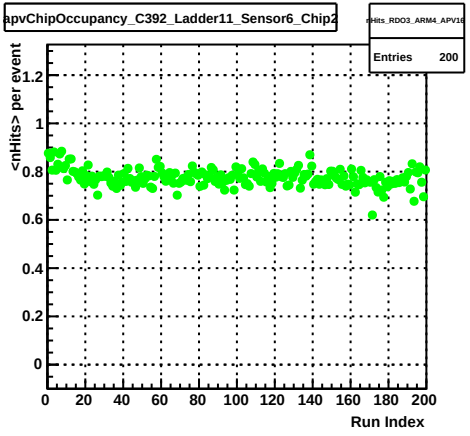

apvChipOccupancy_C387_Ladder11_Sensor5_Chip3
nHits_RDO3_ARM4_APV25
Entries 200
<nHits> per event
Run Index

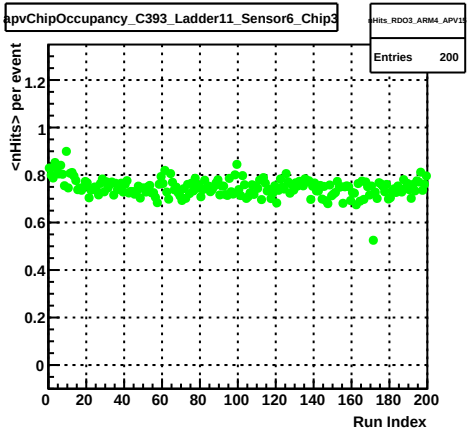

apvChipOccupancy_C388_Ladder11_Sensor5_Chip4
nHits_RDO3_ARM4_APV25
Entries 200
<nHits> per event
Run Index

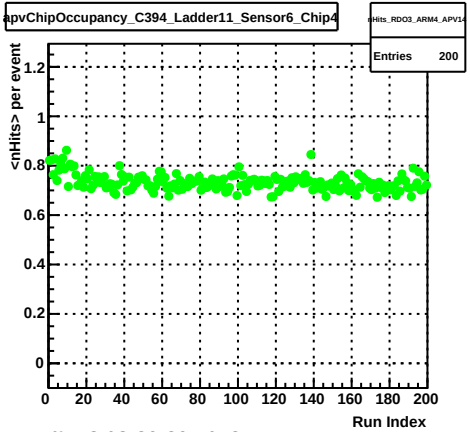

apvChipOccupancy_C389_Ladder11_Sensor5_Chip5
nHits_RDO3_ARM4_APV18
Entries 200
<nHits> per event
Run Index

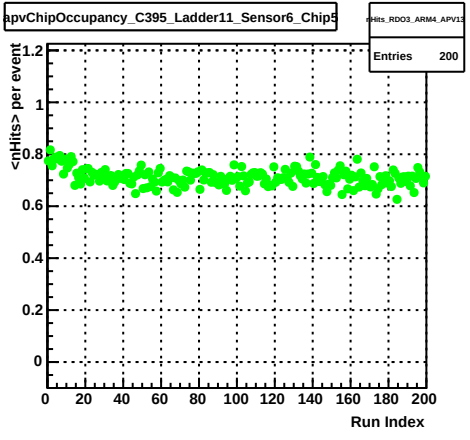

apvChipOccupancy_C390_Ladder11_Sensor5_Chip6
nHits_RDO3_ARM4_APV18
Entries 200
<nHits> per event
Run Index

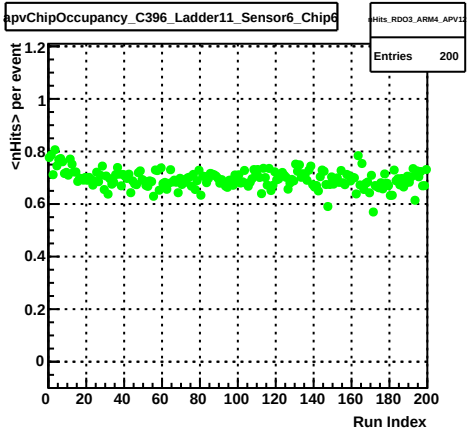

apvChipOccupancy_C391_Ladder11_Sensor6_Chip1
nHits_RDO3_ARM4_APV13
Entries 200
<nHits> per event
Run Index

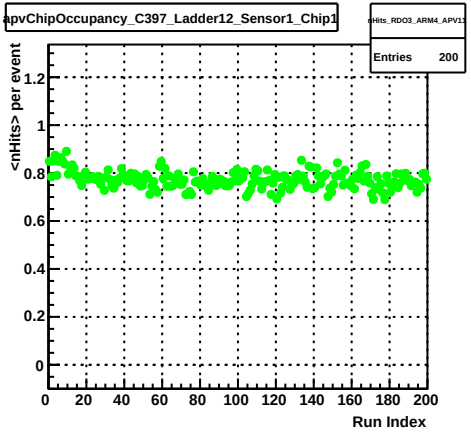

apvChipOccupancy_C392_Ladder11_Sensor6_Chip2
nHits_RDO3_ARM4_APV14
Entries 200
<nHits> per event
Run Index

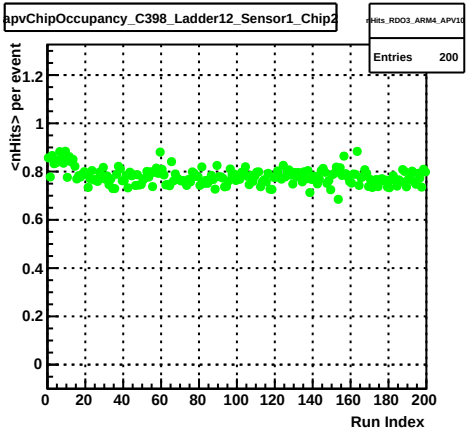

apvChipOccupancy_C393_Ladder11_Sensor6_Chip3
nHits_RDO3_ARM4_APV15
Entries 200
<nHits> per event
Run Index

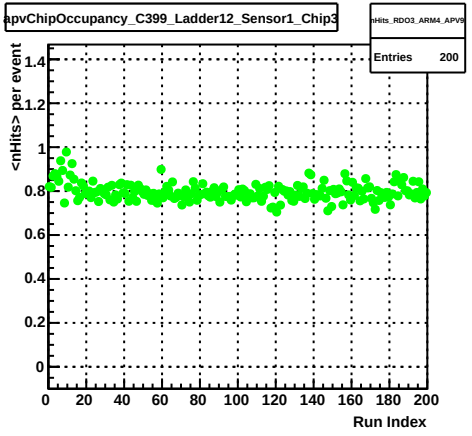

apvChipOccupancy_C394_Ladder11_Sensor6_Chip4
nHits_RDO3_ARM4_APV16
Entries 200
<nHits> per event
Run Index

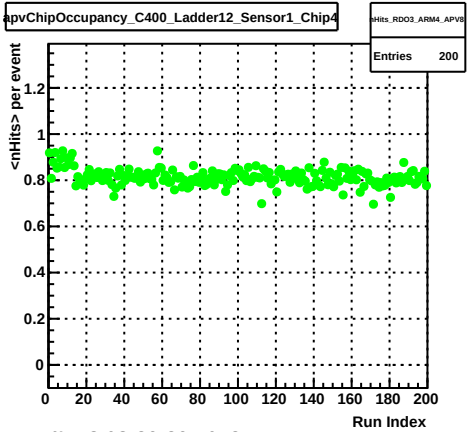

apvChipOccupancy_C395_Ladder11_Sensor6_Chip5
nHits_RDO3_ARM4_APV17
Entries 200
<nHits> per event
Run Index

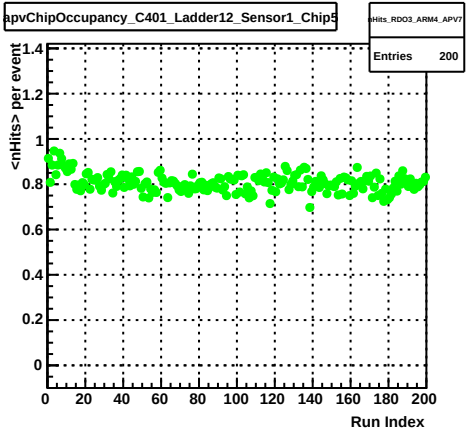

apvChipOccupancy_C396_Ladder11_Sensor6_Chip6
nHits_RDO3_ARM4_APV18
Entries 200
<nHits> per event
Run Index

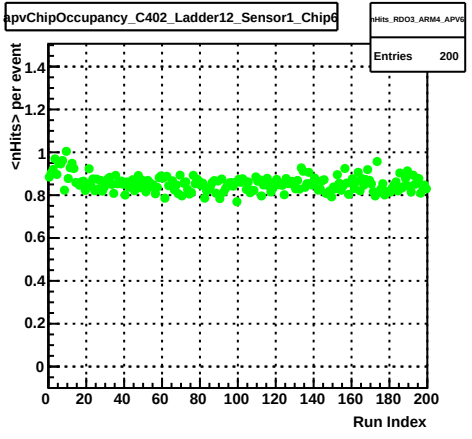

apvChipOccupancy_C397_Ladder12_Sensor1_Chip1
Entries 200
<nHits> per event
Run Index

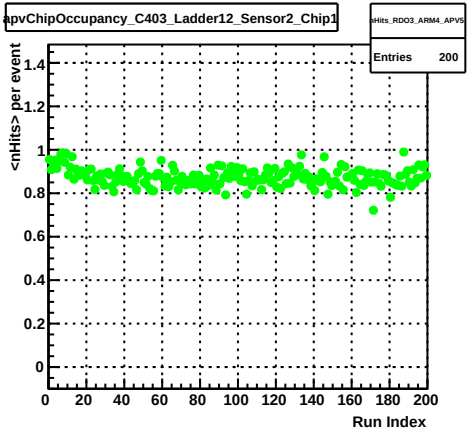

apvChipOccupancy_C398_Ladder12_Sensor1_Chip2
Entries 200
<nHits> per event
Run Index

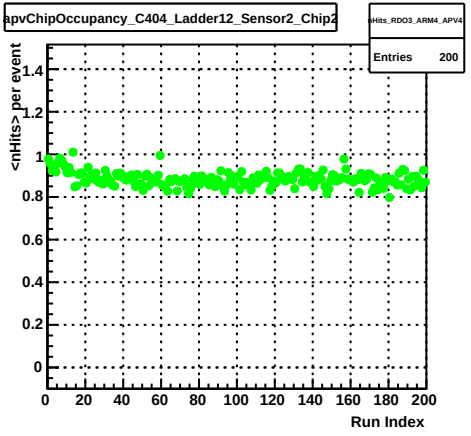

apvChipOccupancy_C399_Ladder12_Sensor1_Chip3
Entries 200
<nHits> per event
Run Index

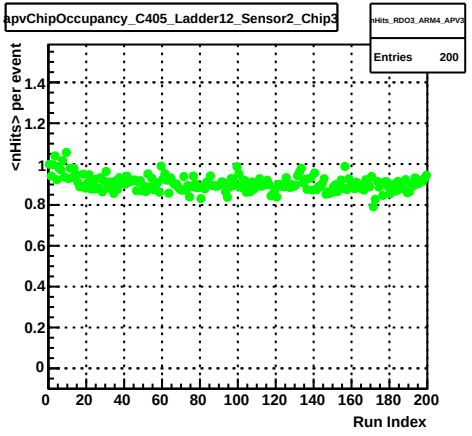

apvChipOccupancy_C400_Ladder12_Sensor1_Chip4
Entries 200
<nHits> per event
Run Index

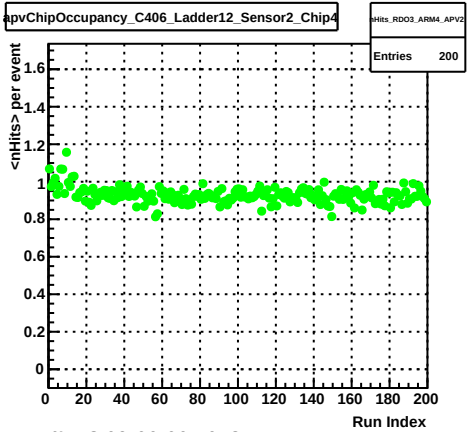

apvChipOccupancy_C401_Ladder12_Sensor1_Chip5
Entries 200
<nHits> per event
Run Index

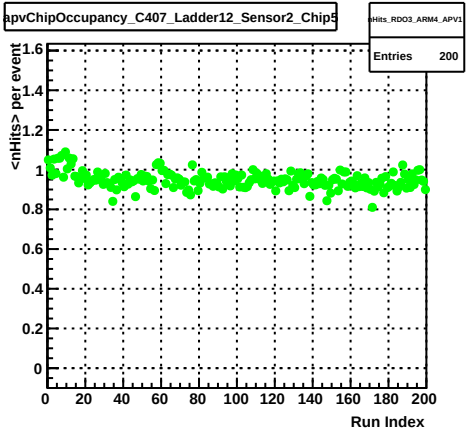

apvChipOccupancy_C402_Ladder12_Sensor1_Chip6
Entries 200
<nHits> per event
Run Index

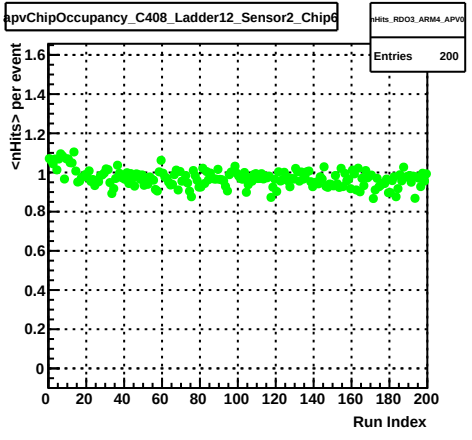

apvChipOccupancy_C403_Ladder12_Sensor2_Chip1
nHits_RD03_ARM4_APV5
Entries 200
<nHits> per event
Run Index

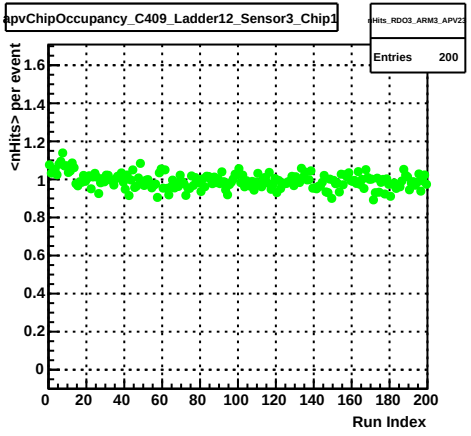

apvChipOccupancy_C404_Ladder12_Sensor2_Chip2
nHits_RD03_ARM4_APV4
Entries 200
<nHits> per event
Run Index

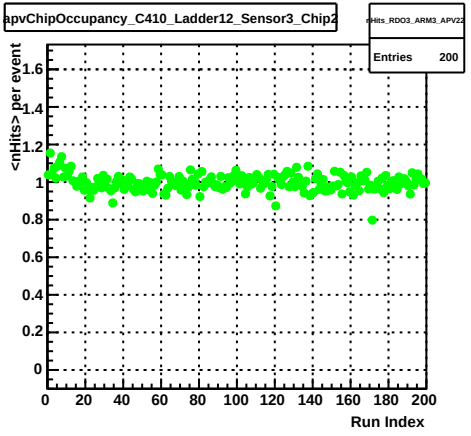

apvChipOccupancy_C405_Ladder12_Sensor2_Chip3
nHits_RD03_ARM4_APV3
Entries 200
<nHits> per event
Run Index

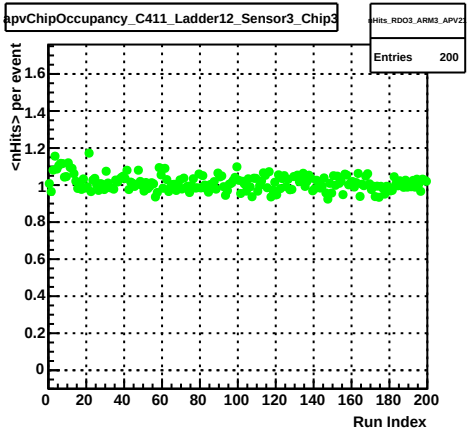

apvChipOccupancy_C406_Ladder12_Sensor2_Chip4
nHits_RD03_ARM4_APV2
Entries 200
<nHits> per event
Run Index

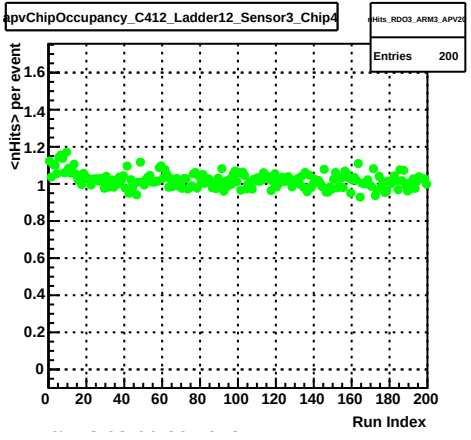

apvChipOccupancy_C407_Ladder12_Sensor2_Chip5
nHits_RD03_ARM4_APV1
Entries 200
<nHits> per event
Run Index

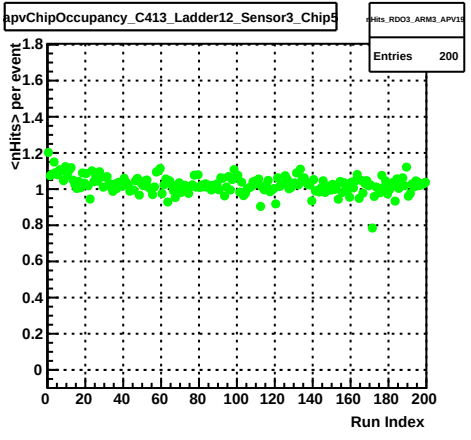

apvChipOccupancy_C408_Ladder12_Sensor2_Chip6
nHits_RD03_ARM4_APV0
Entries 200
<nHits> per event
Run Index

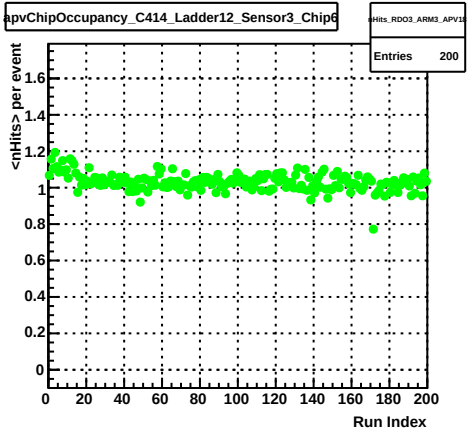

apvChipOccupancy_C409_Ladder12_Sensor3_Chip1
Entries 200
<nHits> per event
Run Index

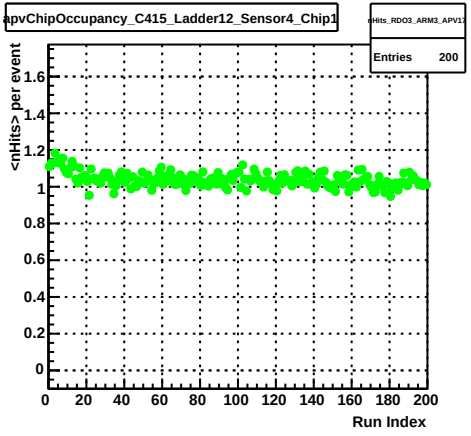

apvChipOccupancy_C410_Ladder12_Sensor3_Chip2
Entries 200
<nHits> per event
Run Index

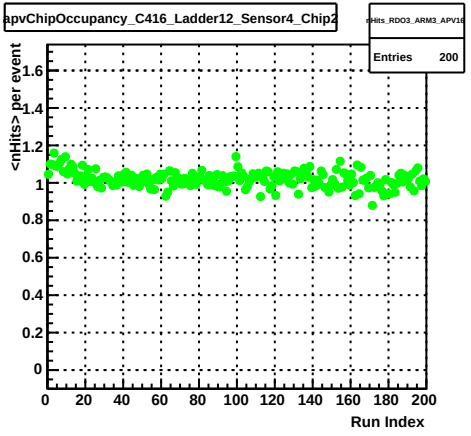

apvChipOccupancy_C411_Ladder12_Sensor3_Chip3
Entries 200
<nHits> per event
Run Index

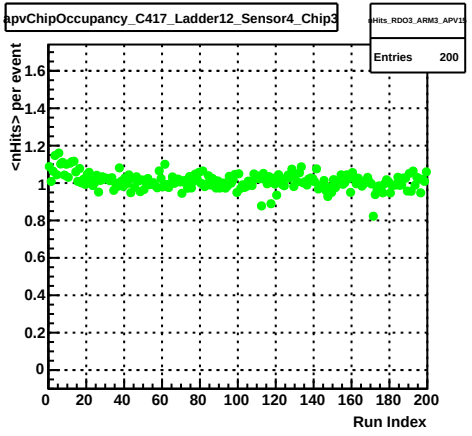

apvChipOccupancy_C412_Ladder12_Sensor3_Chip4
Entries 200
<nHits> per event
Run Index

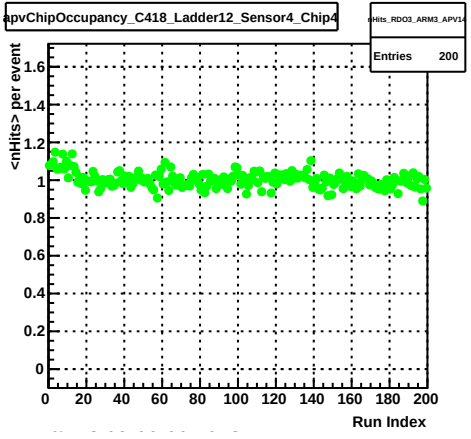

apvChipOccupancy_C413_Ladder12_Sensor3_Chip5
Entries 200
<nHits> per event
Run Index

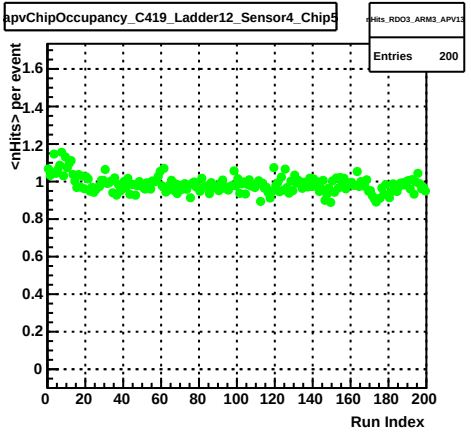

apvChipOccupancy_C414_Ladder12_Sensor3_Chip6
Entries 200
<nHits> per event
Run Index

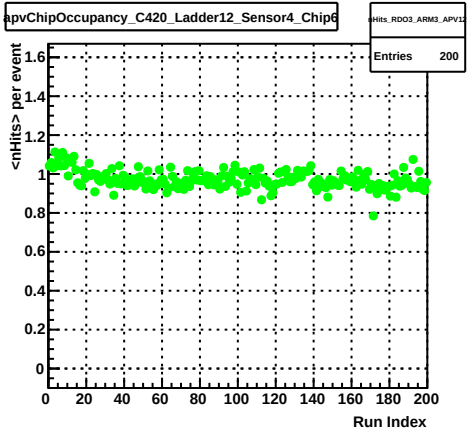

apvChipOccupancy_C415_Ladder12_Sensor4_Chip1
Entries 200
<nHits> per event
Run Index

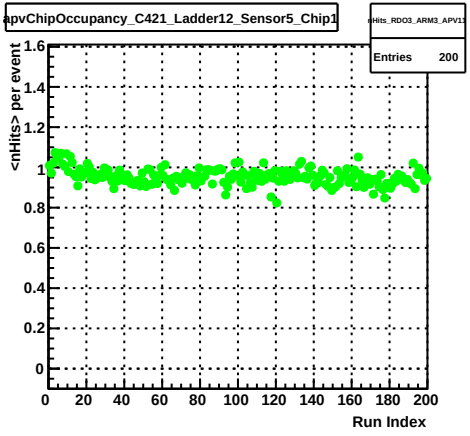

apvChipOccupancy_C416_Ladder12_Sensor4_Chip2
Entries 200
<nHits> per event
Run Index

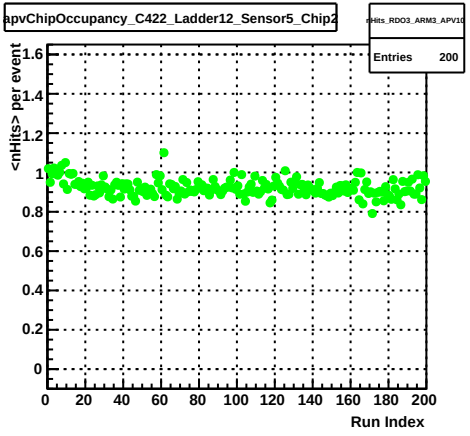

apvChipOccupancy_C417_Ladder12_Sensor4_Chip3
Entries 200
<nHits> per event
Run Index

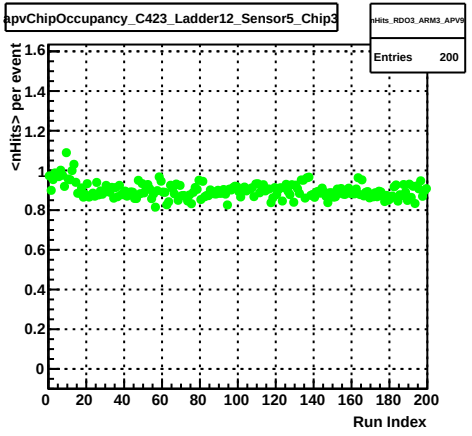

apvChipOccupancy_C418_Ladder12_Sensor4_Chip4
Entries 200
<nHits> per event
Run Index

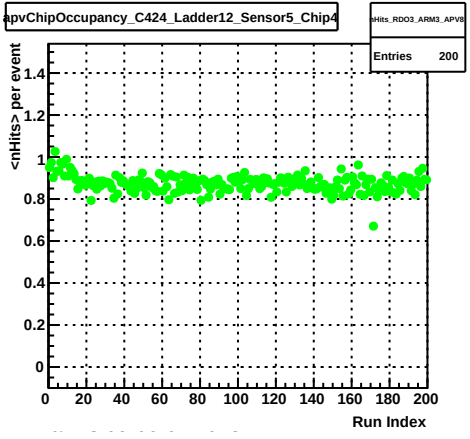

apvChipOccupancy_C419_Ladder12_Sensor4_Chip5
Entries 200
<nHits> per event
Run Index

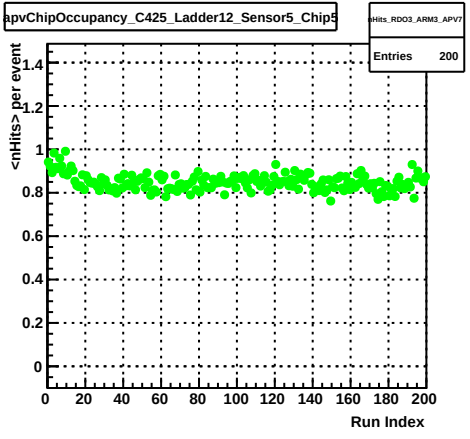

apvChipOccupancy_C420_Ladder12_Sensor4_Chip6
Entries 200
<nHits> per event
Run Index

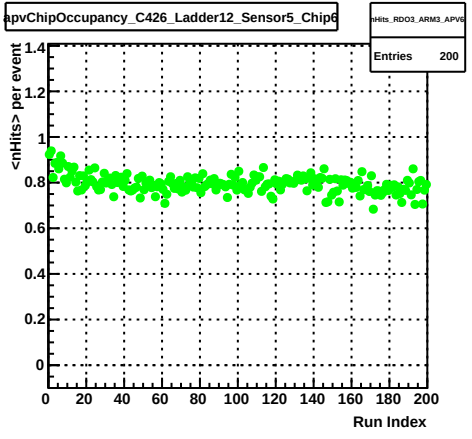

apvChipOccupancy_C421_Ladder12_Sensor5_Chip1
nHits_RDO3_ARM3_APV13
Entries 200
<nHits> per event
Run Index

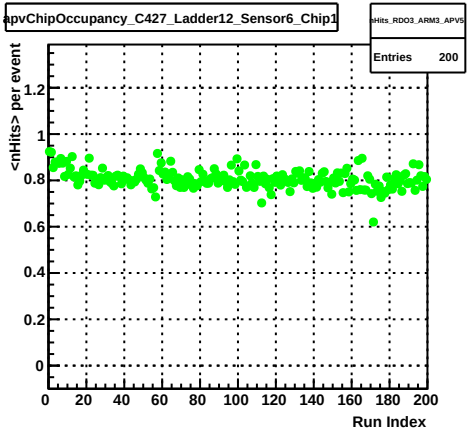

apvChipOccupancy_C422_Ladder12_Sensor5_Chip2
nHits_RDO3_ARM3_APV10
Entries 200
<nHits> per event
Run Index

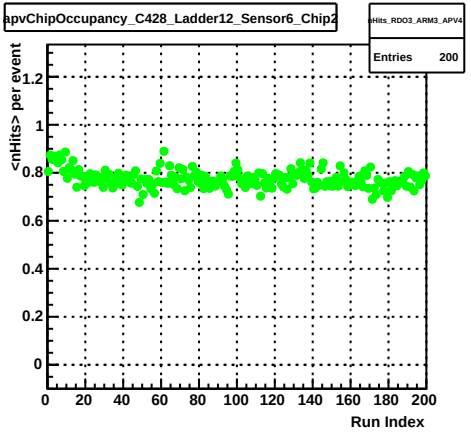

apvChipOccupancy_C423_Ladder12_Sensor5_Chip3
nHits_RDO3_ARM3_APV9
Entries 200
<nHits> per event
Run Index

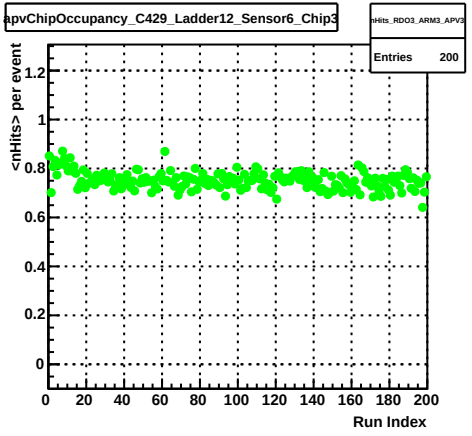

apvChipOccupancy_C424_Ladder12_Sensor5_Chip4
nHits_RDO3_ARM3_APV8
Entries 200
<nHits> per event
Run Index

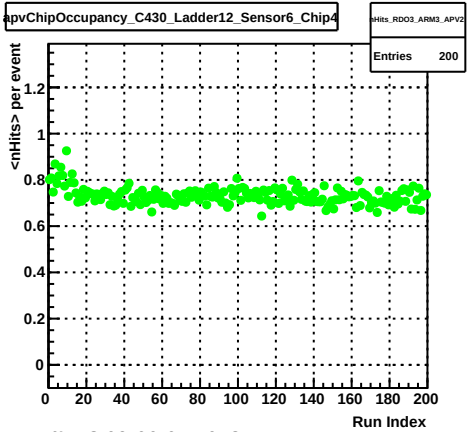

apvChipOccupancy_C425_Ladder12_Sensor5_Chip5
nHits_RDO3_ARM3_APV7
Entries 200
<nHits> per event
Run Index

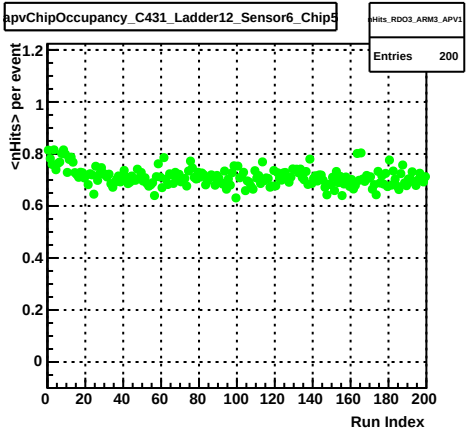

apvChipOccupancy_C426_Ladder12_Sensor5_Chip6
nHits_RDO3_ARM3_APV6
Entries 200
<nHits> per event
Run Index

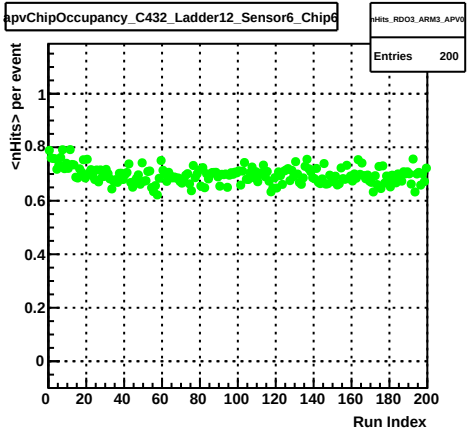

apvChipOccupancy_C427_Ladder12_Sensor6_Chip1
nHits_RD03_ARM3_APV5
Entries 200
<nHits> per event
Run Index

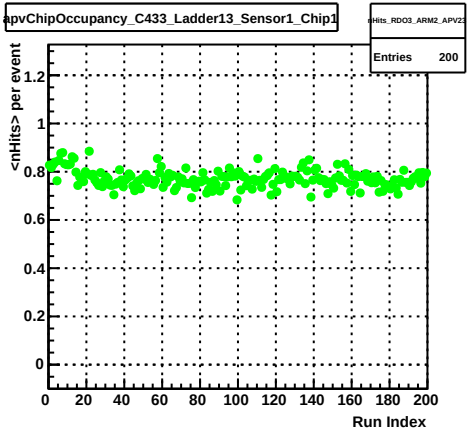

apvChipOccupancy_C428_Ladder12_Sensor6_Chip2
nHits_RD03_ARM3_APV4
Entries 200
<nHits> per event
Run Index

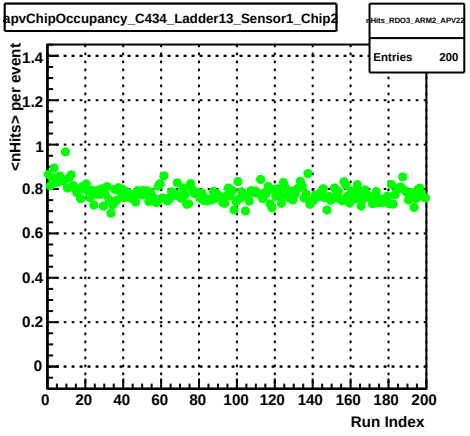

apvChipOccupancy_C429_Ladder12_Sensor6_Chip3
nHits_RD03_ARM3_APV3
Entries 200
<nHits> per event
Run Index

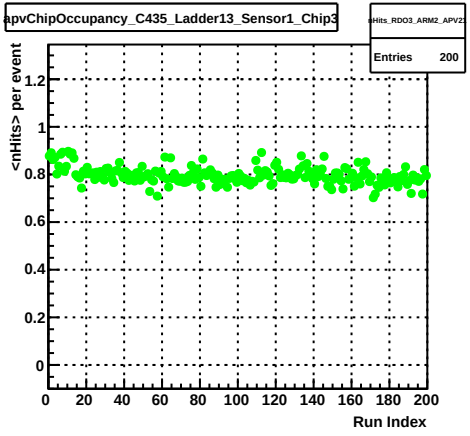

apvChipOccupancy_C430_Ladder12_Sensor6_Chip4
nHits_RD03_ARM3_APV2
Entries 200
<nHits> per event
Run Index

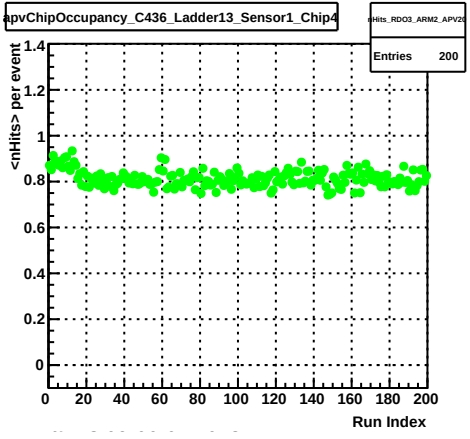

apvChipOccupancy_C431_Ladder12_Sensor6_Chip5
nHits_RD03_ARM3_APV1
Entries 200
<nHits> per event
Run Index

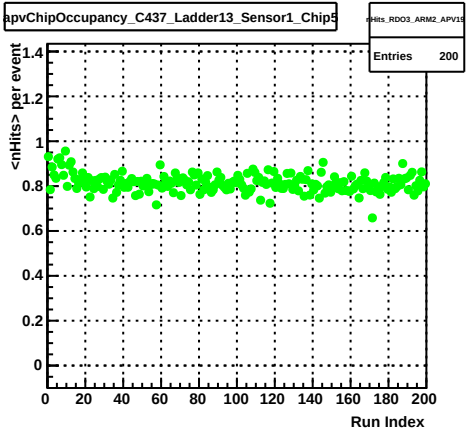

apvChipOccupancy_C432_Ladder12_Sensor6_Chip6
nHits_RD03_ARM3_APV0
Entries 200
<nHits> per event
Run Index

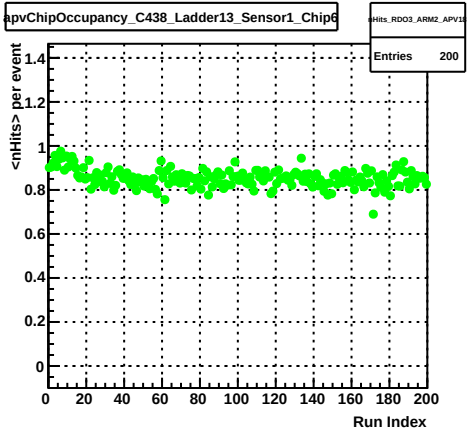

apvChipOccupancy_C433_Ladder13_Sensor1_Chip1
Entries 200
<nHits> per event
Run Index

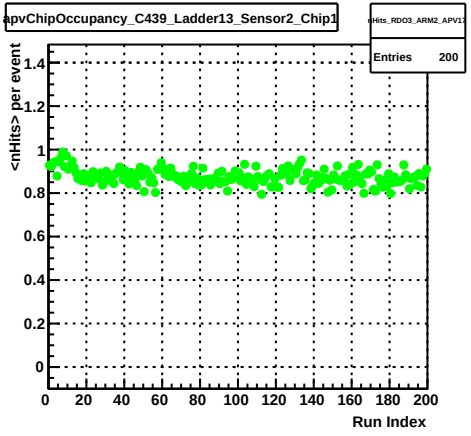

apvChipOccupancy_C434_Ladder13_Sensor1_Chip2
Entries 200
<nHits> per event
Run Index

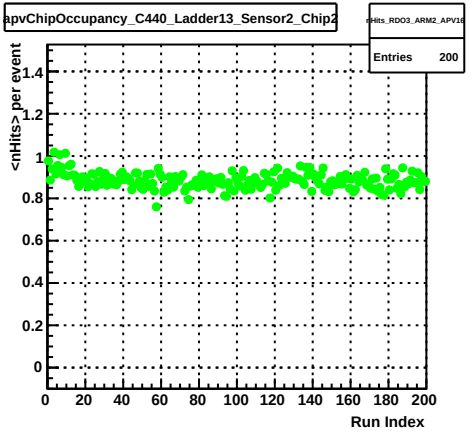

apvChipOccupancy_C435_Ladder13_Sensor1_Chip3
Entries 200
<nHits> per event
Run Index

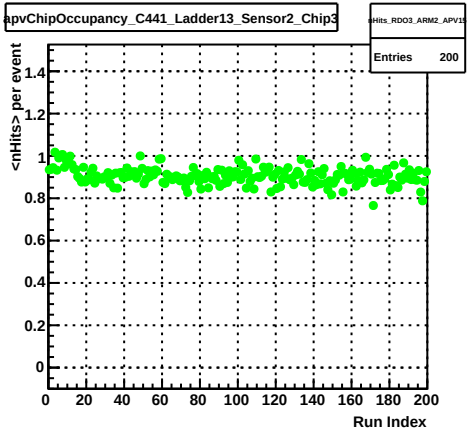

apvChipOccupancy_C436_Ladder13_Sensor1_Chip4
Entries 200
<nHits> per event
Run Index

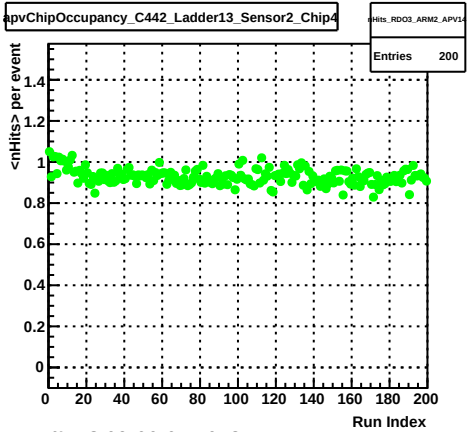

apvChipOccupancy_C437_Ladder13_Sensor1_Chip5
Entries 200
<nHits> per event
Run Index

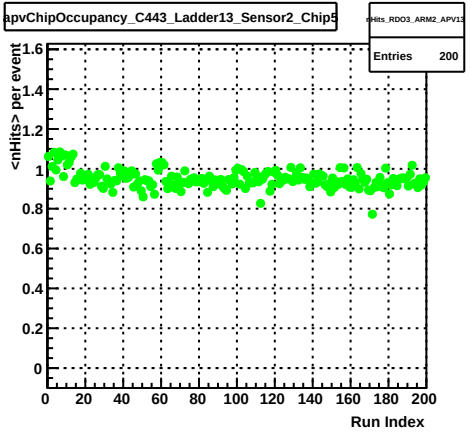

apvChipOccupancy_C438_Ladder13_Sensor1_Chip6
Entries 200
<nHits> per event
Run Index

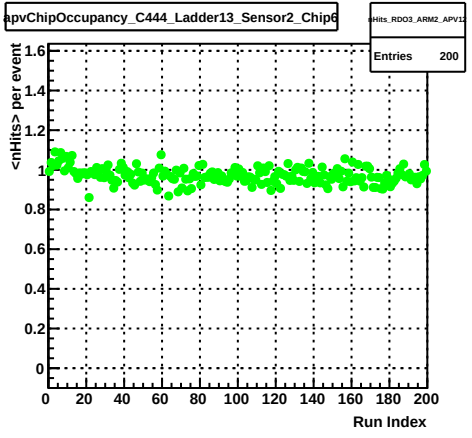

apvChipOccupancy_C439_Ladder13_Sensor2_Chip1
Entries 200
<nHits> per event
Run Index

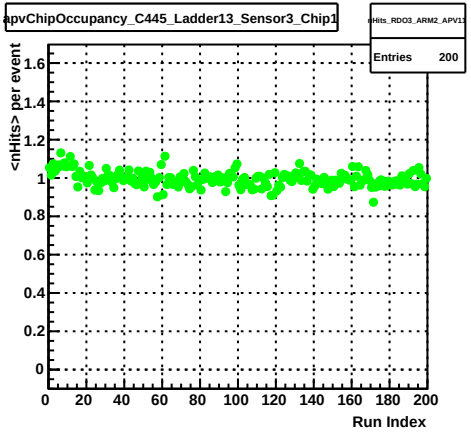

apvChipOccupancy_C440_Ladder13_Sensor2_Chip2
Entries 200
<nHits> per event
Run Index

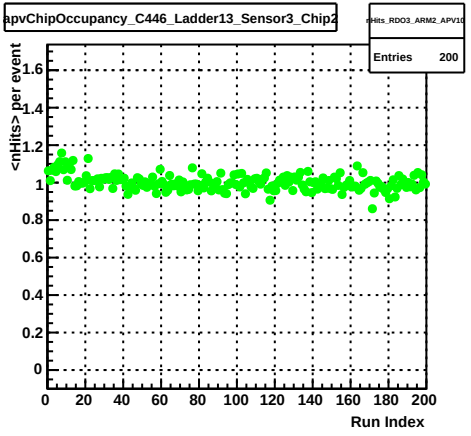

apvChipOccupancy_C441_Ladder13_Sensor2_Chip3
Entries 200
<nHits> per event
Run Index

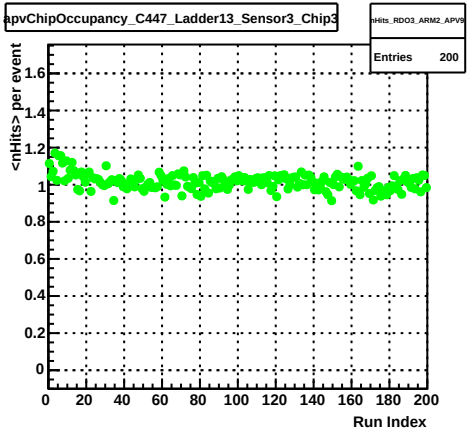

apvChipOccupancy_C442_Ladder13_Sensor2_Chip4
Entries 200
<nHits> per event
Run Index

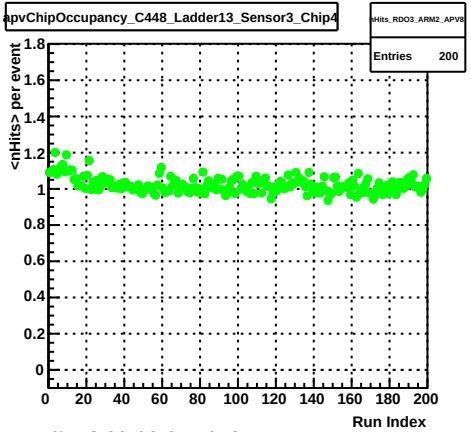

apvChipOccupancy_C443_Ladder13_Sensor2_Chip5
Entries 200
<nHits> per event
Run Index

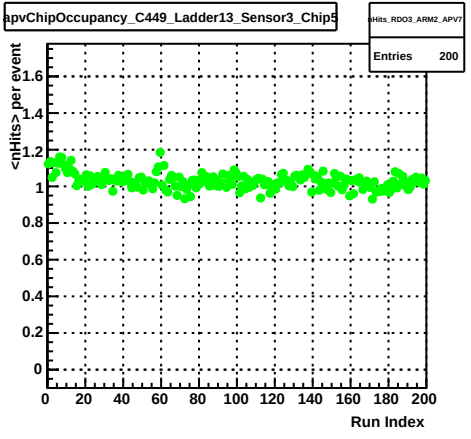

apvChipOccupancy_C444_Ladder13_Sensor2_Chip6
Entries 200
<nHits> per event
Run Index

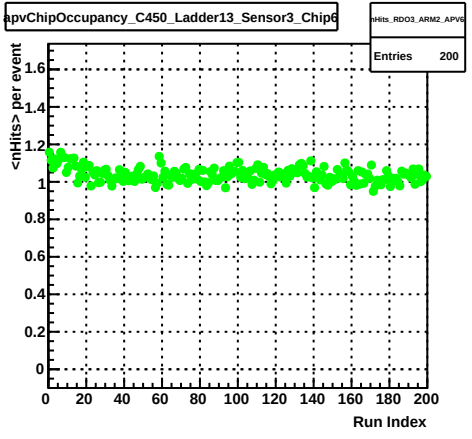

apvChipOccupancy_C445_Ladder13_Sensor3_Chip1
nHits_RDO3_ARM2_APV13
Entries 200
<nHits> per event
Run Index

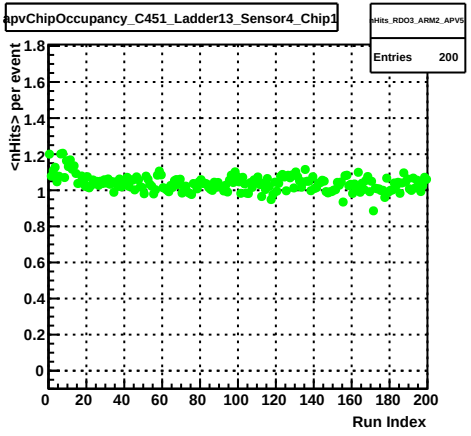

apvChipOccupancy_C446_Ladder13_Sensor3_Chip2
nHits_RDO3_ARM2_APV10
Entries 200
<nHits> per event
Run Index

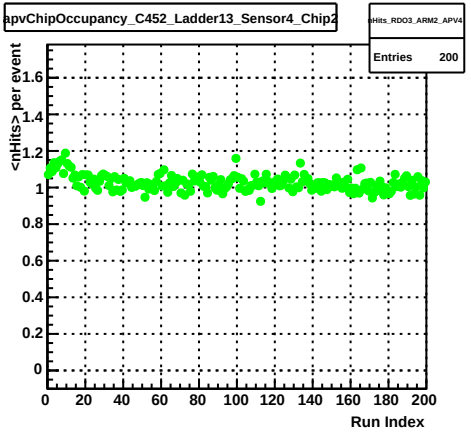

apvChipOccupancy_C447_Ladder13_Sensor3_Chip3
nHits_RDO3_ARM2_APV9
Entries 200
<nHits> per event
Run Index

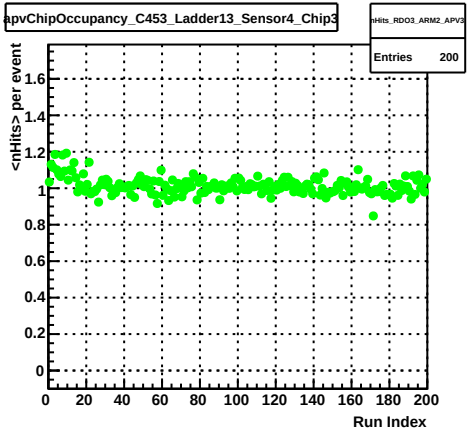

apvChipOccupancy_C448_Ladder13_Sensor3_Chip4
nHits_RDO3_ARM2_APV8
Entries 200
<nHits> per event
Run Index

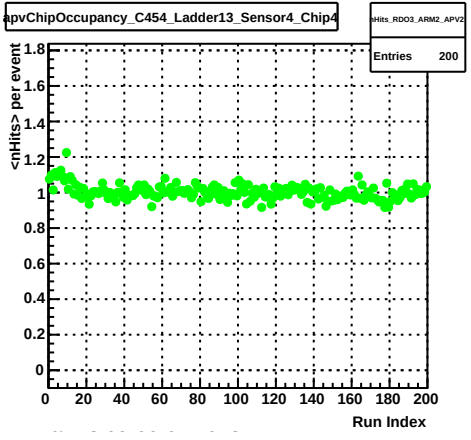

apvChipOccupancy_C449_Ladder13_Sensor3_Chip5
nHits_RDO3_ARM2_APV7
Entries 200
<nHits> per event
Run Index

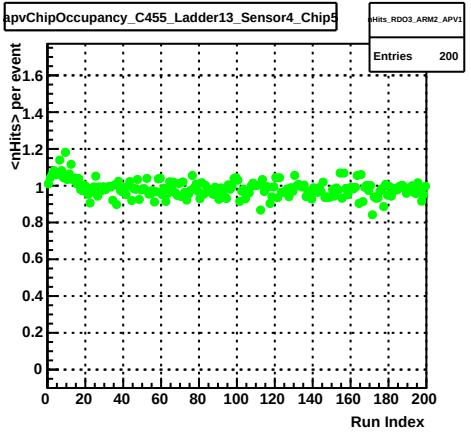

apvChipOccupancy_C450_Ladder13_Sensor3_Chip6
nHits_RDO3_ARM2_APV6
Entries 200
<nHits> per event
Run Index

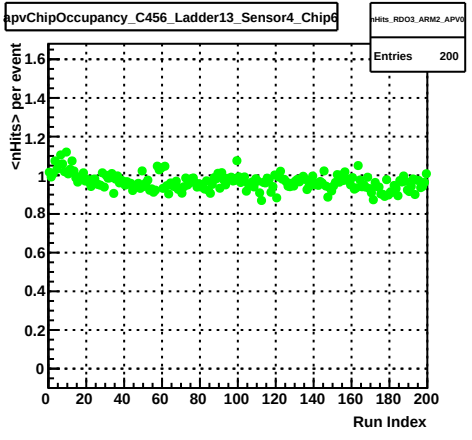

apvChipOccupancy_C451_Ladder13_Sensor4_Chip1
nHits_RD03_ARM2_APV5
Entries 200
<nHits> per event
Run Index

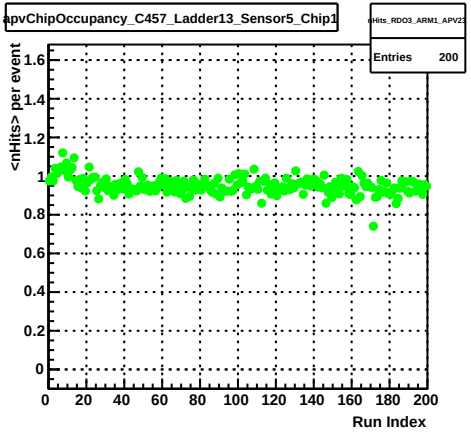

apvChipOccupancy_C452_Ladder13_Sensor4_Chip2
nHits_RD03_ARM2_APV4
Entries 200
<nHits> per event
Run Index

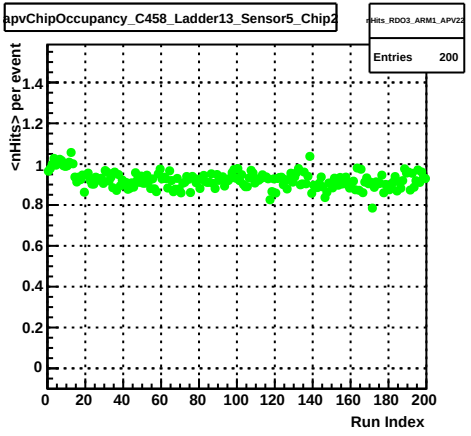

apvChipOccupancy_C453_Ladder13_Sensor4_Chip3
nHits_RD03_ARM2_APV3
Entries 200
<nHits> per event
Run Index

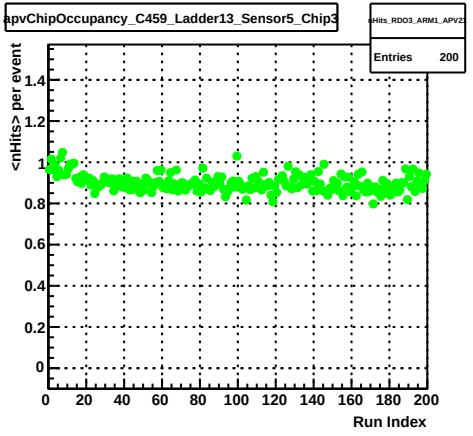

apvChipOccupancy_C454_Ladder13_Sensor4_Chip4
nHits_RD03_ARM2_APV2
Entries 200
<nHits> per event
Run Index

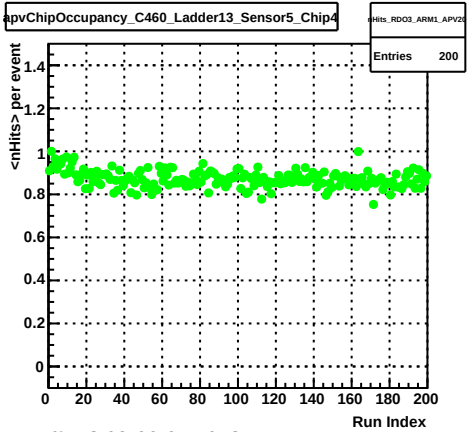

apvChipOccupancy_C455_Ladder13_Sensor4_Chip5
nHits_RD03_ARM2_APV1
Entries 200
<nHits> per event
Run Index

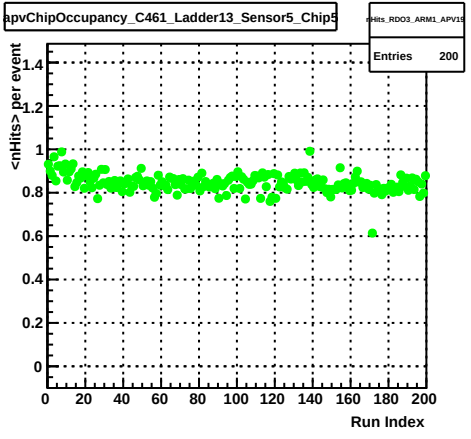

apvChipOccupancy_C456_Ladder13_Sensor4_Chip6
nHits_RD03_ARM2_APV0
Entries 200
<nHits> per event
Run Index

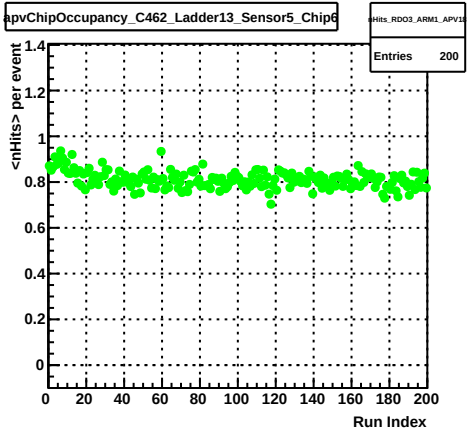

apvChipOccupancy_C457_Ladder13_Sensor5_Chip1
Entries 200
<nHits> per event
Run Index

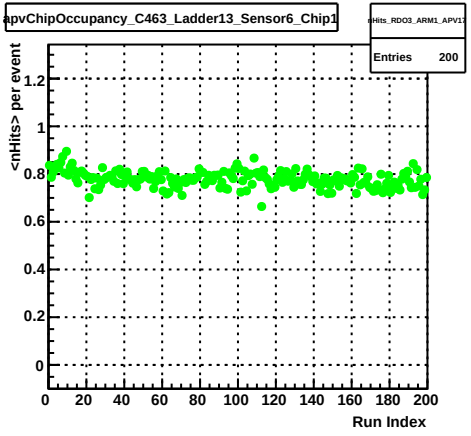

apvChipOccupancy_C458_Ladder13_Sensor5_Chip2
Entries 200
<nHits> per event
Run Index

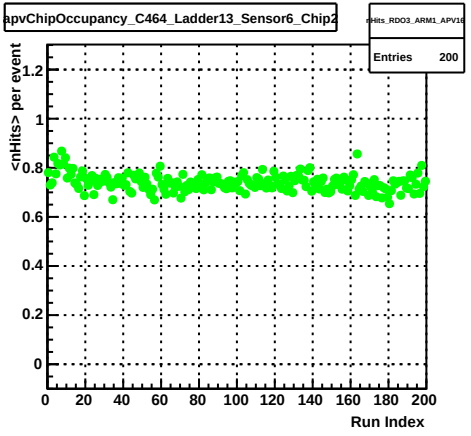

apvChipOccupancy_C459_Ladder13_Sensor5_Chip3
Entries 200
<nHits> per event
Run Index

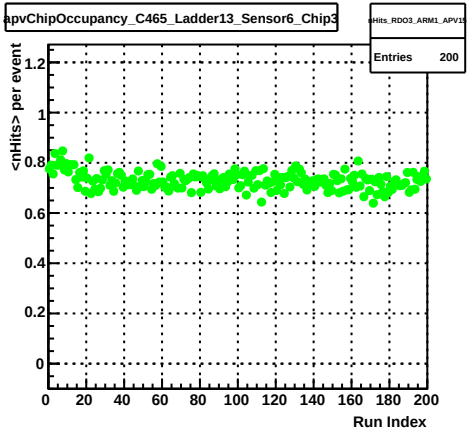

apvChipOccupancy_C460_Ladder13_Sensor5_Chip4
Entries 200
<nHits> per event
Run Index

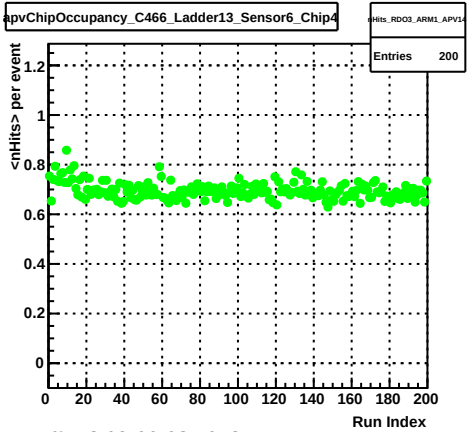

apvChipOccupancy_C461_Ladder13_Sensor5_Chip5
Entries 200
<nHits> per event
Run Index

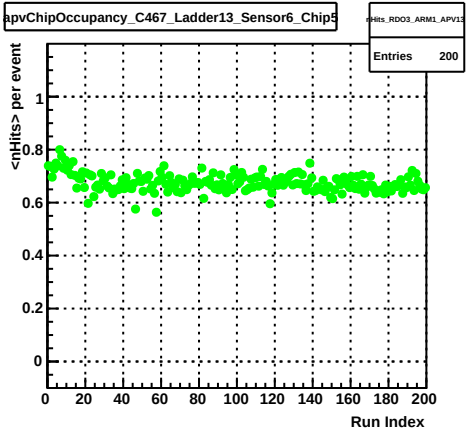

apvChipOccupancy_C462_Ladder13_Sensor5_Chip6
Entries 200
<nHits> per event
Run Index

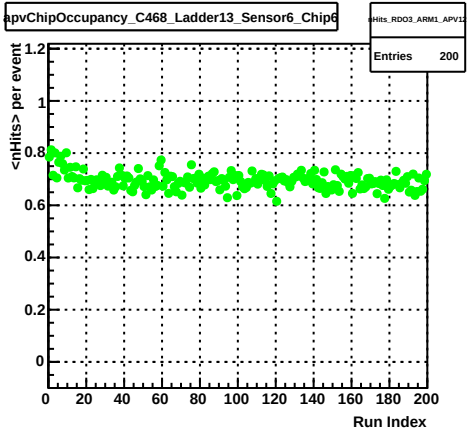

apvChipOccupancy_C463_Ladder13_Sensor6_Chip1
Entries 200
<nHits> per event
Run Index

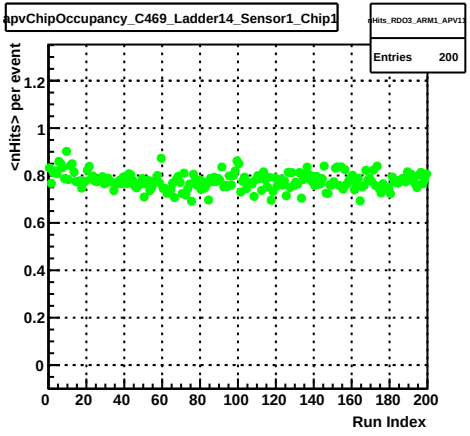

apvChipOccupancy_C464_Ladder13_Sensor6_Chip2
Entries 200
<nHits> per event
Run Index

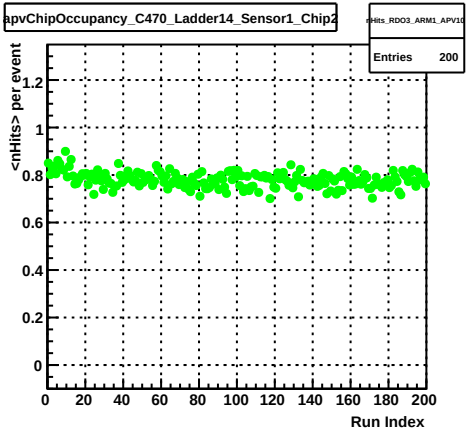

apvChipOccupancy_C465_Ladder13_Sensor6_Chip3
Entries 200
<nHits> per event
Run Index

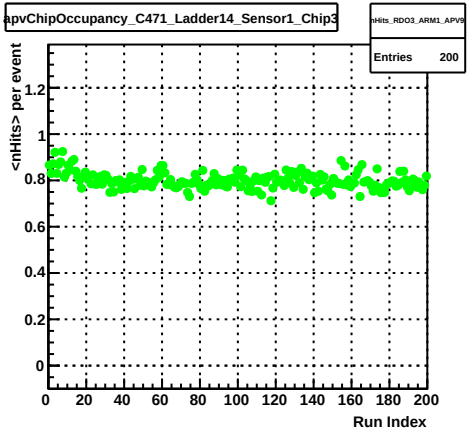

apvChipOccupancy_C466_Ladder13_Sensor6_Chip4
Entries 200
<nHits> per event
Run Index

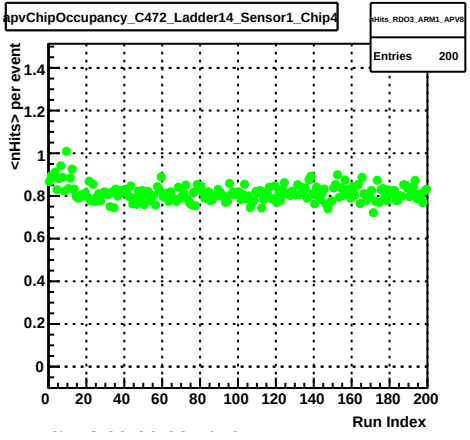

apvChipOccupancy_C467_Ladder13_Sensor6_Chip5
Entries 200
<nHits> per event
Run Index

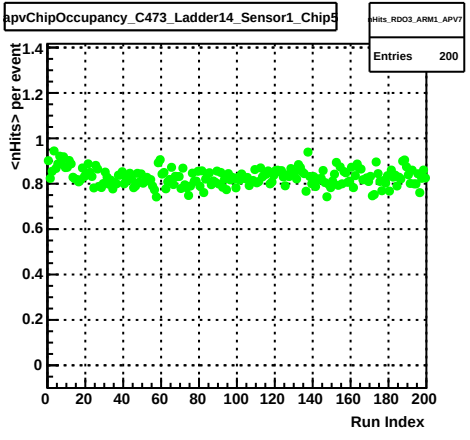

apvChipOccupancy_C468_Ladder13_Sensor6_Chip6
Entries 200
<nHits> per event
Run Index

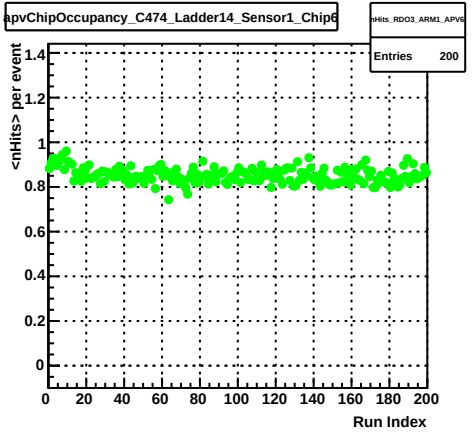

apvChipOccupancy_C469_Ladder14_Sensor1_Chip1
nHits_RDO3_ARM1_APV13
Entries 200
<nHits> per event
Run Index

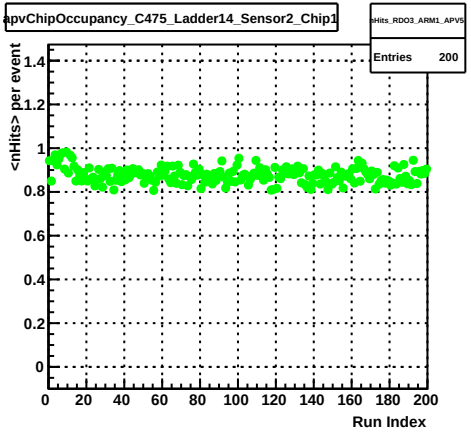

apvChipOccupancy_C470_Ladder14_Sensor1_Chip2
nHits_RDO3_ARM1_APV10
Entries 200
<nHits> per event
Run Index

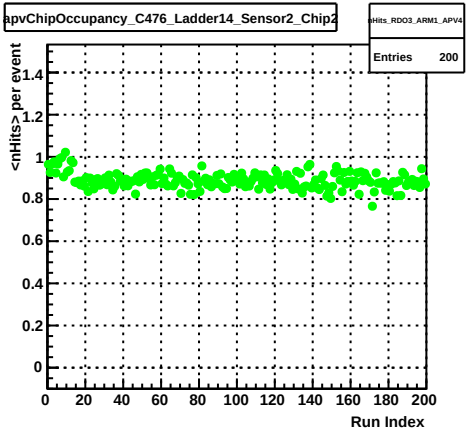

apvChipOccupancy_C471_Ladder14_Sensor1_Chip3
nHits_RDO3_ARM1_APV9
Entries 200
<nHits> per event
Run Index

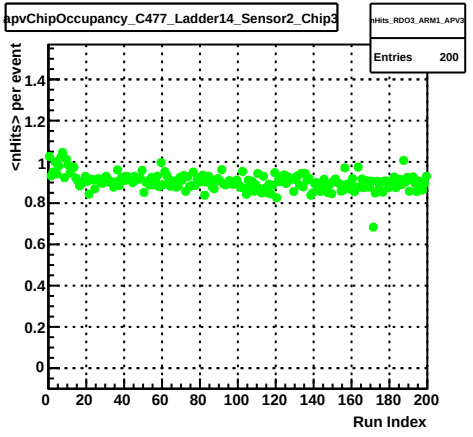

apvChipOccupancy_C472_Ladder14_Sensor1_Chip4
nHits_RDO3_ARM1_APV8
Entries 200
<nHits> per event
Run Index

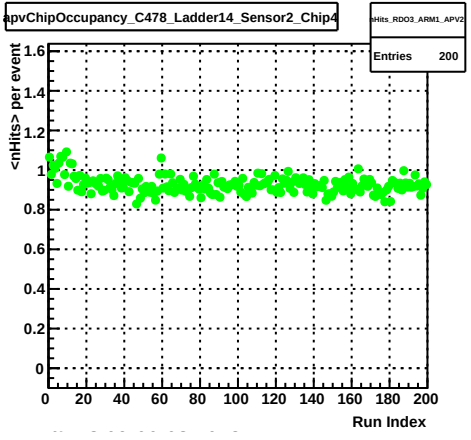

apvChipOccupancy_C473_Ladder14_Sensor1_Chip5
nHits_RDO3_ARM1_APV7
Entries 200
<nHits> per event
Run Index

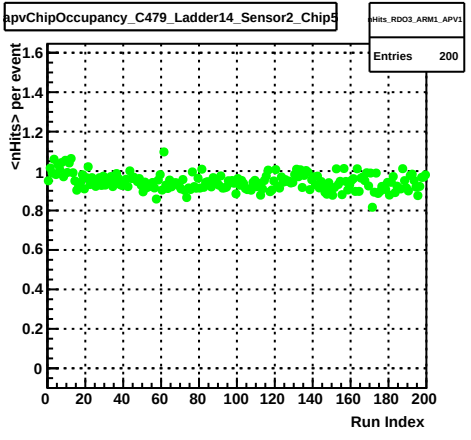

apvChipOccupancy_C474_Ladder14_Sensor1_Chip6
nHits_RDO3_ARM1_APV6
Entries 200
<nHits> per event
Run Index

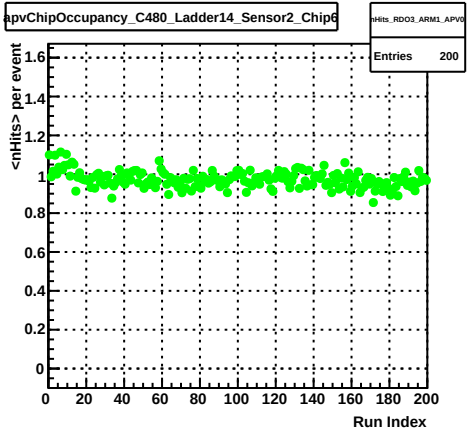

apvChipOccupancy_C475_Ladder14_Sensor2_Chip1
nHits_RD03_ARM1_APV5
Entries 200
<nHits> per event
Run Index

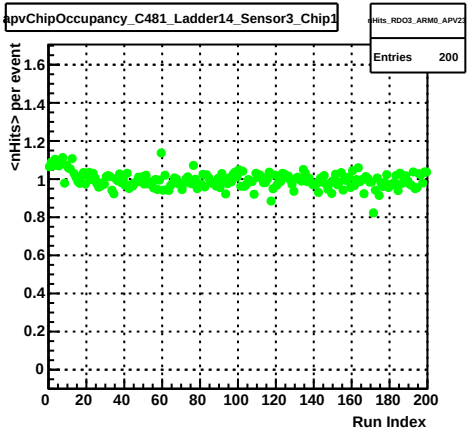

apvChipOccupancy_C476_Ladder14_Sensor2_Chip2
nHits_RD03_ARM1_APV4
Entries 200
<nHits> per event
Run Index

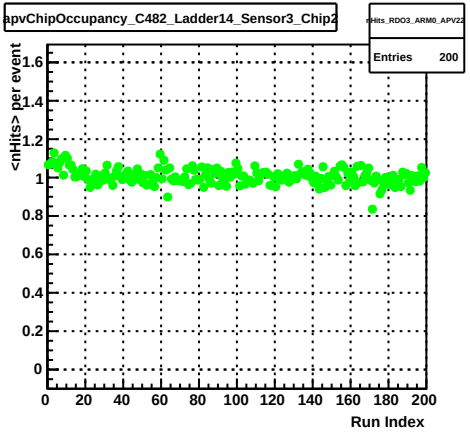

apvChipOccupancy_C477_Ladder14_Sensor2_Chip3
nHits_RD03_ARM1_APV3
Entries 200
<nHits> per event
Run Index

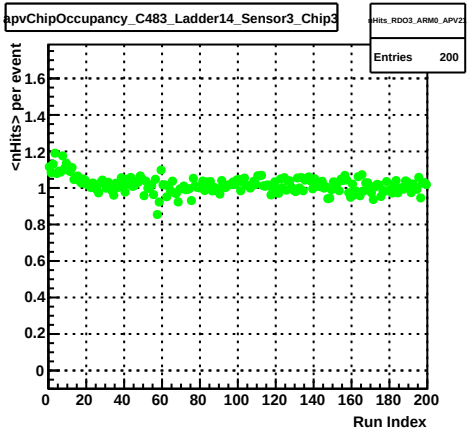

apvChipOccupancy_C478_Ladder14_Sensor2_Chip4
nHits_RD03_ARM1_APV2
Entries 200
<nHits> per event
Run Index

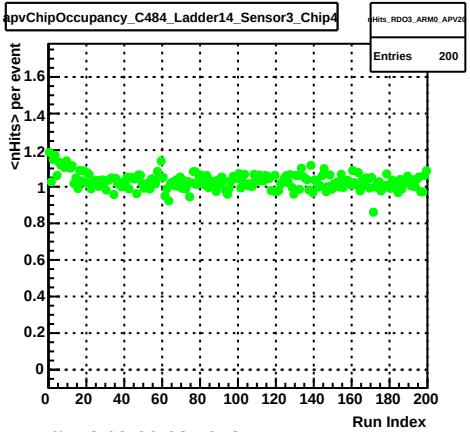

apvChipOccupancy_C479_Ladder14_Sensor2_Chip5
nHits_RD03_ARM1_APV1
Entries 200
<nHits> per event
Run Index

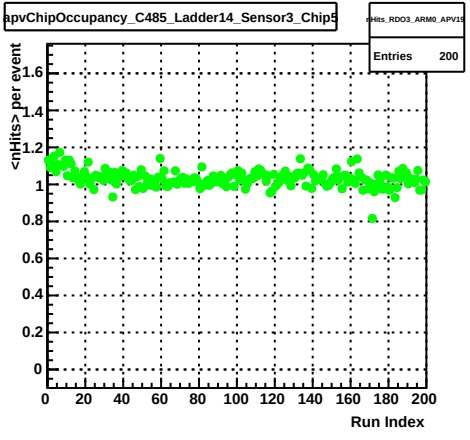

apvChipOccupancy_C480_Ladder14_Sensor2_Chip6
nHits_RD03_ARM1_APV0
Entries 200
<nHits> per event
Run Index

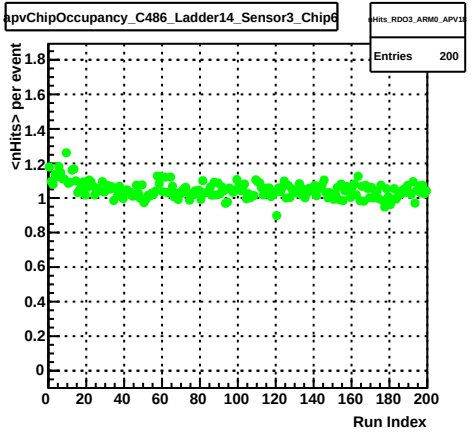

apvChipOccupancy_C481_Ladder14_Sensor3_Chip1
nHits_RDO3_ARM0_APV22
Entries 200
<nHits> per event
Run Index

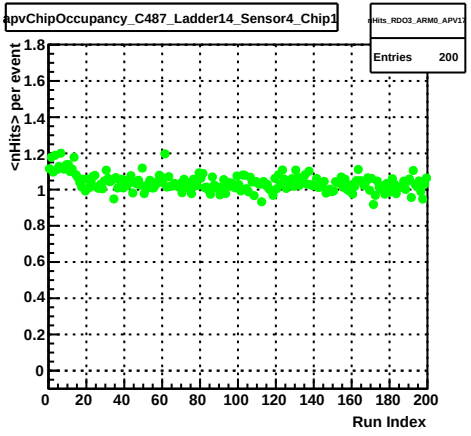

apvChipOccupancy_C482_Ladder14_Sensor3_Chip2
nHits_RDO3_ARM0_APV22
Entries 200
<nHits> per event
Run Index

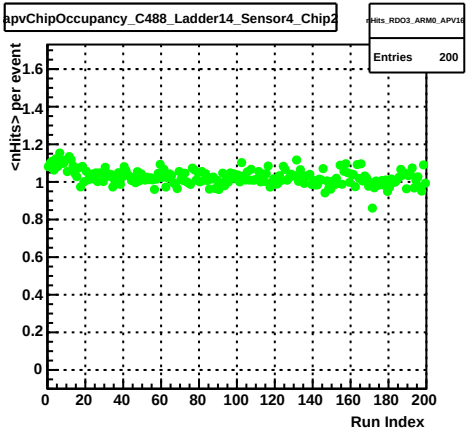

apvChipOccupancy_C483_Ladder14_Sensor3_Chip3
nHits_RDO3_ARM0_APV22
Entries 200
<nHits> per event
Run Index

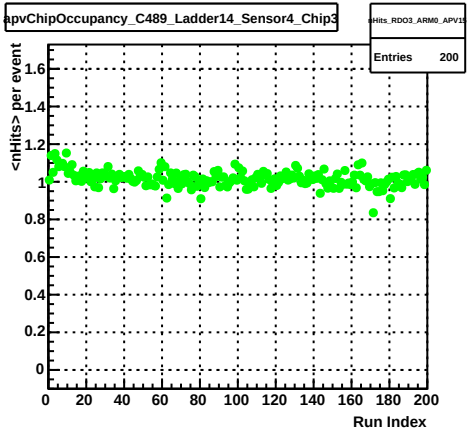

apvChipOccupancy_C484_Ladder14_Sensor3_Chip4
nHits_RDO3_ARM0_APV26
Entries 200
<nHits> per event
Run Index

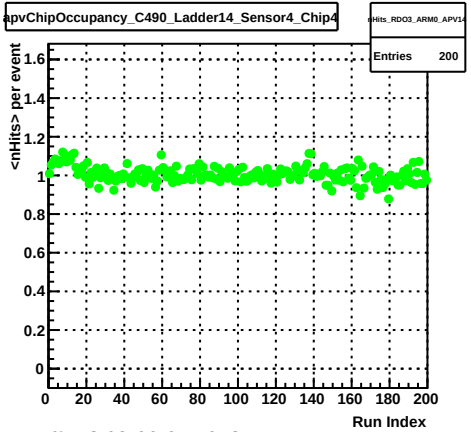

apvChipOccupancy_C485_Ladder14_Sensor3_Chip5
nHits_RDO3_ARM0_APV18
Entries 200
<nHits> per event
Run Index

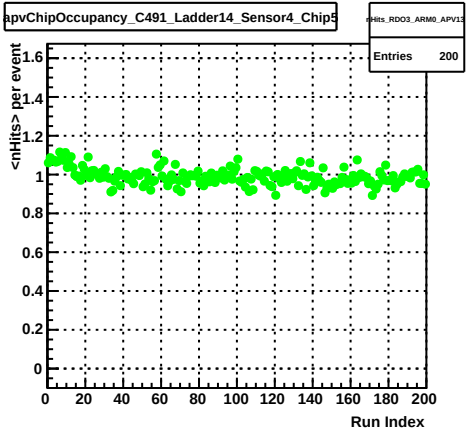

apvChipOccupancy_C486_Ladder14_Sensor3_Chip6
nHits_RDO3_ARM0_APV18
Entries 200
<nHits> per event
Run Index

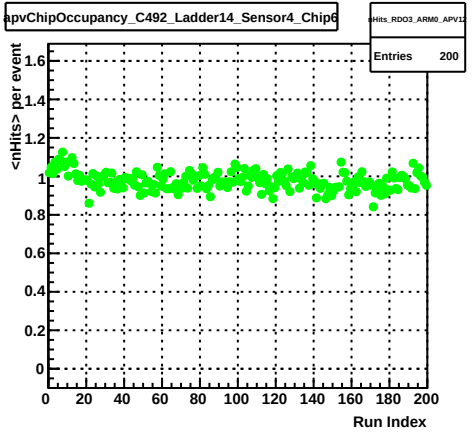

apvChipOccupancy_C487_Ladder14_Sensor4_Chip1
nHits_RDO3_ARM0_APV13
Entries 200
<nHits> per event
Run Index

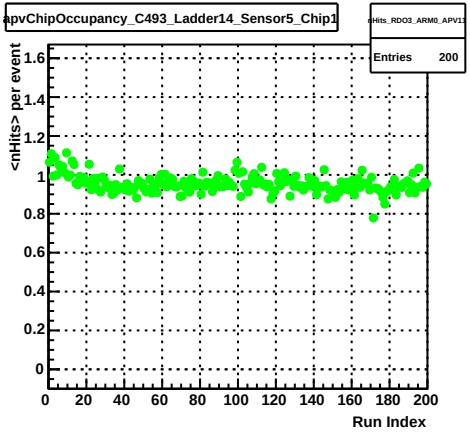

apvChipOccupancy_C488_Ladder14_Sensor4_Chip2
nHits_RDO3_ARM0_APV14
Entries 200
<nHits> per event
Run Index

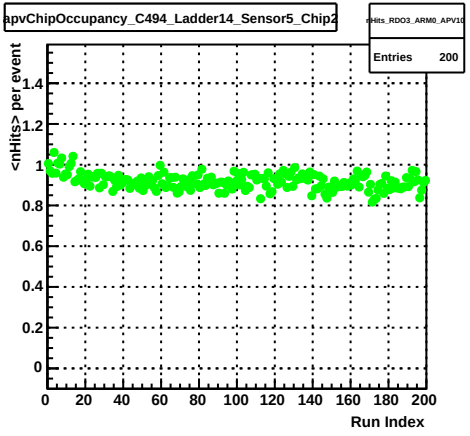

apvChipOccupancy_C489_Ladder14_Sensor4_Chip3
nHits_RDO3_ARM0_APV15
Entries 200
<nHits> per event
Run Index

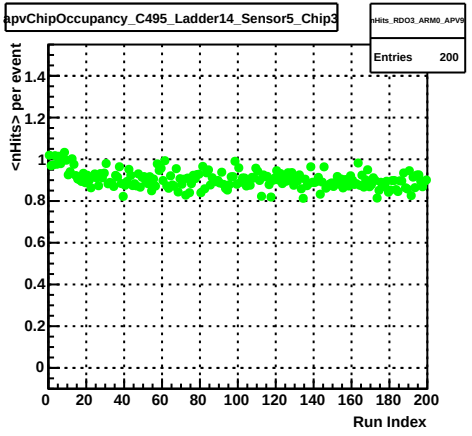

apvChipOccupancy_C490_Ladder14_Sensor4_Chip4
nHits_RDO3_ARM0_APV16
Entries 200
<nHits> per event
Run Index

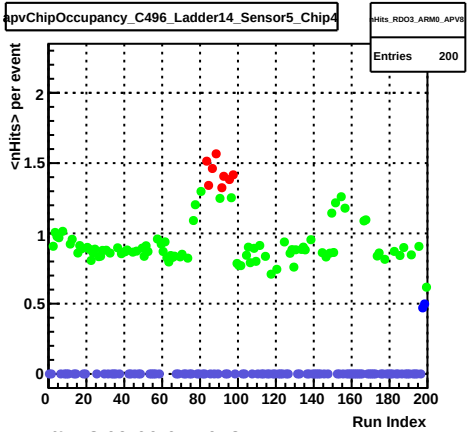

apvChipOccupancy_C491_Ladder14_Sensor4_Chip5
nHits_RDO3_ARM0_APV17
Entries 200
<nHits> per event
Run Index

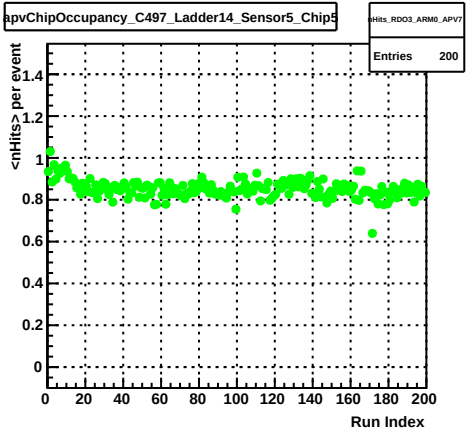

apvChipOccupancy_C492_Ladder14_Sensor4_Chip6
nHits_RDO3_ARM0_APV18
Entries 200
<nHits> per event
Run Index

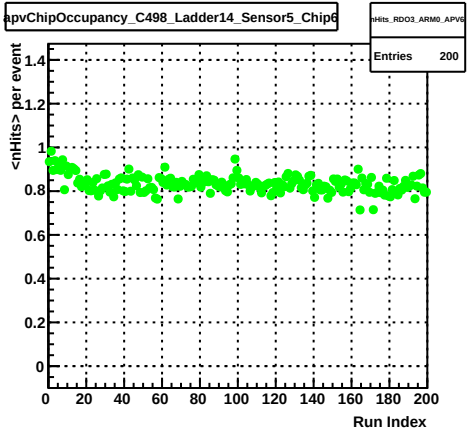

apvChipOccupancy_C493_Ladder14_Sensor5_Chip1
nHits_RDO3_ARM0_APV11
Entries 200
<nHits> per event
Run Index

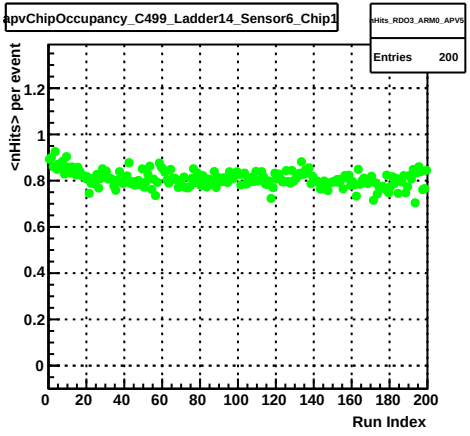

apvChipOccupancy_C494_Ladder14_Sensor5_Chip2
nHits_RDO3_ARM0_APV10
Entries 200
<nHits> per event
Run Index

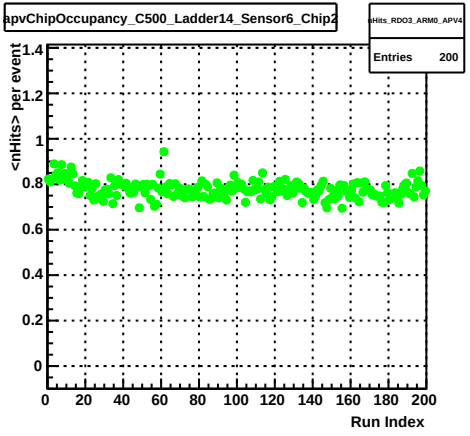

apvChipOccupancy_C495_Ladder14_Sensor5_Chip3
nHits_RDO3_ARM0_APV9
Entries 200
<nHits> per event
Run Index

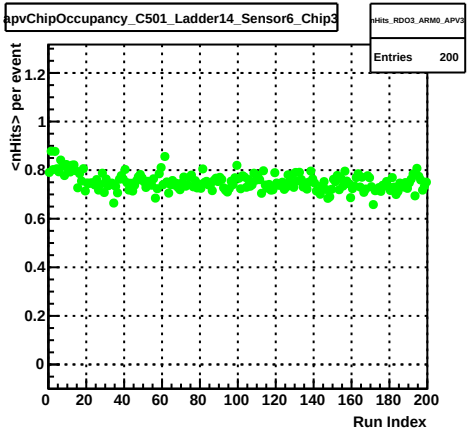

apvChipOccupancy_C496_Ladder14_Sensor5_Chip4
nHits_RDO3_ARM0_APV8
Entries 200
<nHits> per event
Run Index

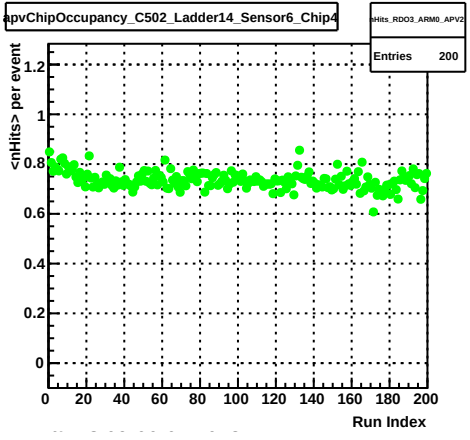

apvChipOccupancy_C497_Ladder14_Sensor5_Chip5
nHits_RDO3_ARM0_APV7
Entries 200
<nHits> per event
Run Index

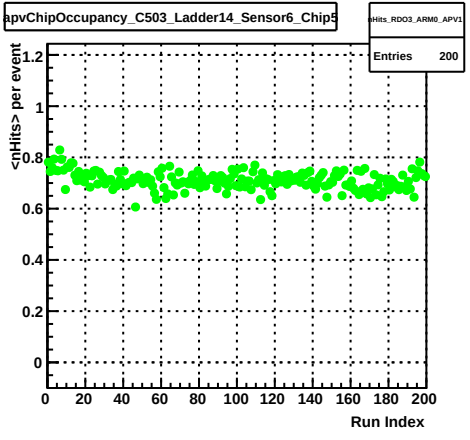

apvChipOccupancy_C498_Ladder14_Sensor5_Chip6
nHits_RDO3_ARM0_APV6
Entries 200
<nHits> per event
Run Index

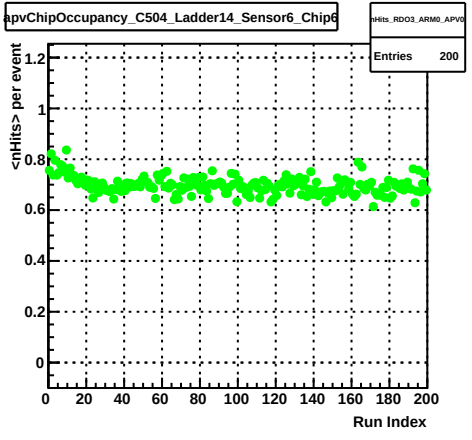

apvChipOccupancy_C499_Ladder14_Sensor6_Chip1
nHits_RDO3_ARM0_APV5
Entries 200
<nHits> per event
Run Index

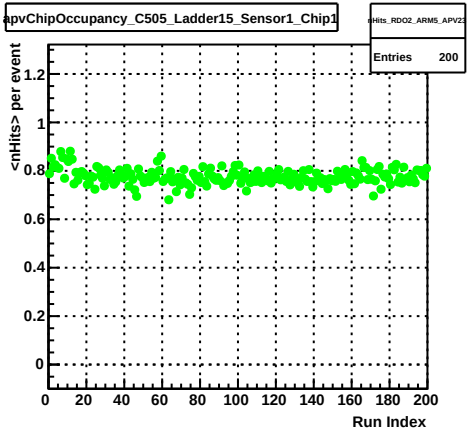

apvChipOccupancy_C500_Ladder14_Sensor6_Chip2
nHits_RDO3_ARM0_APV4
Entries 200
<nHits> per event
Run Index

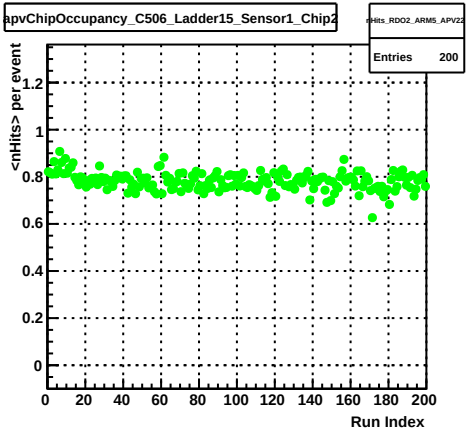

apvChipOccupancy_C501_Ladder14_Sensor6_Chip3
nHits_RDO3_ARM0_APV3
Entries 200
<nHits> per event
Run Index

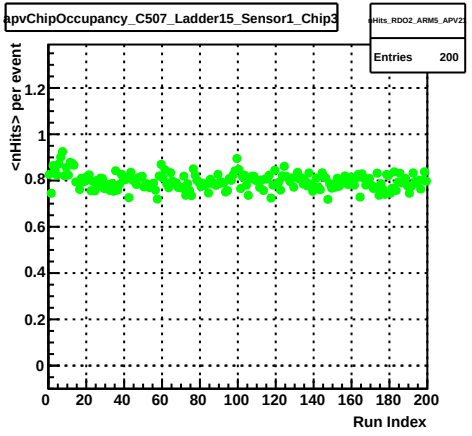

apvChipOccupancy_C502_Ladder14_Sensor6_Chip4
nHits_RDO3_ARM0_APV2
Entries 200
<nHits> per event
Run Index

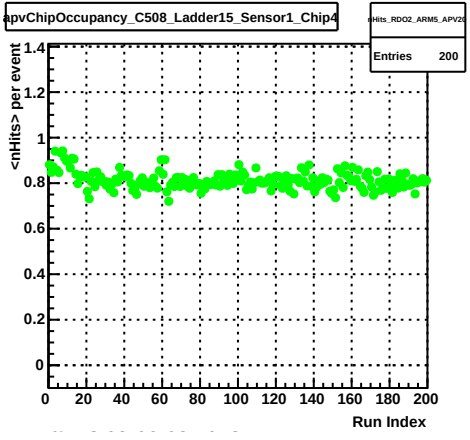

apvChipOccupancy_C503_Ladder14_Sensor6_Chip5
nHits_RDO3_ARM0_APV1
Entries 200
<nHits> per event
Run Index

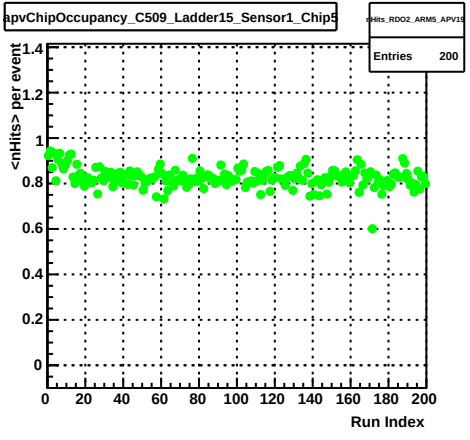

apvChipOccupancy_C504_Ladder14_Sensor6_Chip6
nHits_RDO3_ARM0_APV0
Entries 200
<nHits> per event
Run Index

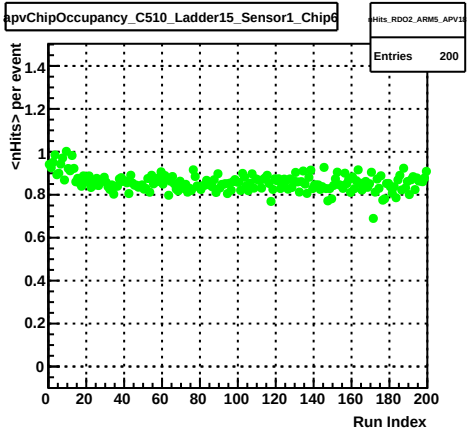

apvChipOccupancy_C505_Ladder15_Sensor1_Chip1
nHits_RDO2_ARM5_APV23
Entries 200
<nHits> per event
Run Index

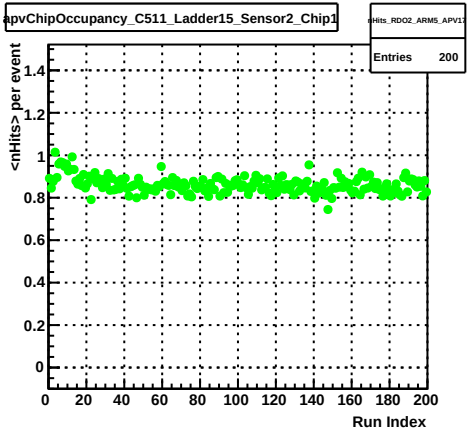

apvChipOccupancy_C506_Ladder15_Sensor1_Chip2
nHits_RDO2_ARM5_APV22
Entries 200
<nHits> per event
Run Index

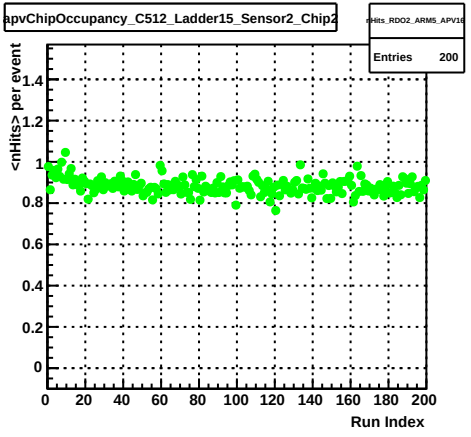

apvChipOccupancy_C507_Ladder15_Sensor1_Chip3
nHits_RDO2_ARM5_APV21
Entries 200
<nHits> per event
Run Index

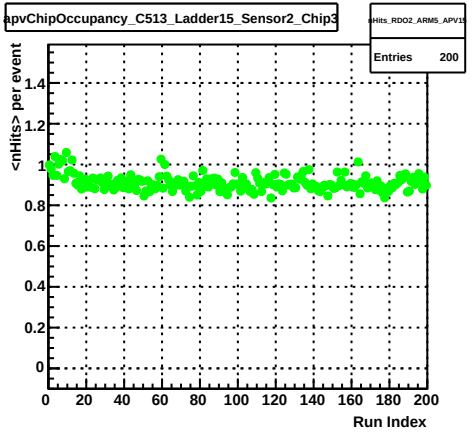

apvChipOccupancy_C508_Ladder15_Sensor1_Chip4
nHits_RDO2_ARM5_APV20
Entries 200
<nHits> per event
Run Index

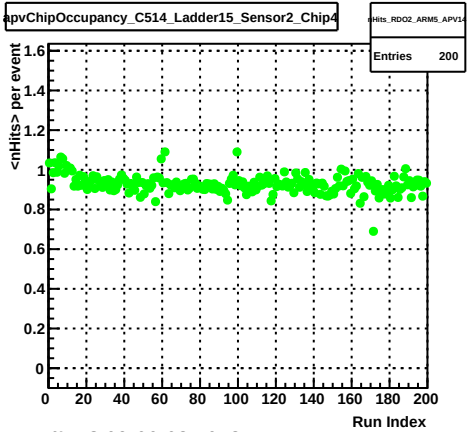

apvChipOccupancy_C509_Ladder15_Sensor1_Chip5
nHits_RDO2_ARM5_APV19
Entries 200
<nHits> per event
Run Index

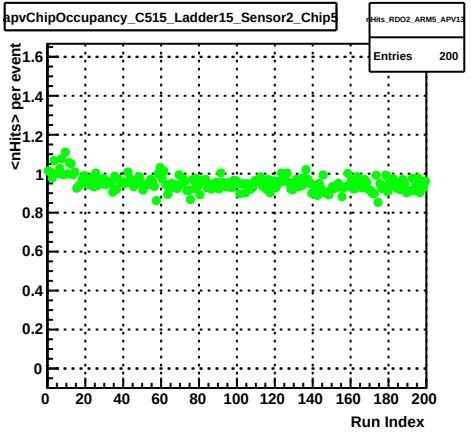

apvChipOccupancy_C510_Ladder15_Sensor1_Chip6
nHits_RDO2_ARM5_APV18
Entries 200
<nHits> per event
Run Index

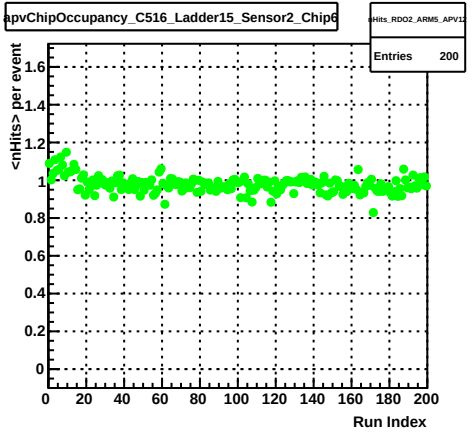

apvChipOccupancy_C511_Ladder15_Sensor2_Chip1
Entries 200
<nHits> per event
Run Index

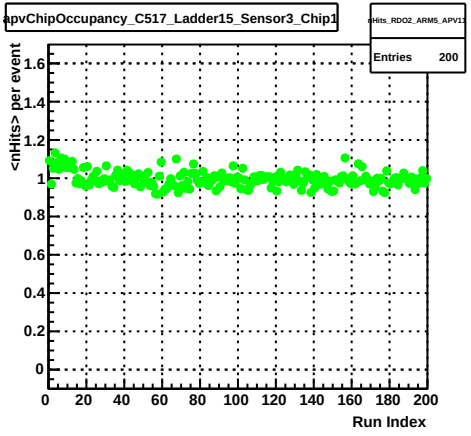

apvChipOccupancy_C512_Ladder15_Sensor2_Chip2
Entries 200
<nHits> per event
Run Index

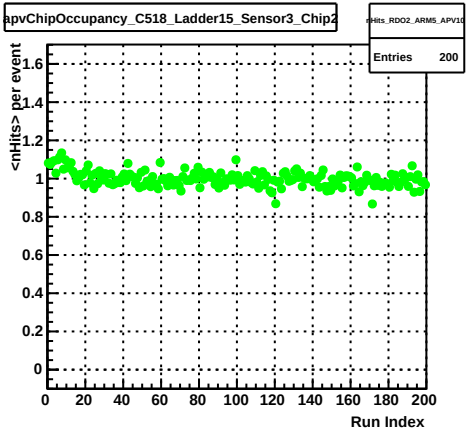

apvChipOccupancy_C513_Ladder15_Sensor2_Chip3
Entries 200
<nHits> per event
Run Index

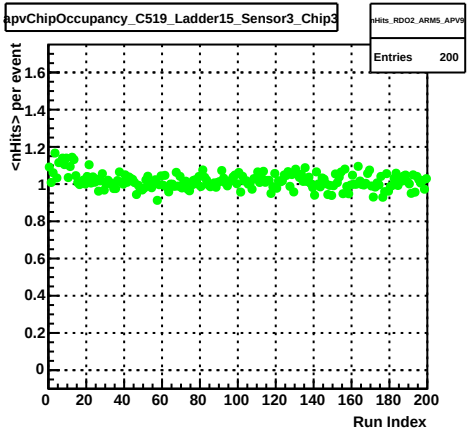

apvChipOccupancy_C514_Ladder15_Sensor2_Chip4
Entries 200
<nHits> per event
Run Index

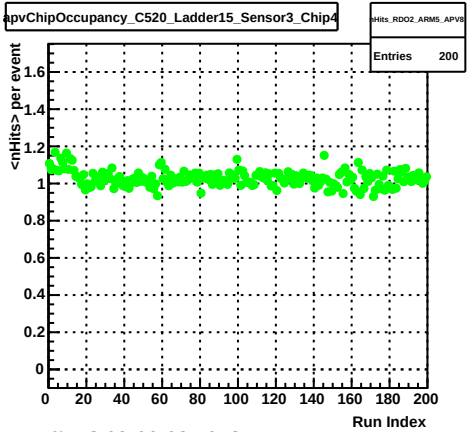

apvChipOccupancy_C515_Ladder15_Sensor2_Chip5
Entries 200
<nHits> per event
Run Index

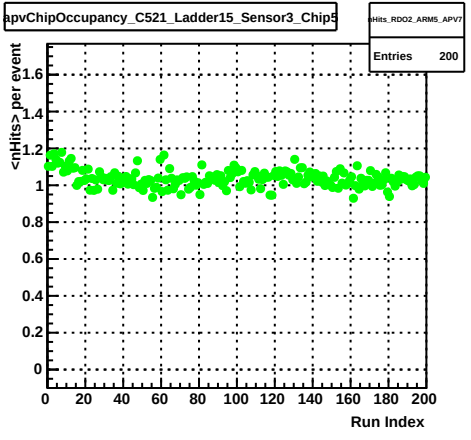

apvChipOccupancy_C516_Ladder15_Sensor2_Chip6
Entries 200
<nHits> per event
Run Index

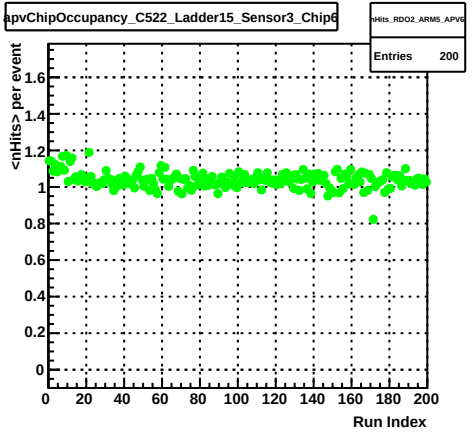

apvChipOccupancy_C517_Ladder15_Sensor3_Chip1
nHits_RDO2_ARM5_APV13
Entries 200
<nHits> per event
Run Index

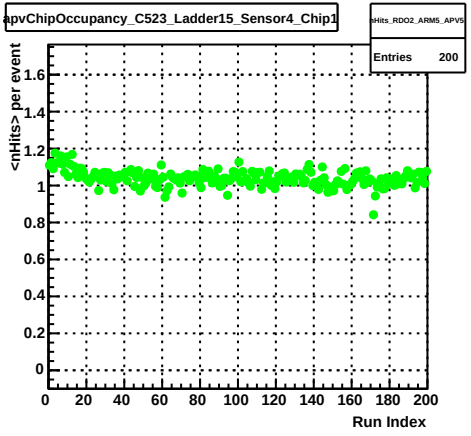

apvChipOccupancy_C518_Ladder15_Sensor3_Chip2
nHits_RDO2_ARM5_APV10
Entries 200
<nHits> per event
Run Index

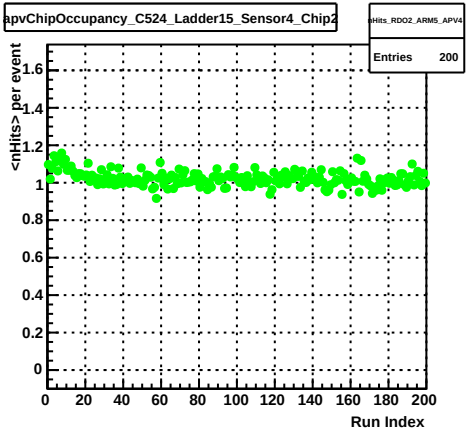

apvChipOccupancy_C519_Ladder15_Sensor3_Chip3
nHits_RDO2_ARM5_APV9
Entries 200
<nHits> per event
Run Index

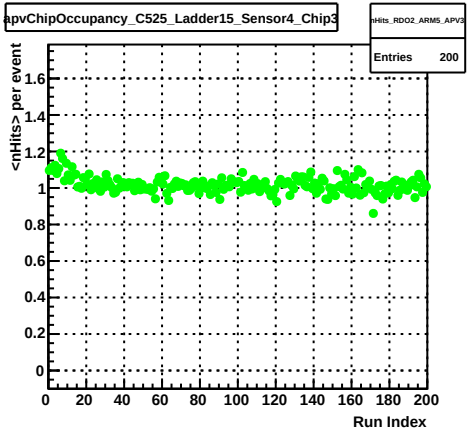

apvChipOccupancy_C520_Ladder15_Sensor3_Chip4
nHits_RDO2_ARM5_APV8
Entries 200
<nHits> per event
Run Index

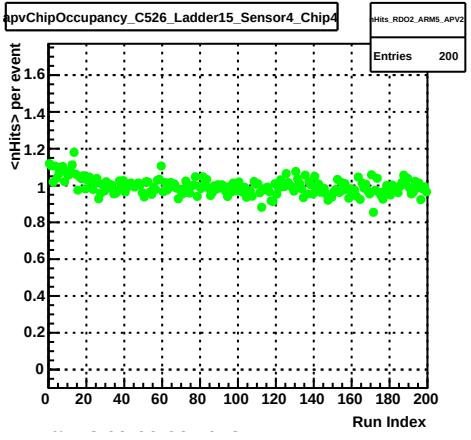

apvChipOccupancy_C521_Ladder15_Sensor3_Chip5
nHits_RDO2_ARM5_APV7
Entries 200
<nHits> per event
Run Index

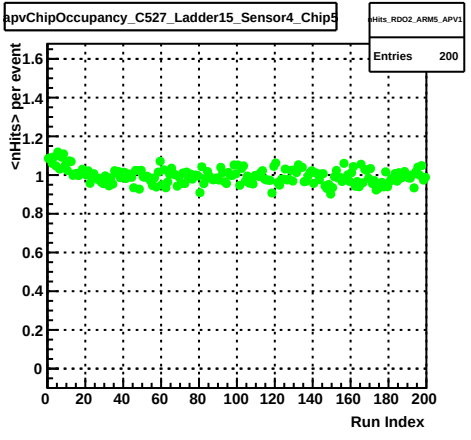

apvChipOccupancy_C522_Ladder15_Sensor3_Chip6
nHits_RDO2_ARM5_APV6
Entries 200
<nHits> per event
Run Index

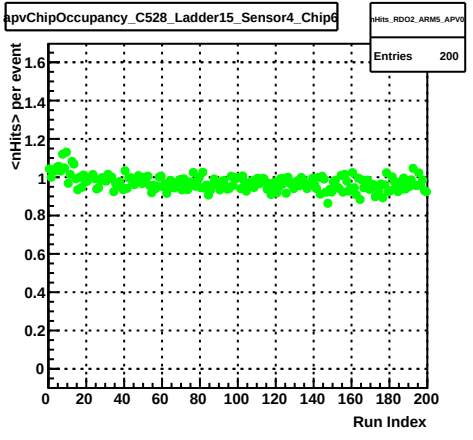

apvChipOccupancy_C523_Ladder15_Sensor4_Chip1
nHits_RDO2_ARM5_APV5
Entries 200
<nHits> per event
Run Index

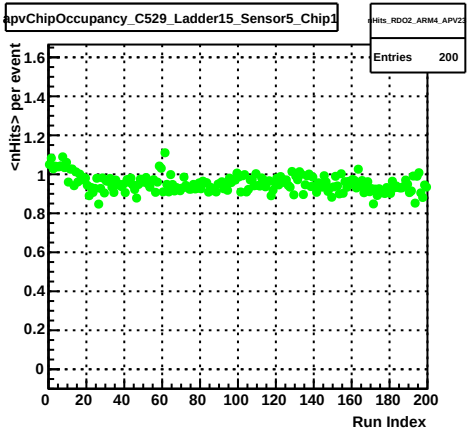

apvChipOccupancy_C524_Ladder15_Sensor4_Chip2
nHits_RDO2_ARM5_APV4
Entries 200
<nHits> per event
Run Index

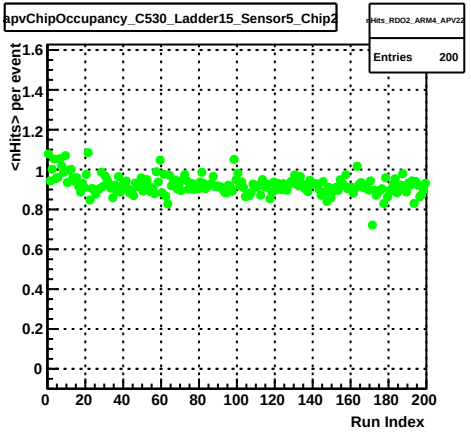

apvChipOccupancy_C525_Ladder15_Sensor4_Chip3
nHits_RDO2_ARM5_APV3
Entries 200
<nHits> per event
Run Index

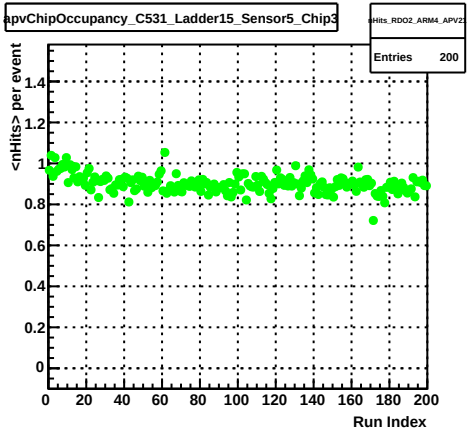

apvChipOccupancy_C526_Ladder15_Sensor4_Chip4
nHits_RDO2_ARM5_APV2
Entries 200
<nHits> per event
Run Index

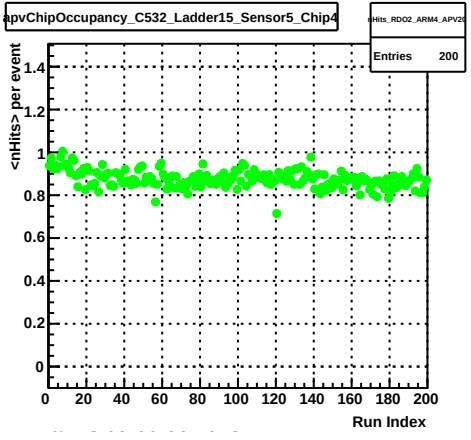

apvChipOccupancy_C527_Ladder15_Sensor4_Chip5
nHits_RDO2_ARM5_APV1
Entries 200
<nHits> per event
Run Index

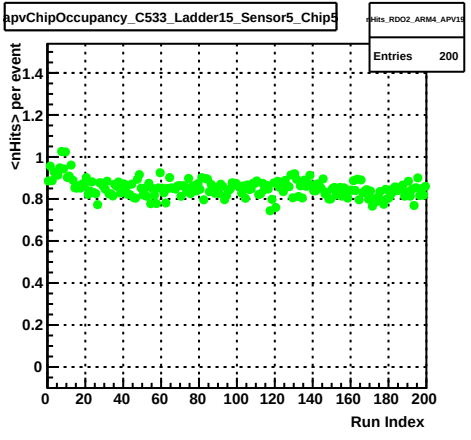

apvChipOccupancy_C528_Ladder15_Sensor4_Chip6
nHits_RDO2_ARM5_APV0
Entries 200
<nHits> per event
Run Index

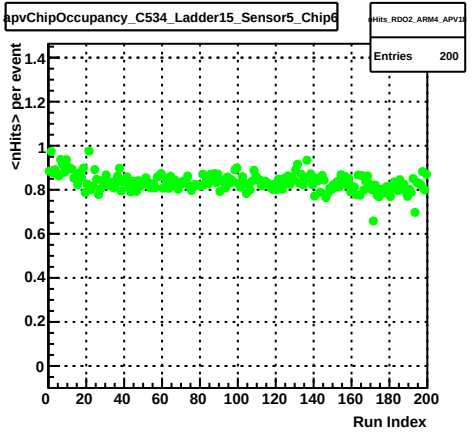

apvChipOccupancy_C529_Ladder15_Sensor5_Chip1
nHits_RDO2_ARM4_APV23
Entries 200
<nHits> per event
Run Index

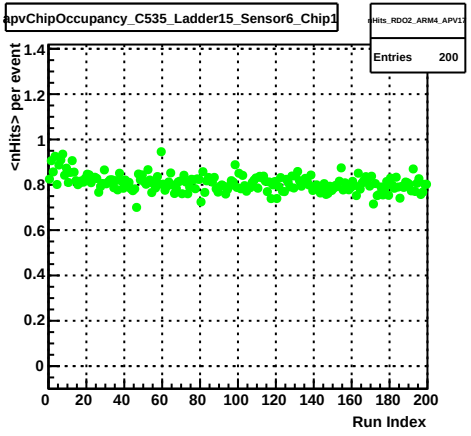

apvChipOccupancy_C530_Ladder15_Sensor5_Chip2
nHits_RDO2_ARM4_APV22
Entries 200
<nHits> per event
Run Index

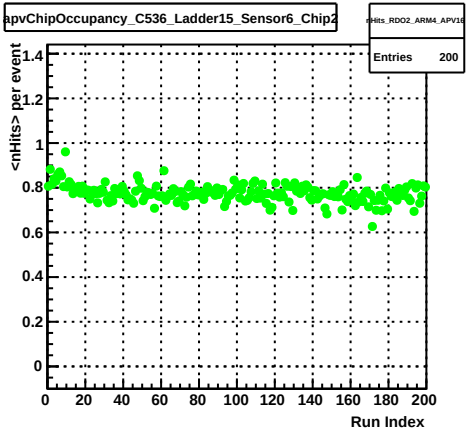

apvChipOccupancy_C531_Ladder15_Sensor5_Chip3
nHits_RDO2_ARM4_APV21
Entries 200
<nHits> per event
Run Index

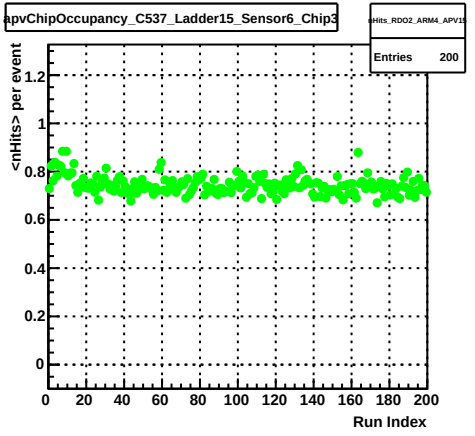

apvChipOccupancy_C532_Ladder15_Sensor5_Chip4
nHits_RDO2_ARM4_APV20
Entries 200
<nHits> per event
Run Index

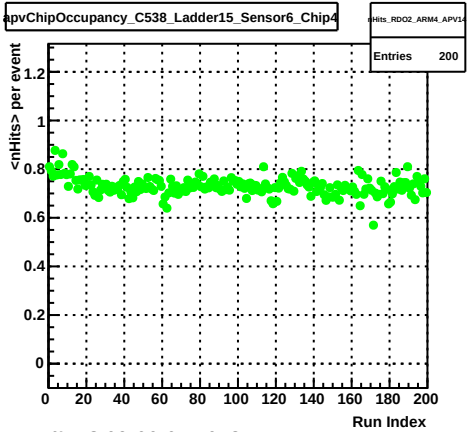

apvChipOccupancy_C533_Ladder15_Sensor5_Chip5
nHits_RDO2_ARM4_APV19
Entries 200
<nHits> per event
Run Index

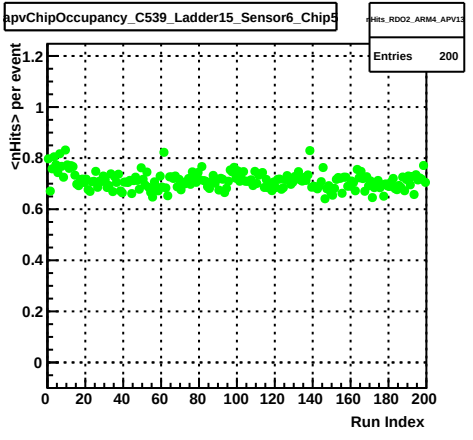

apvChipOccupancy_C534_Ladder15_Sensor5_Chip6
nHits_RDO2_ARM4_APV18
Entries 200
<nHits> per event
Run Index

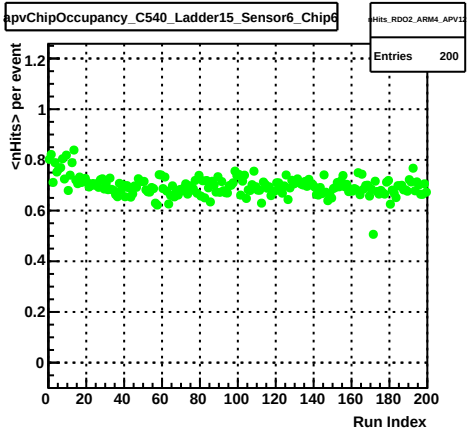

apvChipOccupancy_C535_Ladder15_Sensor6_Chip1
nHits_RDO2_ARM4_APV13
Entries 200
<nHits> per event
Run Index

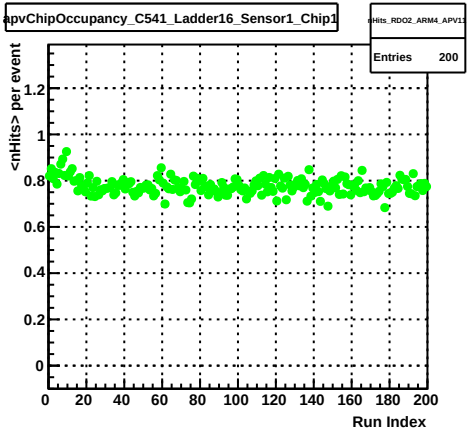

apvChipOccupancy_C536_Ladder15_Sensor6_Chip2
nHits_RDO2_ARM4_APV14
Entries 200
<nHits> per event
Run Index

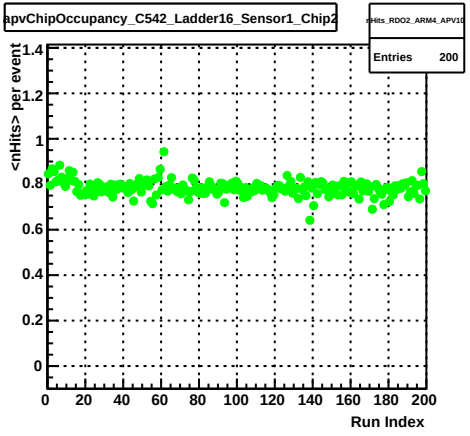

apvChipOccupancy_C537_Ladder15_Sensor6_Chip3
nHits_RDO2_ARM4_APV15
Entries 200
<nHits> per event
Run Index

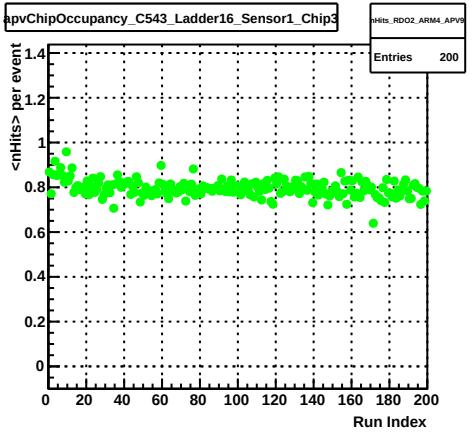

apvChipOccupancy_C538_Ladder15_Sensor6_Chip4
nHits_RDO2_ARM4_APV16
Entries 200
<nHits> per event
Run Index

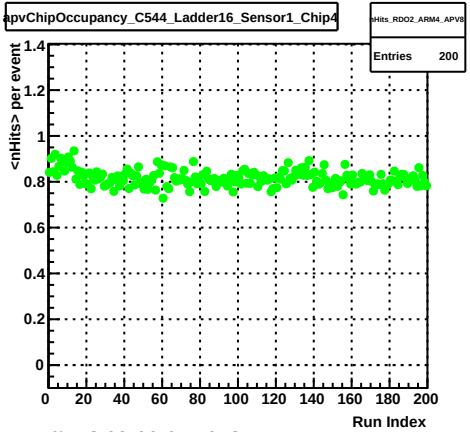

apvChipOccupancy_C539_Ladder15_Sensor6_Chip5
nHits_RDO2_ARM4_APV17
Entries 200
<nHits> per event
Run Index

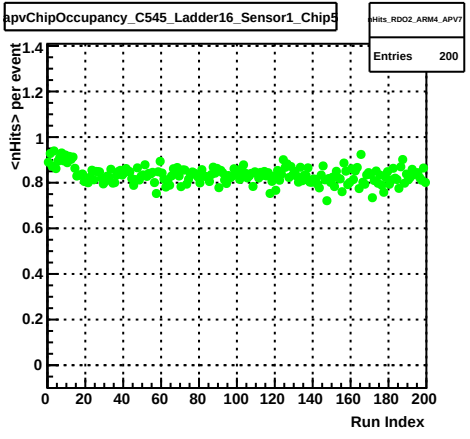

apvChipOccupancy_C540_Ladder15_Sensor6_Chip6
nHits_RDO2_ARM4_APV18
Entries 200
<nHits> per event
Run Index

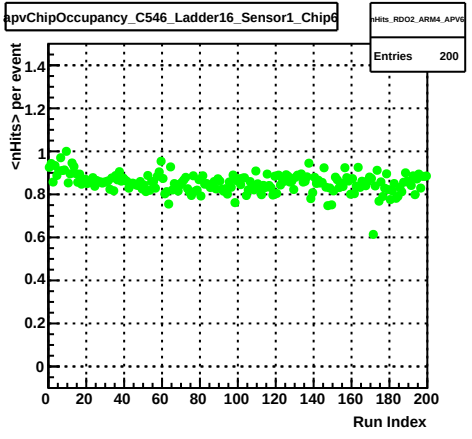

apvChipOccupancy_C541_Ladder16_Sensor1_Chip1
nHits_RDO2_ARM4_APV13
Entries 200
<nHits> per event
Run Index

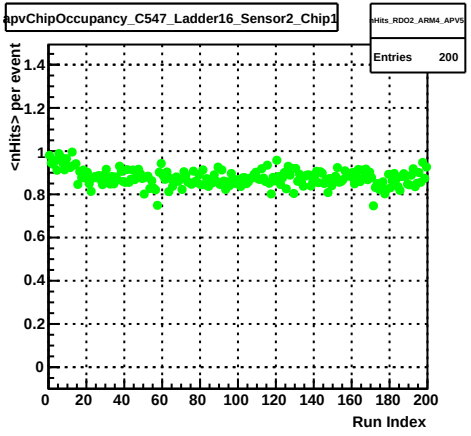

apvChipOccupancy_C542_Ladder16_Sensor1_Chip2
nHits_RDO2_ARM4_APV10
Entries 200
<nHits> per event
Run Index

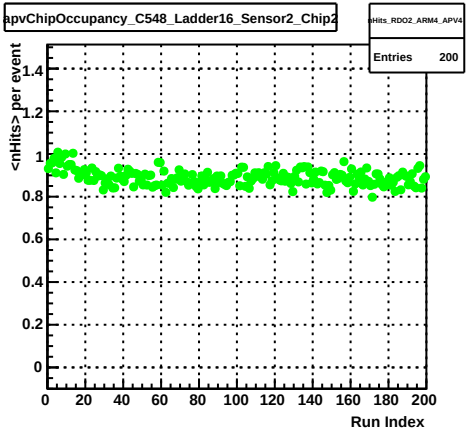

apvChipOccupancy_C543_Ladder16_Sensor1_Chip3
nHits_RDO2_ARM4_APV9
Entries 200
<nHits> per event
Run Index

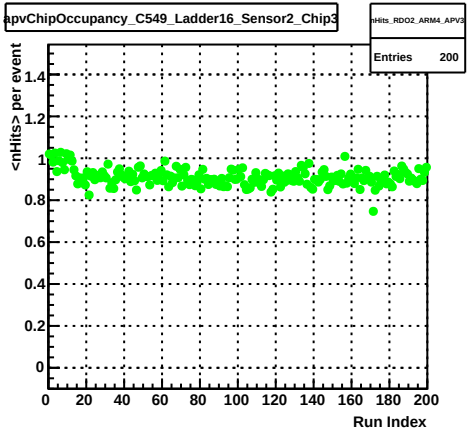

apvChipOccupancy_C544_Ladder16_Sensor1_Chip4
nHits_RDO2_ARM4_APV8
Entries 200
<nHits> per event
Run Index

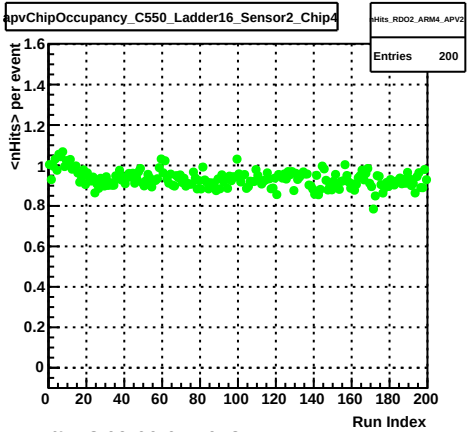

apvChipOccupancy_C545_Ladder16_Sensor1_Chip5
nHits_RDO2_ARM4_APV7
Entries 200
<nHits> per event
Run Index

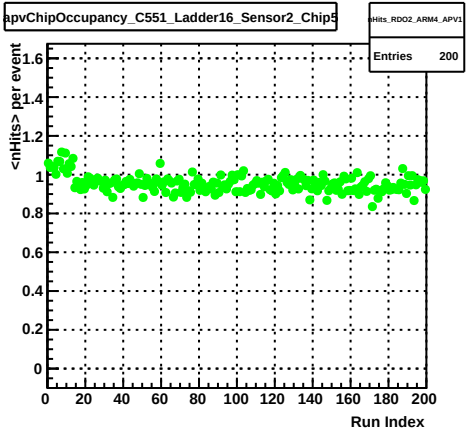

apvChipOccupancy_C546_Ladder16_Sensor1_Chip6
nHits_RDO2_ARM4_APV6
Entries 200
<nHits> per event
Run Index

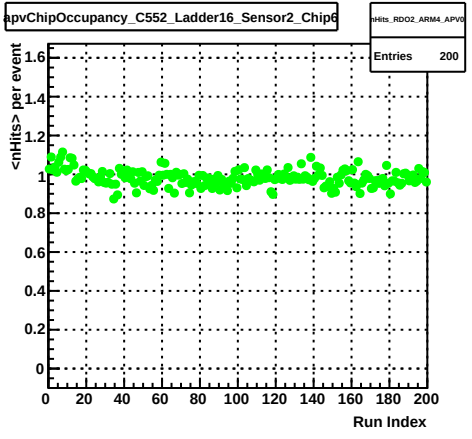

apvChipOccupancy_C547_Ladder16_Sensor2_Chip1
nHits_RDO2_ARM4_APV5
Entries 200
<nHits> per event
Run Index

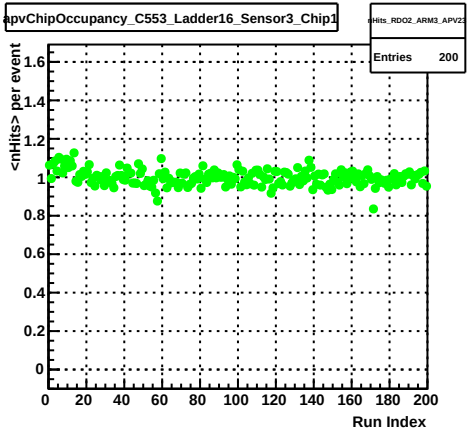

apvChipOccupancy_C548_Ladder16_Sensor2_Chip2
nHits_RDO2_ARM4_APV4
Entries 200
<nHits> per event
Run Index

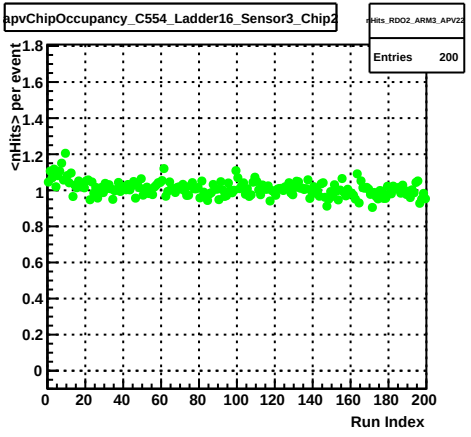

apvChipOccupancy_C549_Ladder16_Sensor2_Chip3
nHits_RDO2_ARM4_APV3
Entries 200
<nHits> per event
Run Index

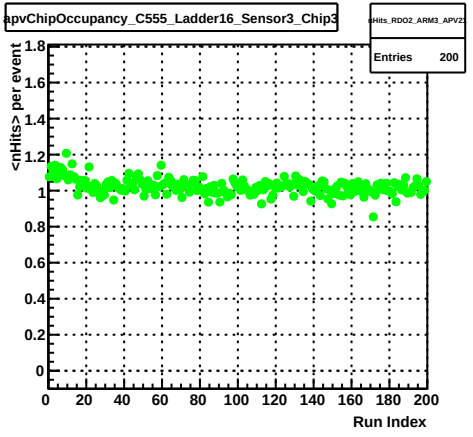

apvChipOccupancy_C550_Ladder16_Sensor2_Chip4
nHits_RDO2_ARM4_APV2
Entries 200
<nHits> per event
Run Index

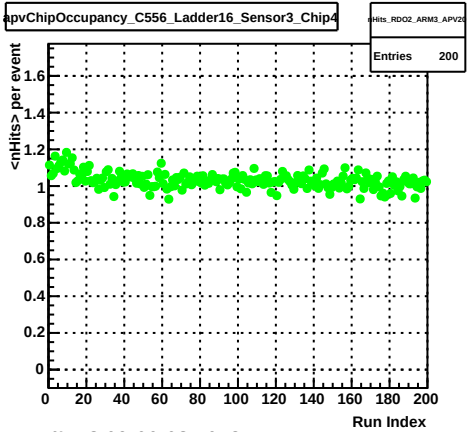

apvChipOccupancy_C551_Ladder16_Sensor2_Chip5
nHits_RDO2_ARM4_APV1
Entries 200
<nHits> per event
Run Index

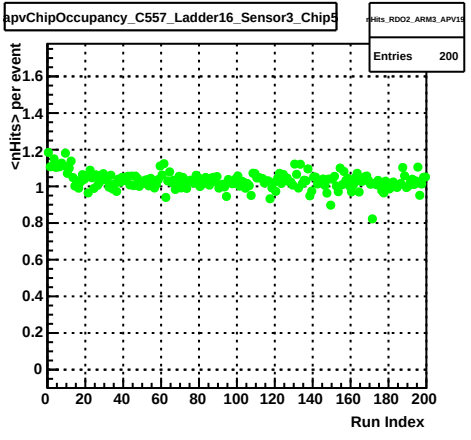

apvChipOccupancy_C552_Ladder16_Sensor2_Chip6
nHits_RDO2_ARM4_APV0
Entries 200
<nHits> per event
Run Index

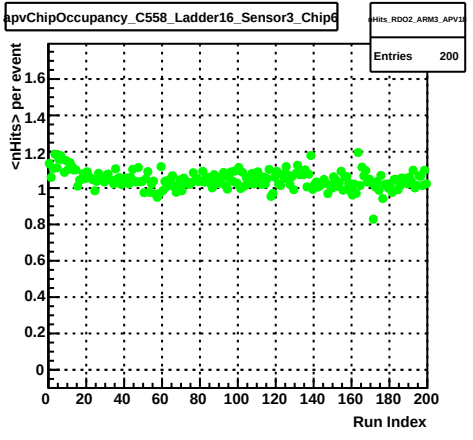

apvChipOccupancy_C553_Ladder16_Sensor3_Chip1
nHits_RDO2_ARM3_APV23
Entries 200
<nHits> per event
Run Index

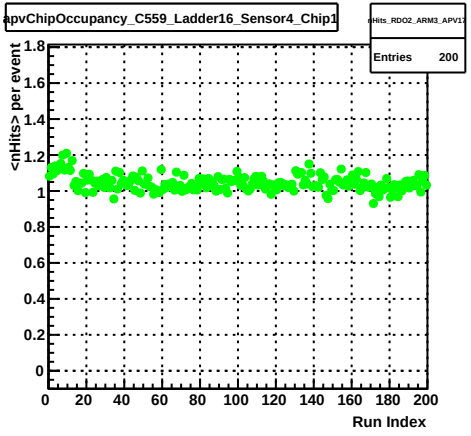

apvChipOccupancy_C554_Ladder16_Sensor3_Chip2
nHits_RDO2_ARM3_APV22
Entries 200
<nHits> per event
Run Index

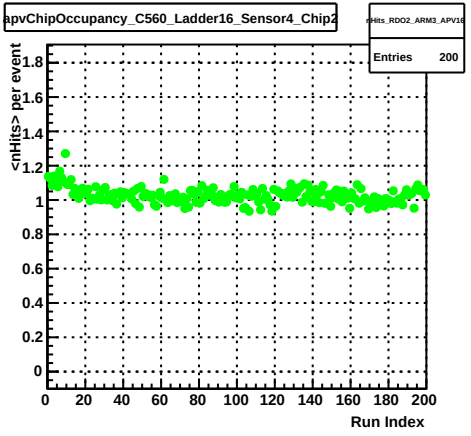

apvChipOccupancy_C555_Ladder16_Sensor3_Chip3
nHits_RDO2_ARM3_APV21
Entries 200
<nHits> per event
Run Index

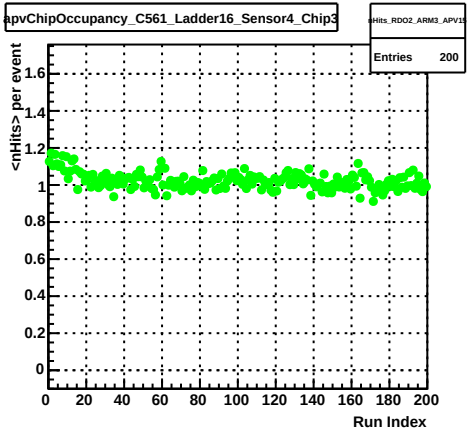

apvChipOccupancy_C556_Ladder16_Sensor3_Chip4
nHits_RDO2_ARM3_APV20
Entries 200
<nHits> per event
Run Index

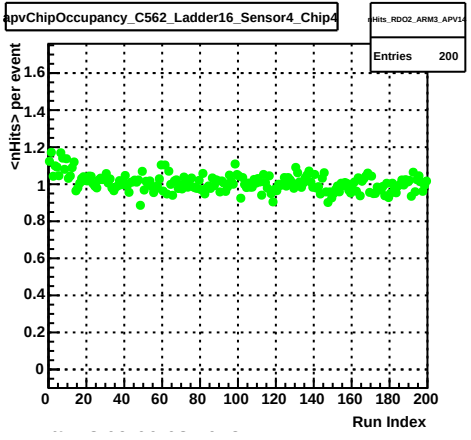

apvChipOccupancy_C557_Ladder16_Sensor3_Chip5
nHits_RDO2_ARM3_APV19
Entries 200
<nHits> per event
Run Index

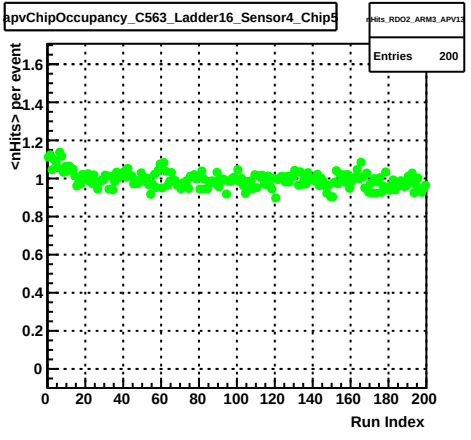

apvChipOccupancy_C558_Ladder16_Sensor3_Chip6
nHits_RDO2_ARM3_APV18
Entries 200
<nHits> per event
Run Index

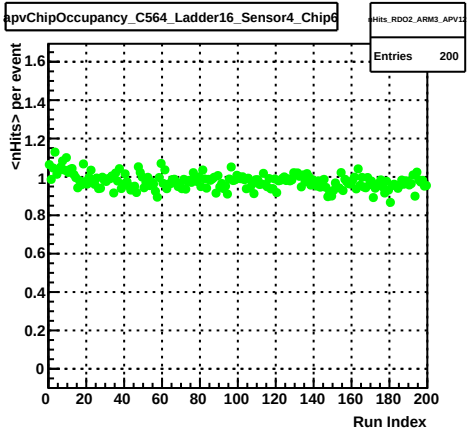

apvChipOccupancy_C559_Ladder16_Sensor4_Chip1
nHits_RDO2_ARM3_APV13
Entries 200
<nHits> per event
Run Index

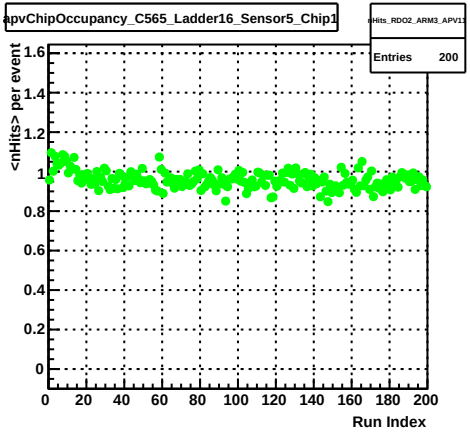

apvChipOccupancy_C560_Ladder16_Sensor4_Chip2
nHits_RDO2_ARM3_APV14
Entries 200
<nHits> per event
Run Index

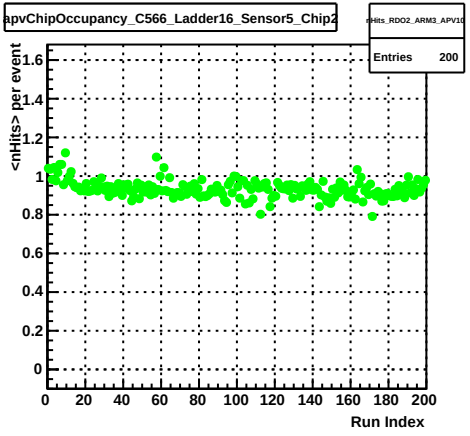

apvChipOccupancy_C561_Ladder16_Sensor4_Chip3
nHits_RDO2_ARM3_APV15
Entries 200
<nHits> per event
Run Index

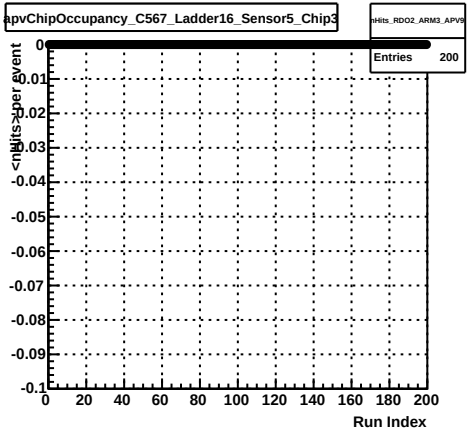

apvChipOccupancy_C562_Ladder16_Sensor4_Chip4
nHits_RDO2_ARM3_APV16
Entries 200
<nHits> per event
Run Index

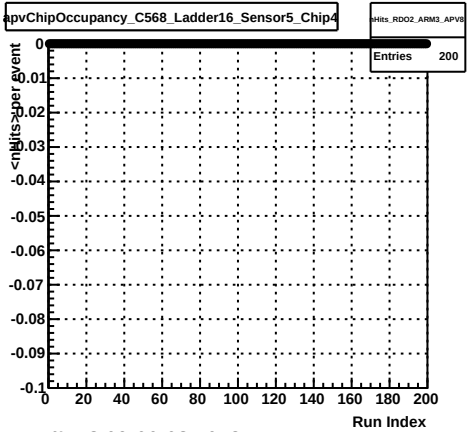

apvChipOccupancy_C563_Ladder16_Sensor4_Chip5
nHits_RDO2_ARM3_APV17
Entries 200
<nHits> per event
Run Index

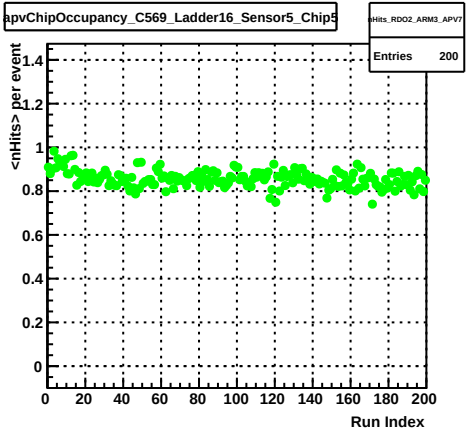

apvChipOccupancy_C564_Ladder16_Sensor4_Chip6
nHits_RDO2_ARM3_APV18
Entries 200
<nHits> per event
Run Index

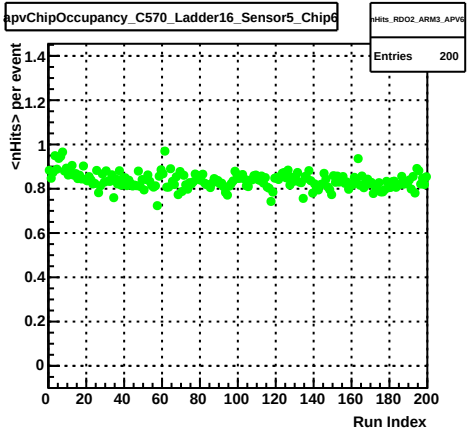

apvChipOccupancy_C565_Ladder16_Sensor5_Chip1
nHits_RDO2_ARM3_APV13
Entries 200
<nHits> per event
Run Index

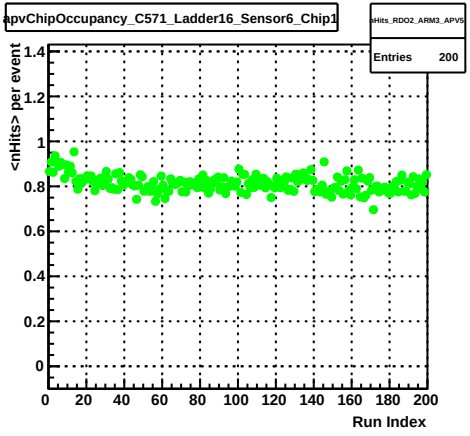

apvChipOccupancy_C566_Ladder16_Sensor5_Chip2
nHits_RDO2_ARM3_APV10
Entries 200
<nHits> per event
Run Index

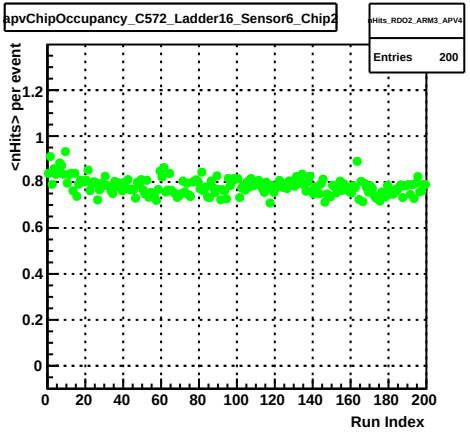

apvChipOccupancy_C567_Ladder16_Sensor5_Chip3
nHits_RDO2_ARM3_APV9
Entries 200
<nHits> per event
Run Index

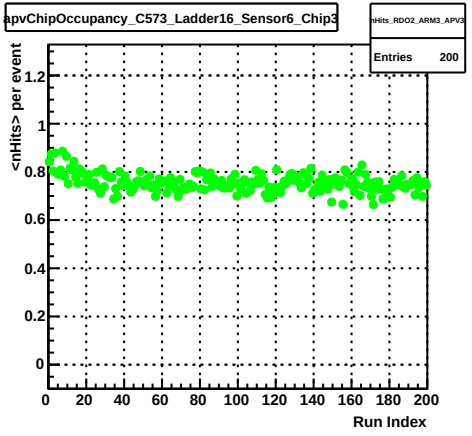

apvChipOccupancy_C568_Ladder16_Sensor5_Chip4
nHits_RDO2_ARM3_APV8
Entries 200
<nHits> per event
Run Index

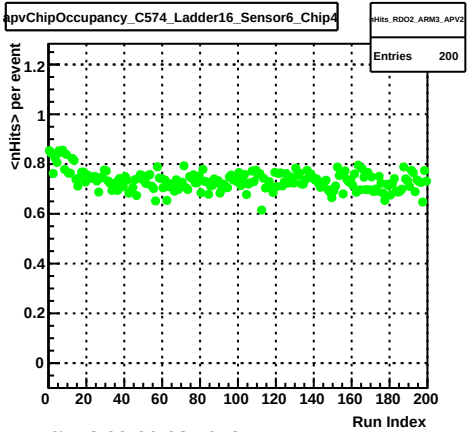

apvChipOccupancy_C569_Ladder16_Sensor5_Chip5
nHits_RDO2_ARM3_APV7
Entries 200
<nHits> per event
Run Index

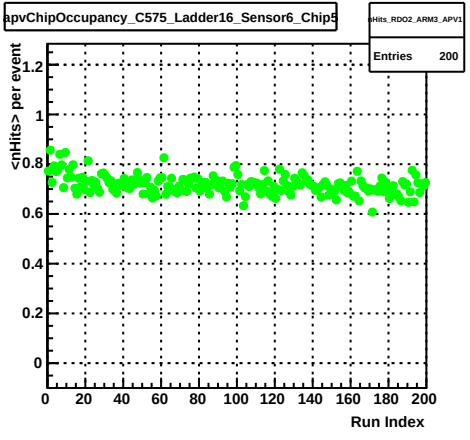

apvChipOccupancy_C570_Ladder16_Sensor5_Chip6
nHits_RDO2_ARM3_APV6
Entries 200
<nHits> per event
Run Index

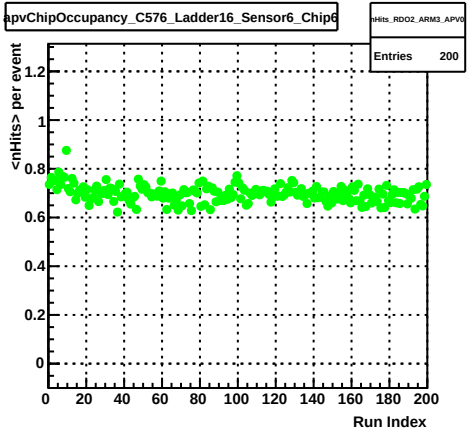

apvChipOccupancy_C571_Ladder16_Sensor6_Chip1
nHits_RDO2_ARM3_APV5
Entries 200
<nHits> per event
Run Index

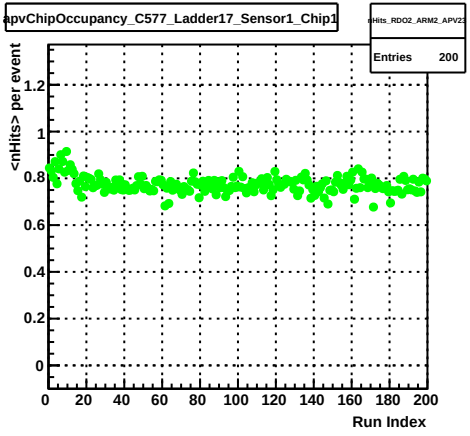

apvChipOccupancy_C572_Ladder16_Sensor6_Chip2
nHits_RDO2_ARM3_APV4
Entries 200
<nHits> per event
Run Index

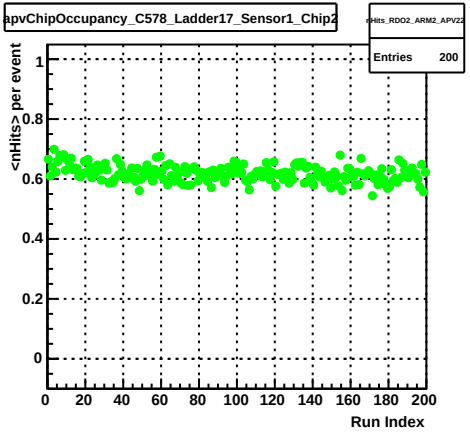

apvChipOccupancy_C573_Ladder16_Sensor6_Chip3
nHits_RDO2_ARM3_APV3
Entries 200
<nHits> per event
Run Index

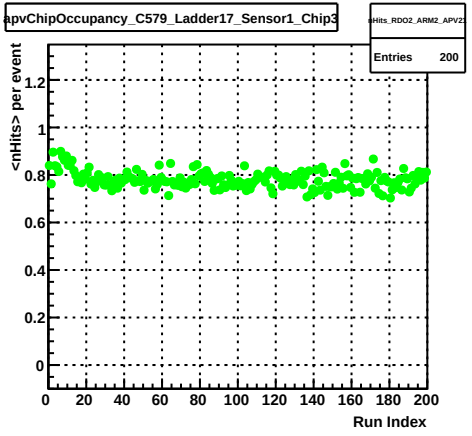

apvChipOccupancy_C574_Ladder16_Sensor6_Chip4
nHits_RDO2_ARM3_APV2
Entries 200
<nHits> per event
Run Index

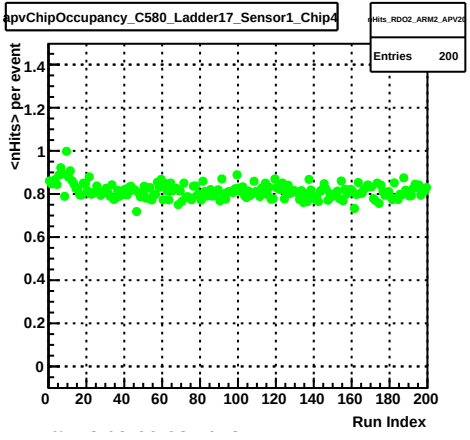

apvChipOccupancy_C575_Ladder16_Sensor6_Chip5
nHits_RDO2_ARM3_APV1
Entries 200
<nHits> per event
Run Index

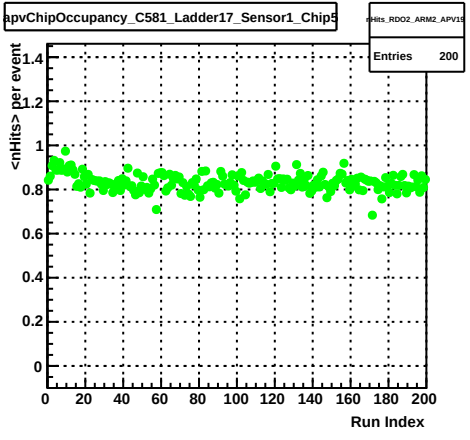

apvChipOccupancy_C576_Ladder16_Sensor6_Chip6
nHits_RDO2_ARM3_APV0
Entries 200
<nHits> per event
Run Index

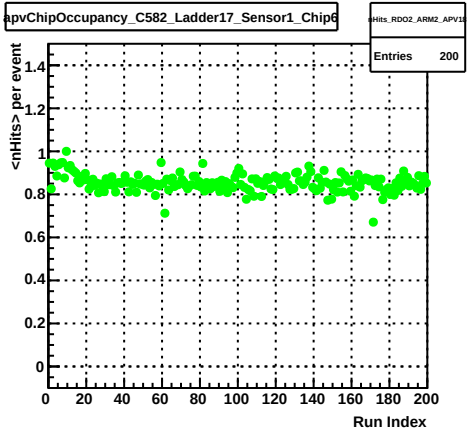

apvChipOccupancy_C577_Ladder17_Sensor1_Chip1
Entries 200
<nHits> per event
Run Index

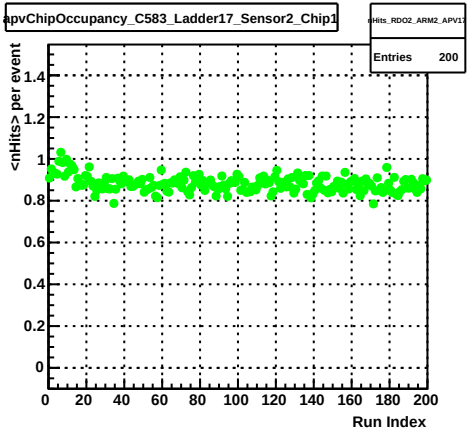

apvChipOccupancy_C578_Ladder17_Sensor1_Chip2
Entries 200
<nHits> per event
Run Index

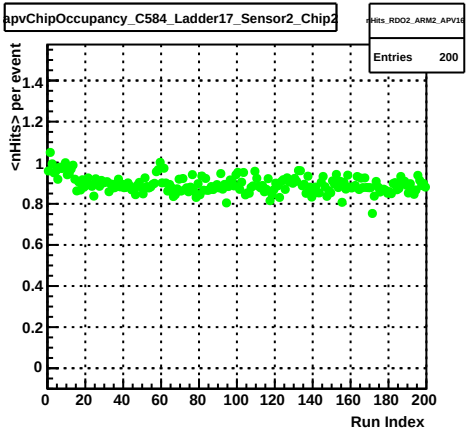

apvChipOccupancy_C579_Ladder17_Sensor1_Chip3
Entries 200
<nHits> per event
Run Index

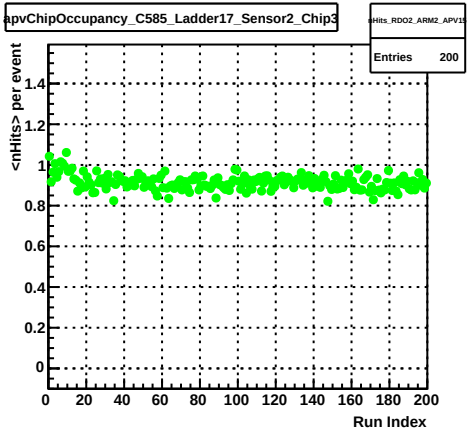

apvChipOccupancy_C580_Ladder17_Sensor1_Chip4
Entries 200
<nHits> per event
Run Index

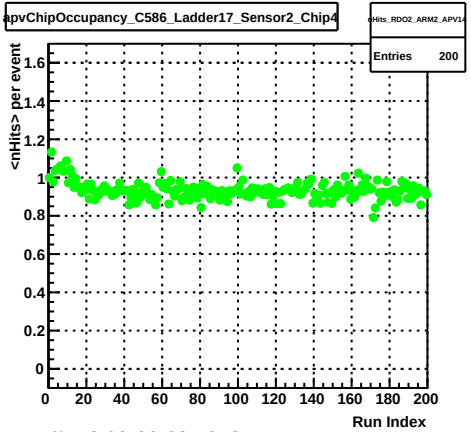

apvChipOccupancy_C581_Ladder17_Sensor1_Chip5
Entries 200
<nHits> per event
Run Index

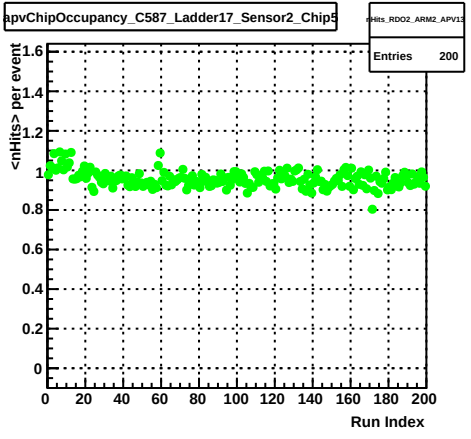

apvChipOccupancy_C582_Ladder17_Sensor1_Chip6
Entries 200
<nHits> per event
Run Index

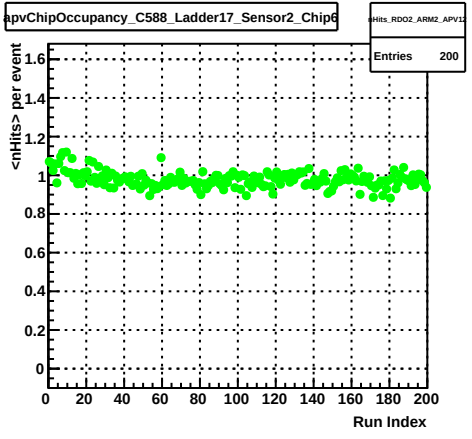

apvChipOccupancy_C583_Ladder17_Sensor2_Chip1
Entries 200
<nHits> per event
Run Index

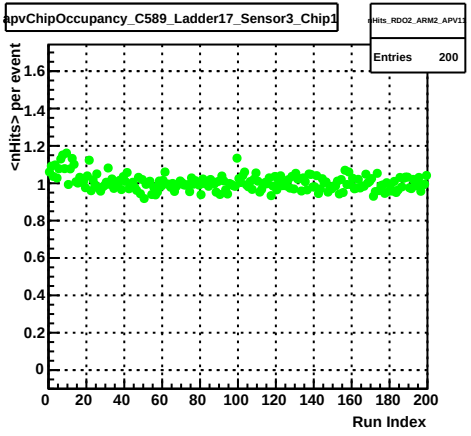

apvChipOccupancy_C584_Ladder17_Sensor2_Chip2
Entries 200
<nHits> per event
Run Index

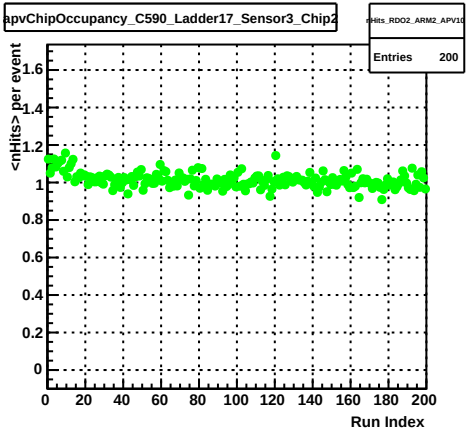

apvChipOccupancy_C585_Ladder17_Sensor2_Chip3
Entries 200
<nHits> per event
Run Index

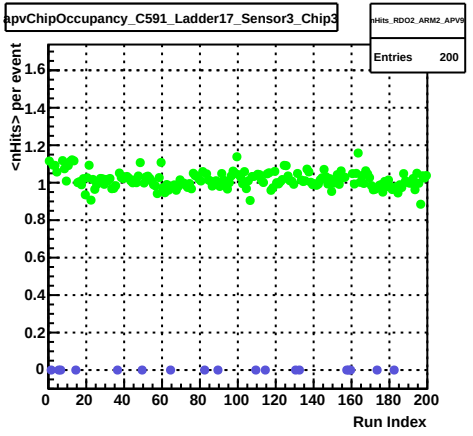

apvChipOccupancy_C586_Ladder17_Sensor2_Chip4
Entries 200
<nHits> per event
Run Index

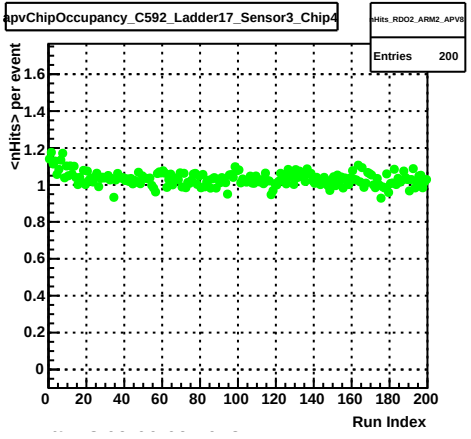

apvChipOccupancy_C587_Ladder17_Sensor2_Chip5
Entries 200
<nHits> per event
Run Index

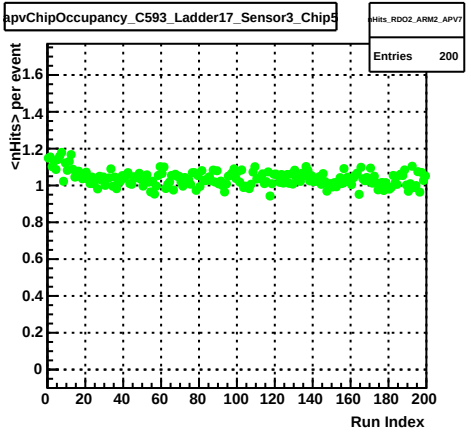

apvChipOccupancy_C588_Ladder17_Sensor2_Chip6
Entries 200
<nHits> per event
Run Index

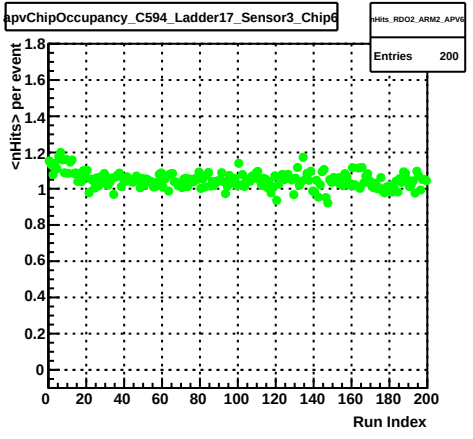

apvChipOccupancy_C589_Ladder17_Sensor3_Chip1
nHits_RDO2_ARM2_APV13
Entries 200
<nHits> per event
Run Index

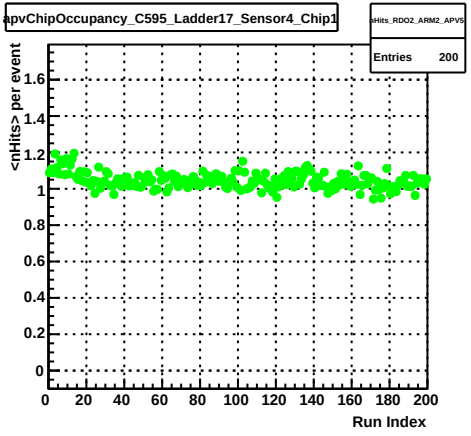

apvChipOccupancy_C590_Ladder17_Sensor3_Chip2
nHits_RDO2_ARM2_APV10
Entries 200
<nHits> per event
Run Index

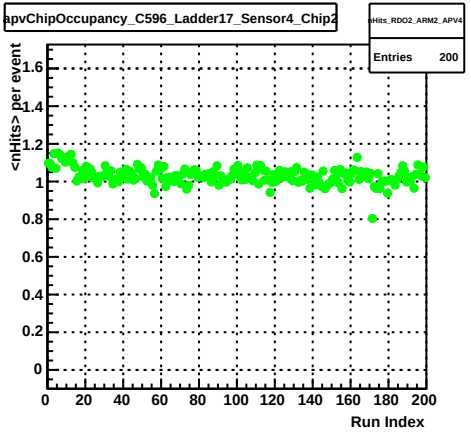

apvChipOccupancy_C591_Ladder17_Sensor3_Chip3
nHits_RDO2_ARM2_APV9
Entries 200
<nHits> per event
Run Index

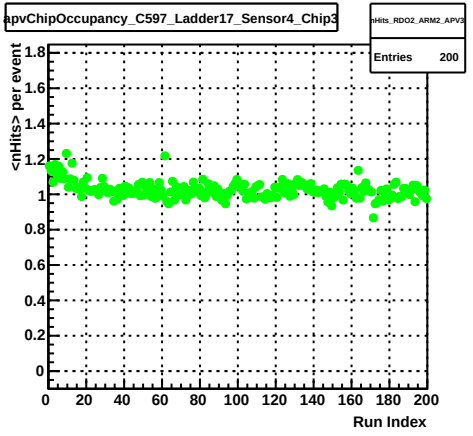

apvChipOccupancy_C592_Ladder17_Sensor3_Chip4
nHits_RDO2_ARM2_APV8
Entries 200
<nHits> per event
Run Index

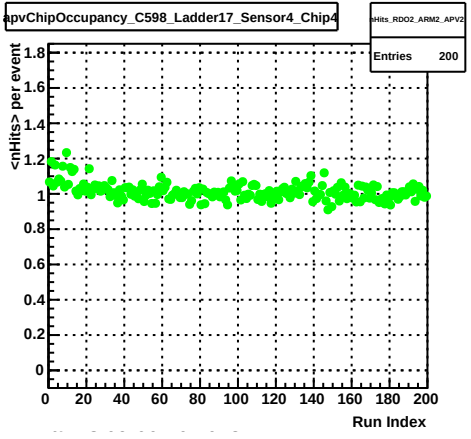

apvChipOccupancy_C593_Ladder17_Sensor3_Chip5
nHits_RDO2_ARM2_APV7
Entries 200
<nHits> per event
Run Index

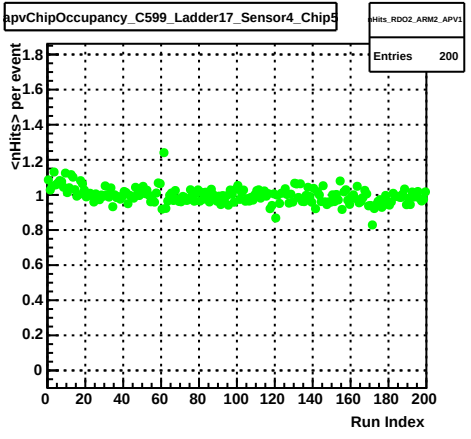

apvChipOccupancy_C594_Ladder17_Sensor3_Chip6
nHits_RDO2_ARM2_APV6
Entries 200
<nHits> per event
Run Index

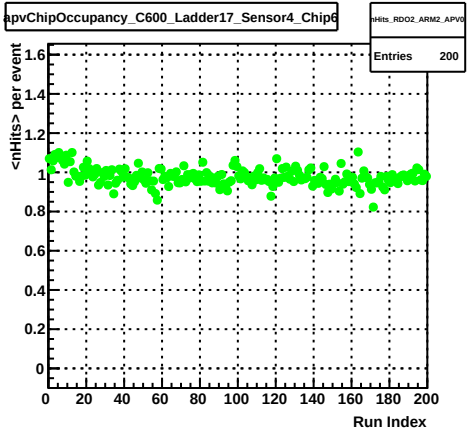

apvChipOccupancy_C595_Ladder17_Sensor4_Chip1
nHits_RDO2_ARM2_APV5
Entries 200
<nHits> per event
Run Index

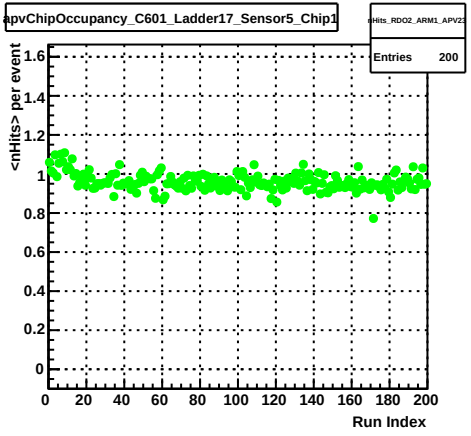

apvChipOccupancy_C596_Ladder17_Sensor4_Chip2
nHits_RDO2_ARM2_APV4
Entries 200
<nHits> per event
Run Index

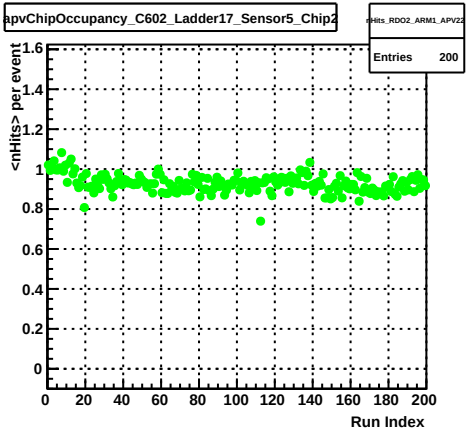

apvChipOccupancy_C597_Ladder17_Sensor4_Chip3
nHits_RDO2_ARM2_APV3
Entries 200
<nHits> per event
Run Index

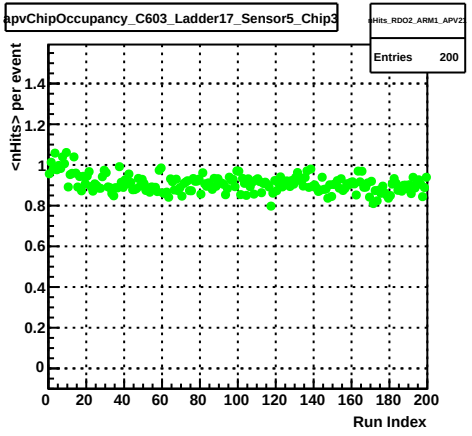

apvChipOccupancy_C598_Ladder17_Sensor4_Chip4
nHits_RDO2_ARM2_APV2
Entries 200
<nHits> per event
Run Index

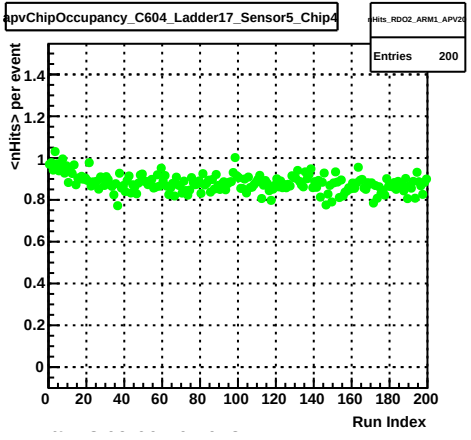

apvChipOccupancy_C599_Ladder17_Sensor4_Chip5
nHits_RDO2_ARM2_APV1
Entries 200
<nHits> per event
Run Index

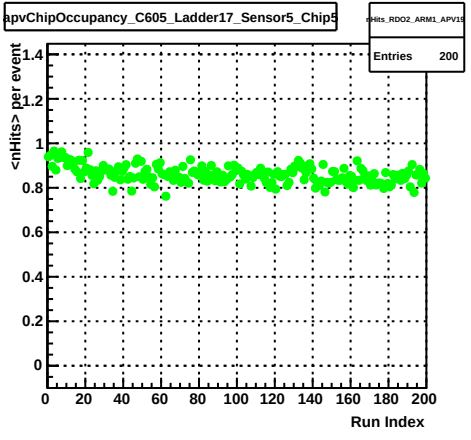

apvChipOccupancy_C600_Ladder17_Sensor4_Chip6
nHits_RDO2_ARM2_APV0
Entries 200
<nHits> per event
Run Index

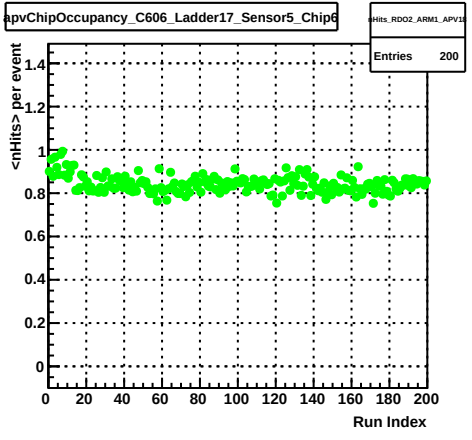

apvChipOccupancy_C601_Ladder17_Sensor5_Chip1
nHits_RDO2_ARM1_APV23
Entries 200
<nHits> per event
Run Index

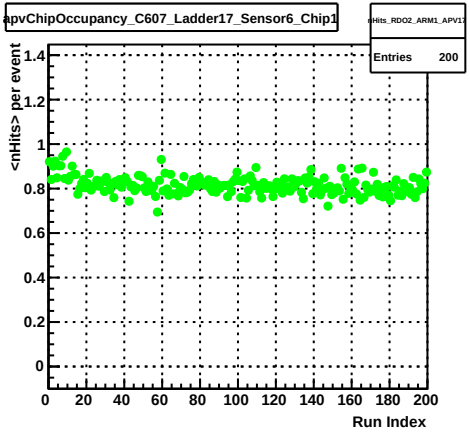

apvChipOccupancy_C602_Ladder17_Sensor5_Chip2
nHits_RDO2_ARM1_APV22
Entries 200
<nHits> per event
Run Index

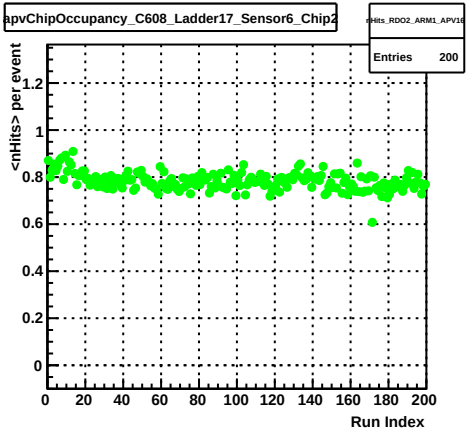

apvChipOccupancy_C603_Ladder17_Sensor5_Chip3
nHits_RDO2_ARM1_APV21
Entries 200
<nHits> per event
Run Index

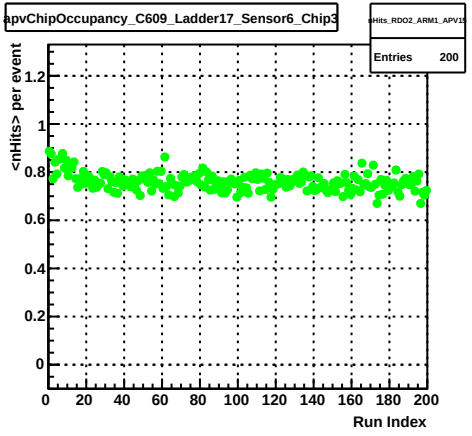

apvChipOccupancy_C604_Ladder17_Sensor5_Chip4
nHits_RDO2_ARM1_APV20
Entries 200
<nHits> per event
Run Index

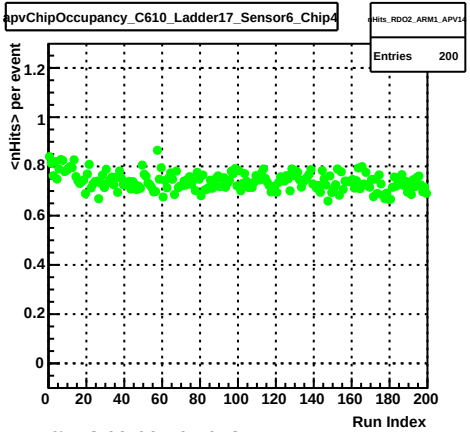

apvChipOccupancy_C605_Ladder17_Sensor5_Chip5
nHits_RDO2_ARM1_APV19
Entries 200
<nHits> per event
Run Index

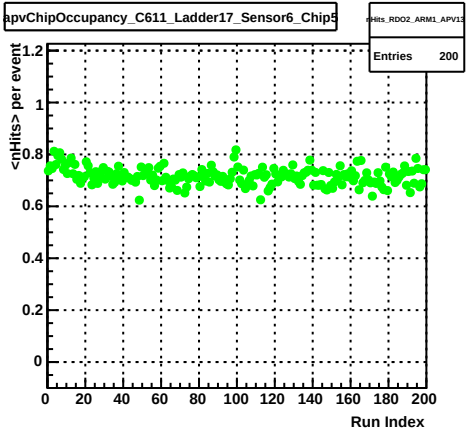

apvChipOccupancy_C606_Ladder17_Sensor5_Chip6
nHits_RDO2_ARM1_APV18
Entries 200
<nHits> per event
Run Index

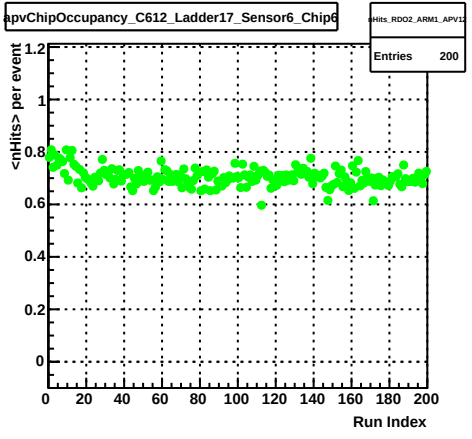

apvChipOccupancy_C607_Ladder17_Sensor6_Chip1
Entries 200
<nHits> per event
Run Index

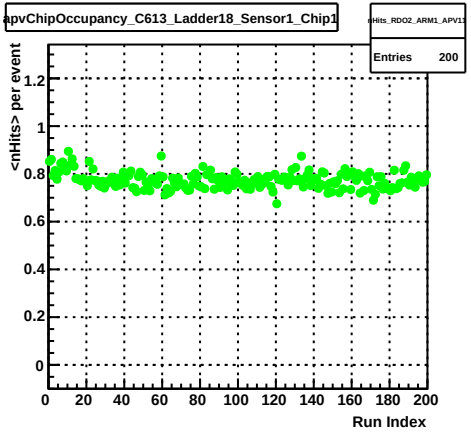

apvChipOccupancy_C608_Ladder17_Sensor6_Chip2
Entries 200
<nHits> per event
Run Index

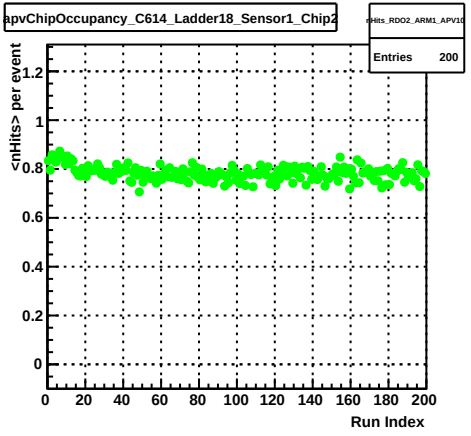

apvChipOccupancy_C609_Ladder17_Sensor6_Chip3
Entries 200
<nHits> per event
Run Index

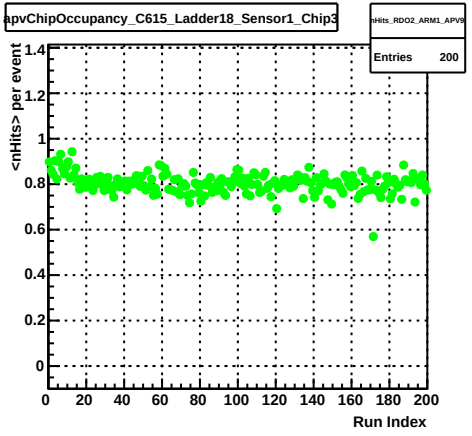

apvChipOccupancy_C610_Ladder17_Sensor6_Chip4
Entries 200
<nHits> per event
Run Index

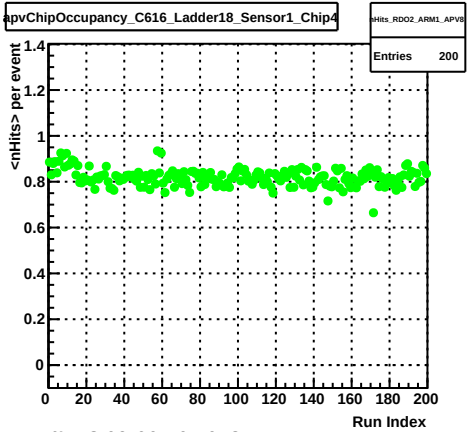

apvChipOccupancy_C611_Ladder17_Sensor6_Chip5
Entries 200
<nHits> per event
Run Index

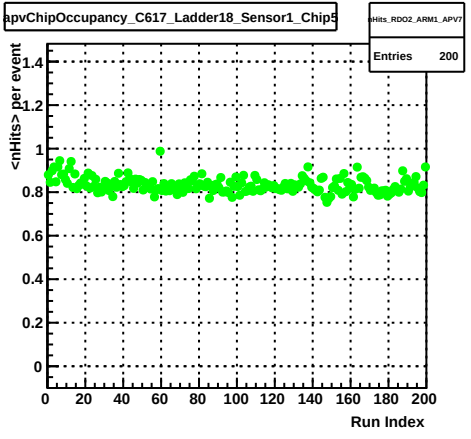

apvChipOccupancy_C612_Ladder17_Sensor6_Chip6
Entries 200
<nHits> per event
Run Index

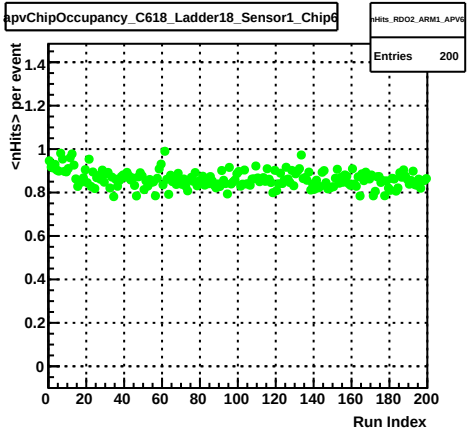

apvChipOccupancy_C613_Ladder18_Sensor1_Chip1
Entries 200
<nHits> per event
Run Index

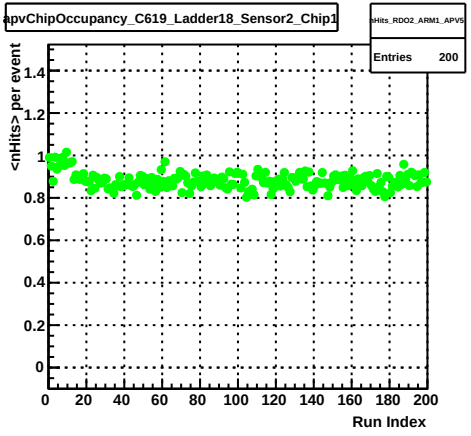

apvChipOccupancy_C614_Ladder18_Sensor1_Chip2
Entries 200
<nHits> per event
Run Index

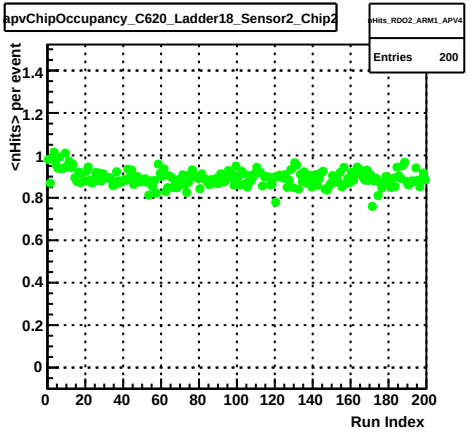

apvChipOccupancy_C615_Ladder18_Sensor1_Chip3
Entries 200
<nHits> per event
Run Index

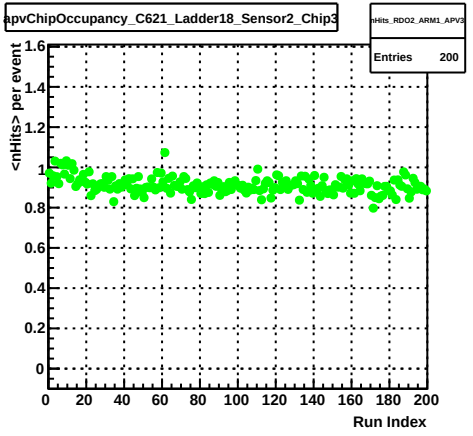

apvChipOccupancy_C616_Ladder18_Sensor1_Chip4
Entries 200
<nHits> per event
Run Index

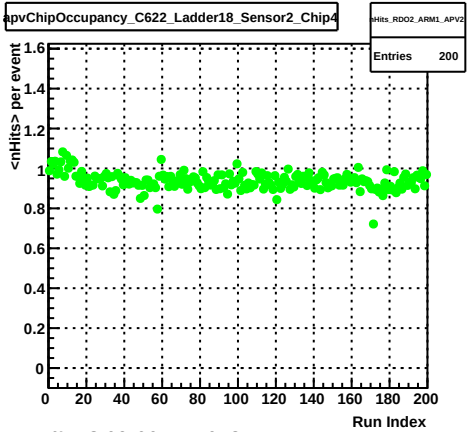

apvChipOccupancy_C617_Ladder18_Sensor1_Chip5
Entries 200
<nHits> per event
Run Index

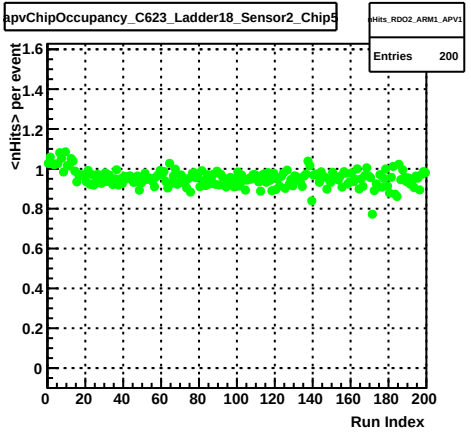

apvChipOccupancy_C618_Ladder18_Sensor1_Chip6
Entries 200
<nHits> per event
Run Index

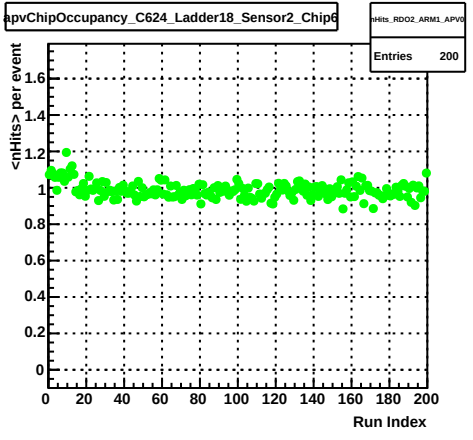

apvChipOccupancy_C619_Ladder18_Sensor2_Chip1
nHits_RDO2_ARM1_APV5
Entries 200
<nHits> per event
Run Index

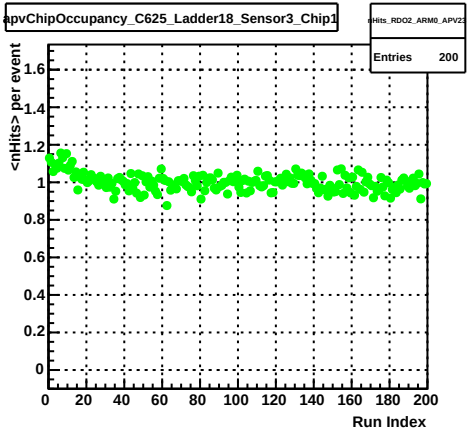

apvChipOccupancy_C620_Ladder18_Sensor2_Chip2
nHits_RDO2_ARM1_APV4
Entries 200
<nHits> per event
Run Index

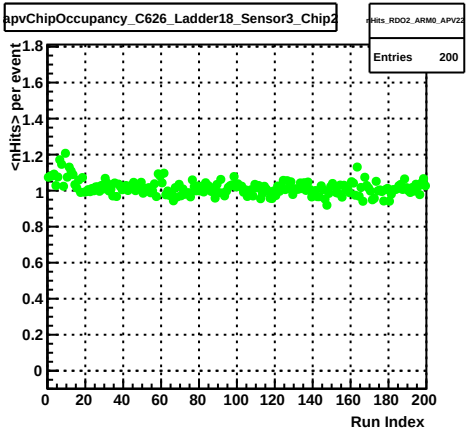

apvChipOccupancy_C621_Ladder18_Sensor2_Chip3
nHits_RDO2_ARM1_APV3
Entries 200
<nHits> per event
Run Index

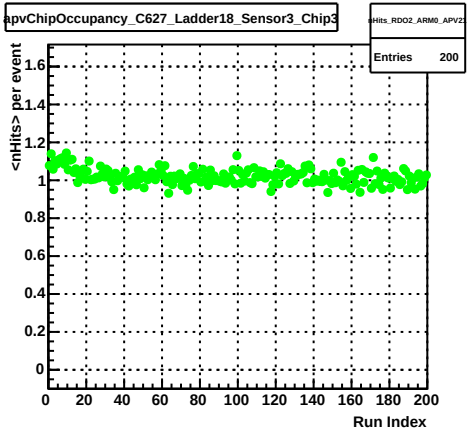

apvChipOccupancy_C622_Ladder18_Sensor2_Chip4
nHits_RDO2_ARM1_APV2
Entries 200
<nHits> per event
Run Index

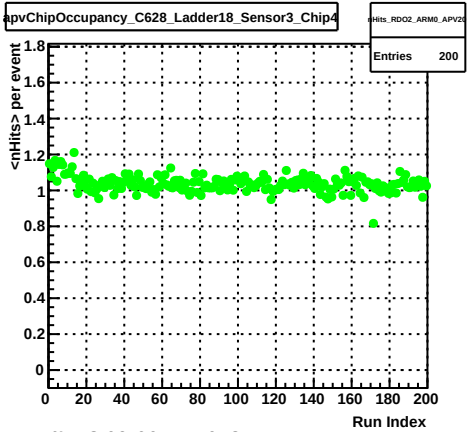

apvChipOccupancy_C623_Ladder18_Sensor2_Chip5
nHits_RDO2_ARM1_APV1
Entries 200
<nHits> per event
Run Index

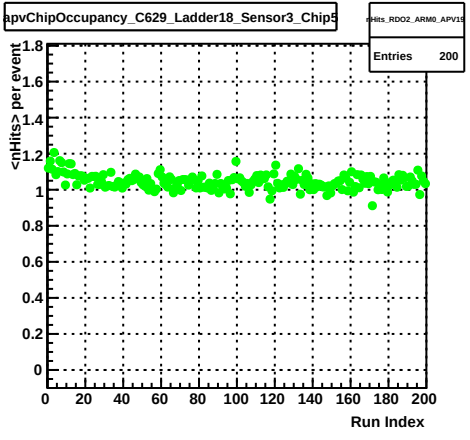

apvChipOccupancy_C624_Ladder18_Sensor2_Chip6
nHits_RDO2_ARM1_APV0
Entries 200
<nHits> per event
Run Index

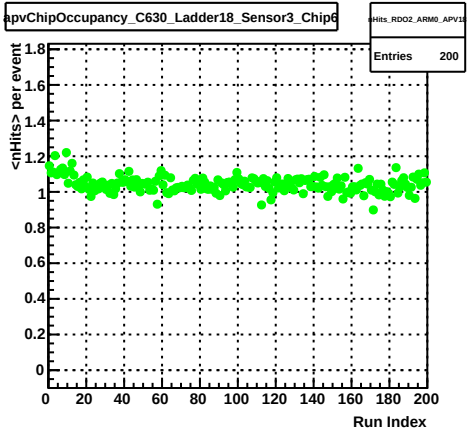

apvChipOccupancy_C625_Ladder18_Sensor3_Chip1
nHits_RDO2_ARM0_APV25
Entries 200
<nHits> per event
Run Index

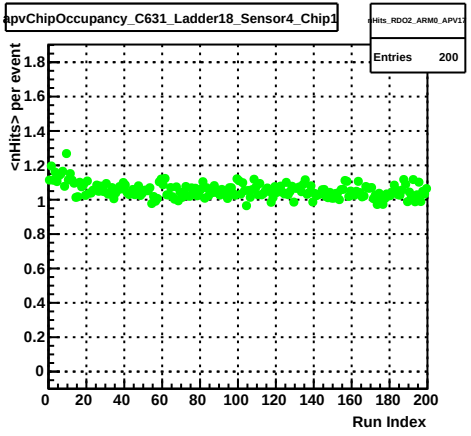

apvChipOccupancy_C626_Ladder18_Sensor3_Chip2
nHits_RDO2_ARM0_APV22
Entries 200
<nHits> per event
Run Index

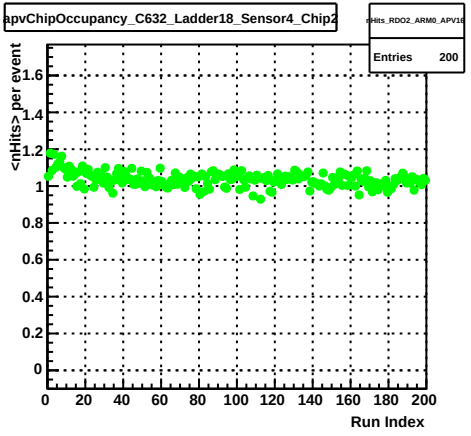

apvChipOccupancy_C627_Ladder18_Sensor3_Chip3
nHits_RDO2_ARM0_APV23
Entries 200
<nHits> per event
Run Index

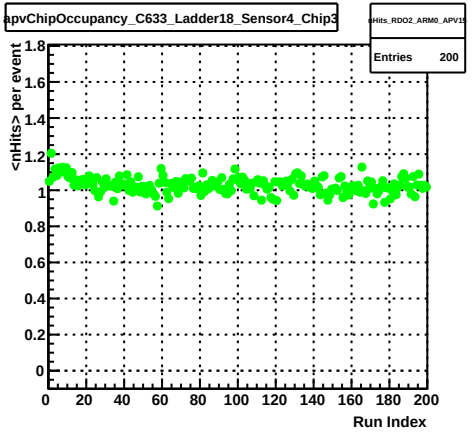

apvChipOccupancy_C628_Ladder18_Sensor3_Chip4
nHits_RDO2_ARM0_APV20
Entries 200
<nHits> per event
Run Index

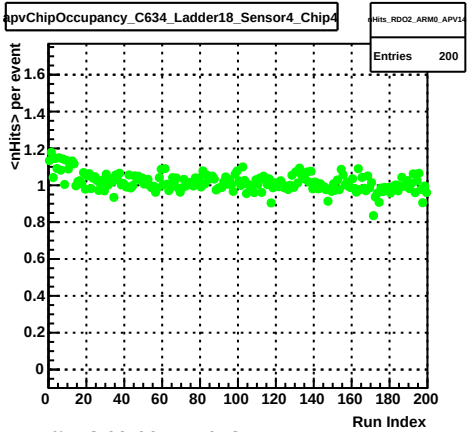

apvChipOccupancy_C629_Ladder18_Sensor3_Chip5
nHits_RDO2_ARM0_APV18
Entries 200
<nHits> per event
Run Index

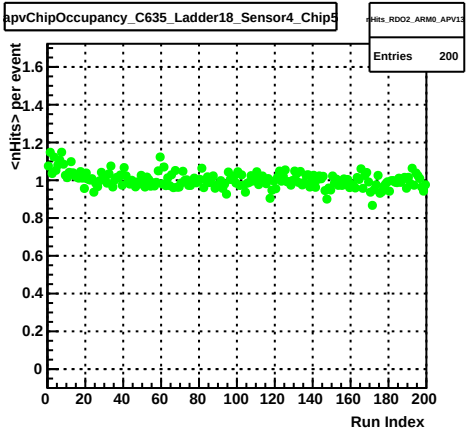

apvChipOccupancy_C630_Ladder18_Sensor3_Chip6
nHits_RDO2_ARM0_APV19
Entries 200
<nHits> per event
Run Index

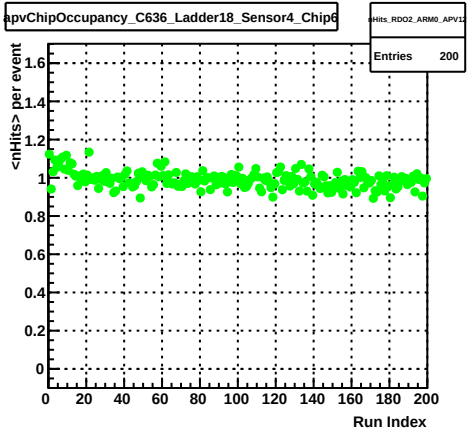

apvChipOccupancy_C631_Ladder18_Sensor4_Chip1
Entries 200
<nHits> per event
Run Index

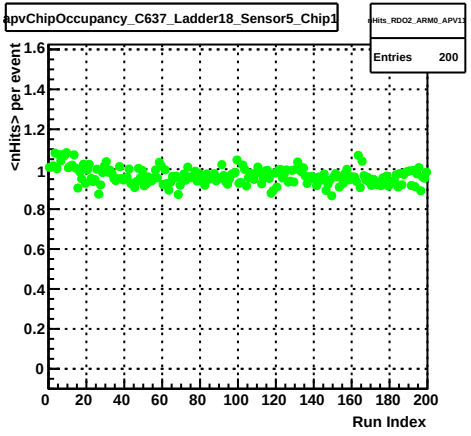

apvChipOccupancy_C632_Ladder18_Sensor4_Chip2
Entries 200
<nHits> per event
Run Index

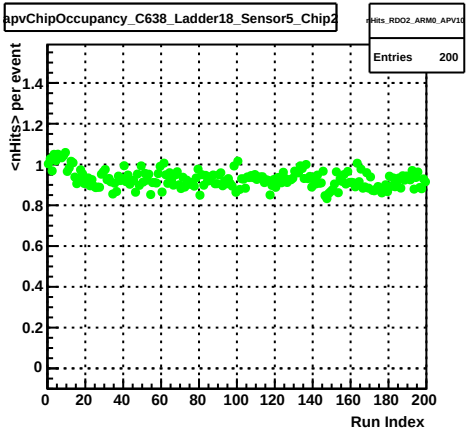

apvChipOccupancy_C633_Ladder18_Sensor4_Chip3
Entries 200
<nHits> per event
Run Index

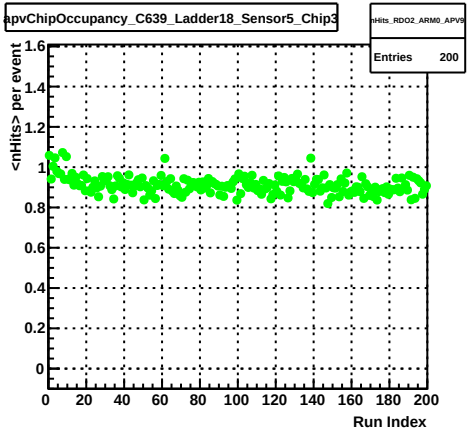

apvChipOccupancy_C634_Ladder18_Sensor4_Chip4
Entries 200
<nHits> per event
Run Index

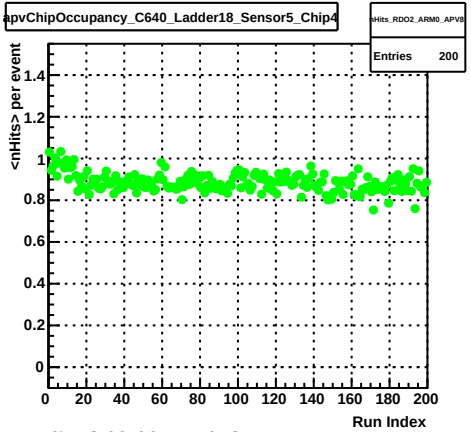

apvChipOccupancy_C635_Ladder18_Sensor4_Chip5
Entries 200
<nHits> per event
Run Index

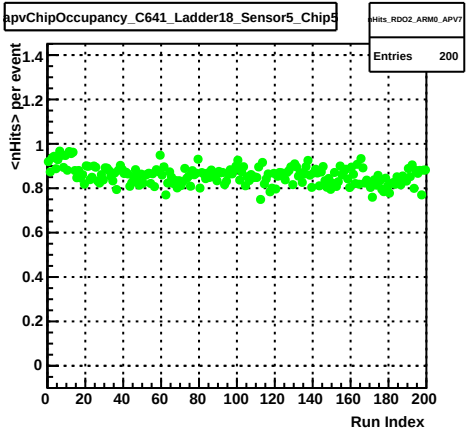

apvChipOccupancy_C636_Ladder18_Sensor4_Chip6
Entries 200
<nHits> per event
Run Index

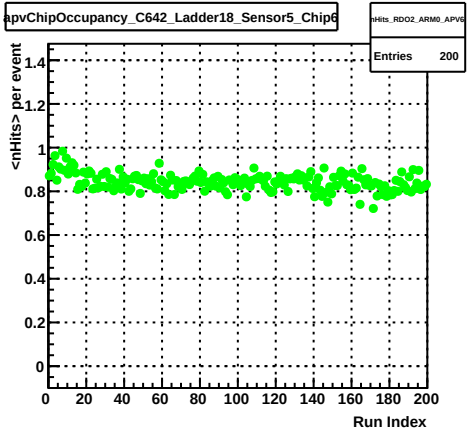

apvChipOccupancy_C637_Ladder18_Sensor5_Chip1
nHits_RDO2_ARM0_APV13
Entries 200
<nHits> per event
Run Index

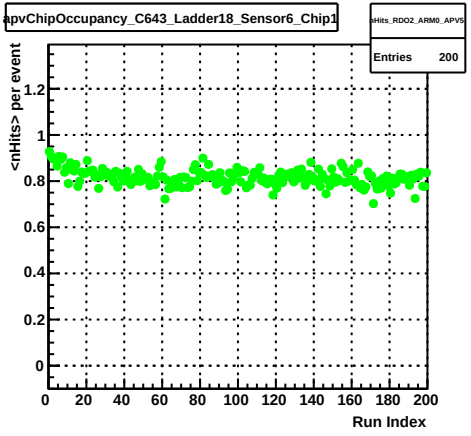

apvChipOccupancy_C638_Ladder18_Sensor5_Chip2
nHits_RDO2_ARM0_APV10
Entries 200
<nHits> per event
Run Index

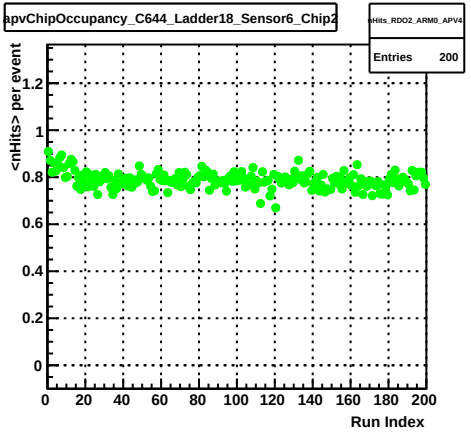

apvChipOccupancy_C639_Ladder18_Sensor5_Chip3
nHits_RDO2_ARM0_APV9
Entries 200
<nHits> per event
Run Index

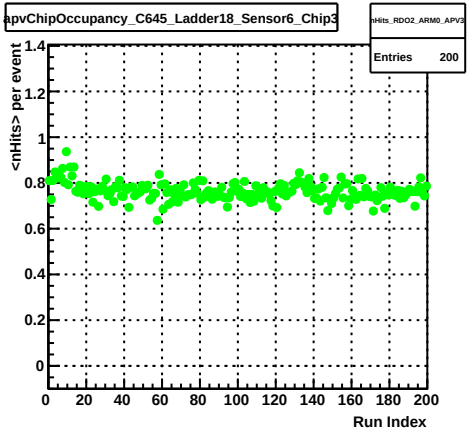

apvChipOccupancy_C640_Ladder18_Sensor5_Chip4
nHits_RDO2_ARM0_APV8
Entries 200
<nHits> per event
Run Index

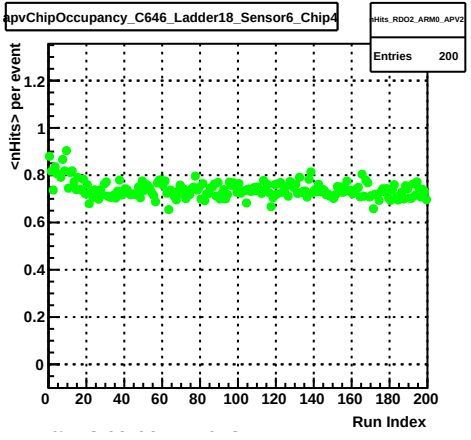

apvChipOccupancy_C641_Ladder18_Sensor5_Chip5
nHits_RDO2_ARM0_APV7
Entries 200
<nHits> per event
Run Index

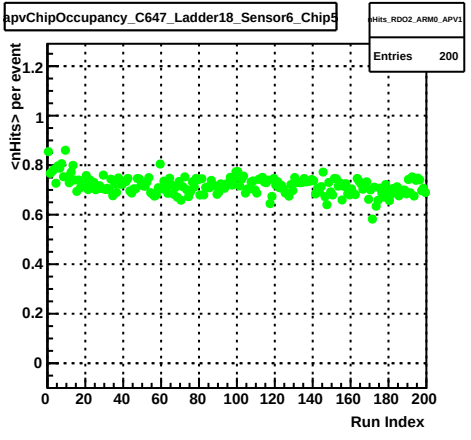

apvChipOccupancy_C642_Ladder18_Sensor5_Chip6
nHits_RDO2_ARM0_APV6
Entries 200
<nHits> per event
Run Index

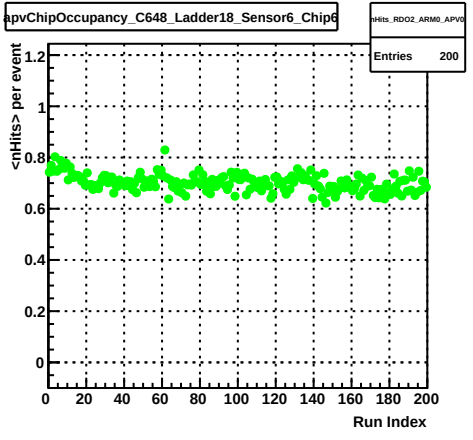

apvChipOccupancy_C643_Ladder18_Sensor6_Chip1
nHits_RDO2_ARM0_APV5
Entries 200
<nHits> per event
Run Index

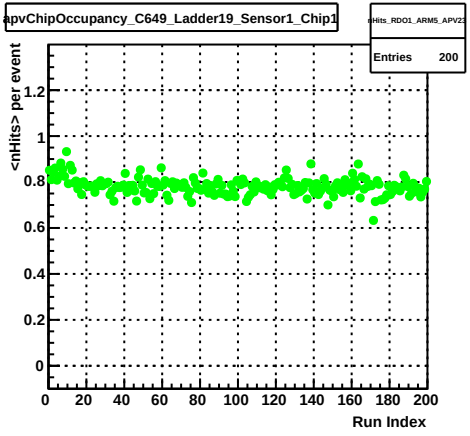

apvChipOccupancy_C644_Ladder18_Sensor6_Chip2
nHits_RDO2_ARM0_APV4
Entries 200
<nHits> per event
Run Index

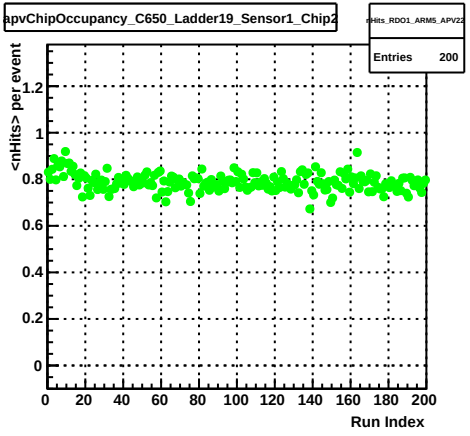

apvChipOccupancy_C645_Ladder18_Sensor6_Chip3
nHits_RDO2_ARM0_APV3
Entries 200
<nHits> per event
Run Index

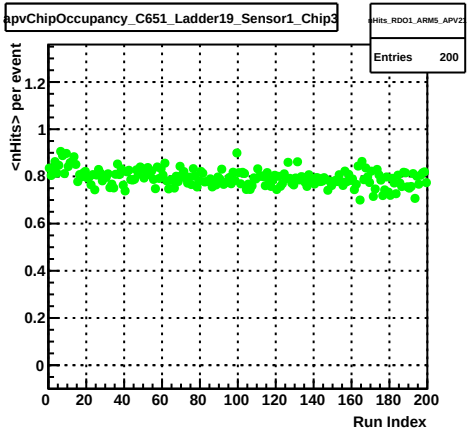

apvChipOccupancy_C646_Ladder18_Sensor6_Chip4
nHits_RDO2_ARM0_APV2
Entries 200
<nHits> per event
Run Index

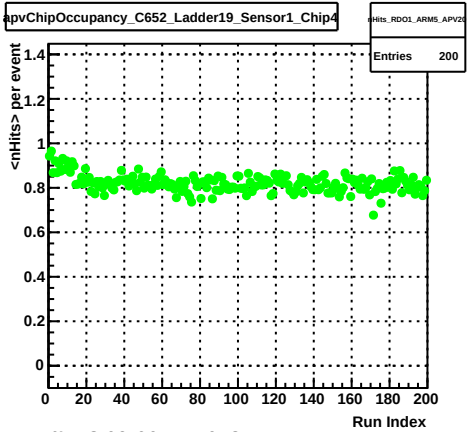

apvChipOccupancy_C647_Ladder18_Sensor6_Chip5
nHits_RDO2_ARM0_APV1
Entries 200
<nHits> per event
Run Index

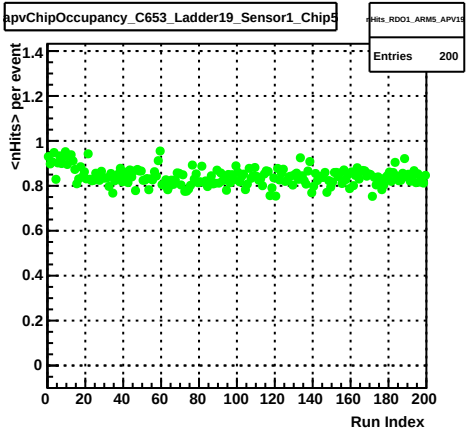

apvChipOccupancy_C648_Ladder18_Sensor6_Chip6
nHits_RDO2_ARM0_APV0
Entries 200
<nHits> per event
Run Index

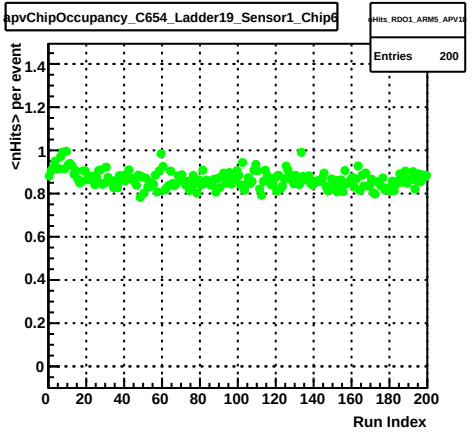

apvChipOccupancy_C649_Ladder19_Sensor1_Chip1
Entries 200
<nHits> per event
Run Index

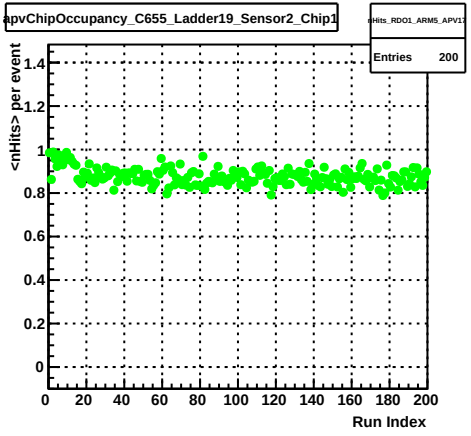

apvChipOccupancy_C650_Ladder19_Sensor1_Chip2
Entries 200
<nHits> per event
Run Index

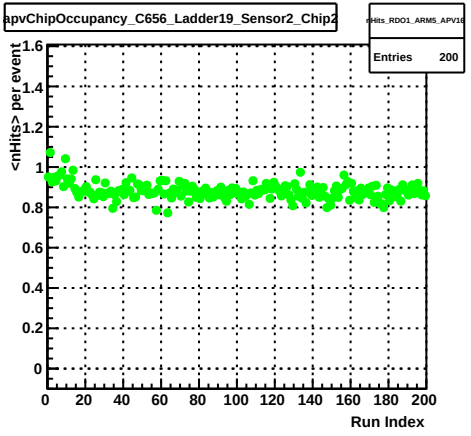

apvChipOccupancy_C651_Ladder19_Sensor1_Chip3
Entries 200
<nHits> per event
Run Index

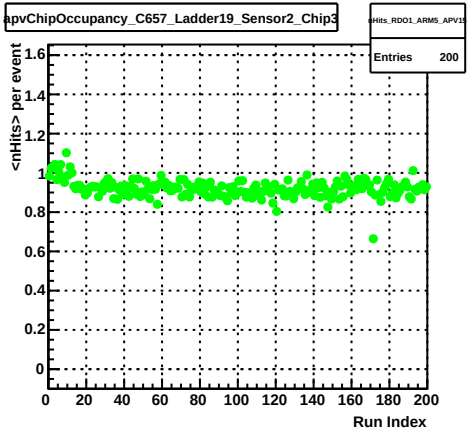

apvChipOccupancy_C652_Ladder19_Sensor1_Chip4
Entries 200
<nHits> per event
Run Index

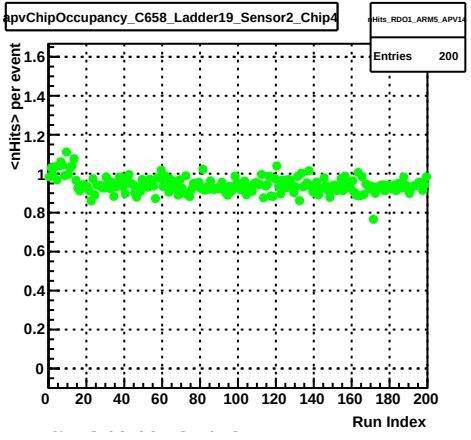

apvChipOccupancy_C653_Ladder19_Sensor1_Chip5
Entries 200
<nHits> per event
Run Index

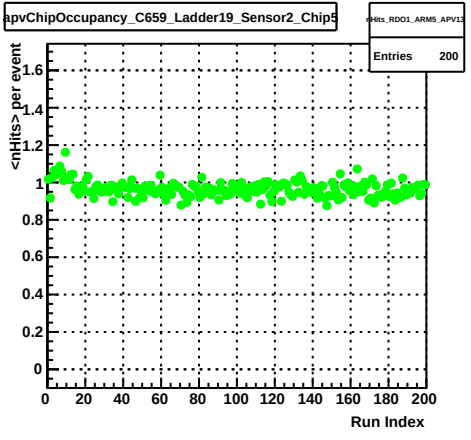

apvChipOccupancy_C654_Ladder19_Sensor1_Chip6
Entries 200
<nHits> per event
Run Index

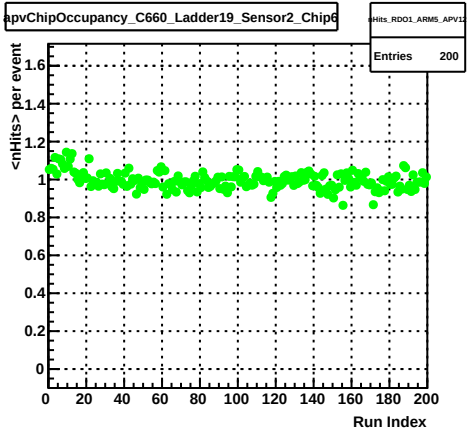

apvChipOccupancy_C655_Ladder19_Sensor2_Chip1
nHits_RDO1_ARM5_APV13
Entries 200
<nHits> per event
Run Index

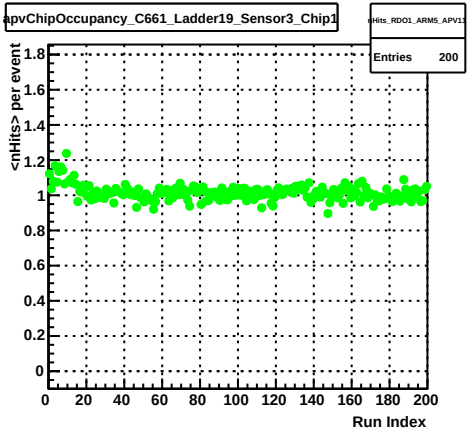

apvChipOccupancy_C656_Ladder19_Sensor2_Chip2
nHits_RDO1_ARM5_APV14
Entries 200
<nHits> per event
Run Index

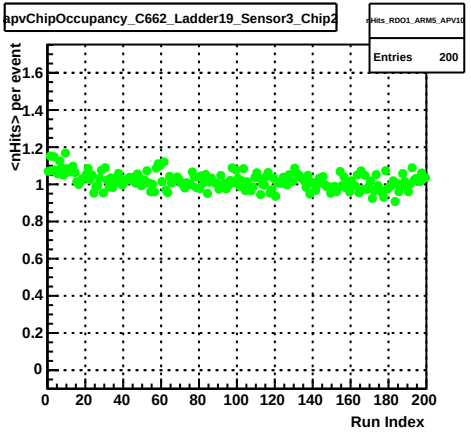

apvChipOccupancy_C657_Ladder19_Sensor2_Chip3
nHits_RDO1_ARM5_APV15
Entries 200
<nHits> per event
Run Index

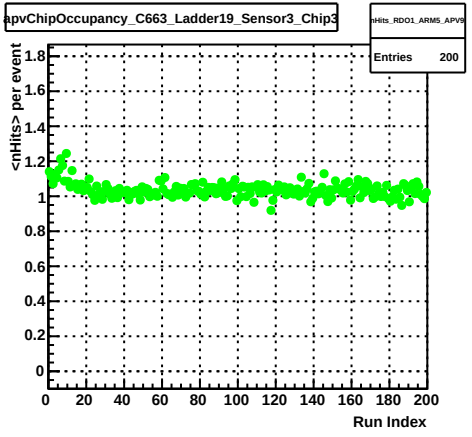

apvChipOccupancy_C658_Ladder19_Sensor2_Chip4
nHits_RDO1_ARM5_APV14
Entries 200
<nHits> per event
Run Index

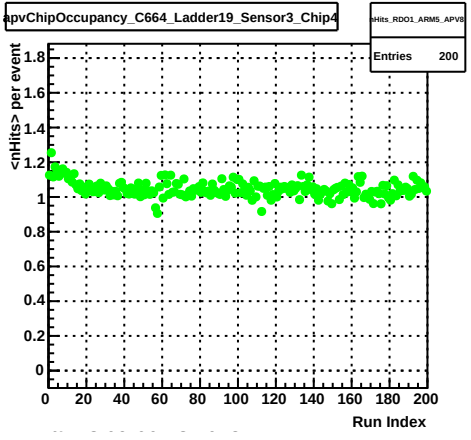

apvChipOccupancy_C659_Ladder19_Sensor2_Chip5
nHits_RDO1_ARM5_APV13
Entries 200
<nHits> per event
Run Index

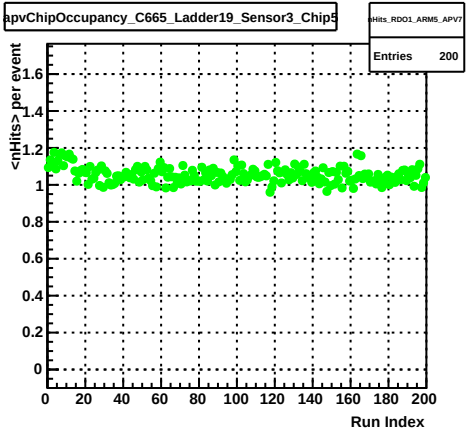

apvChipOccupancy_C660_Ladder19_Sensor2_Chip6
nHits_RDO1_ARM5_APV12
Entries 200
<nHits> per event
Run Index

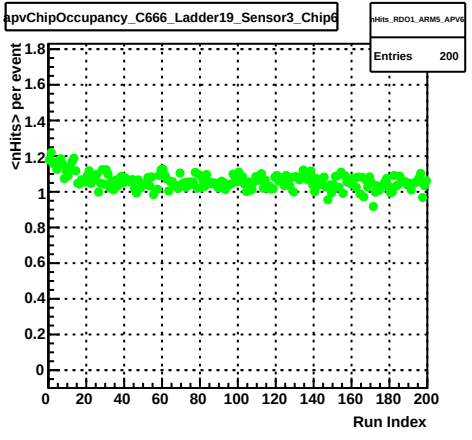

apvChipOccupancy_C661_Ladder19_Sensor3_Chip1
nHits_RD01_ARM5_APV13
Entries 200
<nHits> per event
Run Index

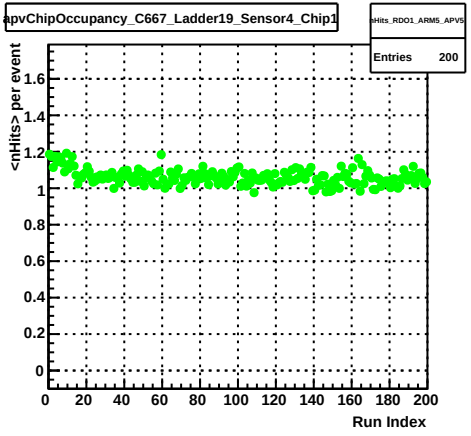

apvChipOccupancy_C662_Ladder19_Sensor3_Chip2
nHits_RD01_ARM5_APV10
Entries 200
<nHits> per event
Run Index

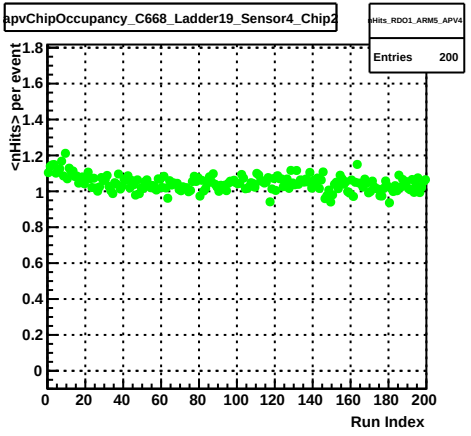

apvChipOccupancy_C663_Ladder19_Sensor3_Chip3
nHits_RD01_ARM5_APV9
Entries 200
<nHits> per event
Run Index

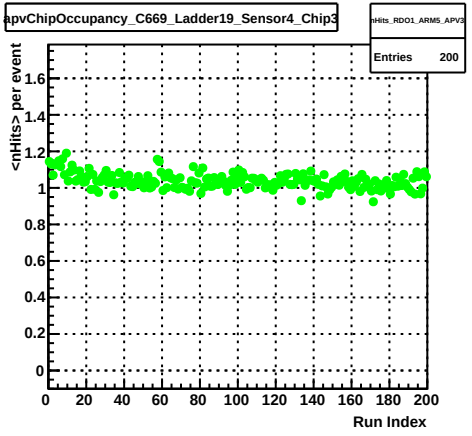

apvChipOccupancy_C664_Ladder19_Sensor3_Chip4
nHits_RD01_ARM5_APV8
Entries 200
<nHits> per event
Run Index

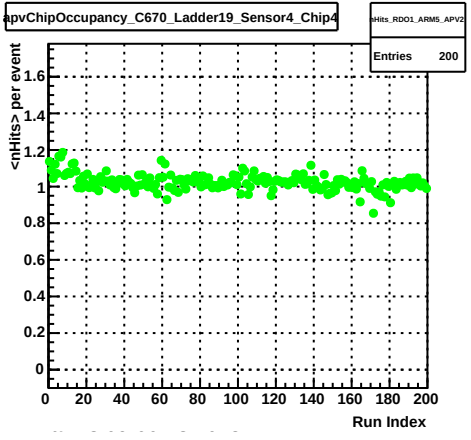

apvChipOccupancy_C665_Ladder19_Sensor3_Chip5
nHits_RD01_ARM5_APV7
Entries 200
<nHits> per event
Run Index

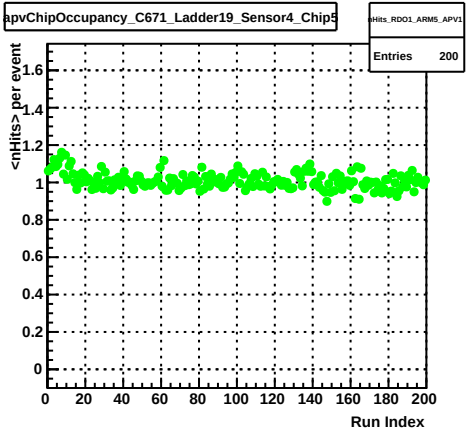

apvChipOccupancy_C666_Ladder19_Sensor3_Chip6
nHits_RD01_ARM5_APV6
Entries 200
<nHits> per event
Run Index

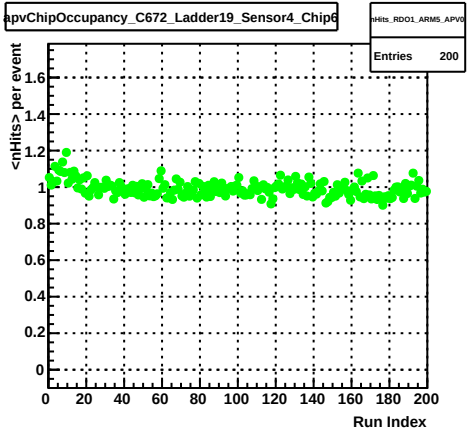

apvChipOccupancy_C667_Ladder19_Sensor4_Chip1
nHits_RD01_ARM5_APV5
Entries 200
<nHits> per event
Run Index

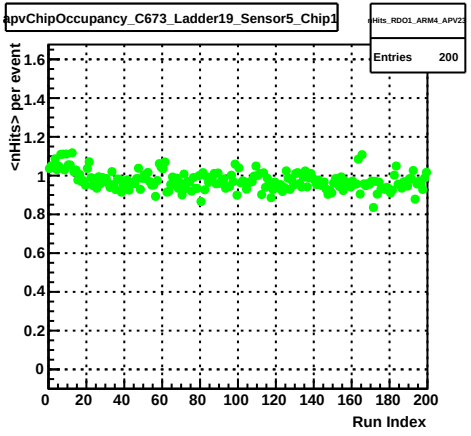

apvChipOccupancy_C668_Ladder19_Sensor4_Chip2
nHits_RD01_ARM5_APV4
Entries 200
<nHits> per event
Run Index

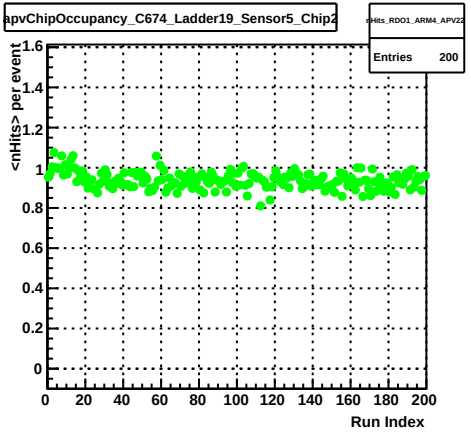

apvChipOccupancy_C669_Ladder19_Sensor4_Chip3
nHits_RD01_ARM5_APV3
Entries 200
<nHits> per event
Run Index

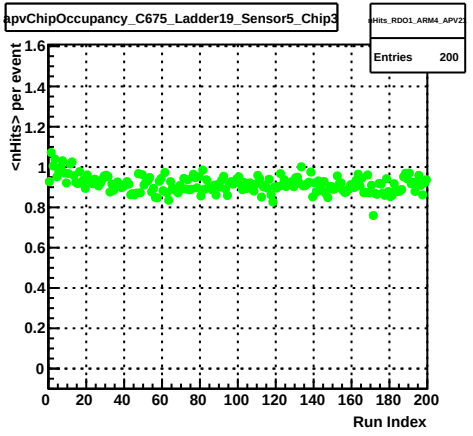

apvChipOccupancy_C670_Ladder19_Sensor4_Chip4
nHits_RD01_ARM5_APV2
Entries 200
<nHits> per event
Run Index

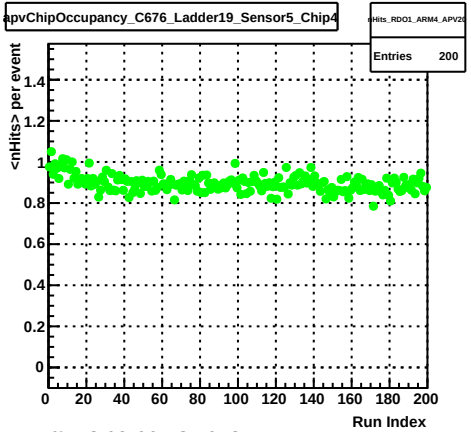

apvChipOccupancy_C671_Ladder19_Sensor4_Chip5
nHits_RD01_ARM5_APV1
Entries 200
<nHits> per event
Run Index

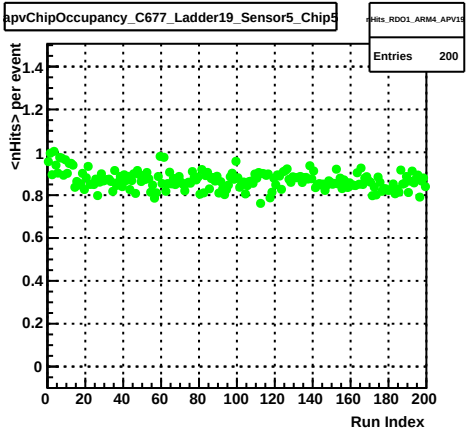

apvChipOccupancy_C672_Ladder19_Sensor4_Chip6
nHits_RD01_ARM5_APV0
Entries 200
<nHits> per event
Run Index

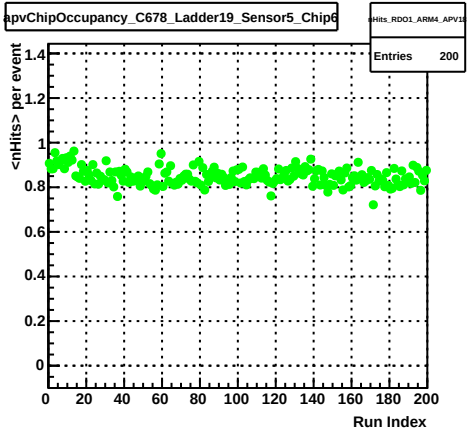

apvChipOccupancy_C673_Ladder19_Sensor5_Chip1
nHits_RDO1_ARM4_APV25
Entries 200
<nHits> per event
Run Index

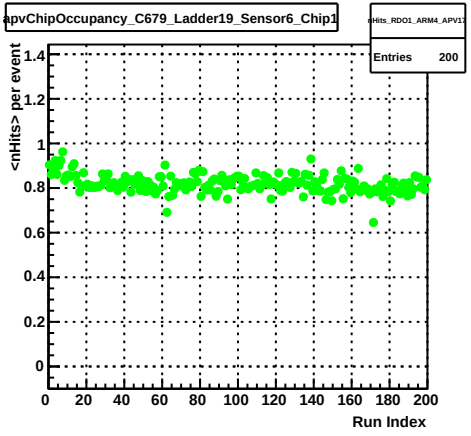

apvChipOccupancy_C674_Ladder19_Sensor5_Chip2
nHits_RDO1_ARM4_APV25
Entries 200
<nHits> per event
Run Index

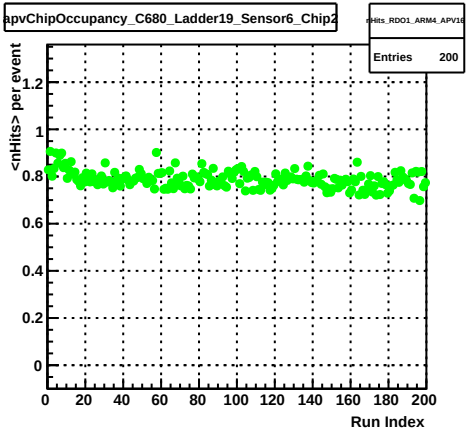

apvChipOccupancy_C675_Ladder19_Sensor5_Chip3
nHits_RDO1_ARM4_APV25
Entries 200
<nHits> per event
Run Index

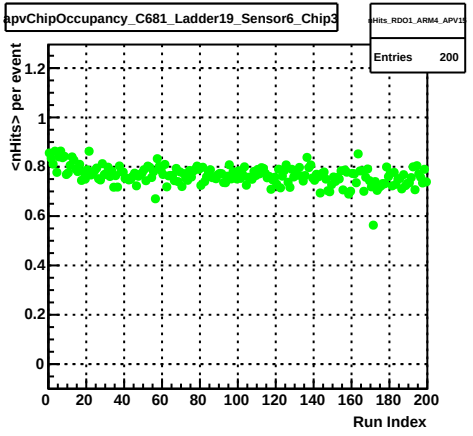

apvChipOccupancy_C676_Ladder19_Sensor5_Chip4
nHits_RDO1_ARM4_APV25
Entries 200
<nHits> per event
Run Index

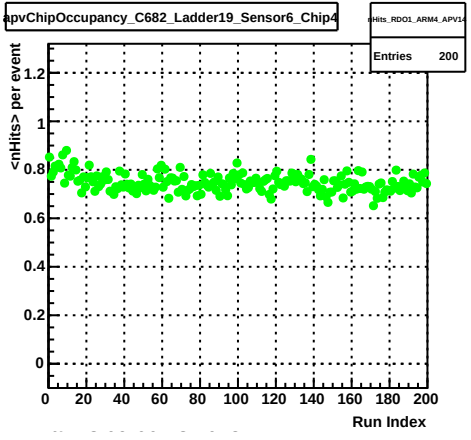

apvChipOccupancy_C677_Ladder19_Sensor5_Chip5
nHits_RDO1_ARM4_APV18
Entries 200
<nHits> per event
Run Index

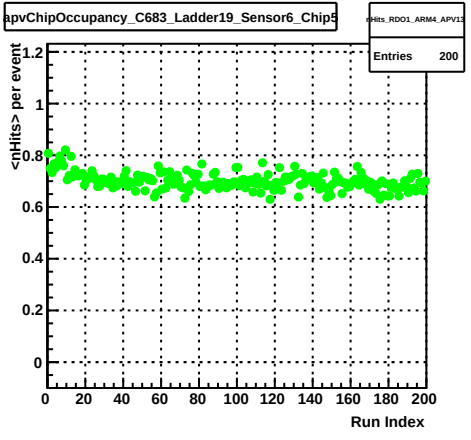

apvChipOccupancy_C678_Ladder19_Sensor5_Chip6
nHits_RDO1_ARM4_APV18
Entries 200
<nHits> per event
Run Index

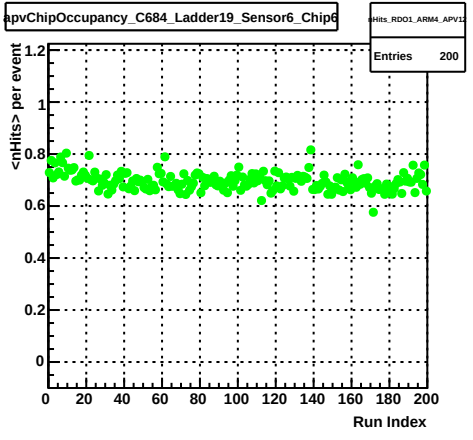

apvChipOccupancy_C679_Ladder19_Sensor6_Chip1
nHits_RDO1_ARM4_APV13
Entries 200
<nHits> per event
Run Index

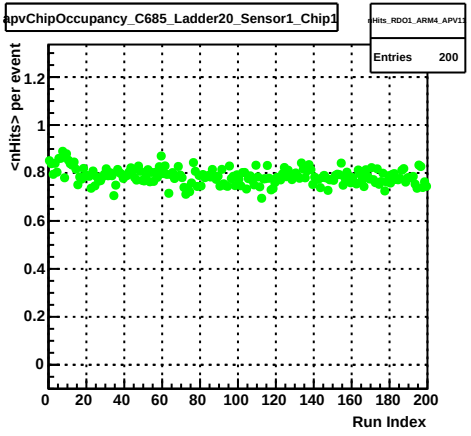

apvChipOccupancy_C680_Ladder19_Sensor6_Chip2
nHits_RDO1_ARM4_APV14
Entries 200
<nHits> per event
Run Index

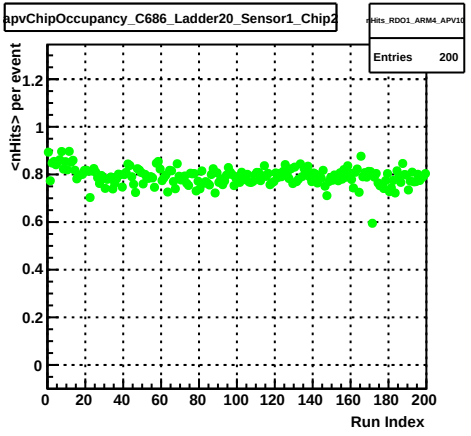

apvChipOccupancy_C681_Ladder19_Sensor6_Chip3
nHits_RDO1_ARM4_APV15
Entries 200
<nHits> per event
Run Index

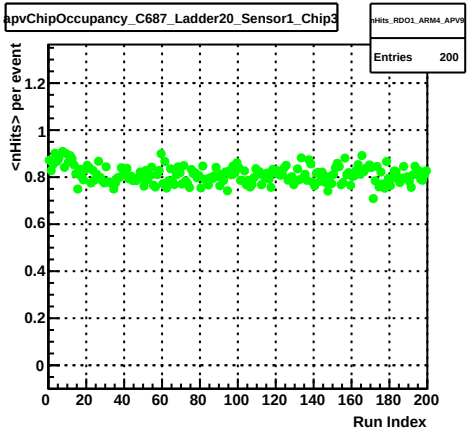

apvChipOccupancy_C682_Ladder19_Sensor6_Chip4
nHits_RDO1_ARM4_APV16
Entries 200
<nHits> per event
Run Index

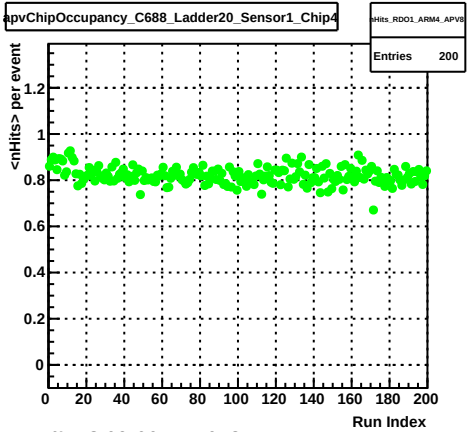

apvChipOccupancy_C683_Ladder19_Sensor6_Chip5
nHits_RDO1_ARM4_APV17
Entries 200
<nHits> per event
Run Index

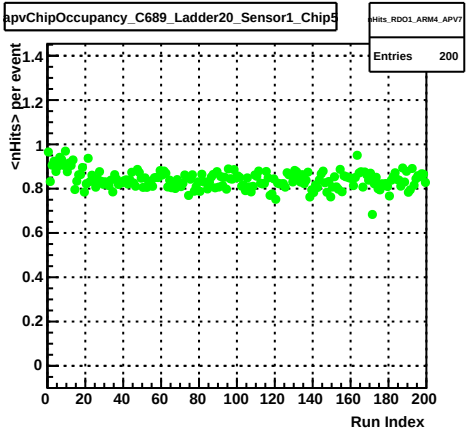

apvChipOccupancy_C684_Ladder19_Sensor6_Chip6
nHits_RDO1_ARM4_APV18
Entries 200
<nHits> per event
Run Index

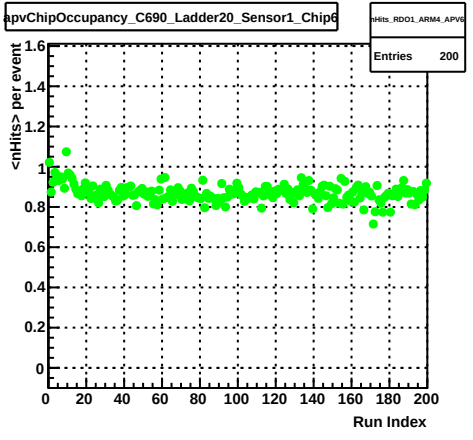

apvChipOccupancy_C685_Ladder20_Sensor1_Chip1
nHits_RDO1_ARM4_APV13
Entries 200
<nHits> per event
Run Index

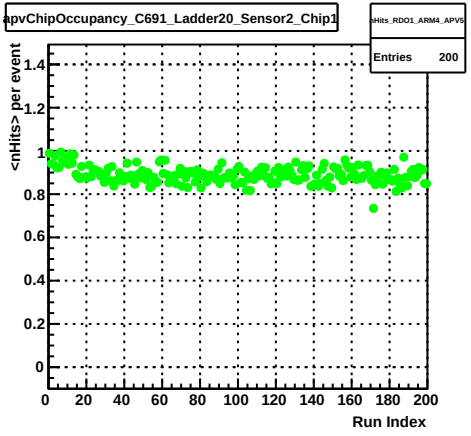

apvChipOccupancy_C686_Ladder20_Sensor1_Chip2
nHits_RDO1_ARM4_APV10
Entries 200
<nHits> per event
Run Index

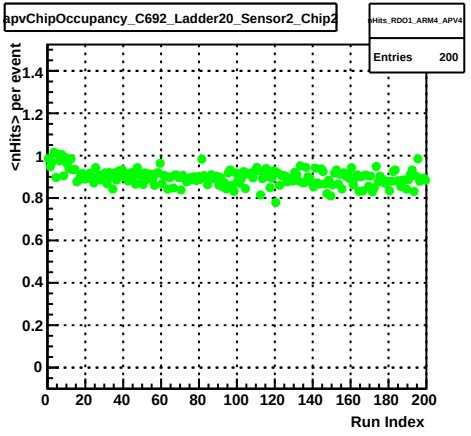

apvChipOccupancy_C687_Ladder20_Sensor1_Chip3
nHits_RDO1_ARM4_APV9
Entries 200
<nHits> per event
Run Index

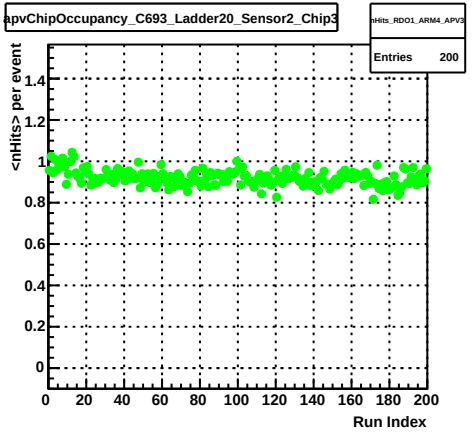

apvChipOccupancy_C688_Ladder20_Sensor1_Chip4
nHits_RDO1_ARM4_APV8
Entries 200
<nHits> per event
Run Index

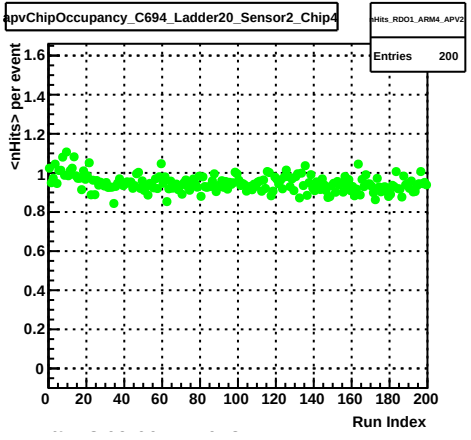

apvChipOccupancy_C689_Ladder20_Sensor1_Chip5
nHits_RDO1_ARM4_APV7
Entries 200
<nHits> per event
Run Index

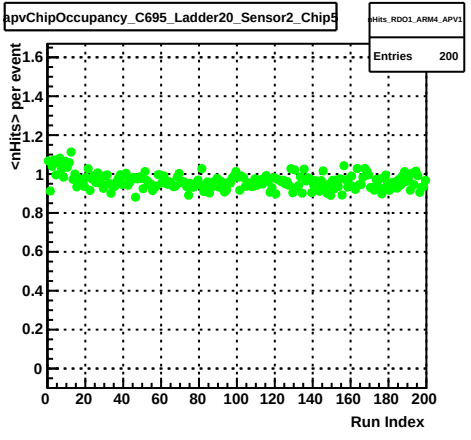

apvChipOccupancy_C690_Ladder20_Sensor1_Chip6
nHits_RDO1_ARM4_APV6
Entries 200
<nHits> per event
Run Index

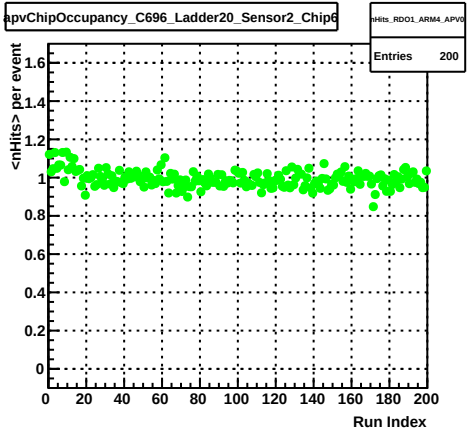

apvChipOccupancy_C691_Ladder20_Sensor2_Chip1
nHits_RD01_ARM4_APV5
Entries 200
<nHits> per event
Run Index

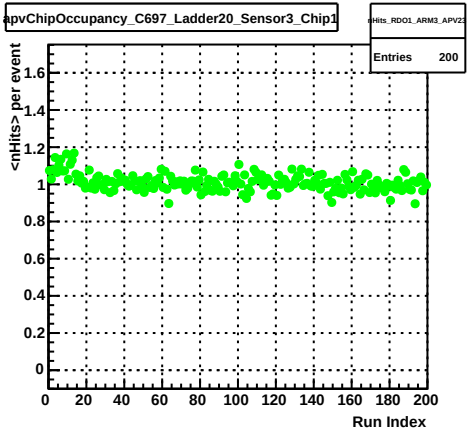

apvChipOccupancy_C692_Ladder20_Sensor2_Chip2
nHits_RD01_ARM4_APV4
Entries 200
<nHits> per event
Run Index

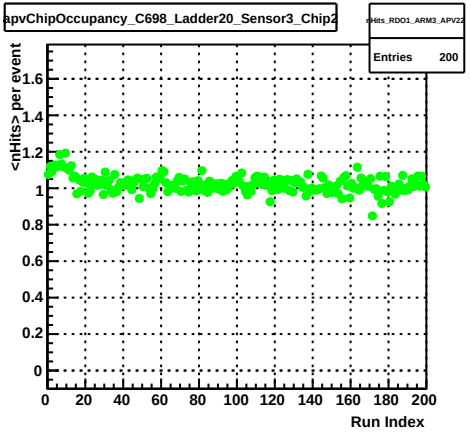

apvChipOccupancy_C693_Ladder20_Sensor2_Chip3
nHits_RD01_ARM4_APV3
Entries 200
<nHits> per event
Run Index

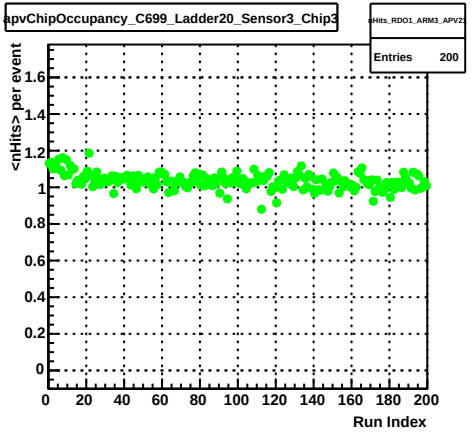

apvChipOccupancy_C694_Ladder20_Sensor2_Chip4
nHits_RD01_ARM4_APV2
Entries 200
<nHits> per event
Run Index

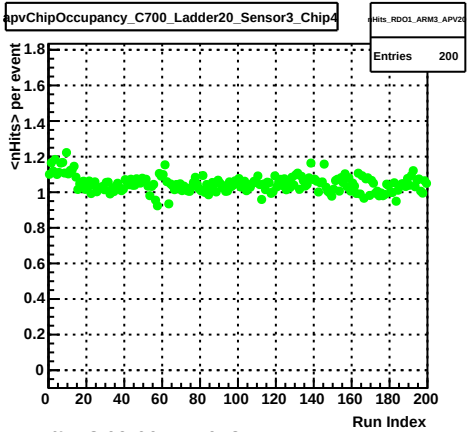

apvChipOccupancy_C695_Ladder20_Sensor2_Chip5
nHits_RD01_ARM4_APV1
Entries 200
<nHits> per event
Run Index

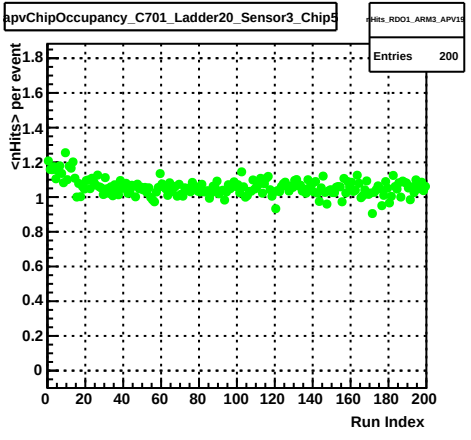

apvChipOccupancy_C696_Ladder20_Sensor2_Chip6
nHits_RD01_ARM4_APV0
Entries 200
<nHits> per event
Run Index

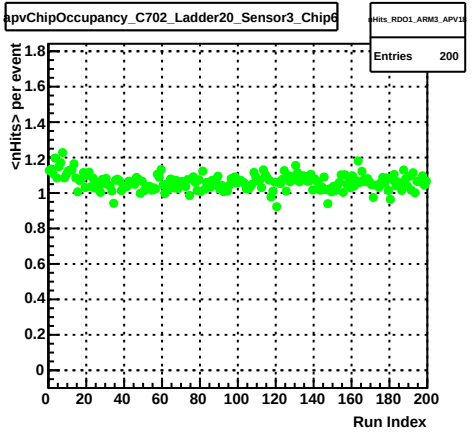

apvChipOccupancy_C697_Ladder20_Sensor3_Chip1
nHits_RDO1_ARM3_APV23
Entries 200
<nHits> per event
Run Index

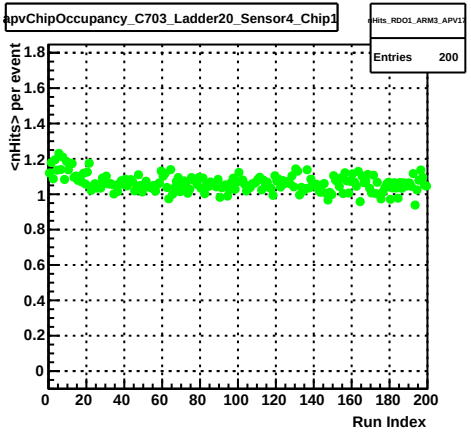

apvChipOccupancy_C698_Ladder20_Sensor3_Chip2
nHits_RDO1_ARM3_APV22
Entries 200
<nHits> per event
Run Index

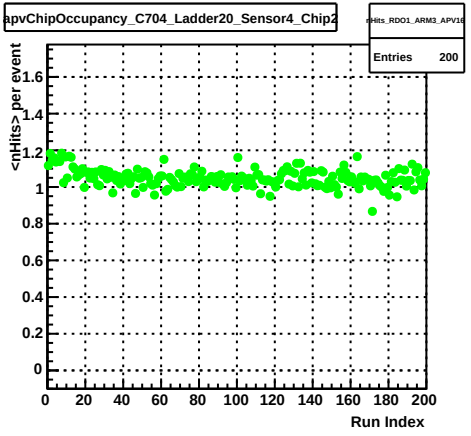

apvChipOccupancy_C699_Ladder20_Sensor3_Chip3
nHits_RDO1_ARM3_APV21
Entries 200
<nHits> per event
Run Index

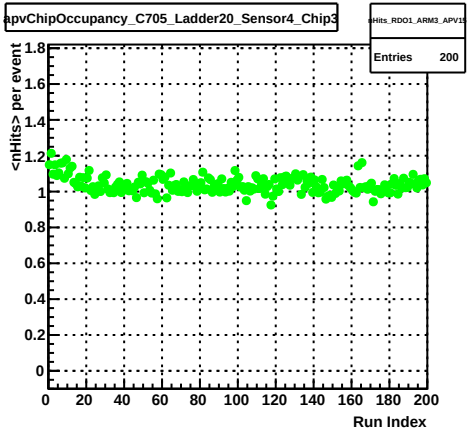

apvChipOccupancy_C700_Ladder20_Sensor3_Chip4
nHits_RDO1_ARM3_APV20
Entries 200
<nHits> per event
Run Index

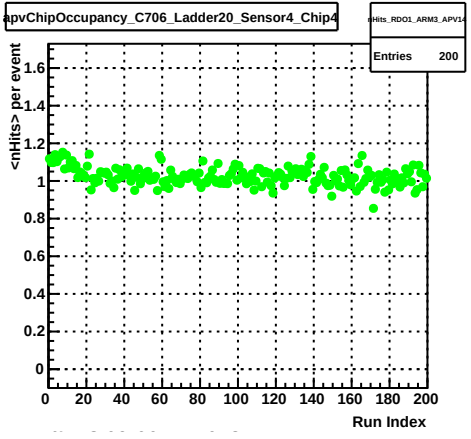

apvChipOccupancy_C701_Ladder20_Sensor3_Chip5
nHits_RDO1_ARM3_APV19
Entries 200
<nHits> per event
Run Index

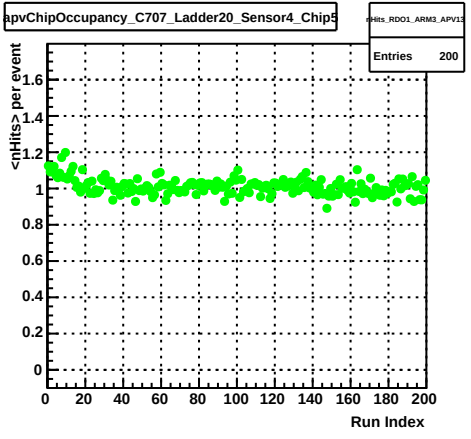

apvChipOccupancy_C702_Ladder20_Sensor3_Chip6
nHits_RDO1_ARM3_APV18
Entries 200
<nHits> per event
Run Index

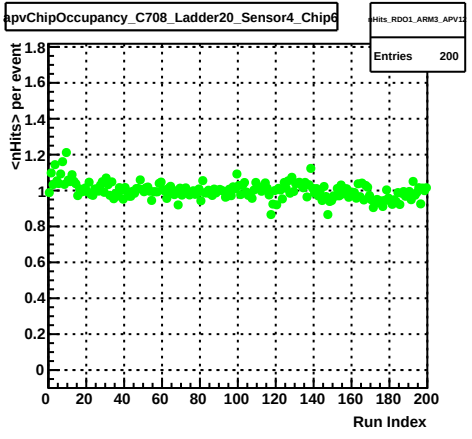

apvChipOccupancy_C703_Ladder20_Sensor4_Chip1
nHits_RDO1_ARM3_APV13
Entries 200
<nHits> per event
Run Index

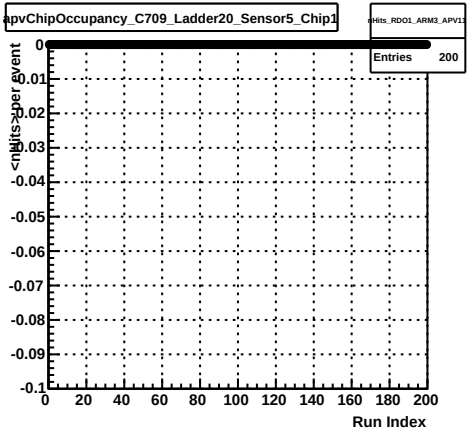

apvChipOccupancy_C704_Ladder20_Sensor4_Chip2
nHits_RDO1_ARM3_APV14
Entries 200
<nHits> per event
Run Index

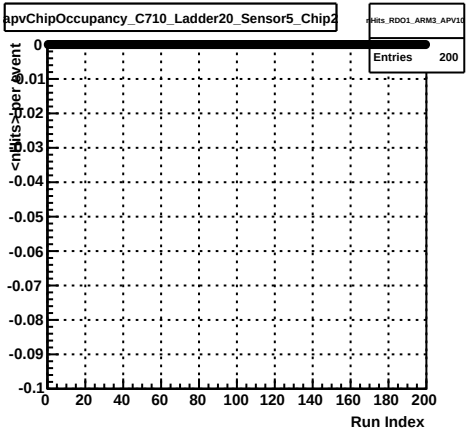

apvChipOccupancy_C705_Ladder20_Sensor4_Chip3
nHits_RDO1_ARM3_APV15
Entries 200
<nHits> per event
Run Index

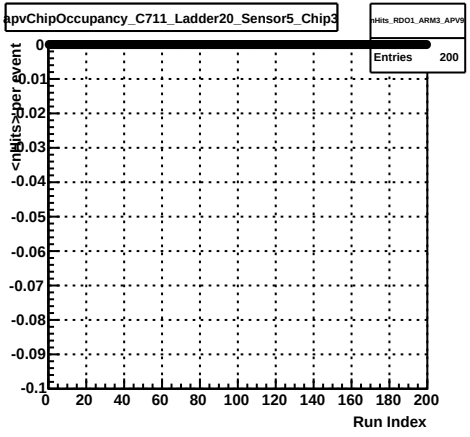

apvChipOccupancy_C706_Ladder20_Sensor4_Chip4
nHits_RDO1_ARM3_APV16
Entries 200
<nHits> per event
Run Index

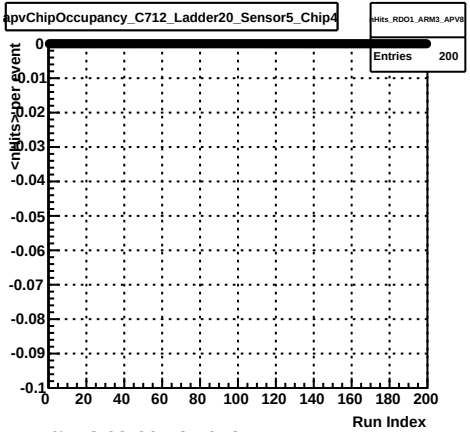

apvChipOccupancy_C707_Ladder20_Sensor4_Chip5
nHits_RDO1_ARM3_APV17
Entries 200
<nHits> per event
Run Index

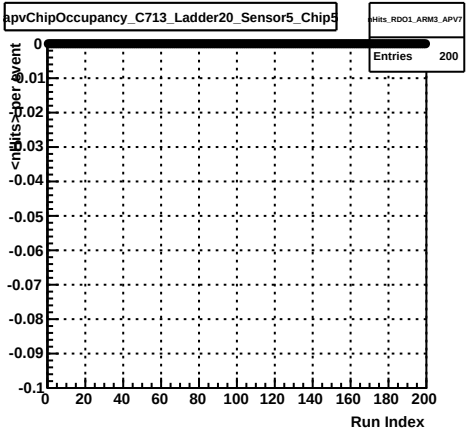

apvChipOccupancy_C708_Ladder20_Sensor4_Chip6
nHits_RDO1_ARM3_APV18
Entries 200
<nHits> per event
Run Index

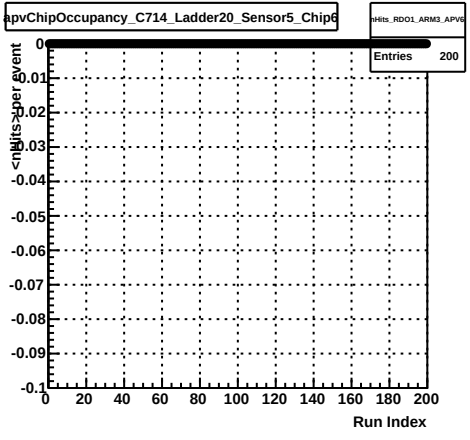

apvChipOccupancy_C709_Ladder20_Sensor5_Chip1
nHits_RDO1_ARM3_APV13
Entries 200
<nHits> per event
Run Index

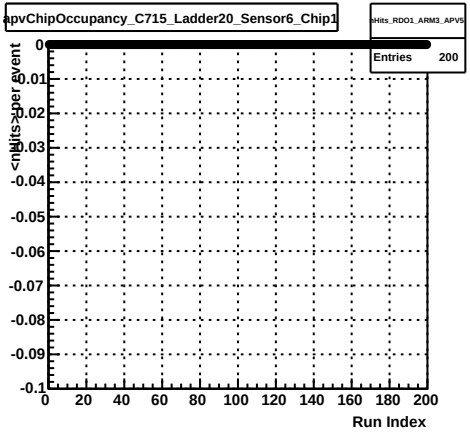

apvChipOccupancy_C710_Ladder20_Sensor5_Chip2
nHits_RDO1_ARM3_APV10
Entries 200
<nHits> per event
Run Index

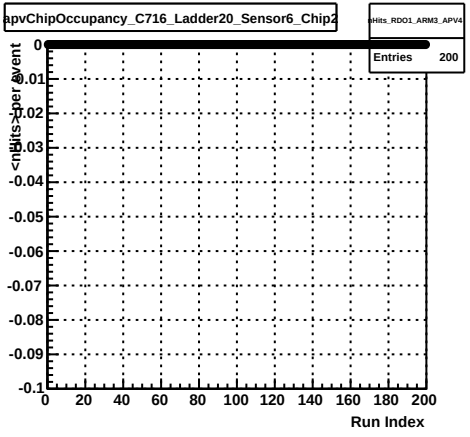

apvChipOccupancy_C711_Ladder20_Sensor5_Chip3
nHits_RDO1_ARM3_APV9
Entries 200
<nHits> per event
Run Index

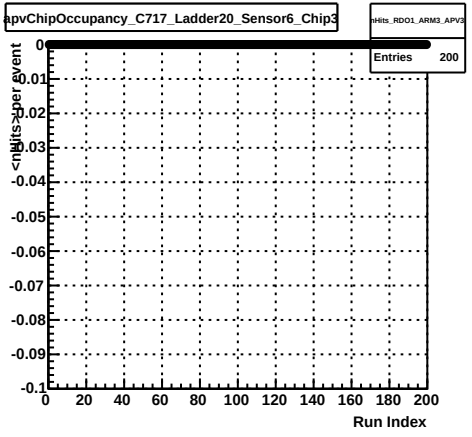

apvChipOccupancy_C712_Ladder20_Sensor5_Chip4
nHits_RDO1_ARM3_APV8
Entries 200
<nHits> per event
Run Index

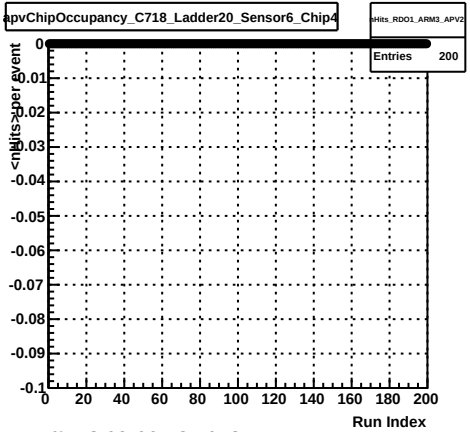

apvChipOccupancy_C713_Ladder20_Sensor5_Chip5
nHits_RDO1_ARM3_APV7
Entries 200
<nHits> per event
Run Index

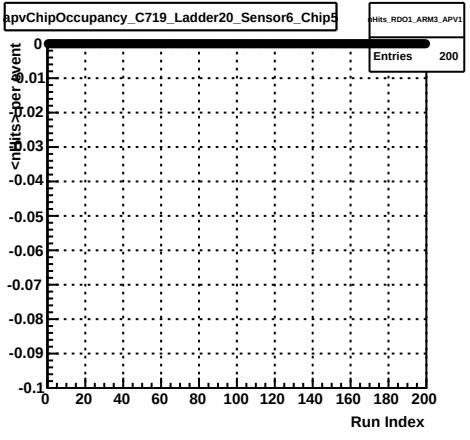

apvChipOccupancy_C714_Ladder20_Sensor5_Chip6
nHits_RDO1_ARM3_APV6
Entries 200
<nHits> per event
Run Index

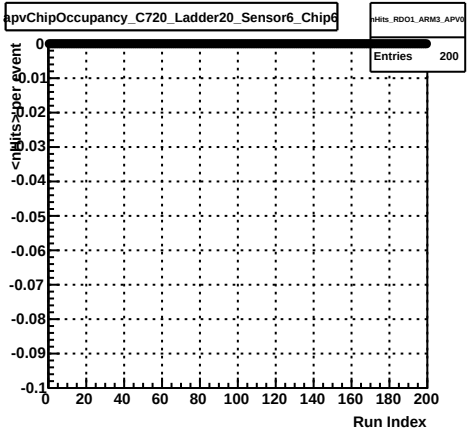

apvChipOccupancy_C715_Ladder20_Sensor6_Chip1
nHits_RD01_ARM3_APV5
Entries 200
<nHits> per event
Run Index

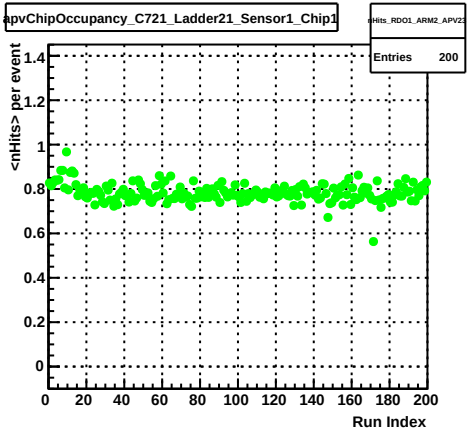

apvChipOccupancy_C716_Ladder20_Sensor6_Chip2
nHits_RD01_ARM3_APV4
Entries 200
<nHits> per event
Run Index

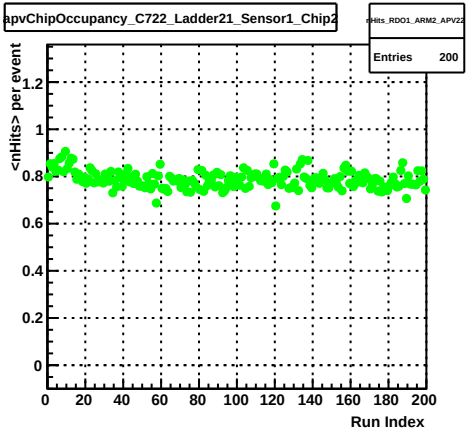

apvChipOccupancy_C717_Ladder20_Sensor6_Chip3
nHits_RD01_ARM3_APV3
Entries 200
<nHits> per event
Run Index

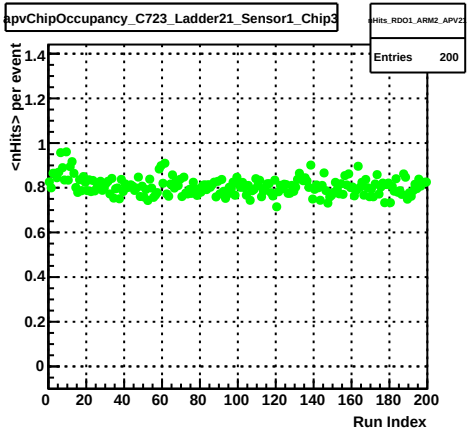

apvChipOccupancy_C718_Ladder20_Sensor6_Chip4
nHits_RD01_ARM3_APV2
Entries 200
<nHits> per event
Run Index

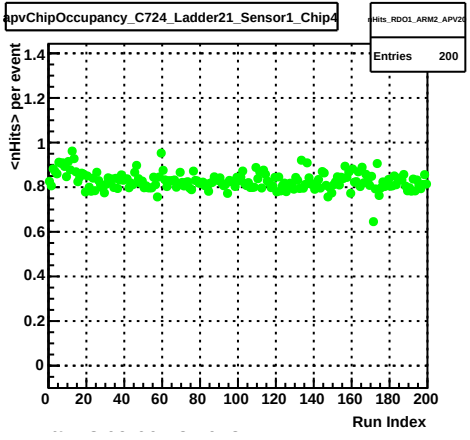

apvChipOccupancy_C719_Ladder20_Sensor6_Chip5
nHits_RD01_ARM3_APV1
Entries 200
<nHits> per event
Run Index

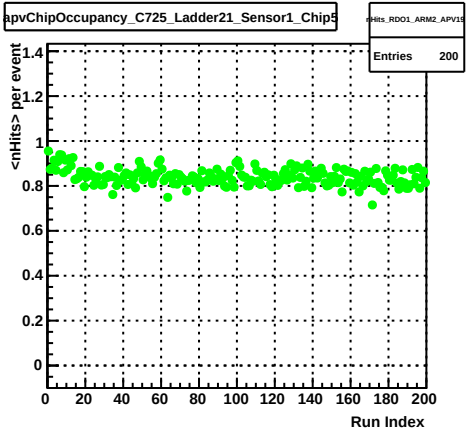

apvChipOccupancy_C720_Ladder20_Sensor6_Chip6
nHits_RD01_ARM3_APV0
Entries 200
<nHits> per event
Run Index

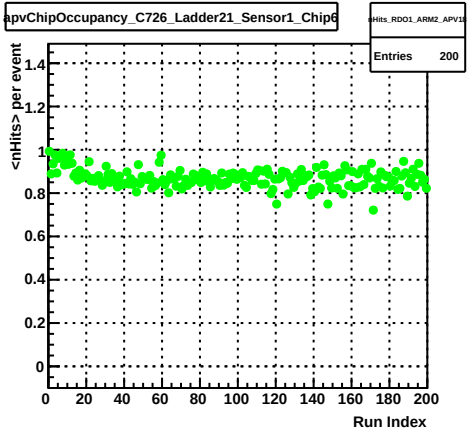

apvChipOccupancy_C721_Ladder21_Sensor1_Chip1
Entries 200
<nHits> per event
Run Index

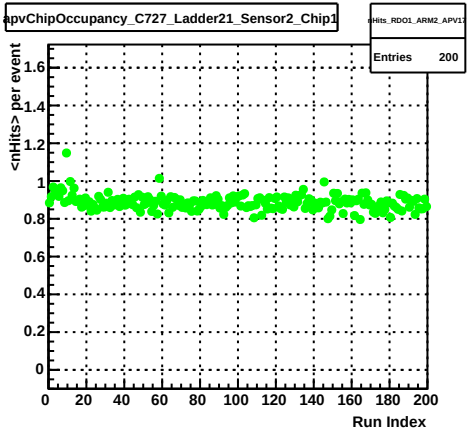

apvChipOccupancy_C722_Ladder21_Sensor1_Chip2
Entries 200
<nHits> per event
Run Index

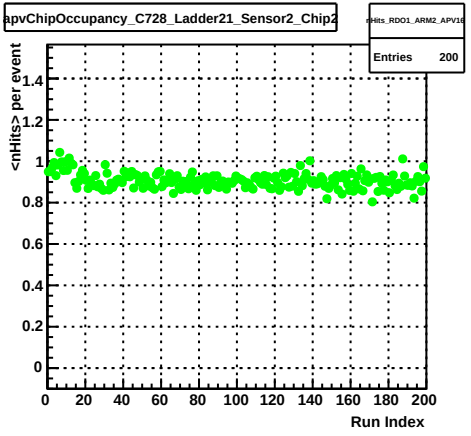

apvChipOccupancy_C723_Ladder21_Sensor1_Chip3
Entries 200
<nHits> per event
Run Index

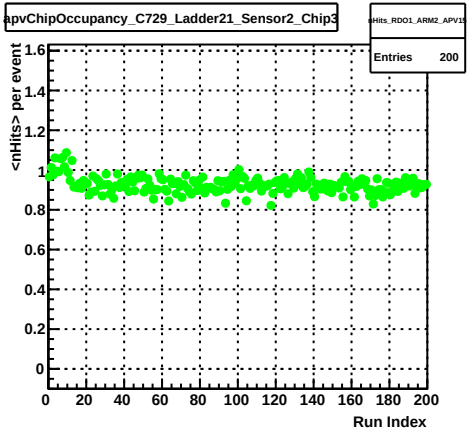

apvChipOccupancy_C724_Ladder21_Sensor1_Chip4
Entries 200
<nHits> per event
Run Index

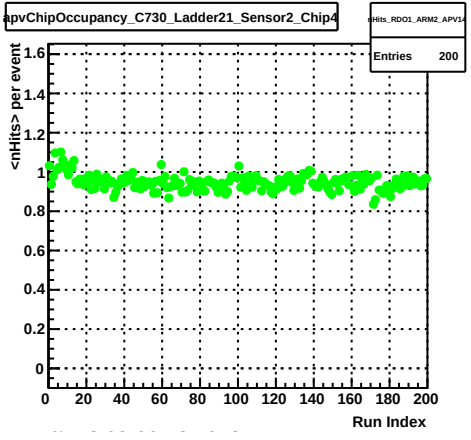

apvChipOccupancy_C725_Ladder21_Sensor1_Chip5
Entries 200
<nHits> per event
Run Index

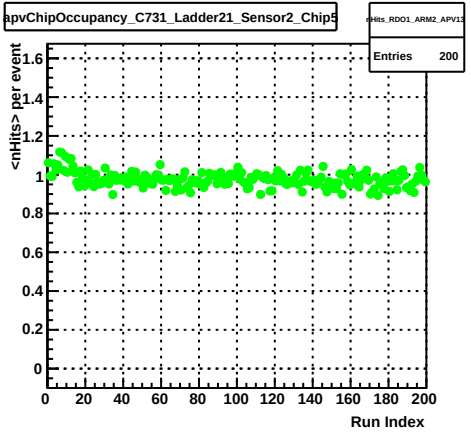

apvChipOccupancy_C726_Ladder21_Sensor1_Chip6
Entries 200
<nHits> per event
Run Index

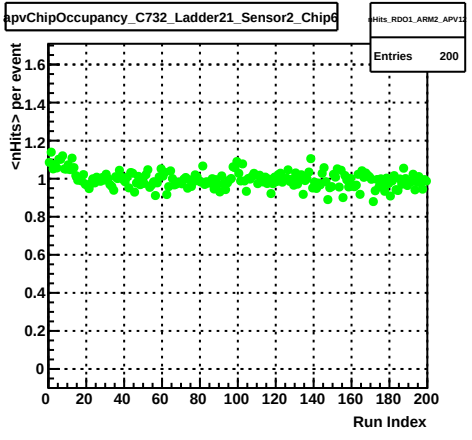

apvChipOccupancy_C727_Ladder21_Sensor2_Chip1
nHits_RD01_ARM2_APV13
Entries 200
<nHits> per event
Run Index

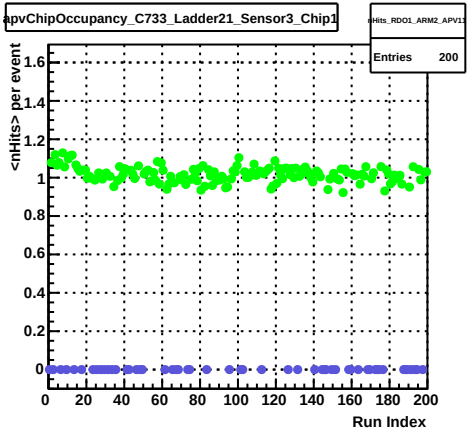

apvChipOccupancy_C728_Ladder21_Sensor2_Chip2
nHits_RD01_ARM2_APV14
Entries 200
<nHits> per event
Run Index

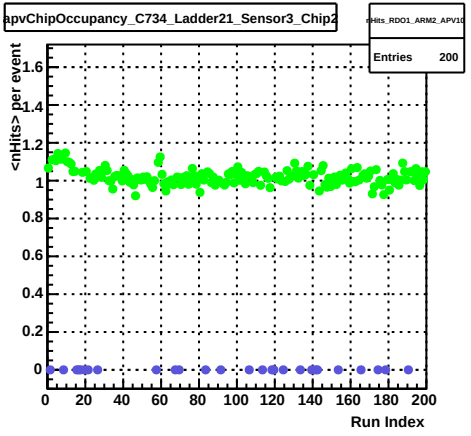

apvChipOccupancy_C729_Ladder21_Sensor2_Chip3
nHits_RD01_ARM2_APV15
Entries 200
<nHits> per event
Run Index

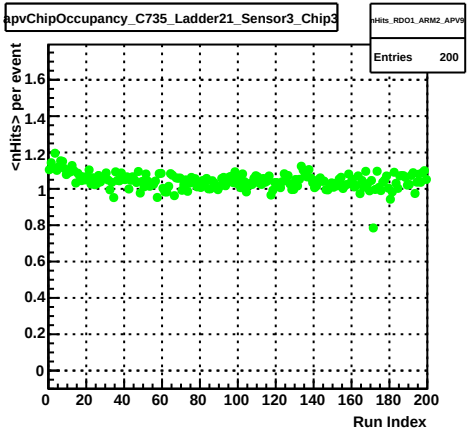

apvChipOccupancy_C730_Ladder21_Sensor2_Chip4
nHits_RD01_ARM2_APV16
Entries 200
<nHits> per event
Run Index

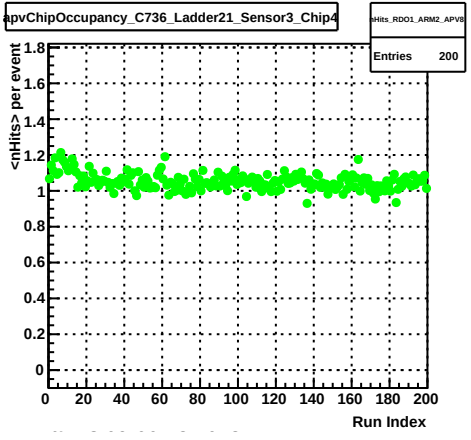

apvChipOccupancy_C731_Ladder21_Sensor2_Chip5
nHits_RD01_ARM2_APV17
Entries 200
<nHits> per event
Run Index

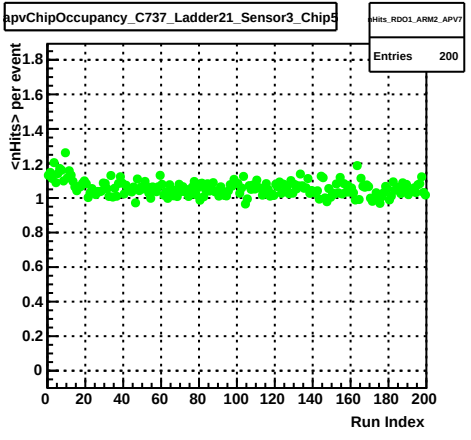

apvChipOccupancy_C732_Ladder21_Sensor2_Chip6
nHits_RD01_ARM2_APV18
Entries 200
<nHits> per event
Run Index

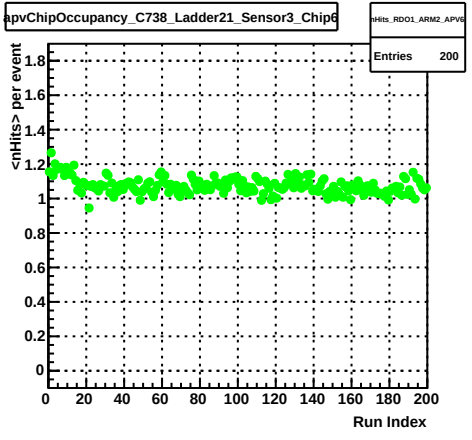

apvChipOccupancy_C733_Ladder21_Sensor3_Chip1
nHits_RDO1_ARM2_APV13
Entries 200
<nHits> per event
Run Index

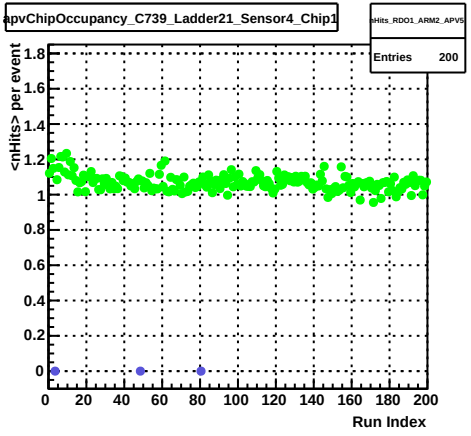

apvChipOccupancy_C734_Ladder21_Sensor3_Chip2
nHits_RDO1_ARM2_APV10
Entries 200
<nHits> per event
Run Index

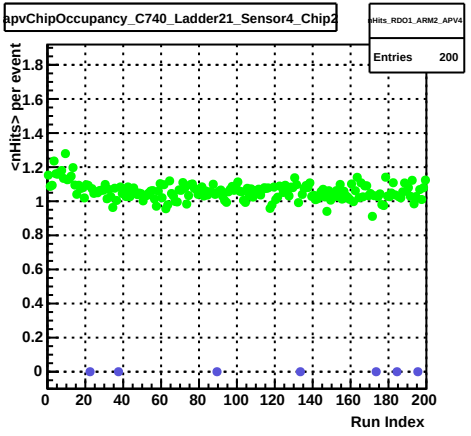

apvChipOccupancy_C735_Ladder21_Sensor3_Chip3
nHits_RDO1_ARM2_APV9
Entries 200
<nHits> per event
Run Index

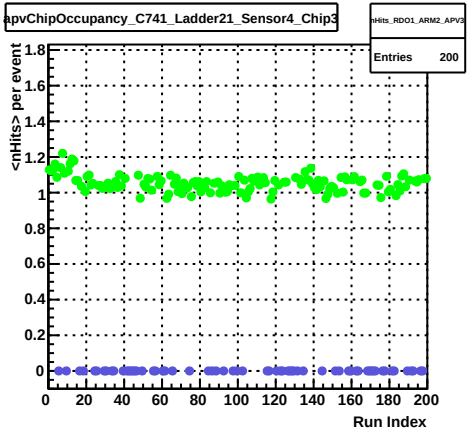

apvChipOccupancy_C736_Ladder21_Sensor3_Chip4
nHits_RDO1_ARM2_APV8
Entries 200
<nHits> per event
Run Index

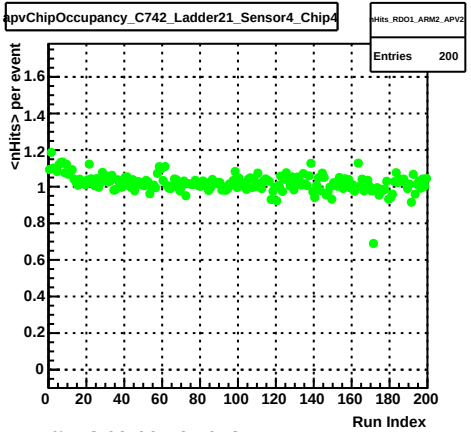

apvChipOccupancy_C737_Ladder21_Sensor3_Chip5
nHits_RDO1_ARM2_APV7
Entries 200
<nHits> per event
Run Index

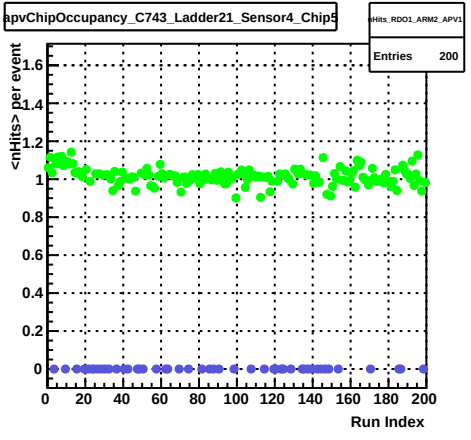

apvChipOccupancy_C738_Ladder21_Sensor3_Chip6
nHits_RDO1_ARM2_APV6
Entries 200
<nHits> per event
Run Index

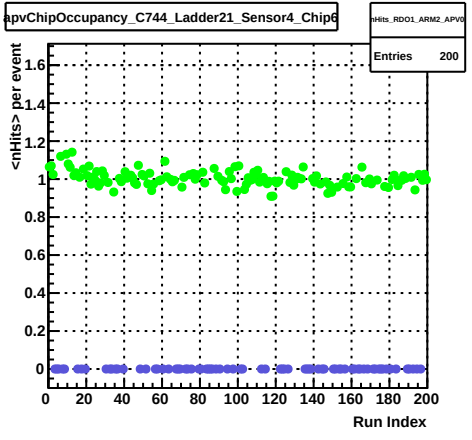

apvChipOccupancy_C739_Ladder21_Sensor4_Chip1
nHits_RD01_ARM2_APV5
Entries 200
<nHits> per event
Run Index

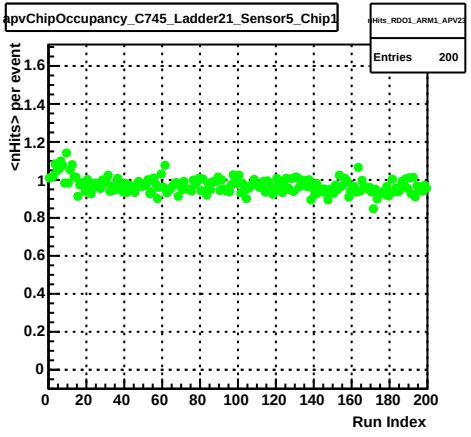

apvChipOccupancy_C740_Ladder21_Sensor4_Chip2
nHits_RD01_ARM2_APV4
Entries 200
<nHits> per event
Run Index

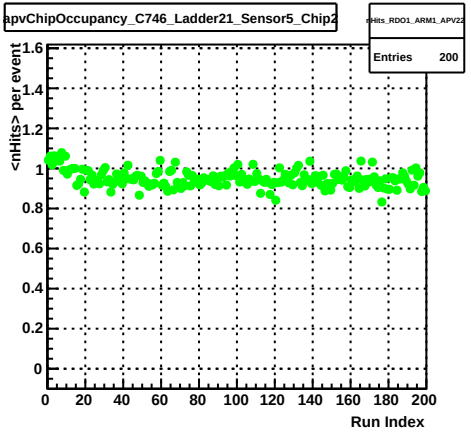

apvChipOccupancy_C741_Ladder21_Sensor4_Chip3
nHits_RD01_ARM2_APV3
Entries 200
<nHits> per event
Run Index

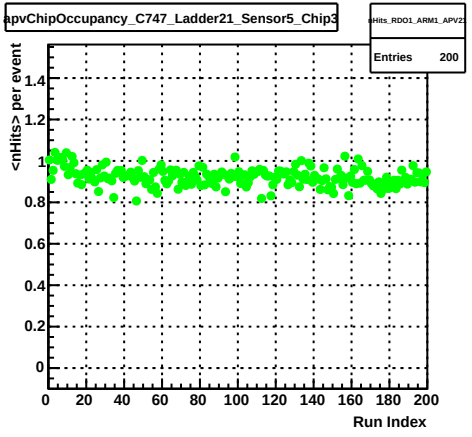

apvChipOccupancy_C742_Ladder21_Sensor4_Chip4
nHits_RD01_ARM2_APV2
Entries 200
<nHits> per event
Run Index

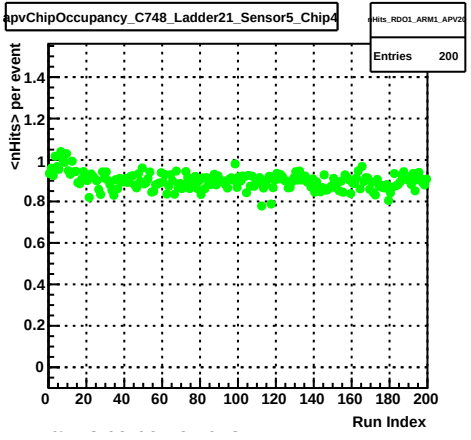

apvChipOccupancy_C743_Ladder21_Sensor4_Chip5
nHits_RD01_ARM2_APV1
Entries 200
<nHits> per event
Run Index

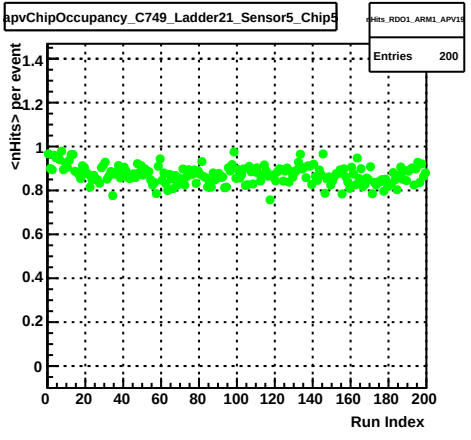

apvChipOccupancy_C744_Ladder21_Sensor4_Chip6
nHits_RD01_ARM2_APV0
Entries 200
<nHits> per event
Run Index

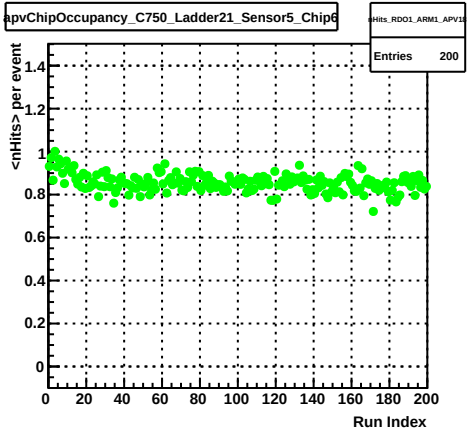

apvChipOccupancy_C745_Ladder21_Sensor5_Chip1
Entries 200
<nHits> per event
Run Index

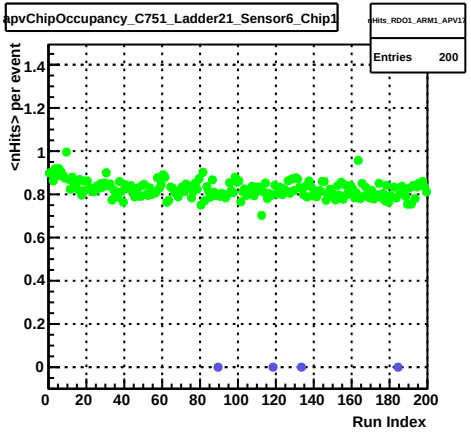

apvChipOccupancy_C746_Ladder21_Sensor5_Chip2
Entries 200
<nHits> per event
Run Index

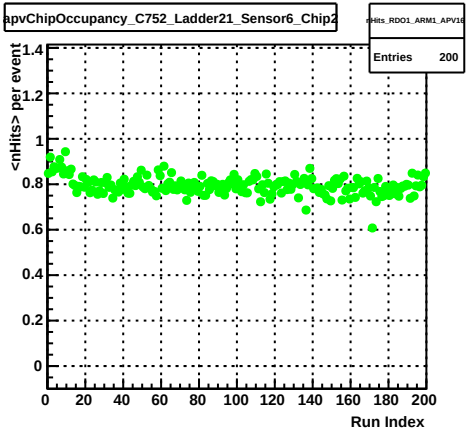

apvChipOccupancy_C747_Ladder21_Sensor5_Chip3
Entries 200
<nHits> per event
Run Index

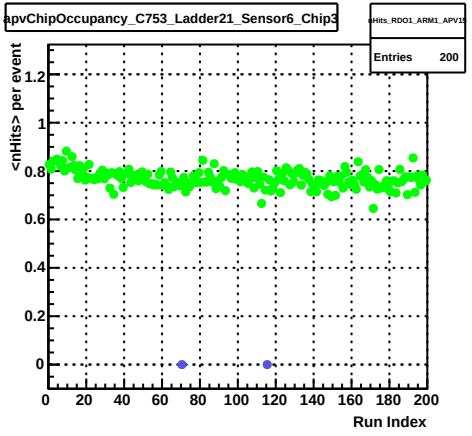

apvChipOccupancy_C748_Ladder21_Sensor5_Chip4
Entries 200
<nHits> per event
Run Index

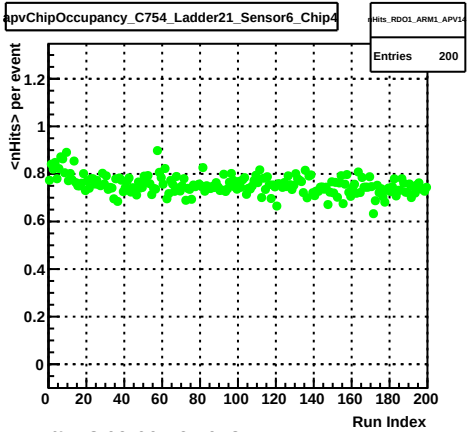

apvChipOccupancy_C749_Ladder21_Sensor5_Chip5
Entries 200
<nHits> per event
Run Index

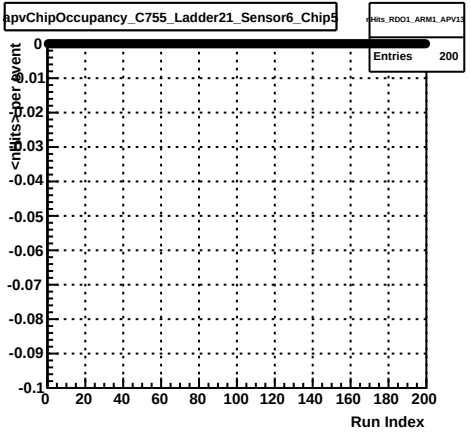

apvChipOccupancy_C750_Ladder21_Sensor5_Chip6
Entries 200
<nHits> per event
Run Index

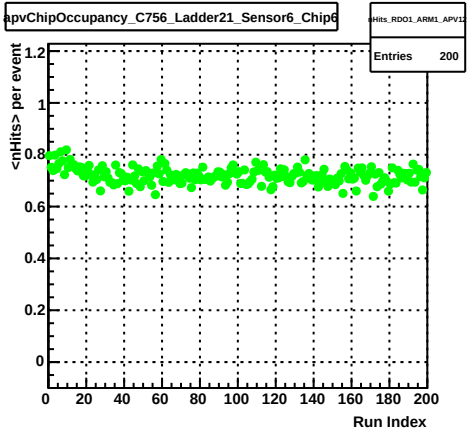

apvChipOccupancy_C751_Ladder21_Sensor6_Chip1
Entries 200
<nHits> per event
Run Index

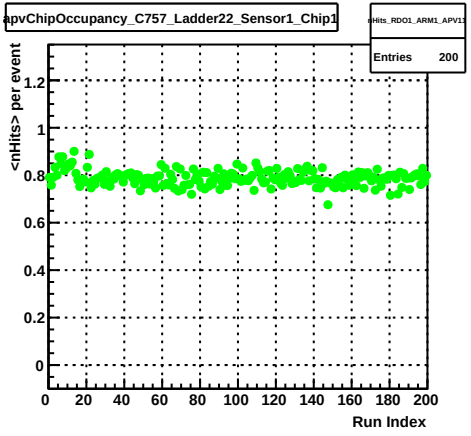

apvChipOccupancy_C752_Ladder21_Sensor6_Chip2
Entries 200
<nHits> per event
Run Index

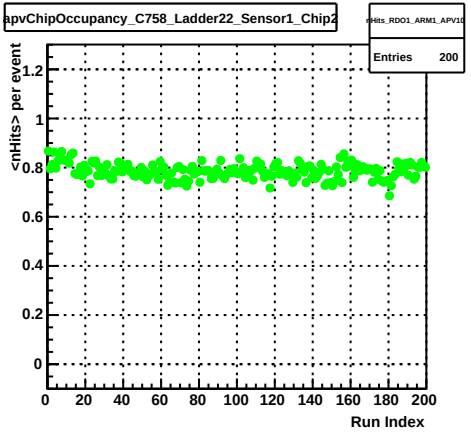

apvChipOccupancy_C753_Ladder21_Sensor6_Chip3
Entries 200
<nHits> per event
Run Index

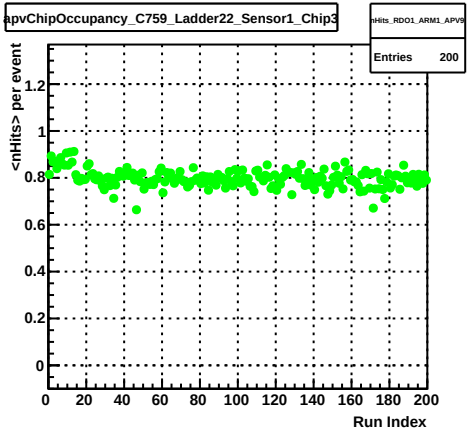

apvChipOccupancy_C754_Ladder21_Sensor6_Chip4
Entries 200
<nHits> per event
Run Index

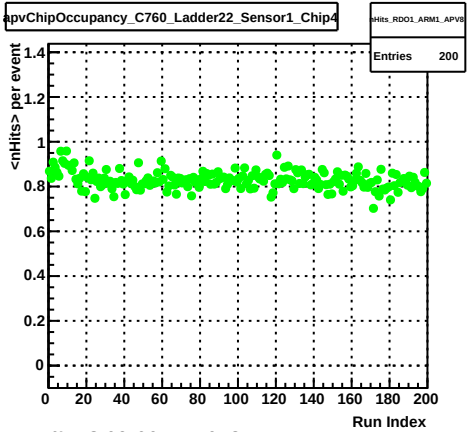

apvChipOccupancy_C755_Ladder21_Sensor6_Chip5
Entries 200
<nHits> per event
Run Index

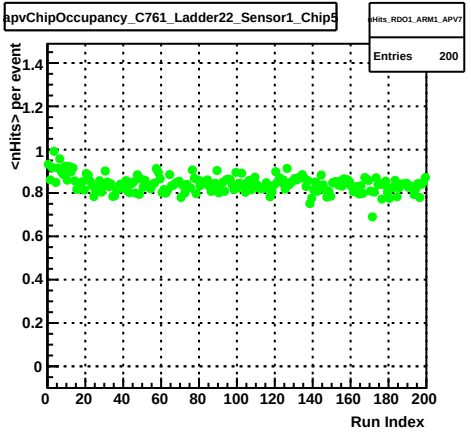

apvChipOccupancy_C756_Ladder21_Sensor6_Chip6
Entries 200
<nHits> per event
Run Index

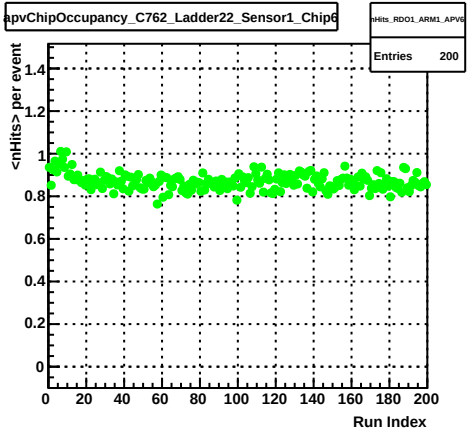

apvChipOccupancy_C757_Ladder22_Sensor1_Chip1
nHits_RDO1_ARM1_APV13
Entries 200
<nHits> per event
Run Index

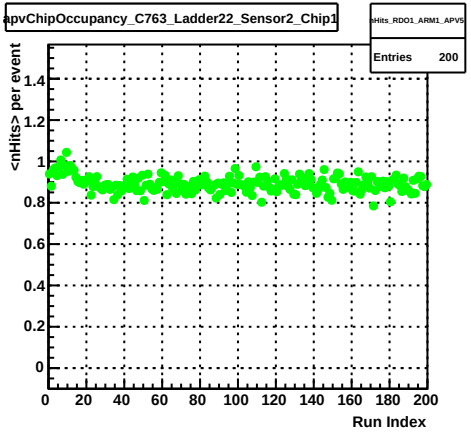

apvChipOccupancy_C758_Ladder22_Sensor1_Chip2
nHits_RDO1_ARM1_APV10
Entries 200
<nHits> per event
Run Index

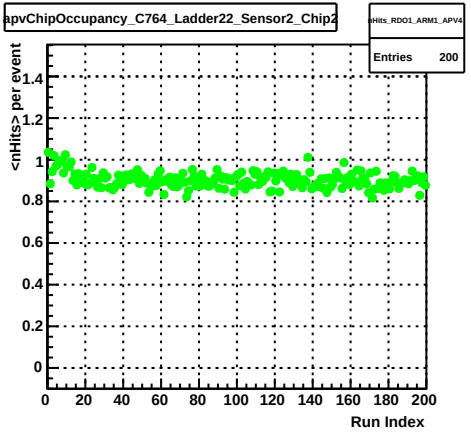

apvChipOccupancy_C759_Ladder22_Sensor1_Chip3
nHits_RDO1_ARM1_APV9
Entries 200
<nHits> per event
Run Index

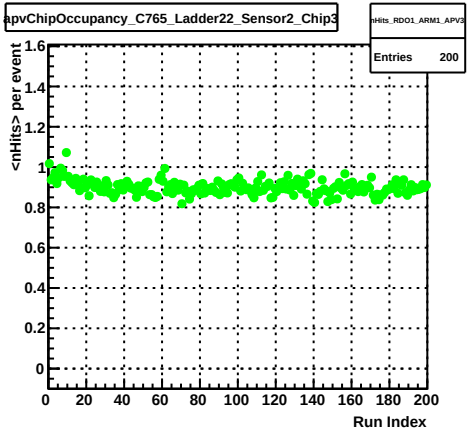

apvChipOccupancy_C760_Ladder22_Sensor1_Chip4
nHits_RDO1_ARM1_APV8
Entries 200
<nHits> per event
Run Index

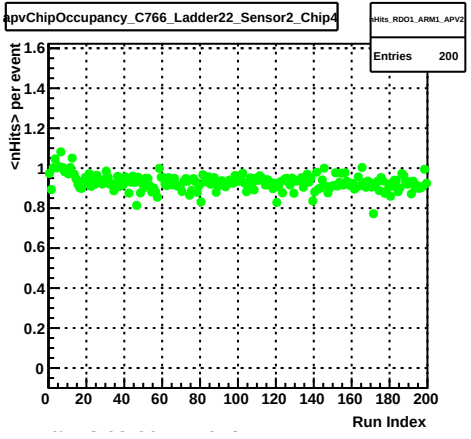

apvChipOccupancy_C761_Ladder22_Sensor1_Chip5
nHits_RDO1_ARM1_APV7
Entries 200
<nHits> per event
Run Index

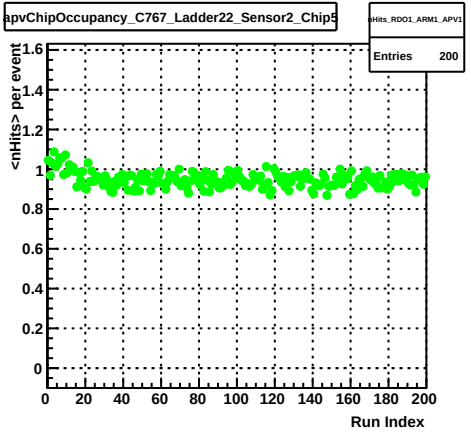

apvChipOccupancy_C762_Ladder22_Sensor1_Chip6
nHits_RDO1_ARM1_APV6
Entries 200
<nHits> per event
Run Index

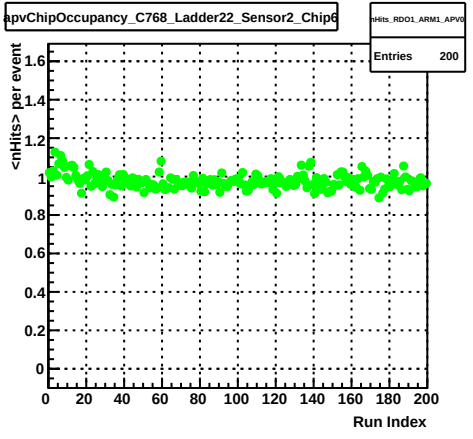

apvChipOccupancy_C763_Ladder22_Sensor2_Chip1
nHits_RD01_ARM1_APV5
Entries 200
<nHits> per event
Run Index

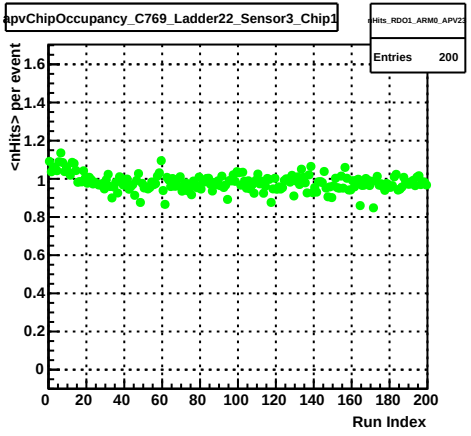

apvChipOccupancy_C764_Ladder22_Sensor2_Chip2
nHits_RD01_ARM1_APV4
Entries 200
<nHits> per event
Run Index

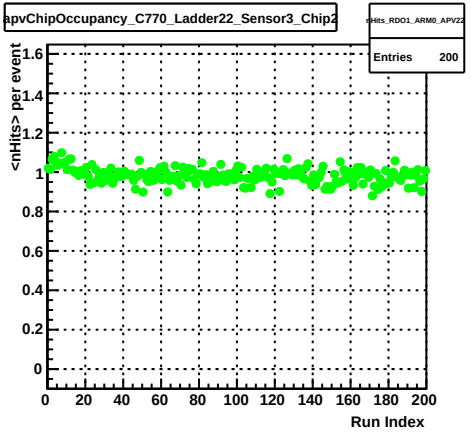

apvChipOccupancy_C765_Ladder22_Sensor2_Chip3
nHits_RD01_ARM1_APV3
Entries 200
<nHits> per event
Run Index

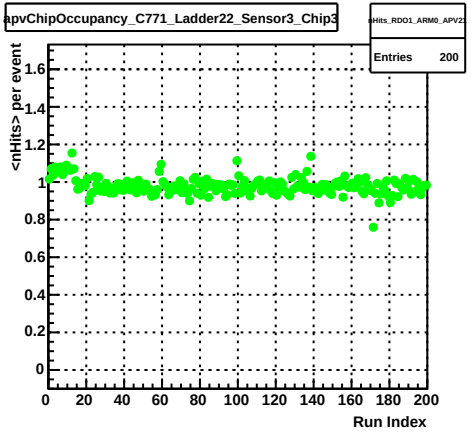

apvChipOccupancy_C766_Ladder22_Sensor2_Chip4
nHits_RD01_ARM1_APV2
Entries 200
<nHits> per event
Run Index

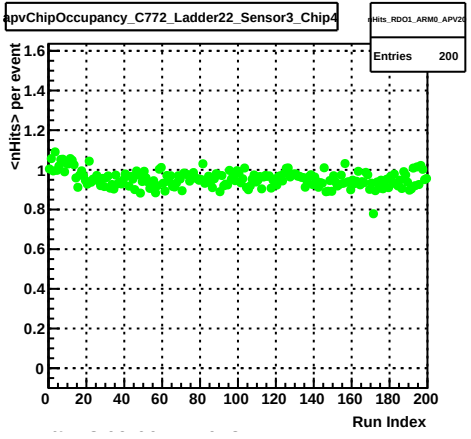

apvChipOccupancy_C767_Ladder22_Sensor2_Chip5
nHits_RD01_ARM1_APV1
Entries 200
<nHits> per event
Run Index

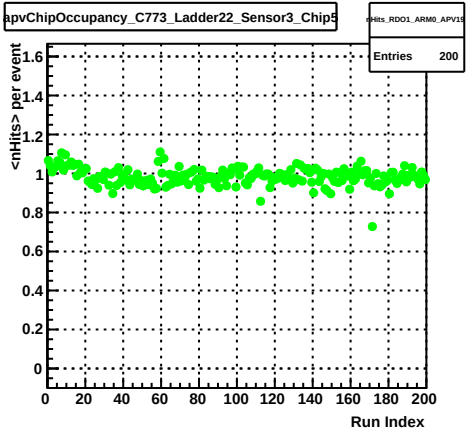

apvChipOccupancy_C768_Ladder22_Sensor2_Chip6
nHits_RD01_ARM1_APV0
Entries 200
<nHits> per event
Run Index

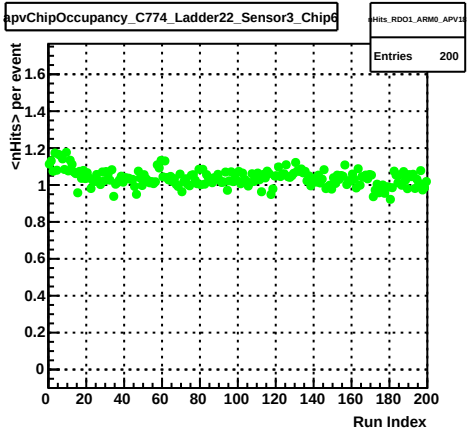

apvChipOccupancy_C769_Ladder22_Sensor3_Chip1
Entries 200
<nHits> per event
Run Index

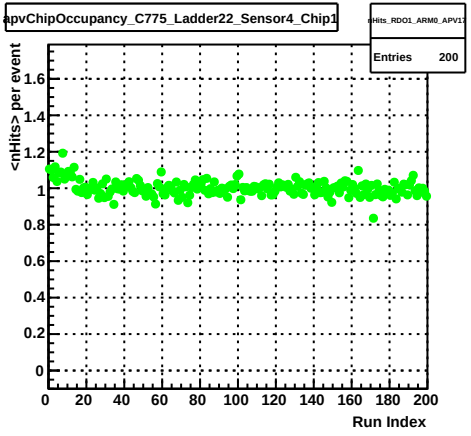

apvChipOccupancy_C770_Ladder22_Sensor3_Chip2
Entries 200
<nHits> per event
Run Index

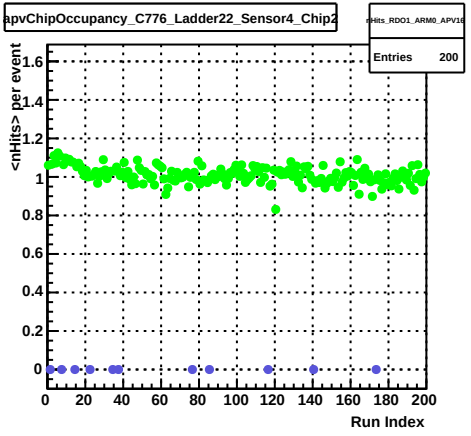

apvChipOccupancy_C771_Ladder22_Sensor3_Chip3
Entries 200
<nHits> per event
Run Index

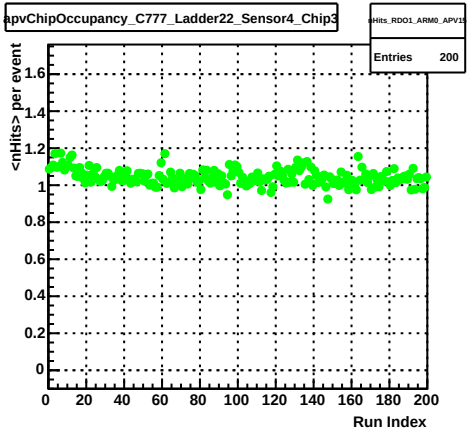

apvChipOccupancy_C772_Ladder22_Sensor3_Chip4
Entries 200
<nHits> per event
Run Index

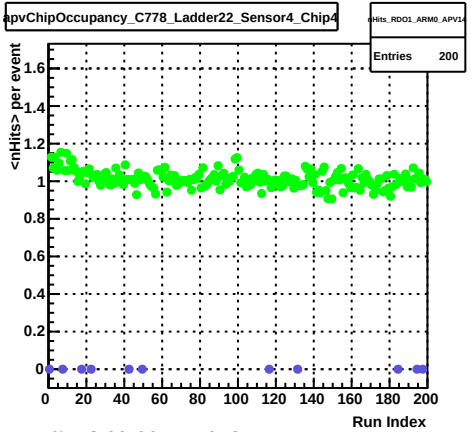

apvChipOccupancy_C773_Ladder22_Sensor3_Chip5
Entries 200
<nHits> per event
Run Index

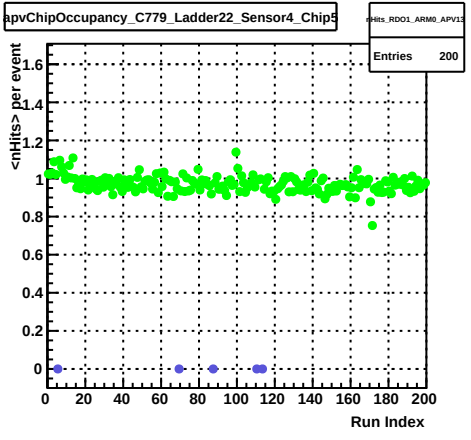

apvChipOccupancy_C774_Ladder22_Sensor3_Chip6
Entries 200
<nHits> per event
Run Index

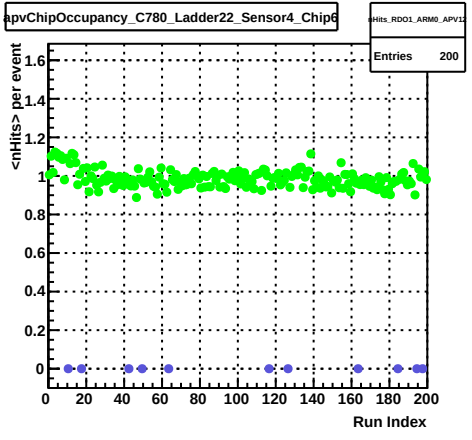

apvChipOccupancy_C775_Ladder22_Sensor4_Chip1
Entries 200
<nHits> per event
Run Index

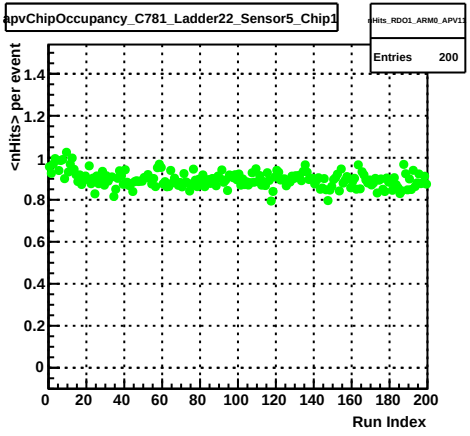

apvChipOccupancy_C776_Ladder22_Sensor4_Chip2
Entries 200
<nHits> per event
Run Index

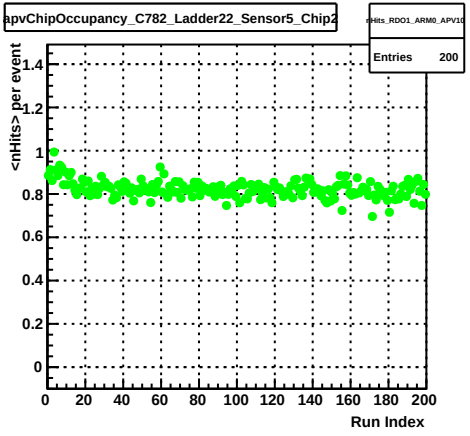

apvChipOccupancy_C777_Ladder22_Sensor4_Chip3
Entries 200
<nHits> per event
Run Index

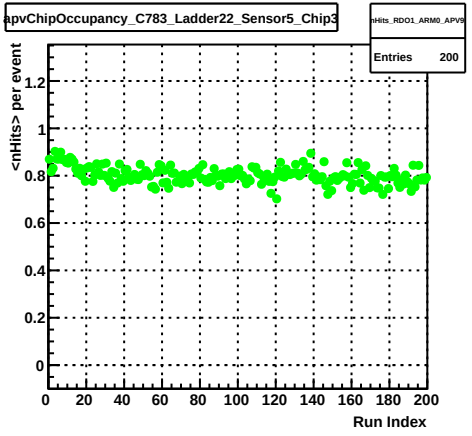

apvChipOccupancy_C778_Ladder22_Sensor4_Chip4
Entries 200
<nHits> per event
Run Index

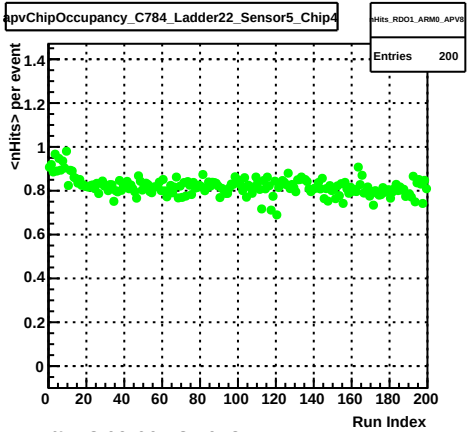

apvChipOccupancy_C779_Ladder22_Sensor4_Chip5
Entries 200
<nHits> per event
Run Index

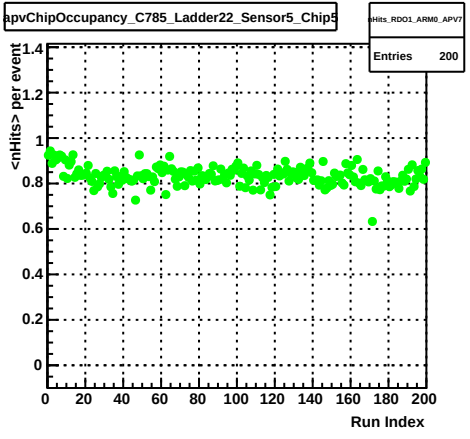

apvChipOccupancy_C780_Ladder22_Sensor4_Chip6
Entries 200
<nHits> per event
Run Index

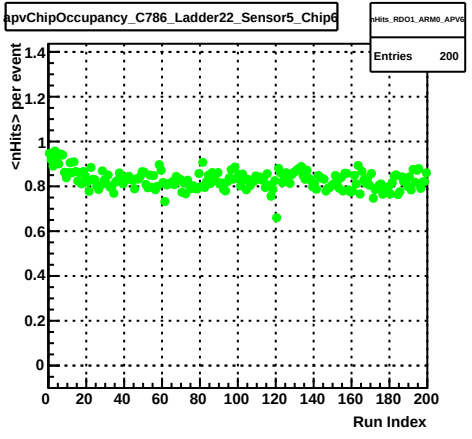

apvChipOccupancy_C781_Ladder22_Sensor5_Chip1
nHits_RDO1_ARM0_APV13
Entries 200
<nHits> per event
Run Index

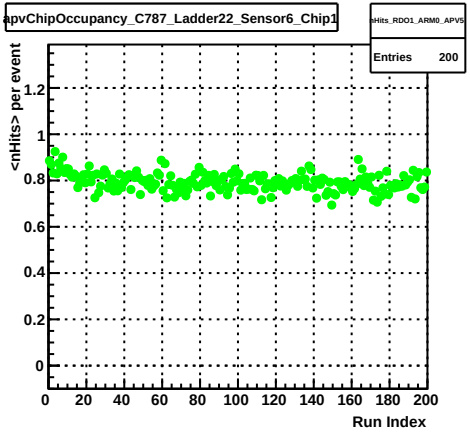

apvChipOccupancy_C782_Ladder22_Sensor5_Chip2
nHits_RDO1_ARM0_APV10
Entries 200
<nHits> per event
Run Index

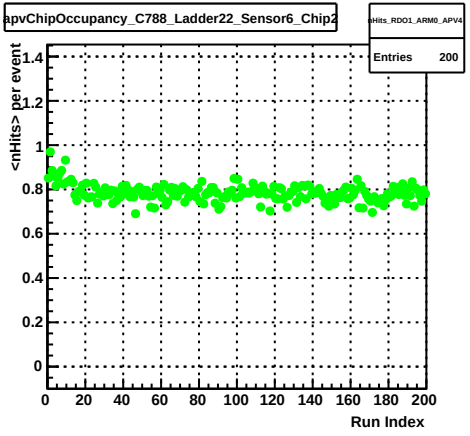

apvChipOccupancy_C783_Ladder22_Sensor5_Chip3
nHits_RDO1_ARM0_APV9
Entries 200
<nHits> per event
Run Index

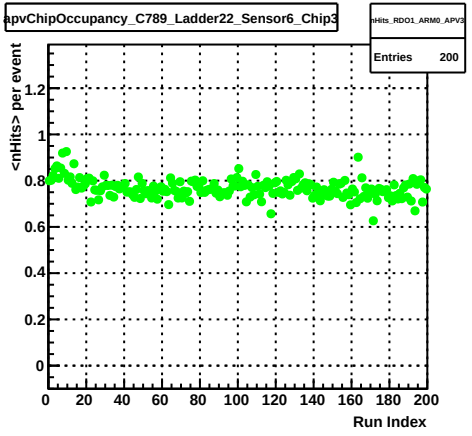

apvChipOccupancy_C784_Ladder22_Sensor5_Chip4
nHits_RDO1_ARM0_APV8
Entries 200
<nHits> per event
Run Index

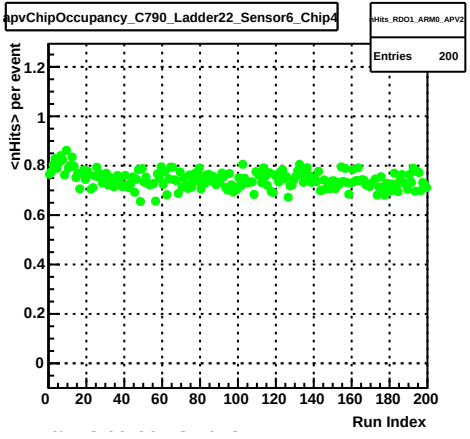

apvChipOccupancy_C785_Ladder22_Sensor5_Chip5
nHits_RDO1_ARM0_APV7
Entries 200
<nHits> per event
Run Index

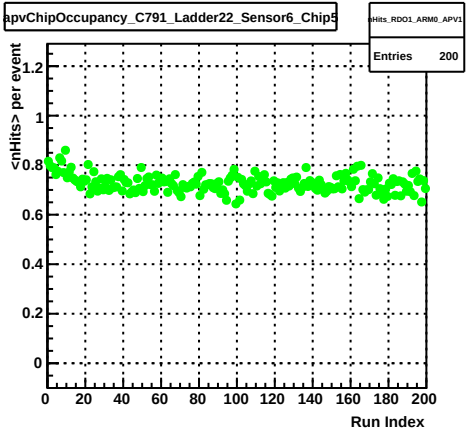

apvChipOccupancy_C786_Ladder22_Sensor5_Chip6
nHits_RDO1_ARM0_APV6
Entries 200
<nHits> per event
Run Index

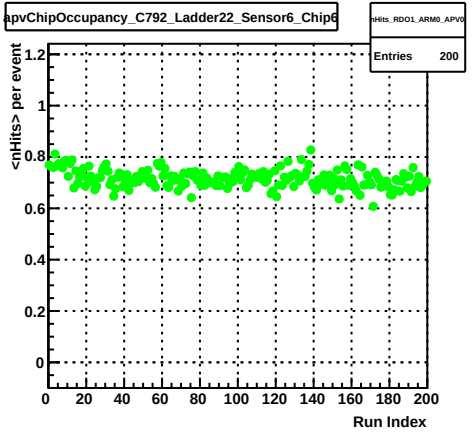

apvChipOccupancy_C787_Ladder22_Sensor6_Chip1
nHits_RDO1_ARM0_APV5
Entries 200
<nHits> per event
Run Index

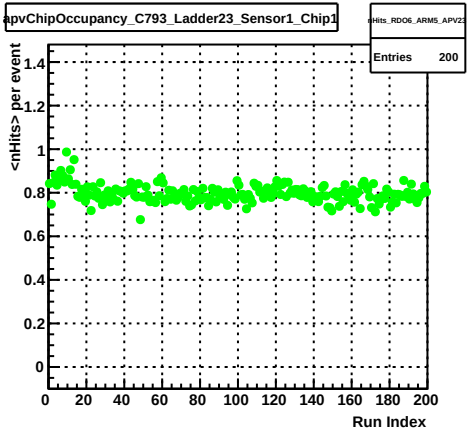

apvChipOccupancy_C788_Ladder22_Sensor6_Chip2
nHits_RDO1_ARM0_APV4
Entries 200
<nHits> per event
Run Index

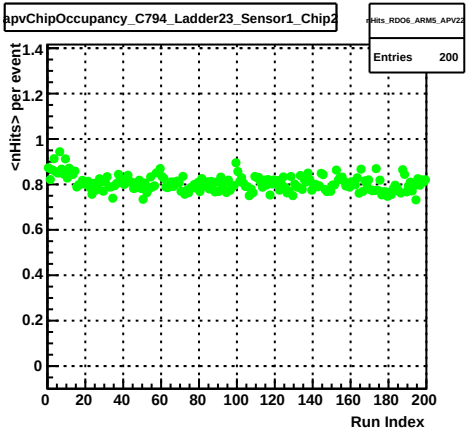

apvChipOccupancy_C789_Ladder22_Sensor6_Chip3
nHits_RDO1_ARM0_APV3
Entries 200
<nHits> per event
Run Index

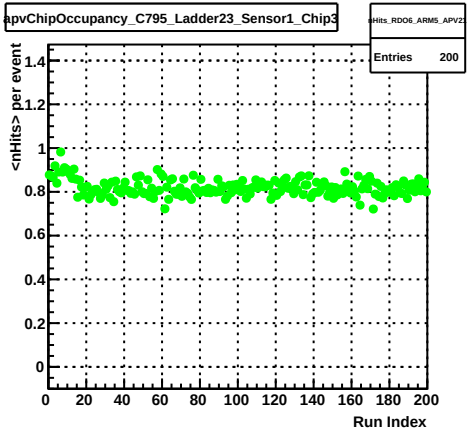

apvChipOccupancy_C790_Ladder22_Sensor6_Chip4
nHits_RDO1_ARM0_APV2
Entries 200
<nHits> per event
Run Index

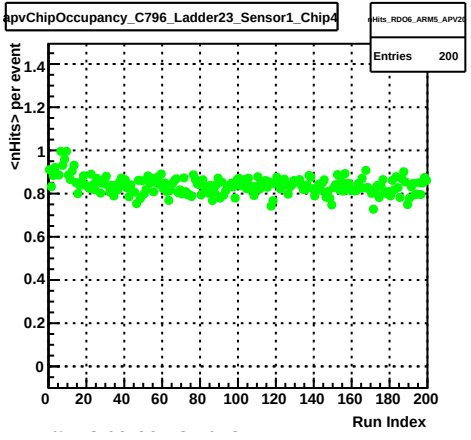

apvChipOccupancy_C791_Ladder22_Sensor6_Chip5
nHits_RDO1_ARM0_APV1
Entries 200
<nHits> per event
Run Index

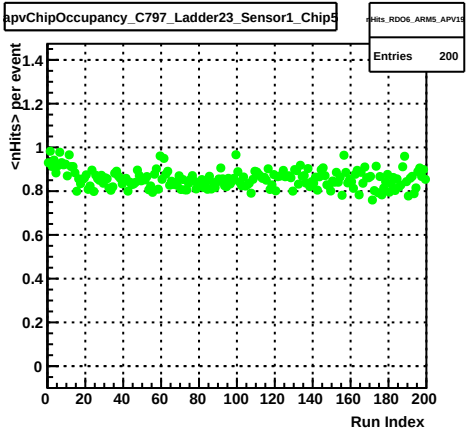

apvChipOccupancy_C792_Ladder22_Sensor6_Chip6
nHits_RDO1_ARM0_APV0
Entries 200
<nHits> per event
Run Index

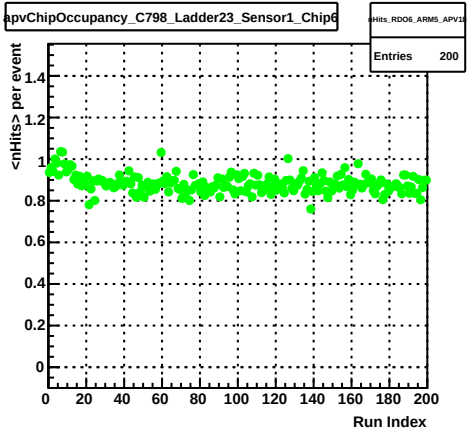

apvChipOccupancy_C793_Ladder23_Sensor1_Chip1
Entries 200
<nHits> per event
Run Index

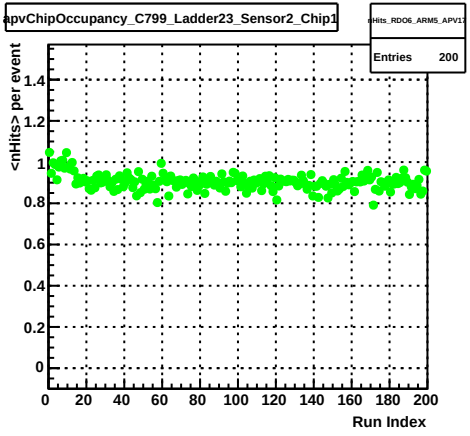

apvChipOccupancy_C794_Ladder23_Sensor1_Chip2
Entries 200
<nHits> per event
Run Index

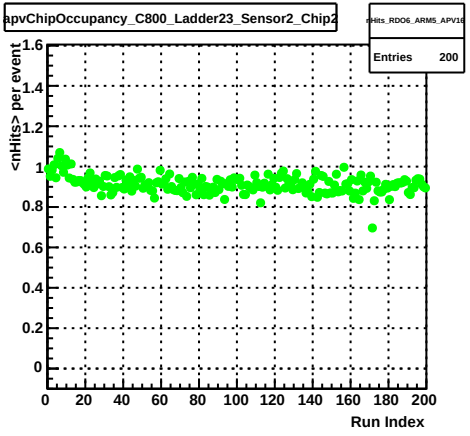

apvChipOccupancy_C795_Ladder23_Sensor1_Chip3
Entries 200
<nHits> per event
Run Index

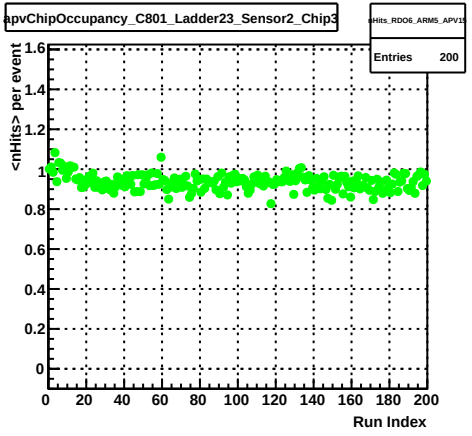

apvChipOccupancy_C796_Ladder23_Sensor1_Chip4
Entries 200
<nHits> per event
Run Index

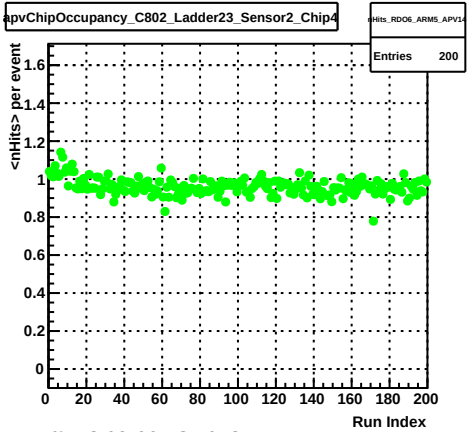

apvChipOccupancy_C797_Ladder23_Sensor1_Chip5
Entries 200
<nHits> per event
Run Index

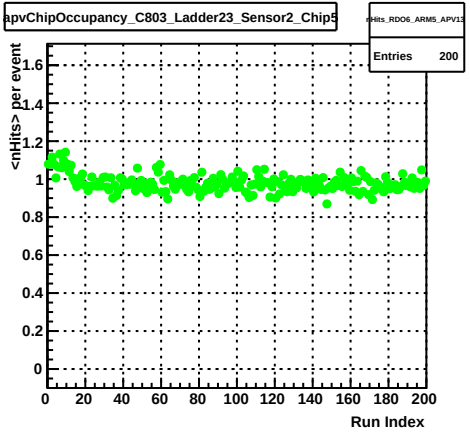

apvChipOccupancy_C798_Ladder23_Sensor1_Chip6
Entries 200
<nHits> per event
Run Index

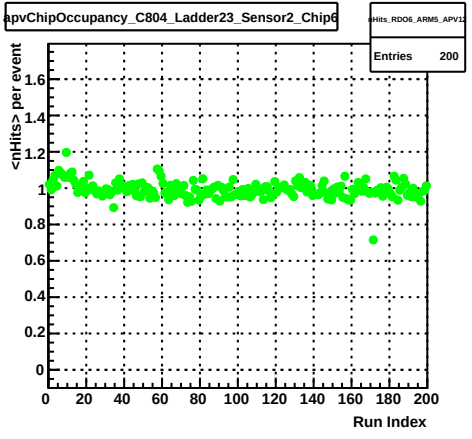

apvChipOccupancy_C799_Ladder23_Sensor2_Chip1
Entries 200
<nHits> per event
Run Index

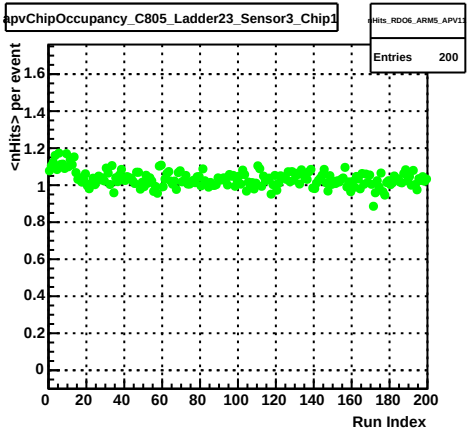

apvChipOccupancy_C800_Ladder23_Sensor2_Chip2
Entries 200
<nHits> per event
Run Index

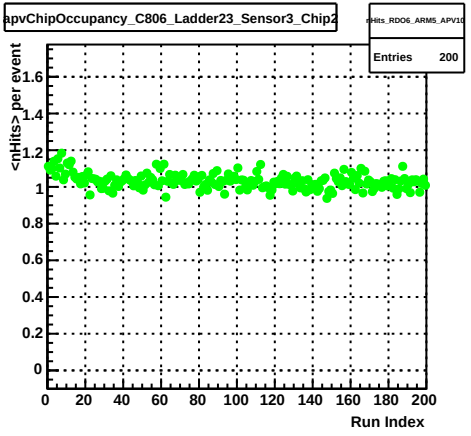

apvChipOccupancy_C801_Ladder23_Sensor2_Chip3
Entries 200
<nHits> per event
Run Index

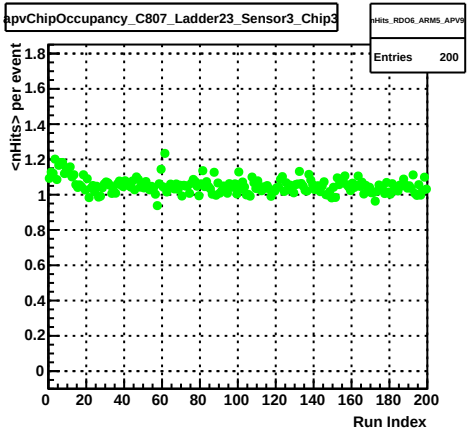

apvChipOccupancy_C802_Ladder23_Sensor2_Chip4
Entries 200
<nHits> per event
Run Index

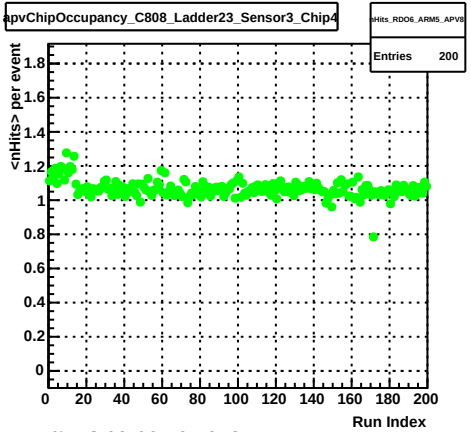

apvChipOccupancy_C803_Ladder23_Sensor2_Chip5
Entries 200
<nHits> per event
Run Index

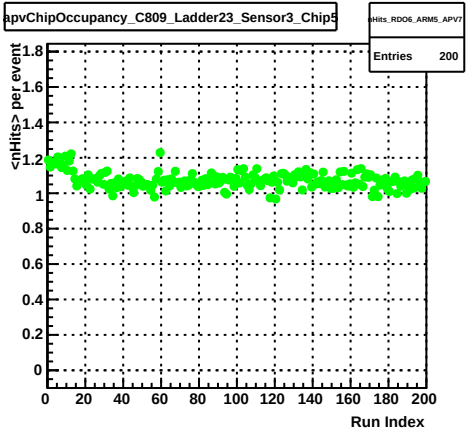

apvChipOccupancy_C804_Ladder23_Sensor2_Chip6
Entries 200
<nHits> per event
Run Index

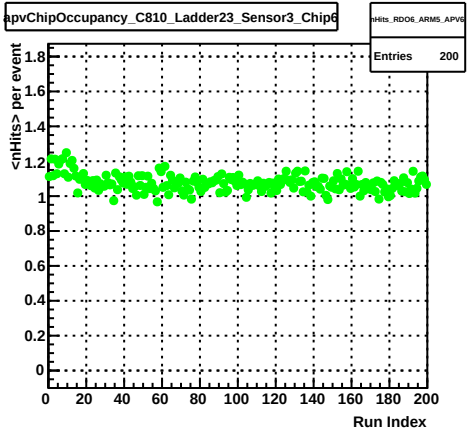

apvChipOccupancy_C805_Ladder23_Sensor3_Chip1
nHits_RDO6_ARM5_APV13
Entries 200
<nHits> per event
Run Index

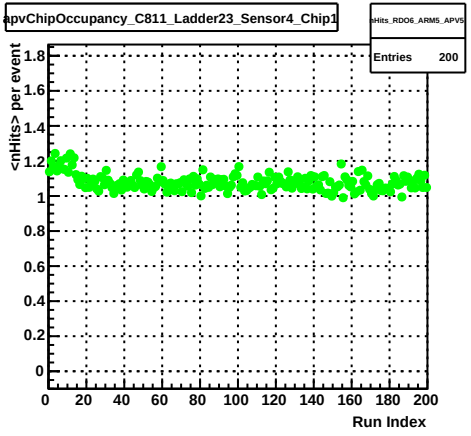

apvChipOccupancy_C806_Ladder23_Sensor3_Chip2
nHits_RDO6_ARM5_APV10
Entries 200
<nHits> per event
Run Index

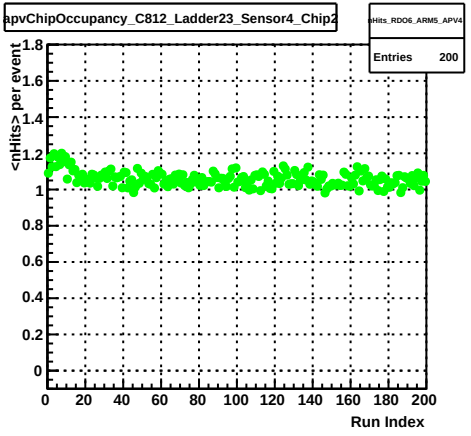

apvChipOccupancy_C807_Ladder23_Sensor3_Chip3
nHits_RDO6_ARM5_APV9
Entries 200
<nHits> per event
Run Index

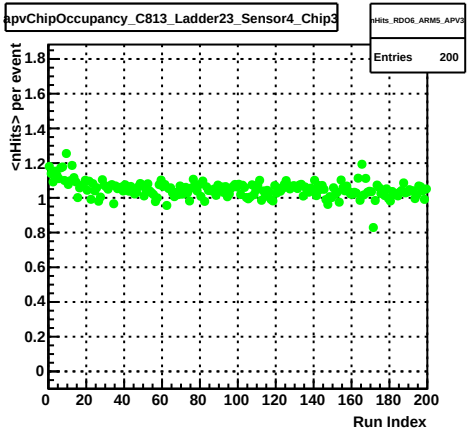

apvChipOccupancy_C808_Ladder23_Sensor3_Chip4
nHits_RDO6_ARM5_APV8
Entries 200
<nHits> per event
Run Index

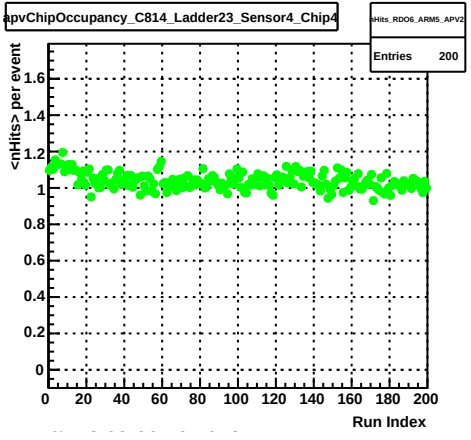

apvChipOccupancy_C809_Ladder23_Sensor3_Chip5
nHits_RDO6_ARM5_APV7
Entries 200
<nHits> per event
Run Index

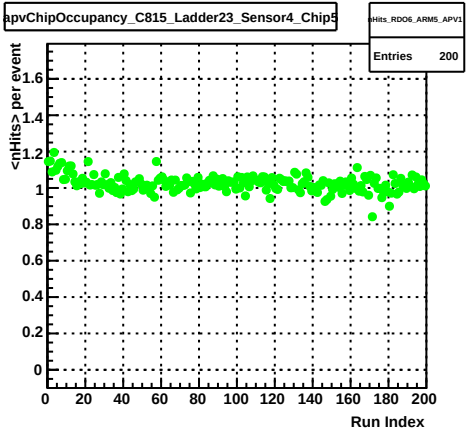

apvChipOccupancy_C810_Ladder23_Sensor3_Chip6
nHits_RDO6_ARM5_APV6
Entries 200
<nHits> per event
Run Index

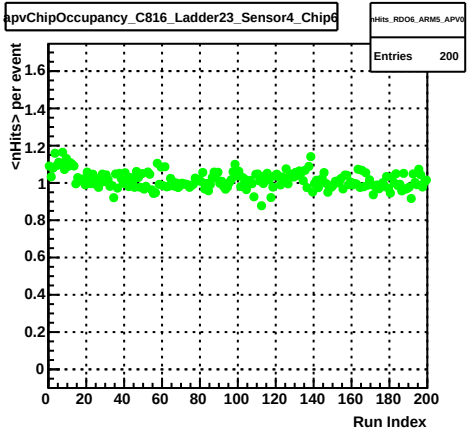

apvChipOccupancy_C811_Ladder23_Sensor4_Chip1
nHits_RDO6_ARM5_APV5
Entries 200
<nHits> per event
Run Index

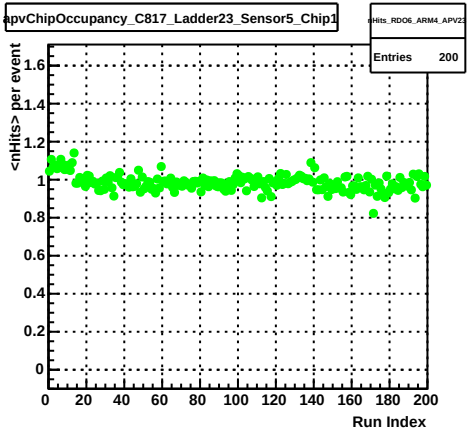

apvChipOccupancy_C812_Ladder23_Sensor4_Chip2
nHits_RDO6_ARM5_APV4
Entries 200
<nHits> per event
Run Index

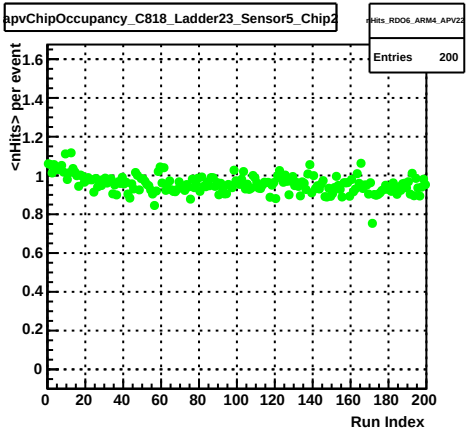

apvChipOccupancy_C813_Ladder23_Sensor4_Chip3
nHits_RDO6_ARM5_APV3
Entries 200
<nHits> per event
Run Index

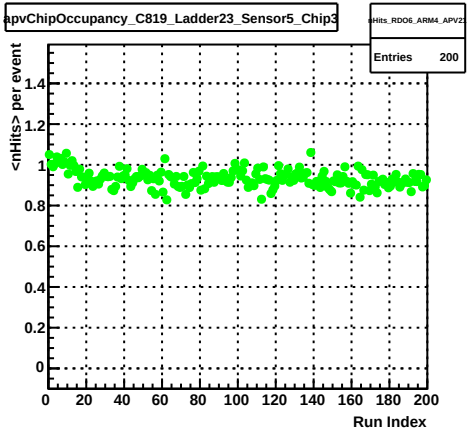

apvChipOccupancy_C814_Ladder23_Sensor4_Chip4
nHits_RDO6_ARM5_APV2
Entries 200
<nHits> per event
Run Index

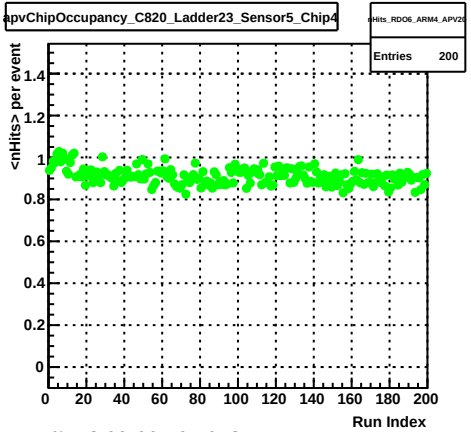

apvChipOccupancy_C815_Ladder23_Sensor4_Chip5
nHits_RDO6_ARM5_APV1
Entries 200
<nHits> per event
Run Index

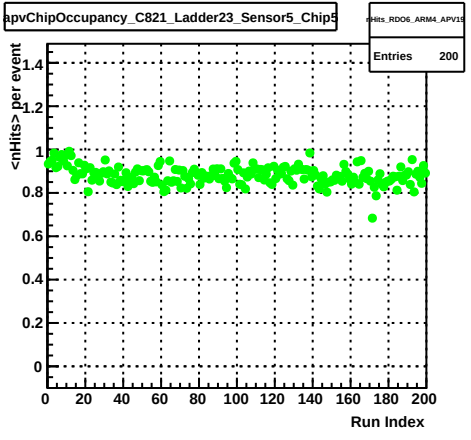

apvChipOccupancy_C816_Ladder23_Sensor4_Chip6
nHits_RDO6_ARM5_APV0
Entries 200
<nHits> per event
Run Index

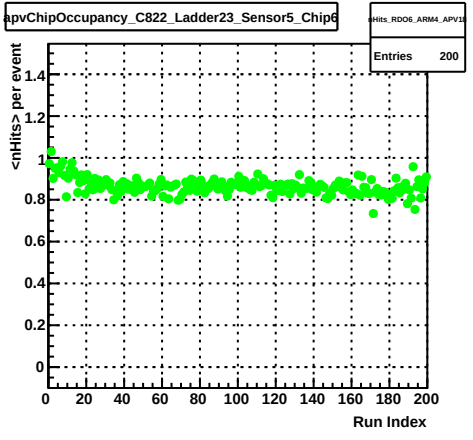

apvChipOccupancy_C817_Ladder23_Sensor5_Chip1
nHits_RD06_ARM4_APV23
Entries 200
<nHits> per event
Run Index

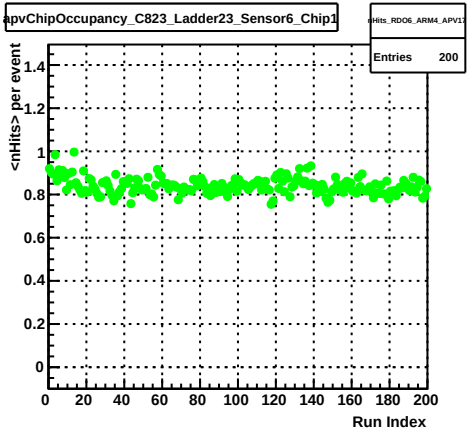

apvChipOccupancy_C818_Ladder23_Sensor5_Chip2
nHits_RD06_ARM4_APV22
Entries 200
<nHits> per event
Run Index

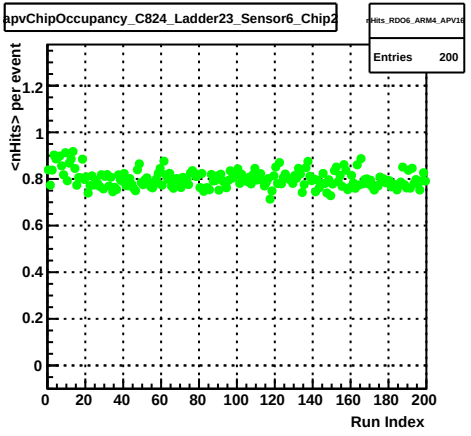

apvChipOccupancy_C819_Ladder23_Sensor5_Chip3
nHits_RD06_ARM4_APV21
Entries 200
<nHits> per event
Run Index

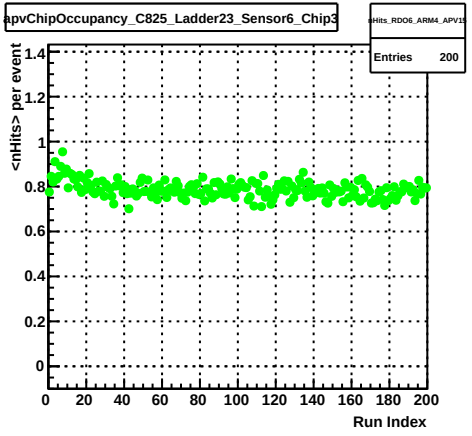

apvChipOccupancy_C820_Ladder23_Sensor5_Chip4
nHits_RD06_ARM4_APV20
Entries 200
<nHits> per event
Run Index

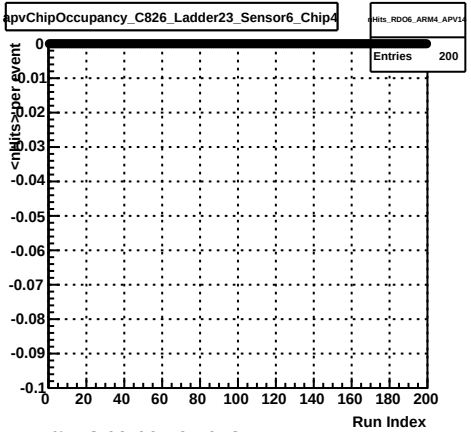

apvChipOccupancy_C821_Ladder23_Sensor5_Chip5
nHits_RD06_ARM4_APV19
Entries 200
<nHits> per event
Run Index

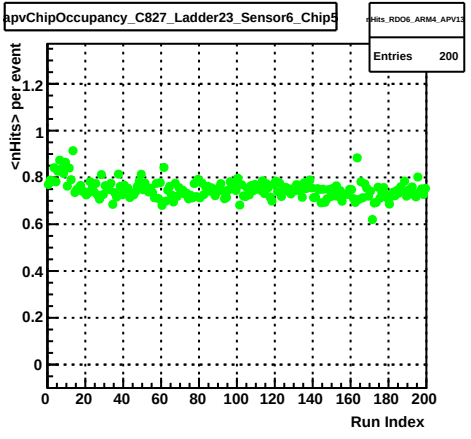

apvChipOccupancy_C822_Ladder23_Sensor5_Chip6
nHits_RD06_ARM4_APV18
Entries 200
<nHits> per event
Run Index

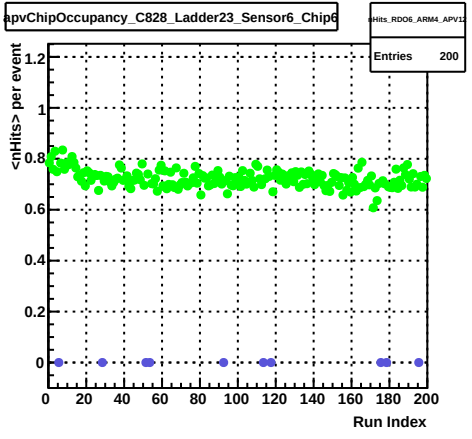

apvChipOccupancy_C823_Ladder23_Sensor6_Chip1
Entries 200
<nHits> per event
Run Index

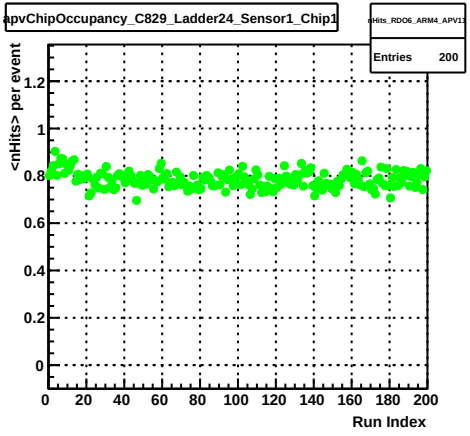

apvChipOccupancy_C824_Ladder23_Sensor6_Chip2
Entries 200
<nHits> per event
Run Index

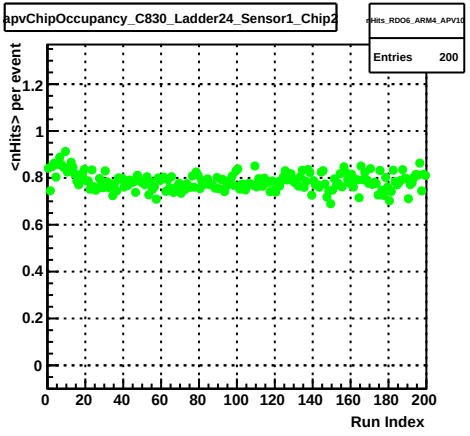

apvChipOccupancy_C825_Ladder23_Sensor6_Chip3
Entries 200
<nHits> per event
Run Index

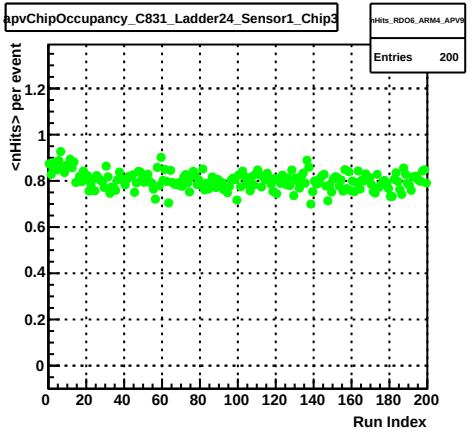

apvChipOccupancy_C826_Ladder23_Sensor6_Chip4
Entries 200
<nHits> per event
Run Index

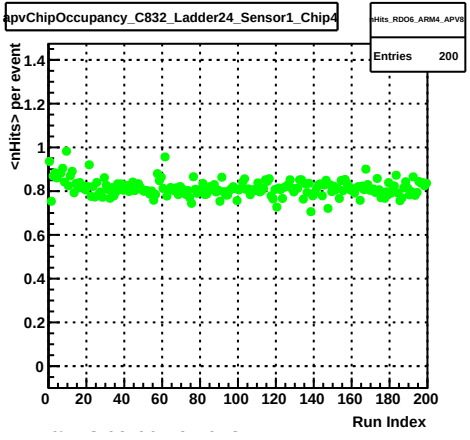

apvChipOccupancy_C827_Ladder23_Sensor6_Chip5
Entries 200
<nHits> per event
Run Index

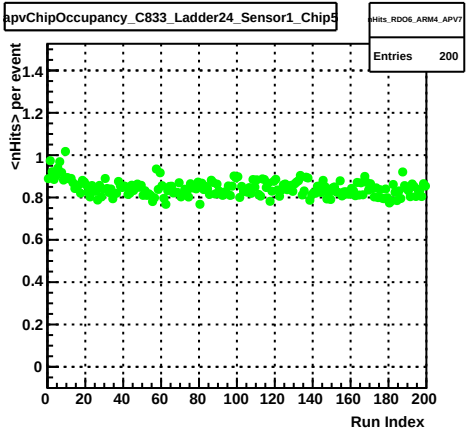

apvChipOccupancy_C828_Ladder23_Sensor6_Chip6
Entries 200
<nHits> per event
Run Index

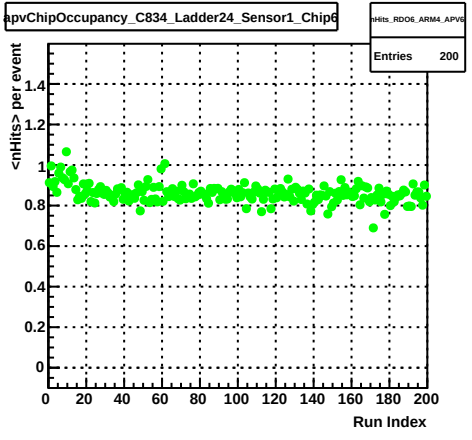

apvChipOccupancy_C829_Ladder24_Sensor1_Chip1
nHits_RDO6_ARM4_APV13
Entries 200
<nHits> per event
Run Index

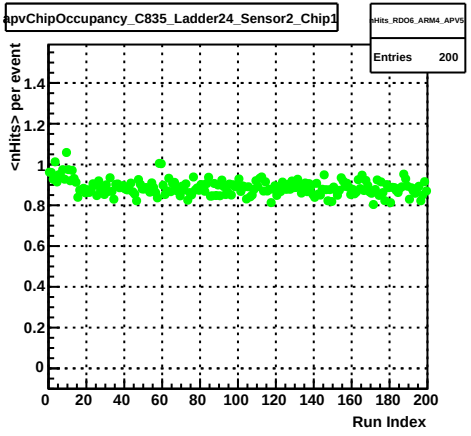

apvChipOccupancy_C830_Ladder24_Sensor1_Chip2
nHits_RDO6_ARM4_APV10
Entries 200
<nHits> per event
Run Index

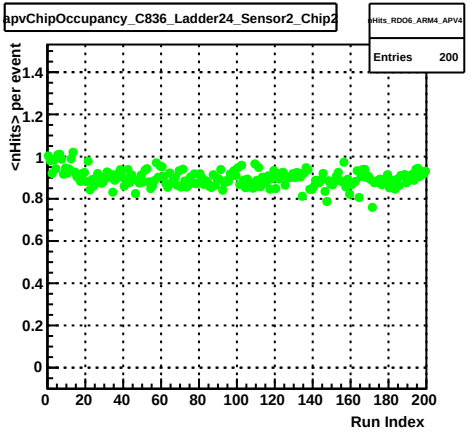

apvChipOccupancy_C831_Ladder24_Sensor1_Chip3
nHits_RDO6_ARM4_APV9
Entries 200
<nHits> per event
Run Index

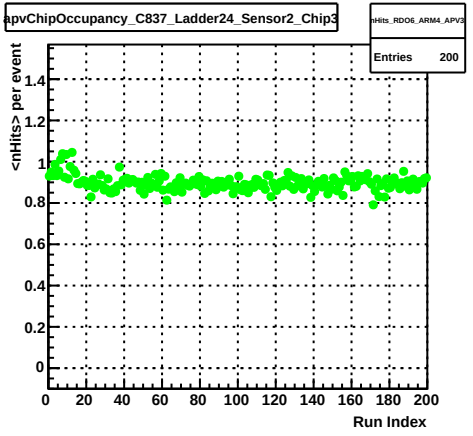

apvChipOccupancy_C832_Ladder24_Sensor1_Chip4
nHits_RDO6_ARM4_APV8
Entries 200
<nHits> per event
Run Index

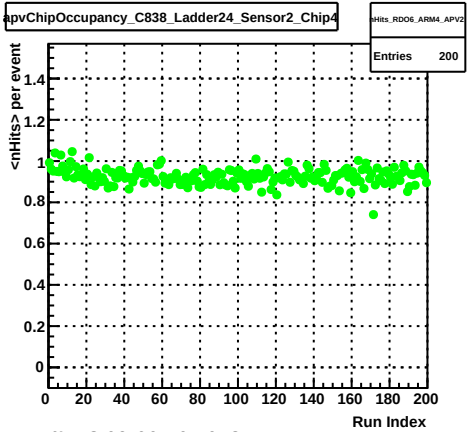

apvChipOccupancy_C833_Ladder24_Sensor1_Chip5
nHits_RDO6_ARM4_APV7
Entries 200
<nHits> per event
Run Index

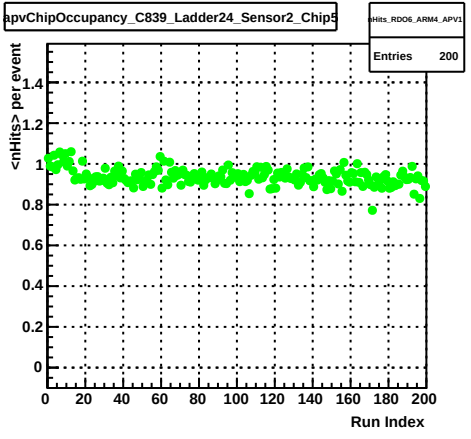

apvChipOccupancy_C834_Ladder24_Sensor1_Chip6
nHits_RDO6_ARM4_APV6
Entries 200
<nHits> per event
Run Index

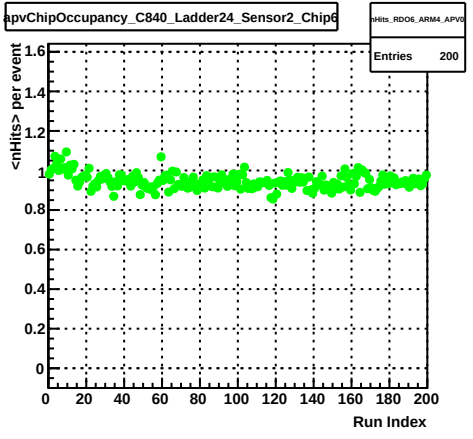

apvChipOccupancy_C835_Ladder24_Sensor2_Chip1
nHits_RDO6_ARM4_APV5
Entries 200
<nHits> per event
Run Index

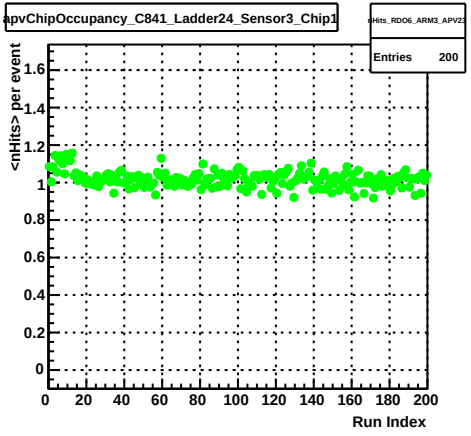

apvChipOccupancy_C836_Ladder24_Sensor2_Chip2
nHits_RDO6_ARM4_APV4
Entries 200
<nHits> per event
Run Index

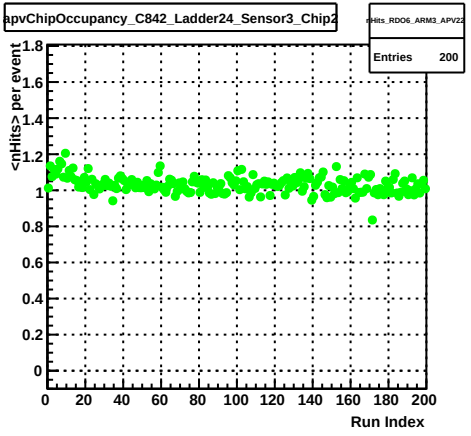

apvChipOccupancy_C837_Ladder24_Sensor2_Chip3
nHits_RDO6_ARM4_APV3
Entries 200
<nHits> per event
Run Index

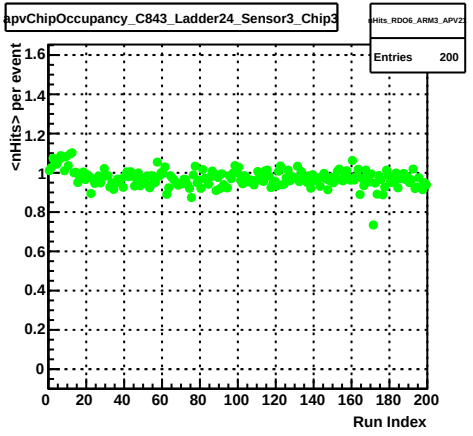

apvChipOccupancy_C838_Ladder24_Sensor2_Chip4
nHits_RDO6_ARM4_APV2
Entries 200
<nHits> per event
Run Index

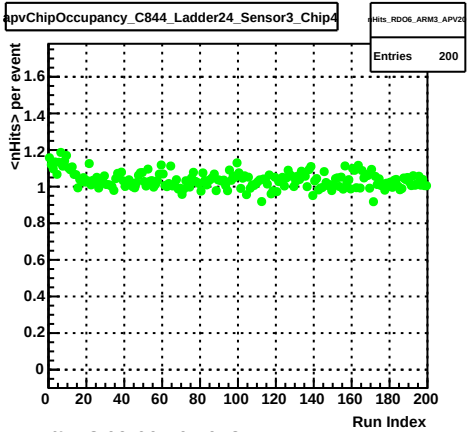

apvChipOccupancy_C839_Ladder24_Sensor2_Chip5
nHits_RDO6_ARM4_APV1
Entries 200
<nHits> per event
Run Index

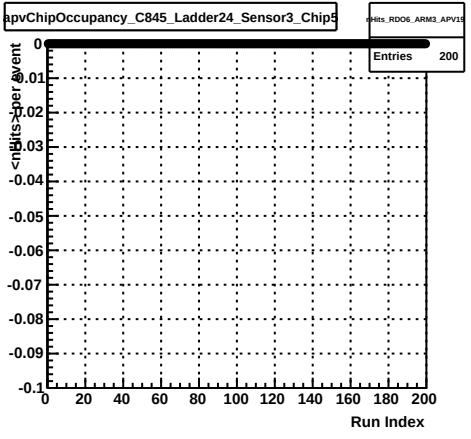

apvChipOccupancy_C840_Ladder24_Sensor2_Chip6
nHits_RDO6_ARM4_APV0
Entries 200
<nHits> per event
Run Index

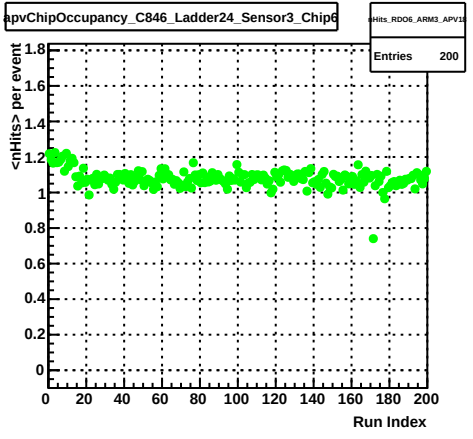

apvChipOccupancy_C841_Ladder24_Sensor3_Chip1
Entries 200
<nHits> per event
Run Index

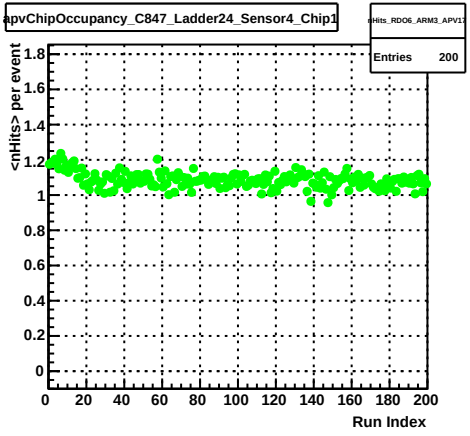

apvChipOccupancy_C842_Ladder24_Sensor3_Chip2
Entries 200
<nHits> per event
Run Index

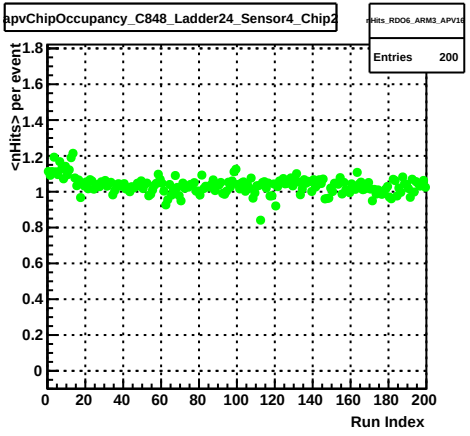

apvChipOccupancy_C843_Ladder24_Sensor3_Chip3
Entries 200
<nHits> per event
Run Index

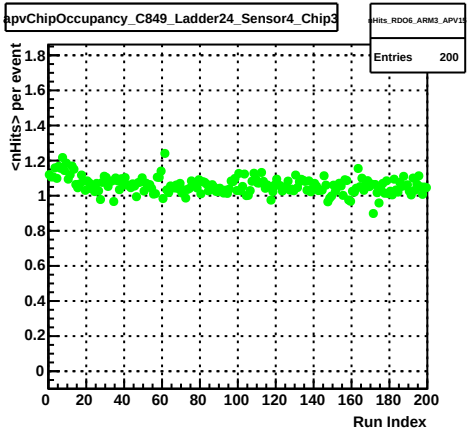

apvChipOccupancy_C844_Ladder24_Sensor3_Chip4
Entries 200
<nHits> per event
Run Index

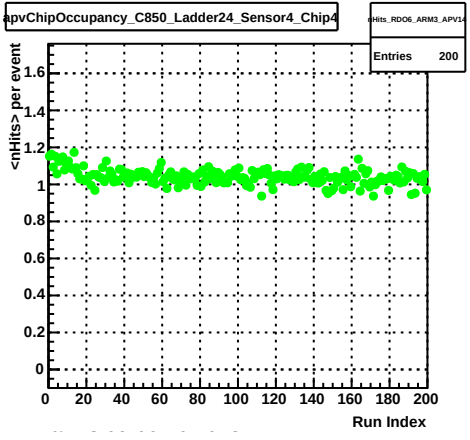

apvChipOccupancy_C845_Ladder24_Sensor3_Chip5
Entries 200
<nHits> per event
Run Index

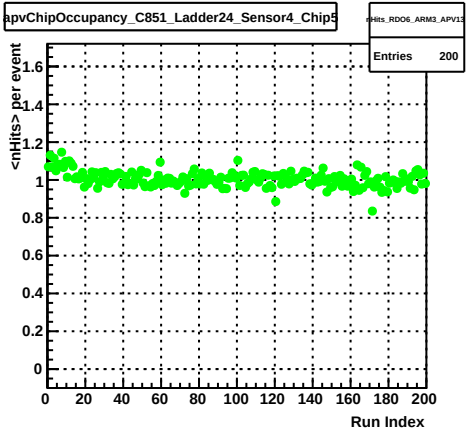

apvChipOccupancy_C846_Ladder24_Sensor3_Chip6
Entries 200
<nHits> per event
Run Index

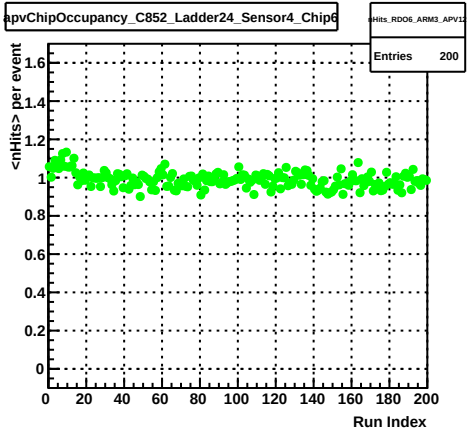

apvChipOccupancy_C847_Ladder24_Sensor4_Chip1
nHits_RDO6_ARM3_APV13
Entries 200
<nHits> per event
Run Index

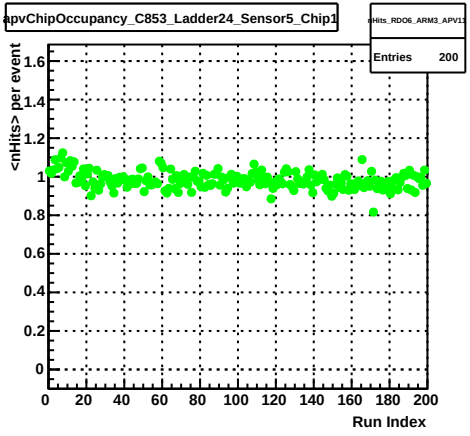

apvChipOccupancy_C848_Ladder24_Sensor4_Chip2
nHits_RDO6_ARM3_APV14
Entries 200
<nHits> per event
Run Index

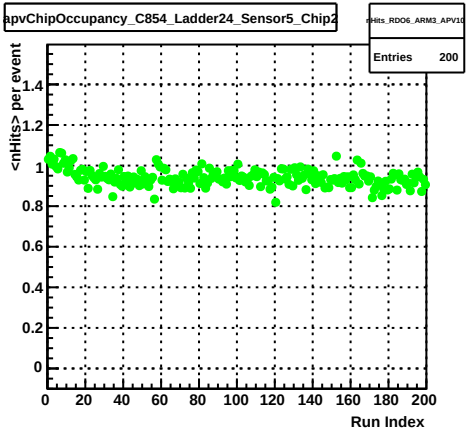

apvChipOccupancy_C849_Ladder24_Sensor4_Chip3
nHits_RDO6_ARM3_APV15
Entries 200
<nHits> per event
Run Index

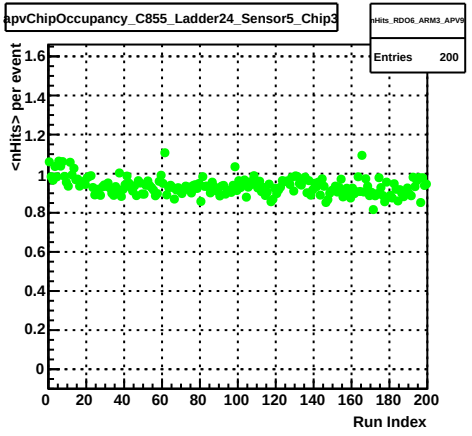

apvChipOccupancy_C850_Ladder24_Sensor4_Chip4
nHits_RDO6_ARM3_APV16
Entries 200
<nHits> per event
Run Index

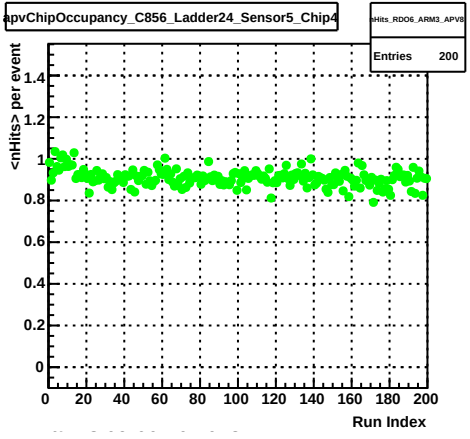

apvChipOccupancy_C851_Ladder24_Sensor4_Chip5
nHits_RDO6_ARM3_APV17
Entries 200
<nHits> per event
Run Index

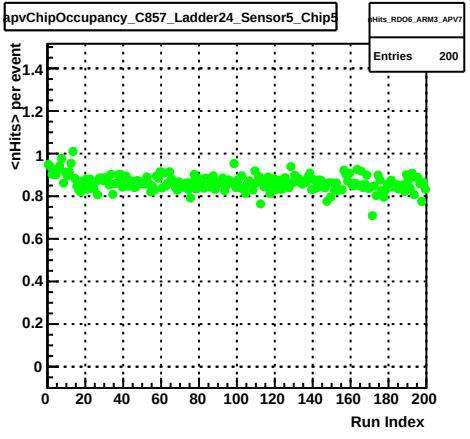

apvChipOccupancy_C852_Ladder24_Sensor4_Chip6
nHits_RDO6_ARM3_APV18
Entries 200
<nHits> per event
Run Index

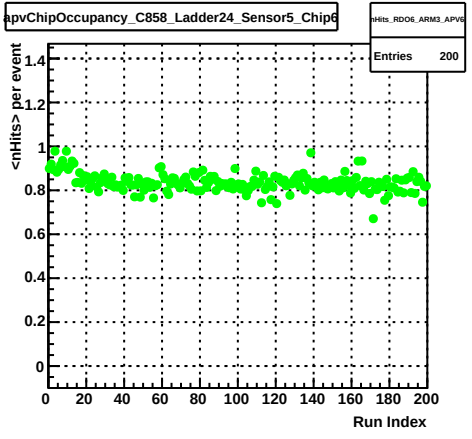

apvChipOccupancy_C853_Ladder24_Sensor5_Chip1
nHits_RDO6_ARM3_APV13
Entries 200
<nHits> per event
Run Index

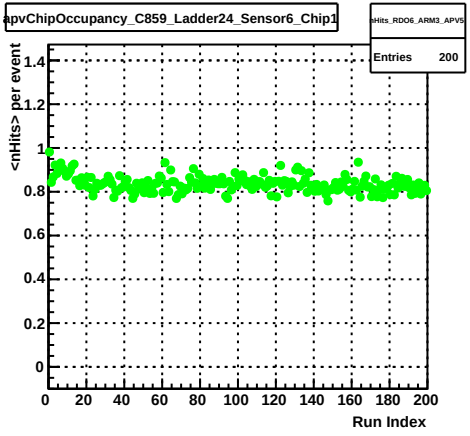

apvChipOccupancy_C854_Ladder24_Sensor5_Chip2
nHits_RDO6_ARM3_APV10
Entries 200
<nHits> per event
Run Index

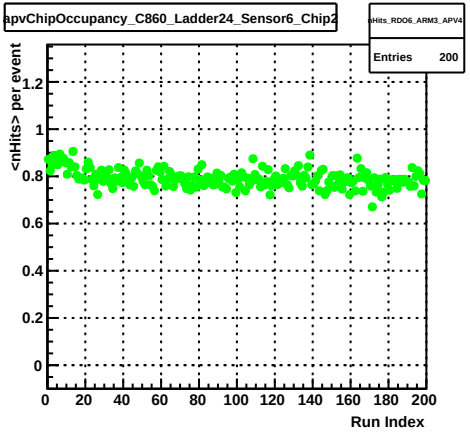

apvChipOccupancy_C855_Ladder24_Sensor5_Chip3
nHits_RDO6_ARM3_APV9
Entries 200
<nHits> per event
Run Index

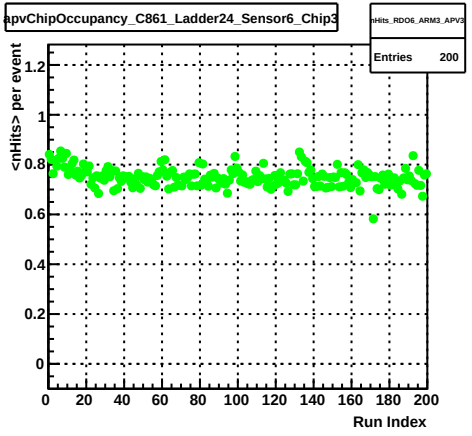

apvChipOccupancy_C856_Ladder24_Sensor5_Chip4
nHits_RDO6_ARM3_APV8
Entries 200
<nHits> per event
Run Index

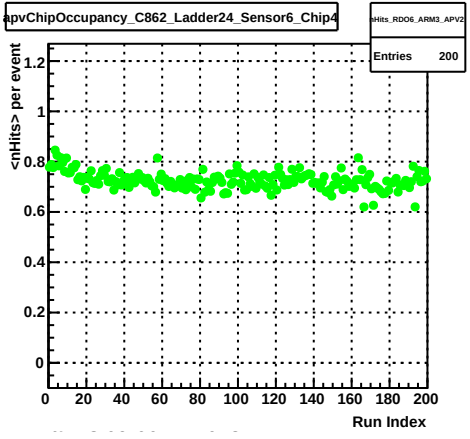

apvChipOccupancy_C857_Ladder24_Sensor5_Chip5
nHits_RDO6_ARM3_APV7
Entries 200
<nHits> per event
Run Index

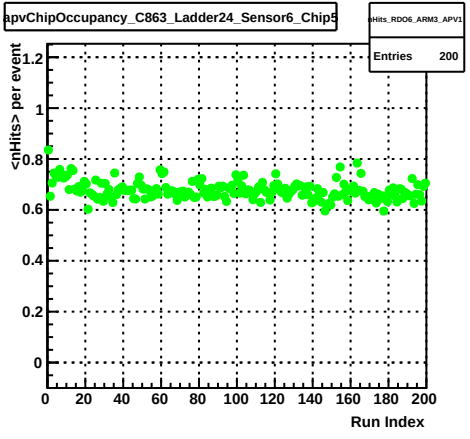

apvChipOccupancy_C858_Ladder24_Sensor5_Chip6
nHits_RDO6_ARM3_APV6
Entries 200
<nHits> per event
Run Index

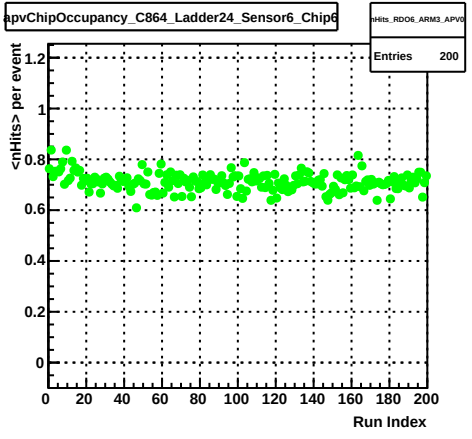

apvChipOccupancy_C859_Ladder24_Sensor6_Chip1
nHits_RDO6_ARM3_APV5
Entries 200
<nHits> per event
Run Index


apvChipOccupancy_C860_Ladder24_Sensor6_Chip2
nHits_RDO6_ARM3_APV4
Entries 200
<nHits> per event
Run Index


apvChipOccupancy_C861_Ladder24_Sensor6_Chip3
nHits_RDO6_ARM3_APV3
Entries 200
<nHits> per event
Run Index


apvChipOccupancy_C862_Ladder24_Sensor6_Chip4
nHits_RDO6_ARM3_APV2
Entries 200
<nHits> per event
Run Index


apvChipOccupancy_C863_Ladder24_Sensor6_Chip5
nHits_RDO6_ARM3_APV1
Entries 200
<nHits> per event
Run Index


apvChipOccupancy_C864_Ladder24_Sensor6_Chip6
nHits_RDO6_ARM3_APV0
Entries 200
<nHits> per event
Run Index