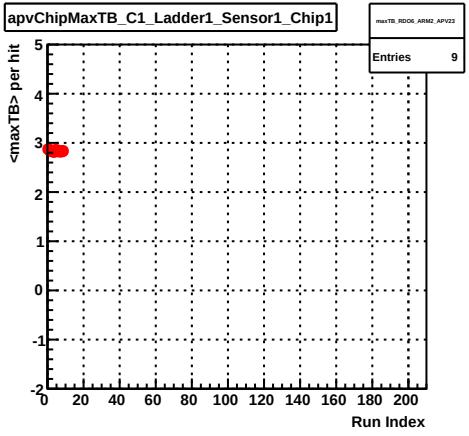
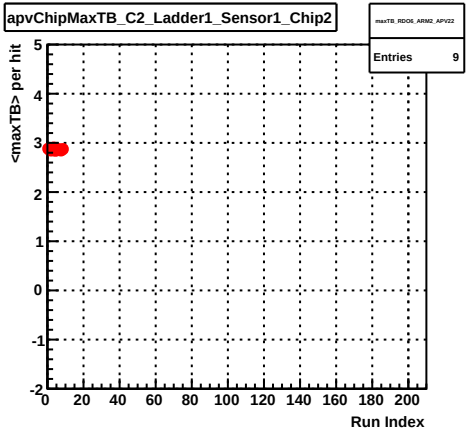
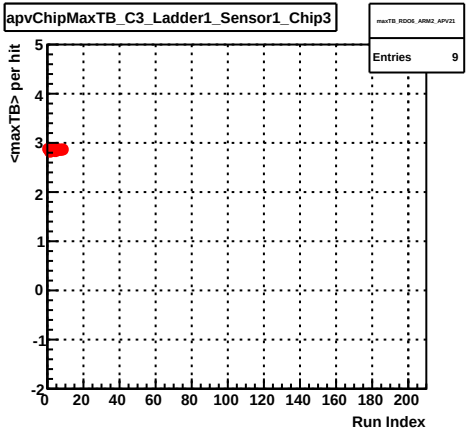
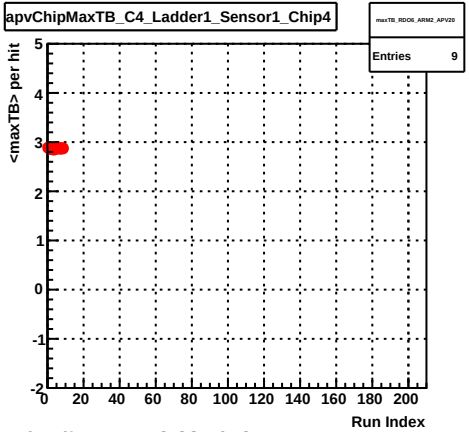
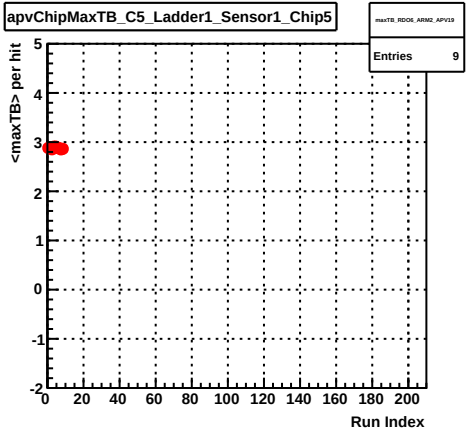
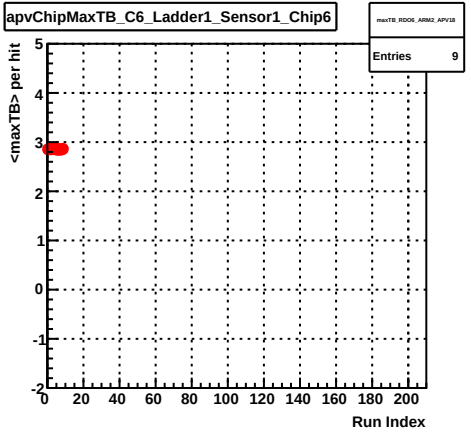

apvChipMaxTB_C1_Ladder1_Sensor1_Chip1
maxTB_RDO6_ARM2_APV23
Entries 9
<maxTB> per hit
Run Index

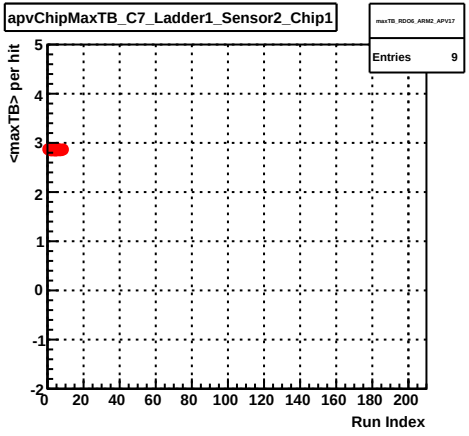

apvChipMaxTB_C2_Ladder1_Sensor1_Chip2
maxTB_RDO6_ARM2_APV22
Entries 9
<maxTB> per hit
Run Index

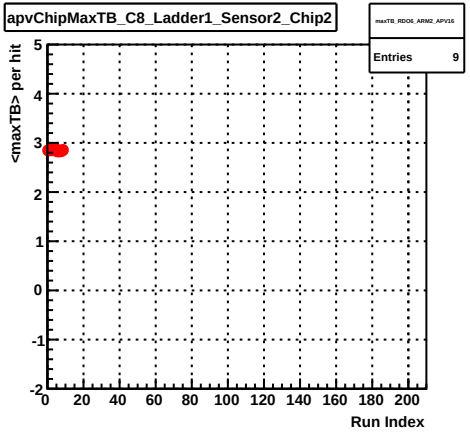

apvChipMaxTB_C3_Ladder1_Sensor1_Chip3
maxTB_RDO6_ARM2_APV21
Entries 9
<maxTB> per hit
Run Index

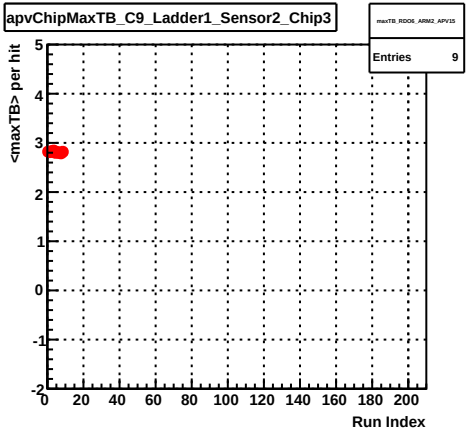

apvChipMaxTB_C4_Ladder1_Sensor1_Chip4
maxTB_RDO6_ARM2_APV20
Entries 9
<maxTB> per hit
Run Index

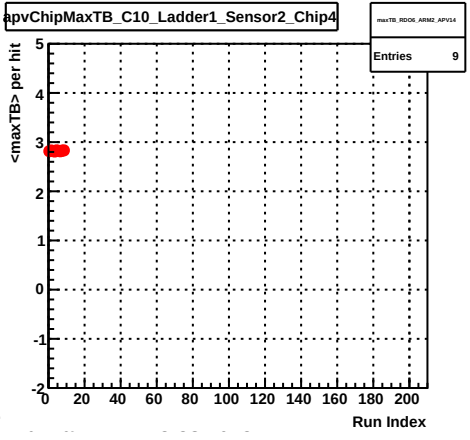

apvChipMaxTB_C5_Ladder1_Sensor1_Chip5
maxTB_RDO6_ARM2_APV19
Entries 9
<maxTB> per hit
Run Index

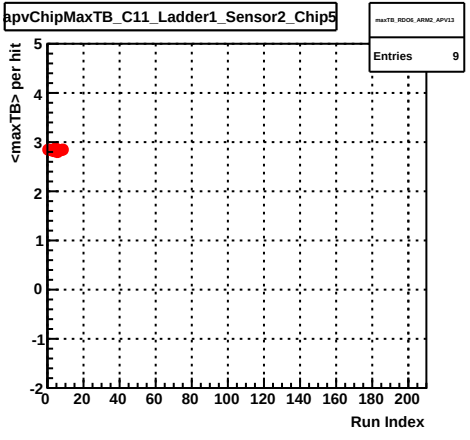

apvChipMaxTB_C6_Ladder1_Sensor1_Chip6
maxTB_RDO6_ARM2_APV18
Entries 9
<maxTB> per hit
Run Index

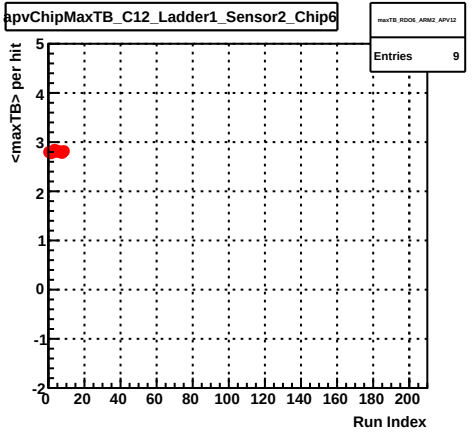

apvChipMaxTB_C7_Ladder1_Sensor2_Chip1
maxTB_RDO6_ARM2_APV17
Entries 9
<maxTB> per hit
Run Index

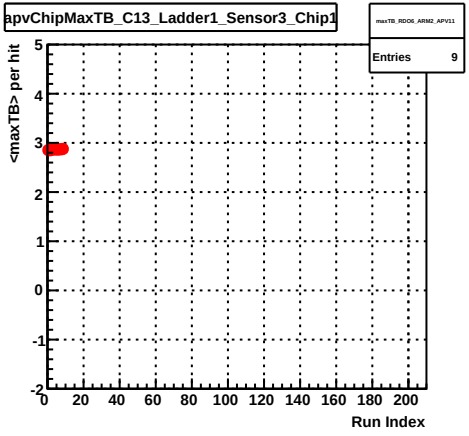

apvChipMaxTB_C8_Ladder1_Sensor2_Chip2
maxTB_RDO6_ARM2_APV16
Entries 9
<maxTB> per hit
Run Index

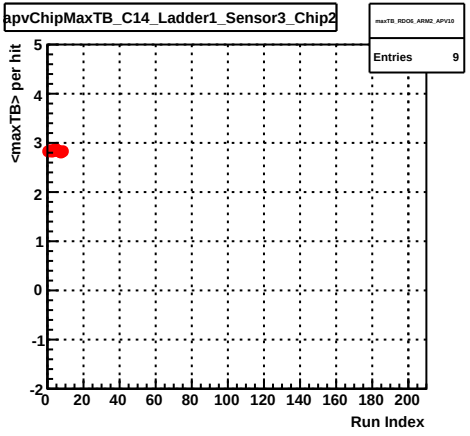

apvChipMaxTB_C9_Ladder1_Sensor2_Chip3
maxTB_RDO6_ARM2_APV15
Entries 9
<maxTB> per hit
Run Index

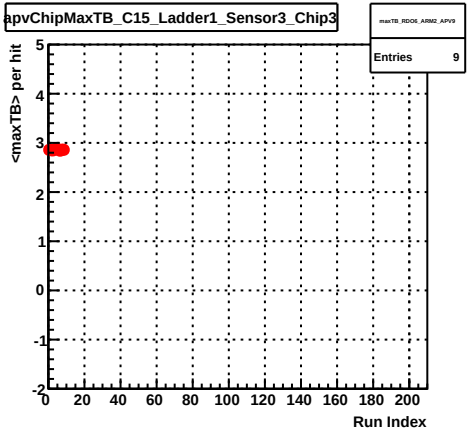

apvChipMaxTB_C10_Ladder1_Sensor2_Chip4
maxTB_RDO6_ARM2_APV14
Entries 9
<maxTB> per hit
Run Index

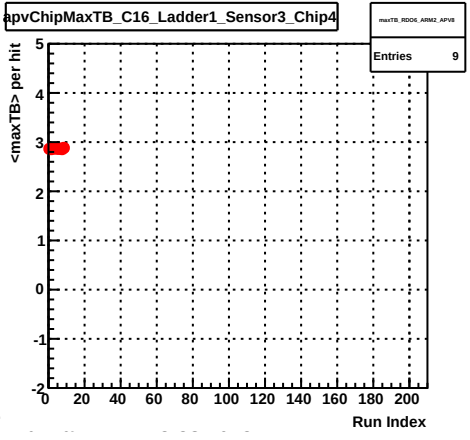

apvChipMaxTB_C11_Ladder1_Sensor2_Chip5
maxTB_RDO6_ARM2_APV13
Entries 9
<maxTB> per hit
Run Index

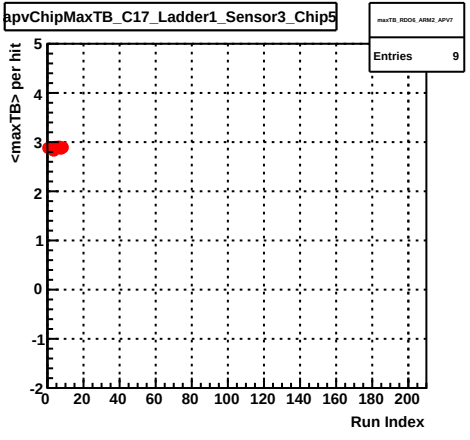

apvChipMaxTB_C12_Ladder1_Sensor2_Chip6
maxTB_RDO6_ARM2_APV12
Entries 9
<maxTB> per hit
Run Index

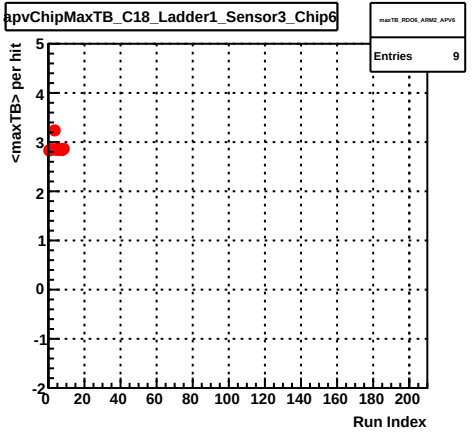

apvChipMaxTB_C13_Ladder1_Sensor3_Chip1
maxTB_RD06_ARM2_APV11
Entries 9
<maxTB> per hit
Run Index

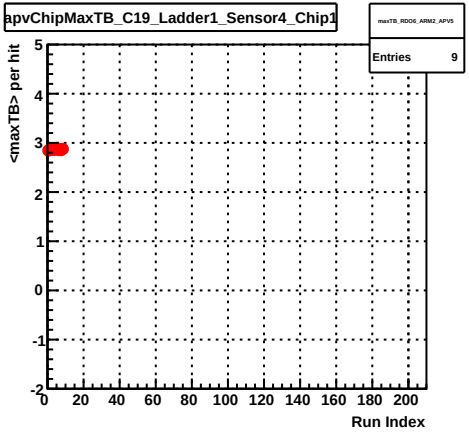

apvChipMaxTB_C14_Ladder1_Sensor3_Chip2
maxTB_RD06_ARM2_APV10
Entries 9
<maxTB> per hit
Run Index

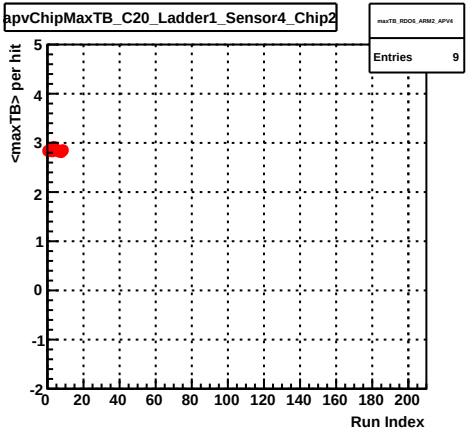

apvChipMaxTB_C15_Ladder1_Sensor3_Chip3
maxTB_RD06_ARM2_APV9
Entries 9
<maxTB> per hit
Run Index

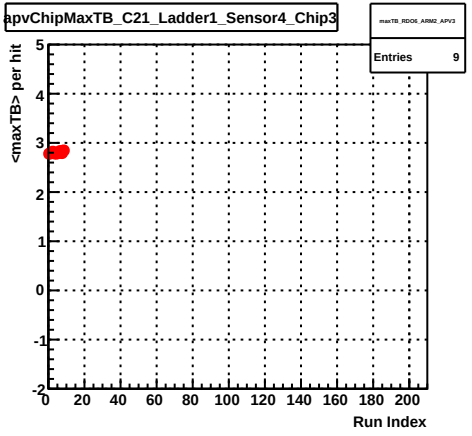

apvChipMaxTB_C16_Ladder1_Sensor3_Chip4
maxTB_RD06_ARM2_APV8
Entries 9
<maxTB> per hit
Run Index

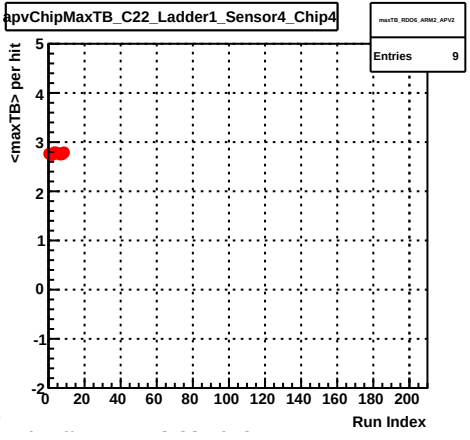

apvChipMaxTB_C17_Ladder1_Sensor3_Chip5
maxTB_RD06_ARM2_APV7
Entries 9
<maxTB> per hit
Run Index

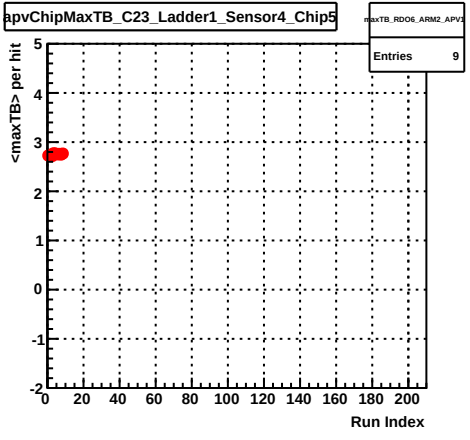

apvChipMaxTB_C18_Ladder1_Sensor3_Chip6
maxTB_RD06_ARM2_APV6
Entries 9
<maxTB> per hit
Run Index

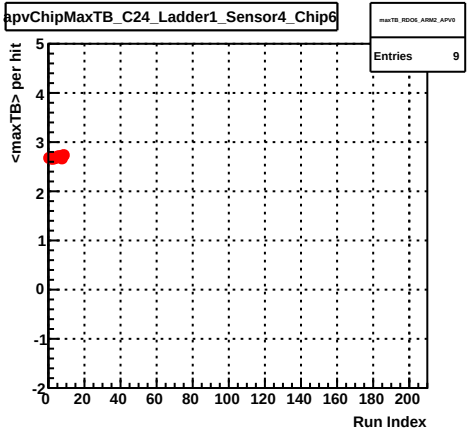

apvChipMaxTB_C19_Ladder1_Sensor4_Chip1
maxTB_RDO6_ARM2_APV5
Entries 9
<maxTB> per hit
Run Index

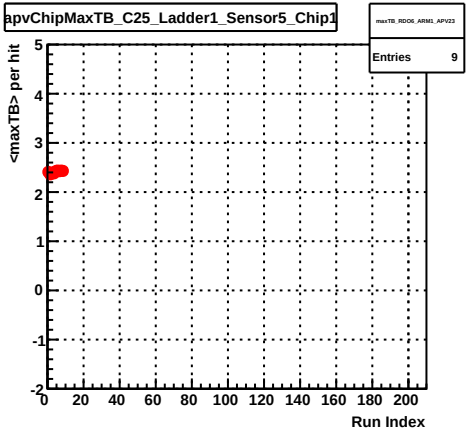

apvChipMaxTB_C20_Ladder1_Sensor4_Chip2
maxTB_RDO6_ARM2_APV4
Entries 9
<maxTB> per hit
Run Index

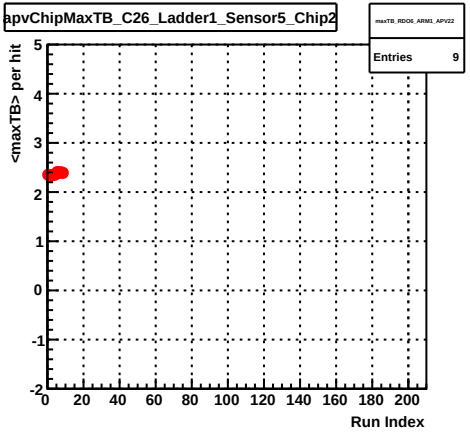

apvChipMaxTB_C21_Ladder1_Sensor4_Chip3
maxTB_RDO6_ARM2_APV3
Entries 9
<maxTB> per hit
Run Index

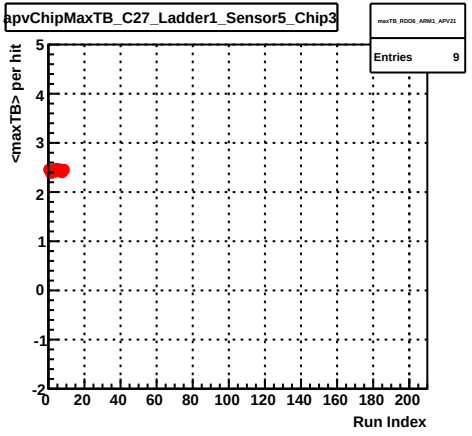

apvChipMaxTB_C22_Ladder1_Sensor4_Chip4
maxTB_RDO6_ARM2_APV2
Entries 9
<maxTB> per hit
Run Index

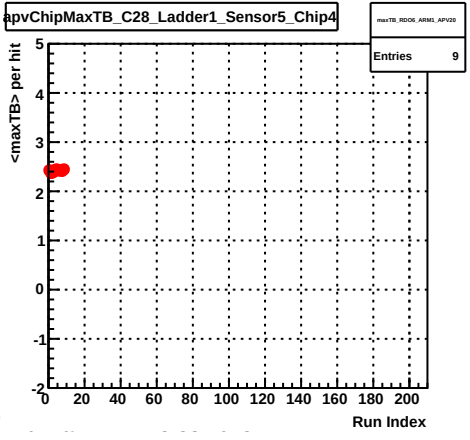

apvChipMaxTB_C23_Ladder1_Sensor4_Chip5
maxTB_RDO6_ARM2_APV1
Entries 9
<maxTB> per hit
Run Index

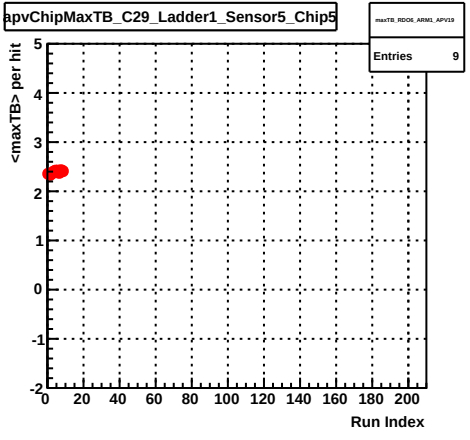

apvChipMaxTB_C24_Ladder1_Sensor4_Chip6
maxTB_RDO6_ARM2_APV0
Entries 9
<maxTB> per hit
Run Index

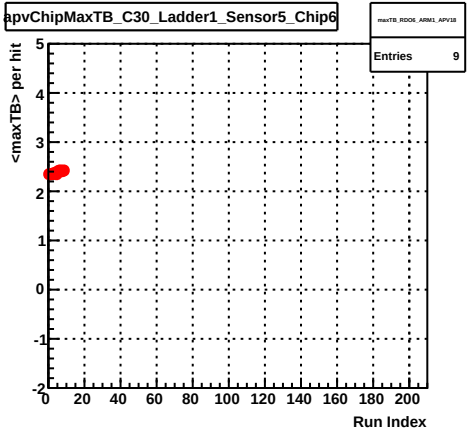

apvChipMaxTB_C25_Ladder1_Sensor5_Chip1
maxTB_RDO6_ARM1_APV23
Entries 9
<maxTB> per hit
Run Index

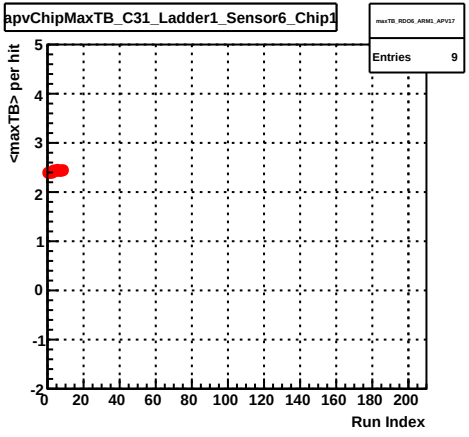

apvChipMaxTB_C26_Ladder1_Sensor5_Chip2
maxTB_RDO6_ARM1_APV22
Entries 9
<maxTB> per hit
Run Index

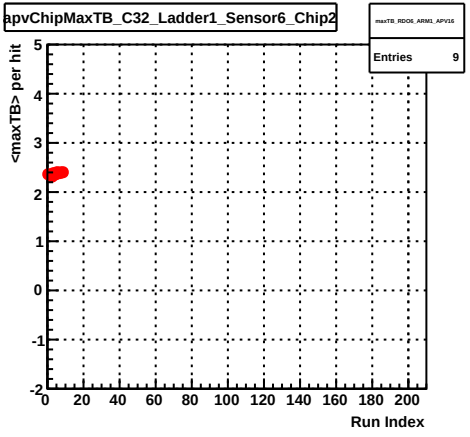

apvChipMaxTB_C27_Ladder1_Sensor5_Chip3
maxTB_RDO6_ARM1_APV21
Entries 9
<maxTB> per hit
Run Index

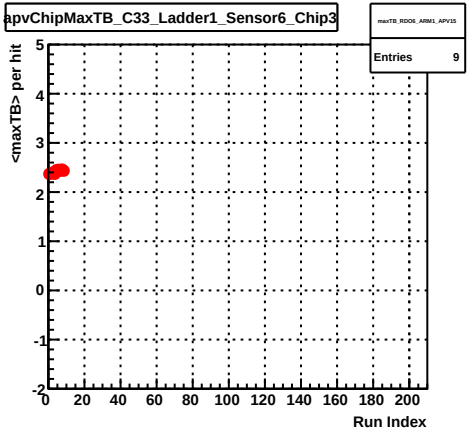

apvChipMaxTB_C28_Ladder1_Sensor5_Chip4
maxTB_RDO6_ARM1_APV20
Entries 9
<maxTB> per hit
Run Index

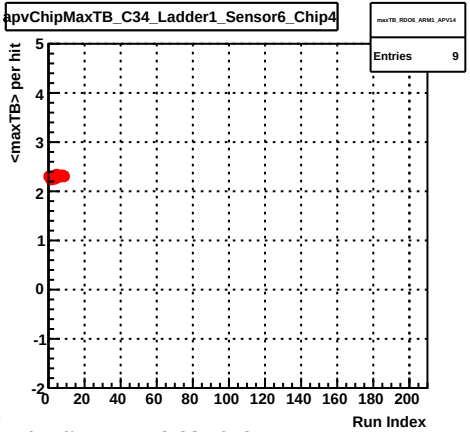

apvChipMaxTB_C29_Ladder1_Sensor5_Chip5
maxTB_RDO6_ARM1_APV19
Entries 9
<maxTB> per hit
Run Index

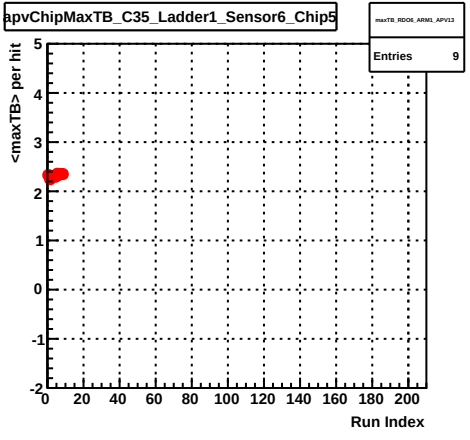

apvChipMaxTB_C30_Ladder1_Sensor5_Chip6
maxTB_RDO6_ARM1_APV18
Entries 9
<maxTB> per hit
Run Index

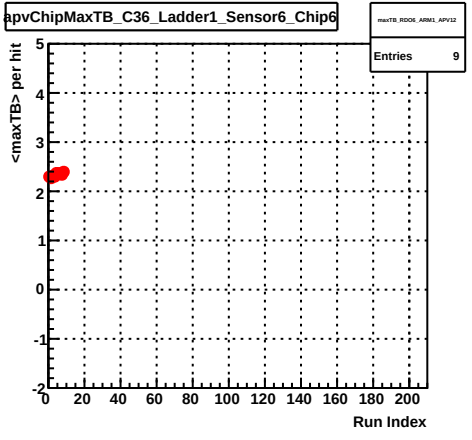

apvChipMaxTB_C31_Ladder1_Sensor6_Chip1
maxTB_RD06_ARM1_APV17
Entries 9
<maxTB> per hit
Run Index

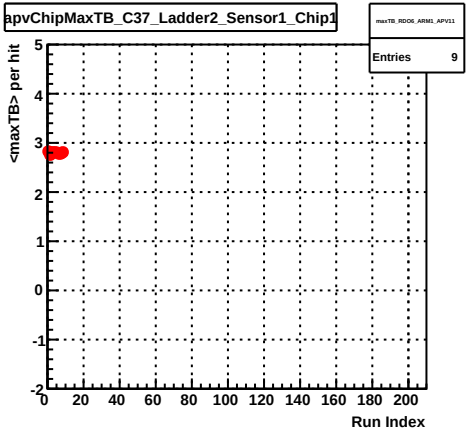

apvChipMaxTB_C32_Ladder1_Sensor6_Chip2
maxTB_RD06_ARM1_APV16
Entries 9
<maxTB> per hit
Run Index

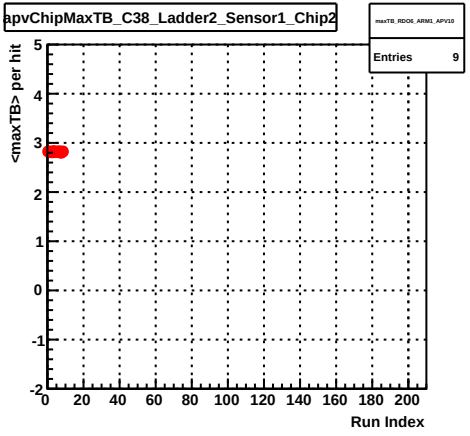

apvChipMaxTB_C33_Ladder1_Sensor6_Chip3
maxTB_RD06_ARM1_APV15
Entries 9
<maxTB> per hit
Run Index

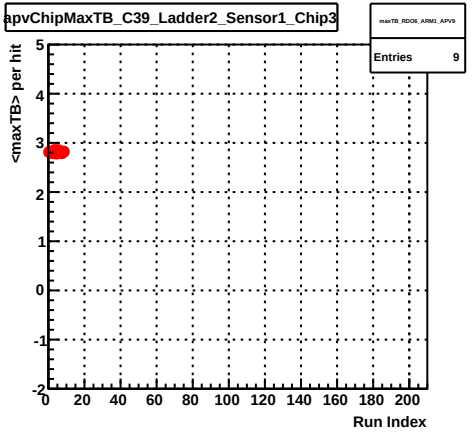

apvChipMaxTB_C34_Ladder1_Sensor6_Chip4
maxTB_RD06_ARM1_APV14
Entries 9
<maxTB> per hit
Run Index

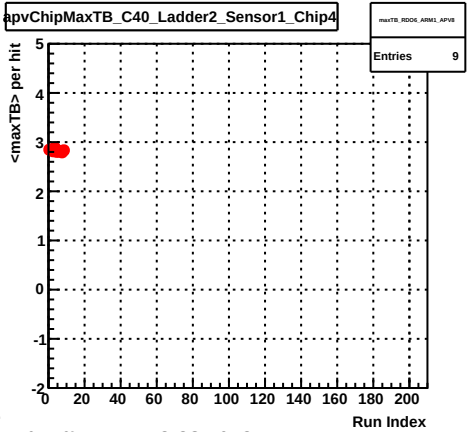

apvChipMaxTB_C35_Ladder1_Sensor6_Chip5
maxTB_RD06_ARM1_APV13
Entries 9
<maxTB> per hit
Run Index

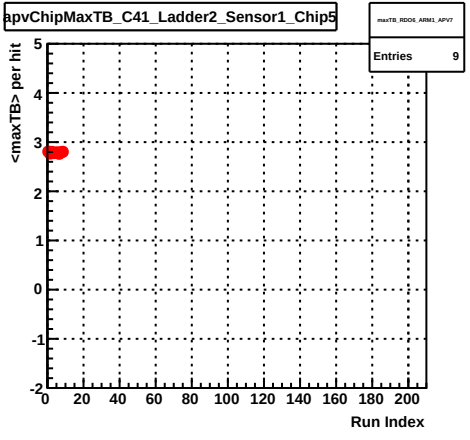

apvChipMaxTB_C36_Ladder1_Sensor6_Chip6
maxTB_RD06_ARM1_APV12
Entries 9
<maxTB> per hit
Run Index

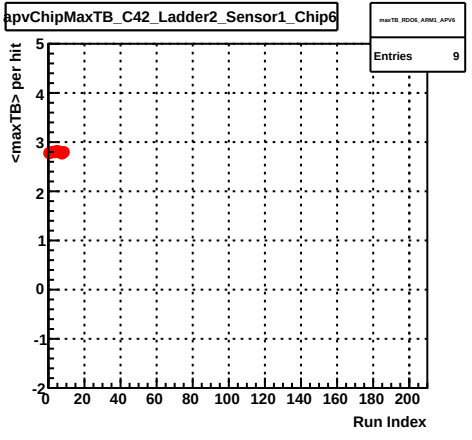

apvChipMaxTB_C37_Ladder2_Sensor1_Chip1
maxTB_RD06_ARM1_APV11
Entries 9
<maxTB> per hit
Run Index

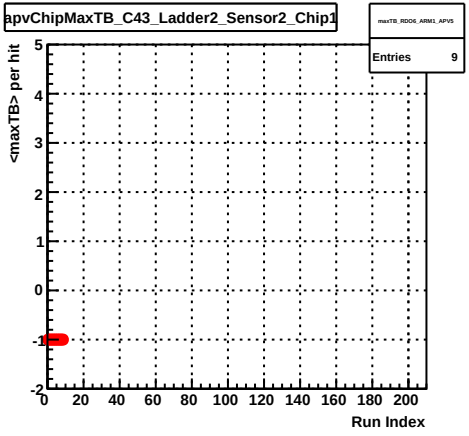

apvChipMaxTB_C38_Ladder2_Sensor1_Chip2
maxTB_RD06_ARM1_APV10
Entries 9
<maxTB> per hit
Run Index

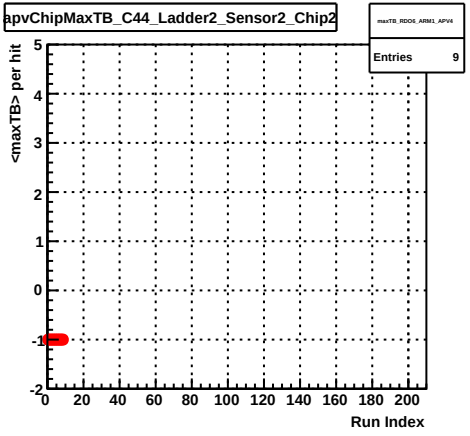

apvChipMaxTB_C39_Ladder2_Sensor1_Chip3
maxTB_RD06_ARM1_APV9
Entries 9
<maxTB> per hit
Run Index

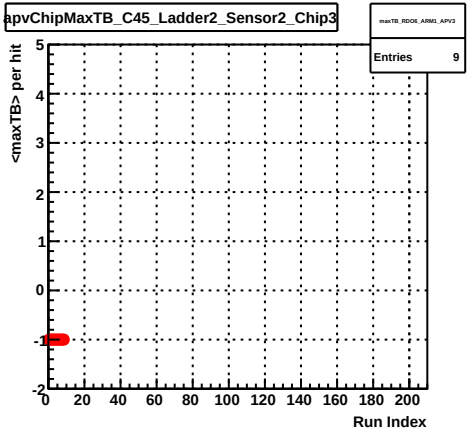

apvChipMaxTB_C40_Ladder2_Sensor1_Chip4
maxTB_RD06_ARM1_APV8
Entries 9
<maxTB> per hit
Run Index

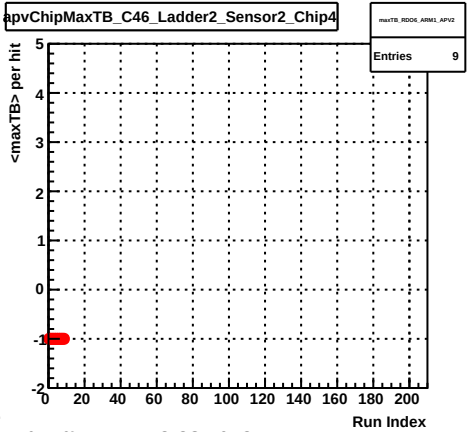

apvChipMaxTB_C41_Ladder2_Sensor1_Chip5
maxTB_RD06_ARM1_APV7
Entries 9
<maxTB> per hit
Run Index

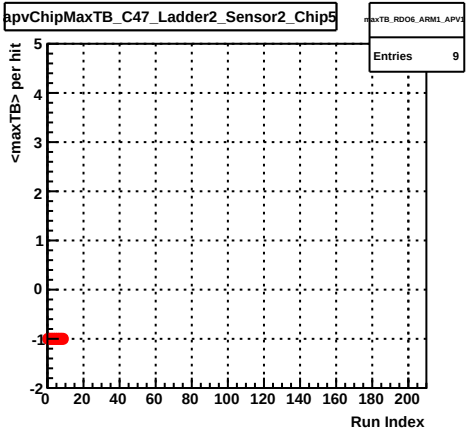

apvChipMaxTB_C42_Ladder2_Sensor1_Chip6
maxTB_RD06_ARM1_APV6
Entries 9
<maxTB> per hit
Run Index

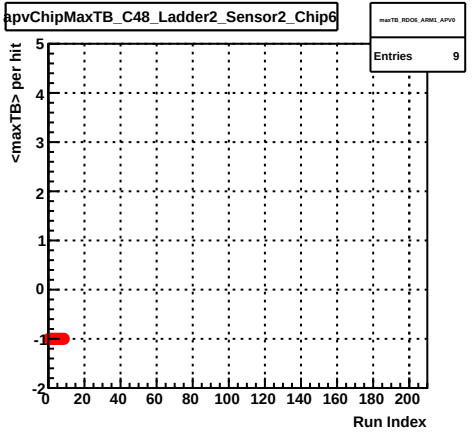

apvChipMaxTB_C43_Ladder2_Sensor2_Chip1
maxTB_RDO6_ARM1_APV5
Entries 9
<maxTB> per hit
Run Index

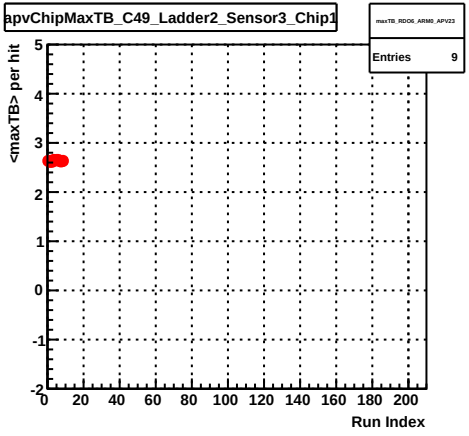

apvChipMaxTB_C44_Ladder2_Sensor2_Chip2
maxTB_RDO6_ARM1_APV4
Entries 9
<maxTB> per hit
Run Index

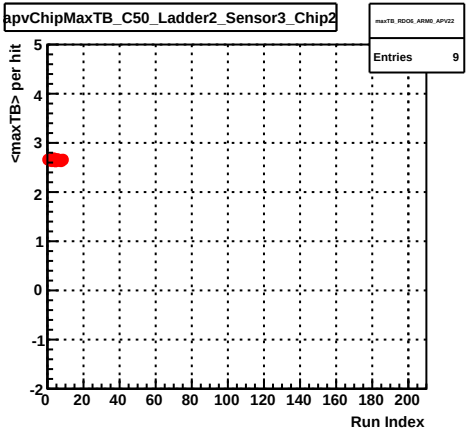

apvChipMaxTB_C45_Ladder2_Sensor2_Chip3
maxTB_RDO6_ARM1_APV3
Entries 9
<maxTB> per hit
Run Index

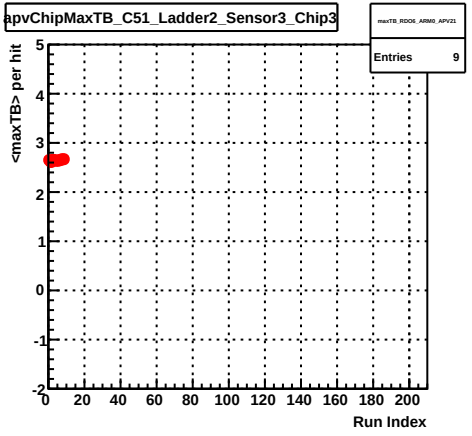

apvChipMaxTB_C46_Ladder2_Sensor2_Chip4
maxTB_RDO6_ARM1_APV2
Entries 9
<maxTB> per hit
Run Index

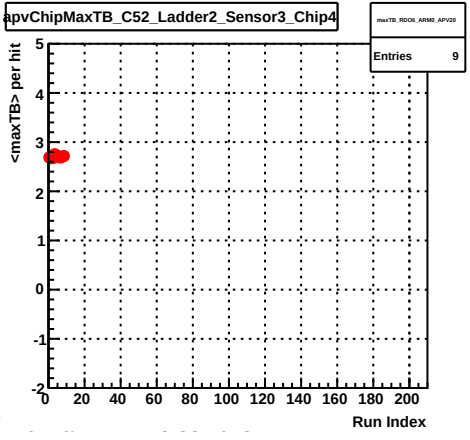

apvChipMaxTB_C47_Ladder2_Sensor2_Chip5
maxTB_RDO6_ARM1_APV1
Entries 9
<maxTB> per hit
Run Index

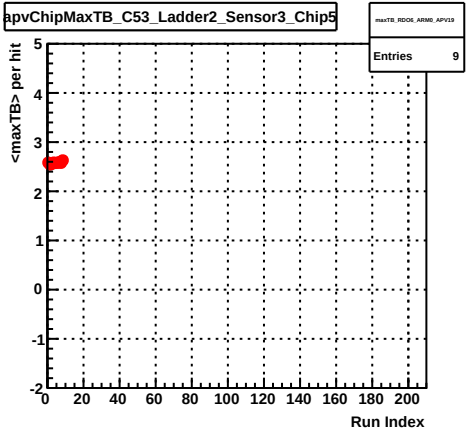

apvChipMaxTB_C48_Ladder2_Sensor2_Chip6
maxTB_RDO6_ARM1_APV0
Entries 9
<maxTB> per hit
Run Index

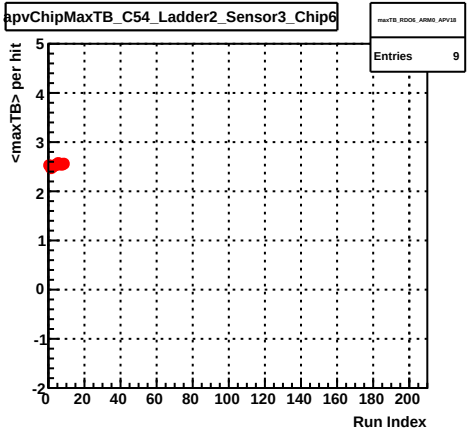

apvChipMaxTB_C49_Ladder2_Sensor3_Chip1
maxTB_RD06_ARM0_APV23
Entries 9
<maxTB> per hit
Run Index

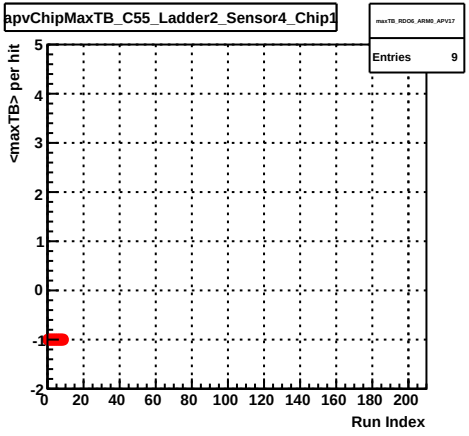

apvChipMaxTB_C50_Ladder2_Sensor3_Chip2
maxTB_RD06_ARM0_APV22
Entries 9
<maxTB> per hit
Run Index

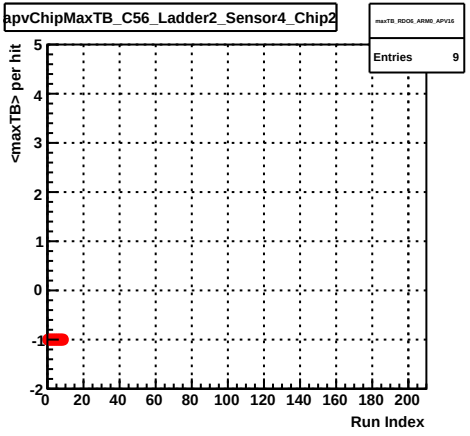

apvChipMaxTB_C51_Ladder2_Sensor3_Chip3
maxTB_RD06_ARM0_APV21
Entries 9
<maxTB> per hit
Run Index

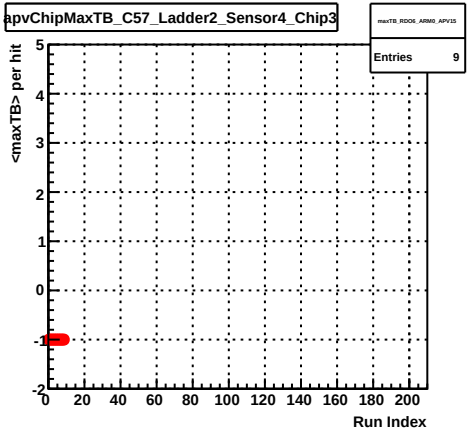

apvChipMaxTB_C52_Ladder2_Sensor3_Chip4
maxTB_RD06_ARM0_APV20
Entries 9
<maxTB> per hit
Run Index

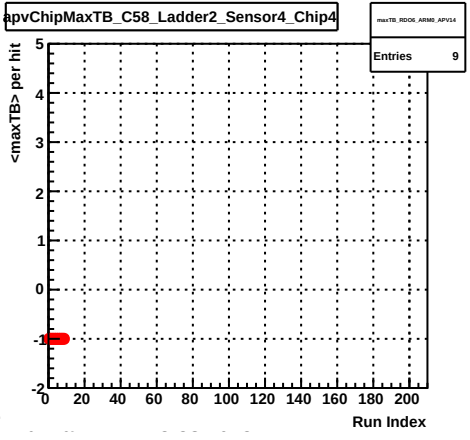

apvChipMaxTB_C53_Ladder2_Sensor3_Chip5
maxTB_RD06_ARM0_APV19
Entries 9
<maxTB> per hit
Run Index

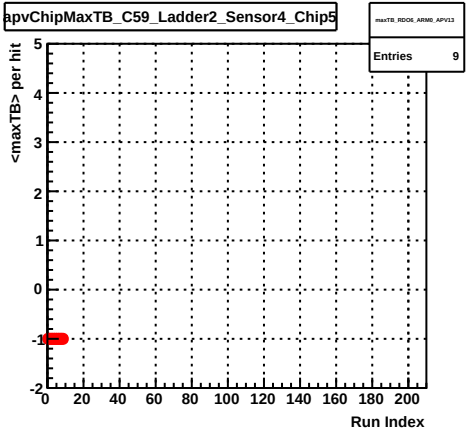

apvChipMaxTB_C54_Ladder2_Sensor3_Chip6
maxTB_RD06_ARM0_APV18
Entries 9
<maxTB> per hit
Run Index

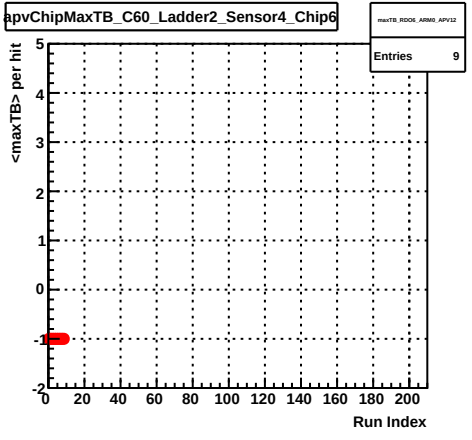

apvChipMaxTB_C55_Ladder2_Sensor4_Chip1
maxTB_RD06_ARM0_APV17
Entries 9
<maxTB> per hit
Run Index

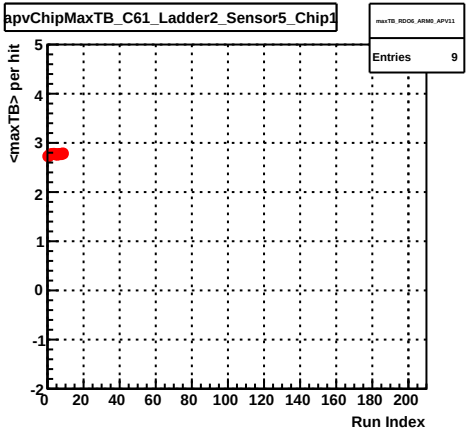

apvChipMaxTB_C56_Ladder2_Sensor4_Chip2
maxTB_RD06_ARM0_APV16
Entries 9
<maxTB> per hit
Run Index

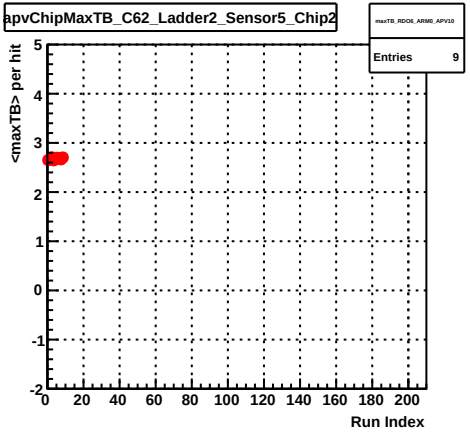

apvChipMaxTB_C57_Ladder2_Sensor4_Chip3
maxTB_RD06_ARM0_APV15
Entries 9
<maxTB> per hit
Run Index

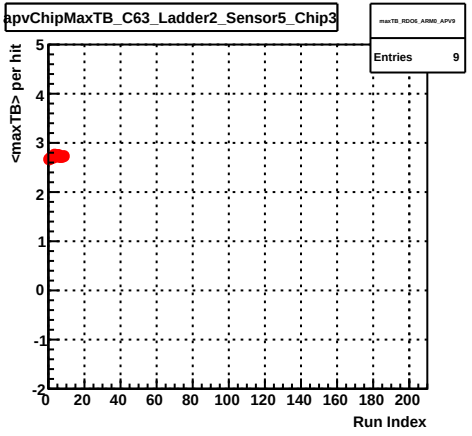

apvChipMaxTB_C58_Ladder2_Sensor4_Chip4
maxTB_RD06_ARM0_APV14
Entries 9
<maxTB> per hit
Run Index

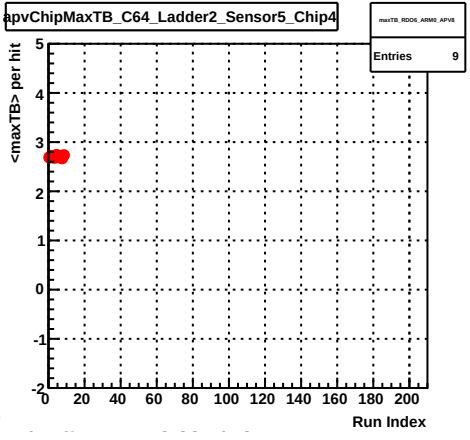

apvChipMaxTB_C59_Ladder2_Sensor4_Chip5
maxTB_RD06_ARM0_APV13
Entries 9
<maxTB> per hit
Run Index

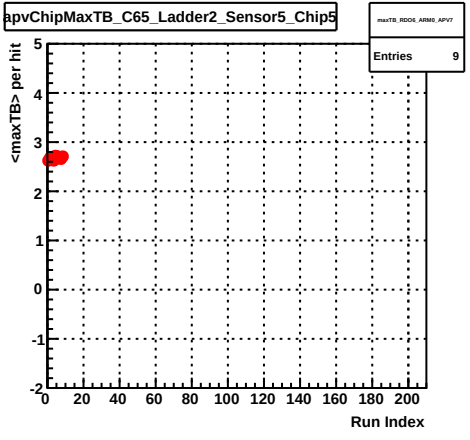

apvChipMaxTB_C60_Ladder2_Sensor4_Chip6
maxTB_RD06_ARM0_APV12
Entries 9
<maxTB> per hit
Run Index

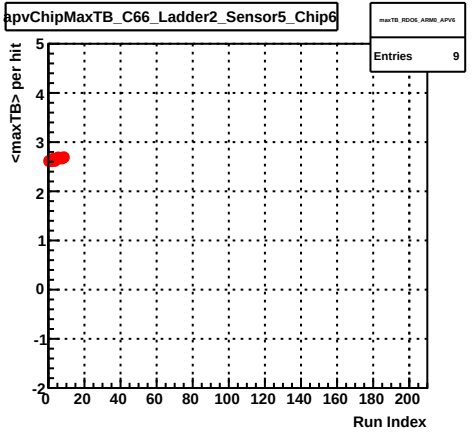

apvChipMaxTB_C61_Ladder2_Sensor5_Chip1
maxTB_RD06_ARM0_APV11
Entries 9
<maxTB> per hit
Run Index

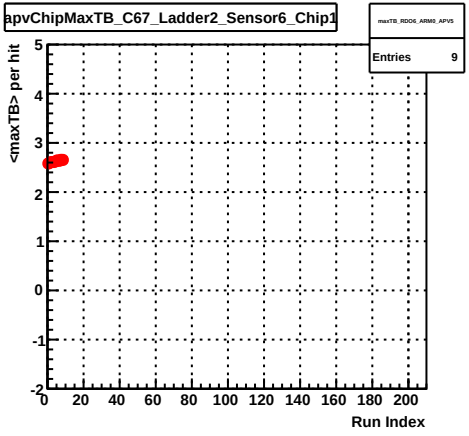

apvChipMaxTB_C62_Ladder2_Sensor5_Chip2
maxTB_RD06_ARM0_APV10
Entries 9
<maxTB> per hit
Run Index

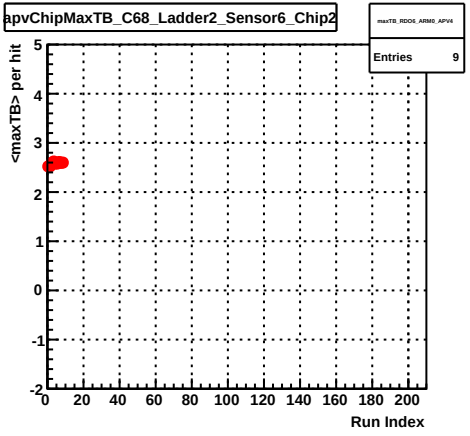

apvChipMaxTB_C63_Ladder2_Sensor5_Chip3
maxTB_RD06_ARM0_APV9
Entries 9
<maxTB> per hit
Run Index

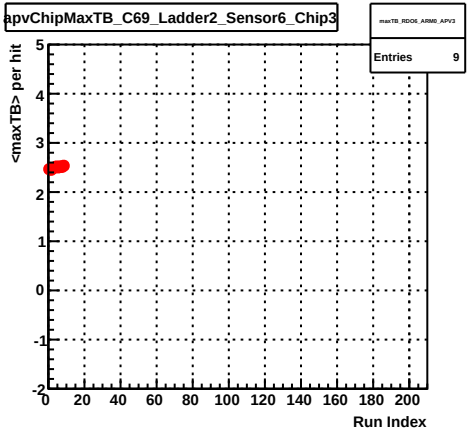

apvChipMaxTB_C64_Ladder2_Sensor5_Chip4
maxTB_RD06_ARM0_APV8
Entries 9
<maxTB> per hit
Run Index

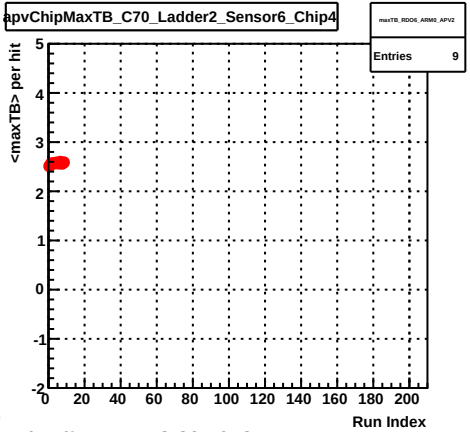

apvChipMaxTB_C65_Ladder2_Sensor5_Chip5
maxTB_RD06_ARM0_APV7
Entries 9
<maxTB> per hit
Run Index

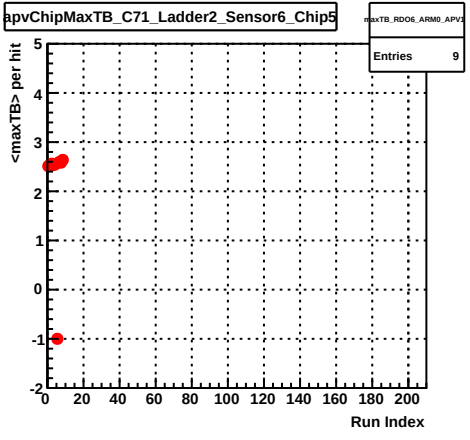

apvChipMaxTB_C66_Ladder2_Sensor5_Chip6
maxTB_RD06_ARM0_APV6
Entries 9
<maxTB> per hit
Run Index

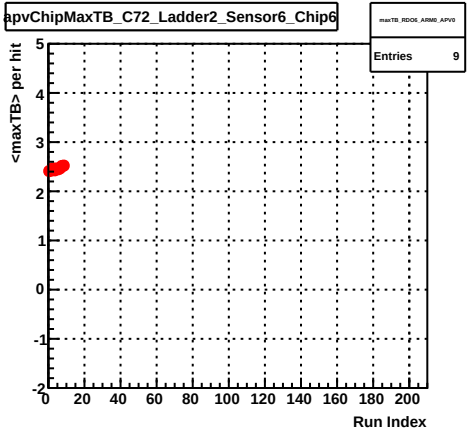

apvChipMaxTB_C67_Ladder2_Sensor6_Chip1
maxTB_RDO6_ARM0_APV5
Entries 9
<maxTB> per hit
Run Index

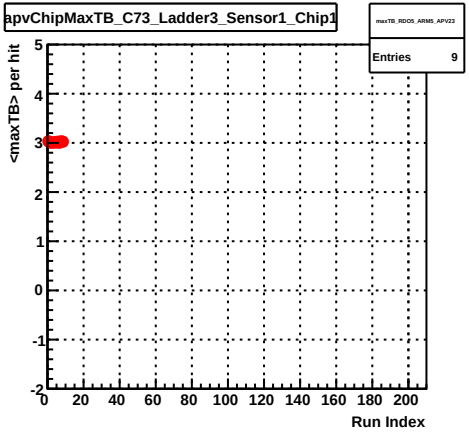

apvChipMaxTB_C68_Ladder2_Sensor6_Chip2
maxTB_RDO6_ARM0_APV4
Entries 9
<maxTB> per hit
Run Index

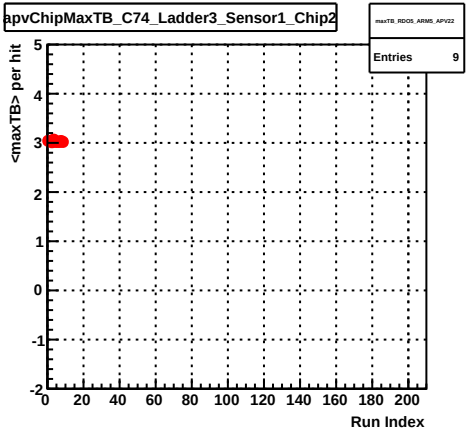

apvChipMaxTB_C69_Ladder2_Sensor6_Chip3
maxTB_RDO6_ARM0_APV3
Entries 9
<maxTB> per hit
Run Index

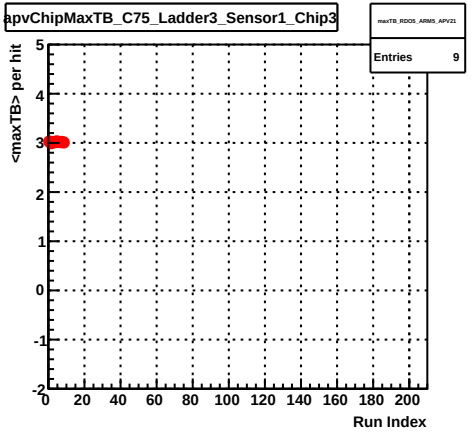

apvChipMaxTB_C70_Ladder2_Sensor6_Chip4
maxTB_RDO6_ARM0_APV2
Entries 9
<maxTB> per hit
Run Index

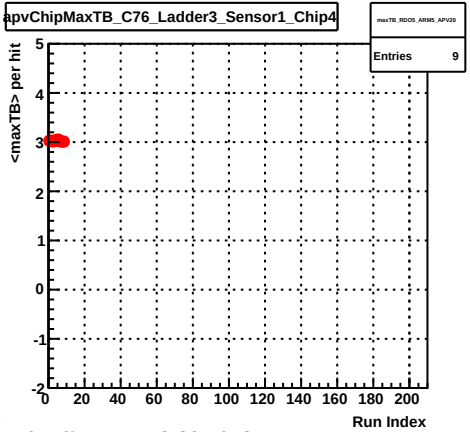

apvChipMaxTB_C71_Ladder2_Sensor6_Chip5
maxTB_RDO6_ARM0_APV1
Entries 9
<maxTB> per hit
Run Index

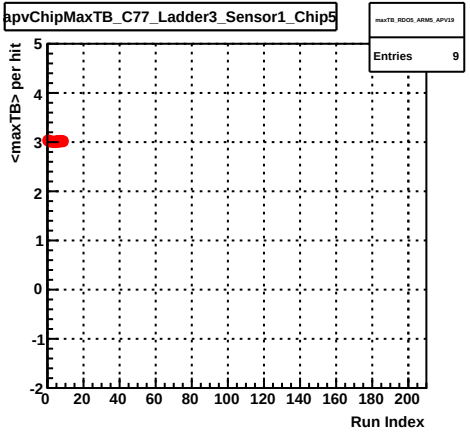

apvChipMaxTB_C72_Ladder2_Sensor6_Chip6
maxTB_RDO6_ARM0_APV0
Entries 9
<maxTB> per hit
Run Index

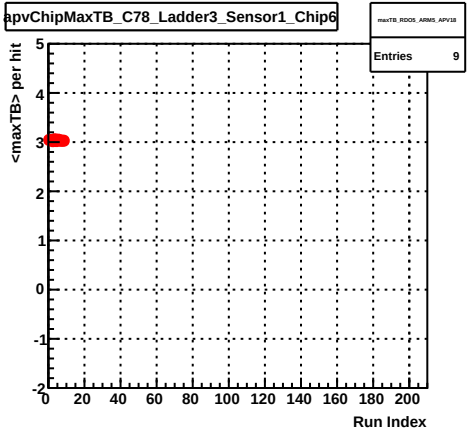

apvChipMaxTB_C73_Ladder3_Sensor1_Chip1
maxTB_RDOS_ARMS_APV23
Entries 9
<maxTB> per hit
Run Index

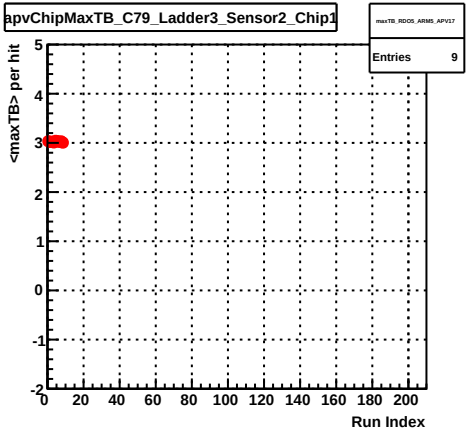

apvChipMaxTB_C74_Ladder3_Sensor1_Chip2
maxTB_RDOS_ARMS_APV22
Entries 9
<maxTB> per hit
Run Index

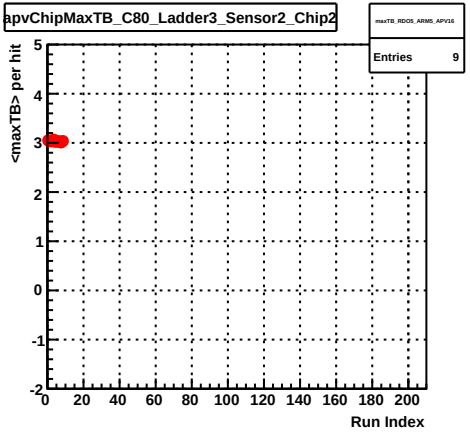

apvChipMaxTB_C75_Ladder3_Sensor1_Chip3
maxTB_RDOS_ARMS_APV21
Entries 9
<maxTB> per hit
Run Index

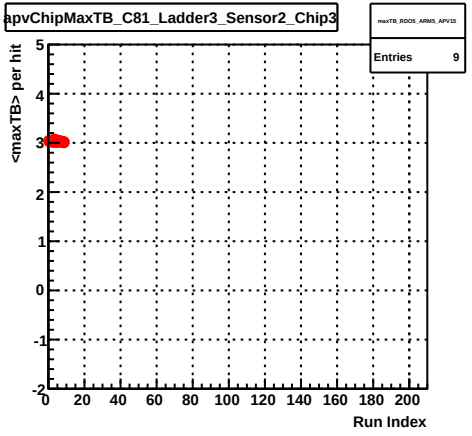

apvChipMaxTB_C76_Ladder3_Sensor1_Chip4
maxTB_RDOS_ARMS_APV20
Entries 9
<maxTB> per hit
Run Index

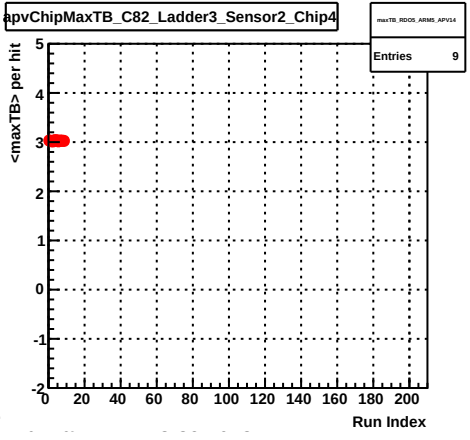

apvChipMaxTB_C77_Ladder3_Sensor1_Chip5
maxTB_RDOS_ARMS_APV19
Entries 9
<maxTB> per hit
Run Index

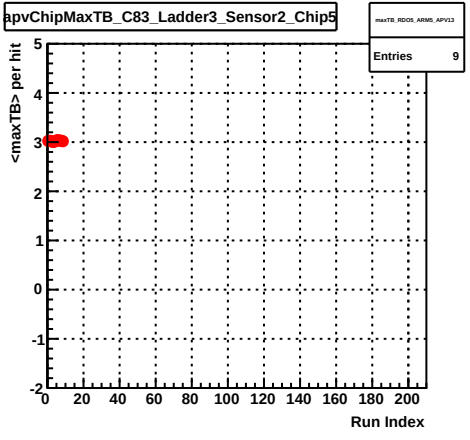

apvChipMaxTB_C78_Ladder3_Sensor1_Chip6
maxTB_RDOS_ARMS_APV18
Entries 9
<maxTB> per hit
Run Index

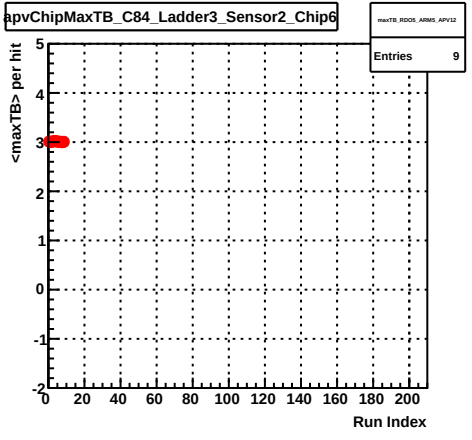

apvChipMaxTB_C79_Ladder3_Sensor2_Chip1
maxTB_RDO5_ARM5_APV17
Entries 9
<maxTB> per hit
Run Index

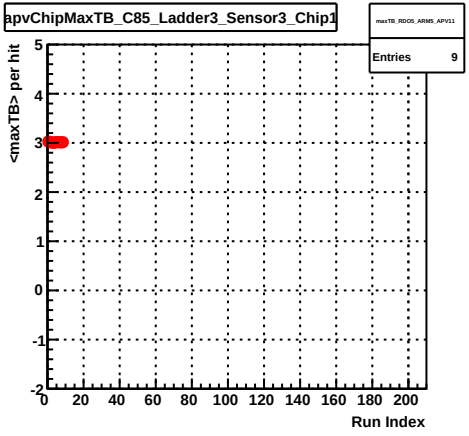

apvChipMaxTB_C80_Ladder3_Sensor2_Chip2
maxTB_RDO5_ARM5_APV16
Entries 9
<maxTB> per hit
Run Index

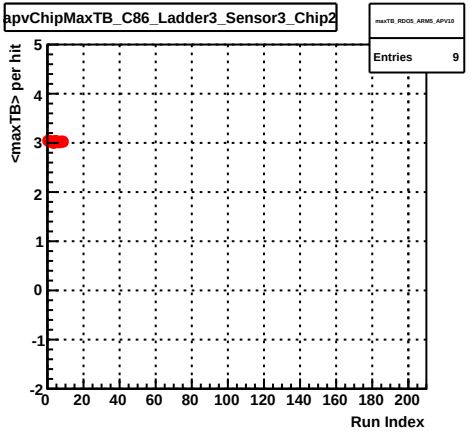

apvChipMaxTB_C81_Ladder3_Sensor2_Chip3
maxTB_RDO5_ARM5_APV15
Entries 9
<maxTB> per hit
Run Index

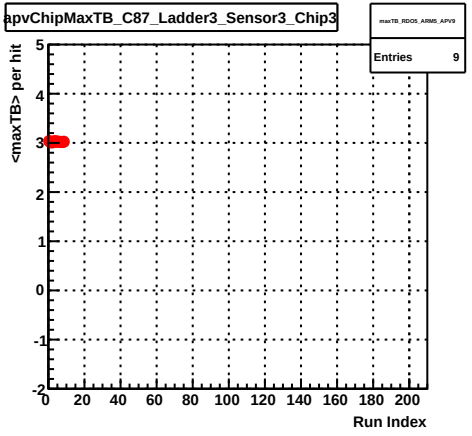

apvChipMaxTB_C82_Ladder3_Sensor2_Chip4
maxTB_RDO5_ARM5_APV14
Entries 9
<maxTB> per hit
Run Index

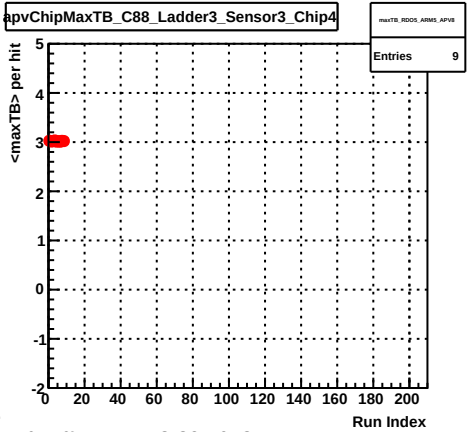

apvChipMaxTB_C83_Ladder3_Sensor2_Chip5
maxTB_RDO5_ARM5_APV13
Entries 9
<maxTB> per hit
Run Index

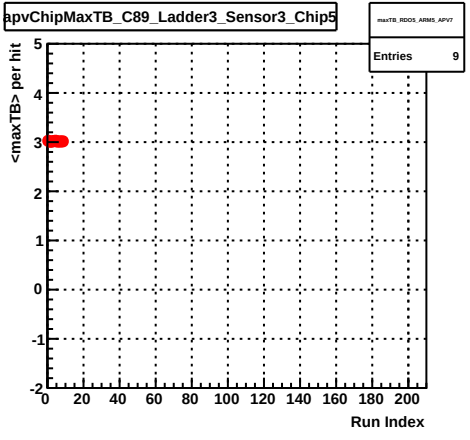

apvChipMaxTB_C84_Ladder3_Sensor2_Chip6
maxTB_RDO5_ARM5_APV12
Entries 9
<maxTB> per hit
Run Index

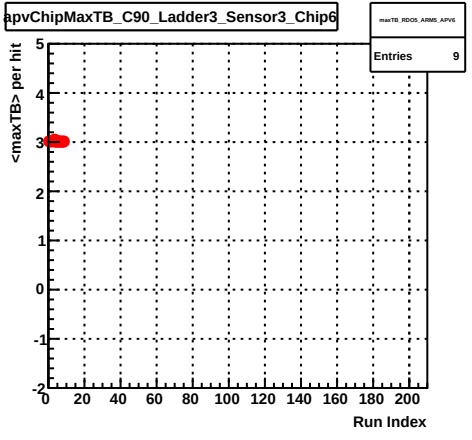

apvChipMaxTB_C85_Ladder3_Sensor3_Chip1
maxTB_RDO5_ARM5_APV11
Entries 9
<maxTB> per hit
Run Index

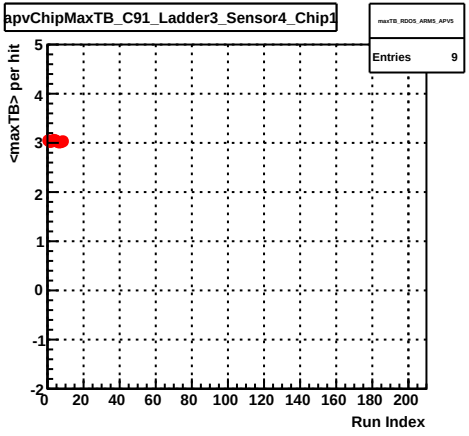

apvChipMaxTB_C86_Ladder3_Sensor3_Chip2
maxTB_RDO5_ARM5_APV10
Entries 9
<maxTB> per hit
Run Index

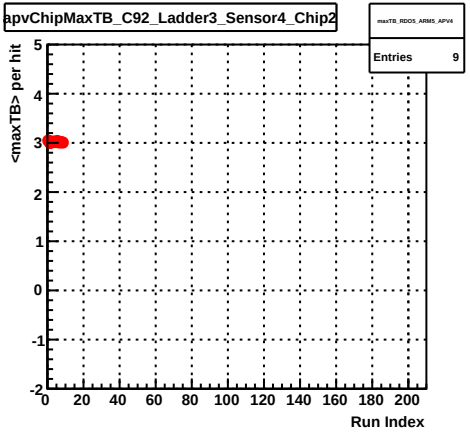

apvChipMaxTB_C87_Ladder3_Sensor3_Chip3
maxTB_RDO5_ARM5_APV9
Entries 9
<maxTB> per hit
Run Index

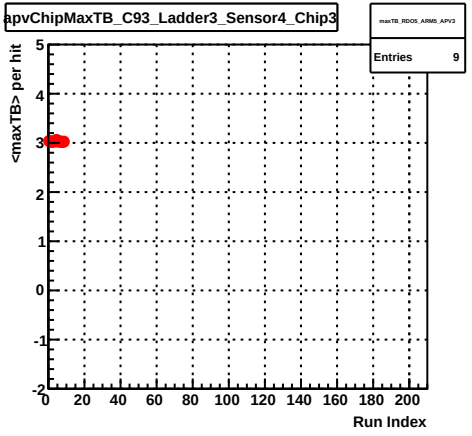

apvChipMaxTB_C88_Ladder3_Sensor3_Chip4
maxTB_RDO5_ARM5_APV8
Entries 9
<maxTB> per hit
Run Index

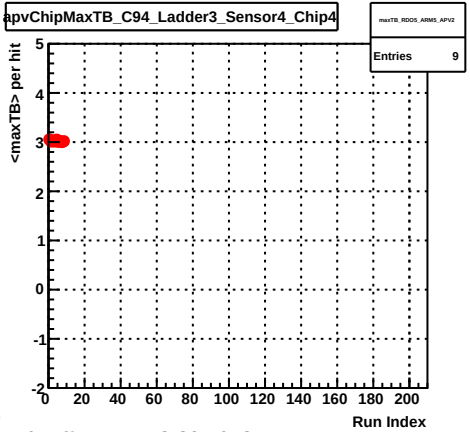

apvChipMaxTB_C89_Ladder3_Sensor3_Chip5
maxTB_RDO5_ARM5_APV7
Entries 9
<maxTB> per hit
Run Index

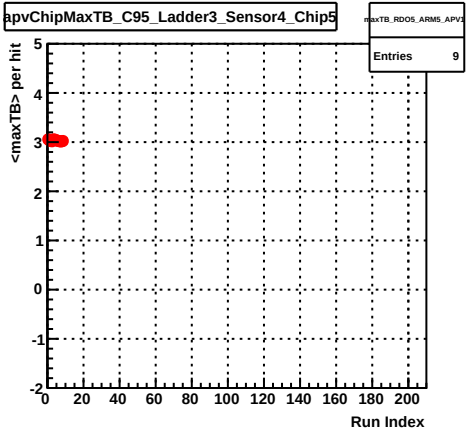

apvChipMaxTB_C90_Ladder3_Sensor3_Chip6
maxTB_RDO5_ARM5_APV6
Entries 9
<maxTB> per hit
Run Index

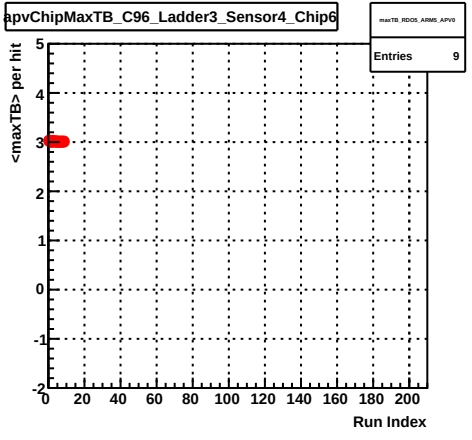

apvChipMaxTB_C91_Ladder3_Sensor4_Chip1
maxTB_RDO5_ARM5_APV5
Entries 9
<maxTB> per hit
Run Index

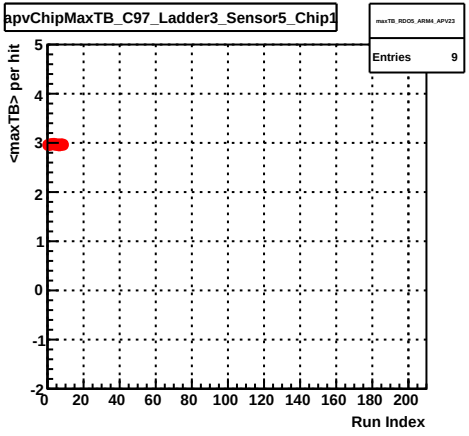

apvChipMaxTB_C92_Ladder3_Sensor4_Chip2
maxTB_RDO5_ARM5_APV4
Entries 9
<maxTB> per hit
Run Index

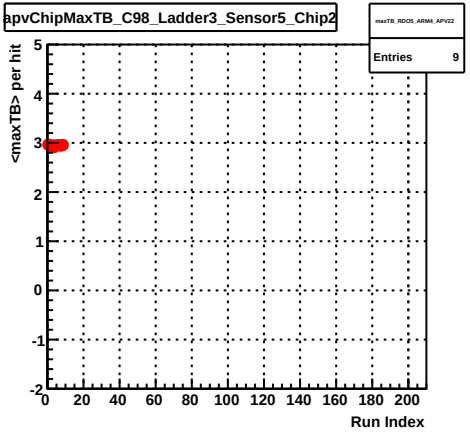

apvChipMaxTB_C93_Ladder3_Sensor4_Chip3
maxTB_RDO5_ARM5_APV3
Entries 9
<maxTB> per hit
Run Index

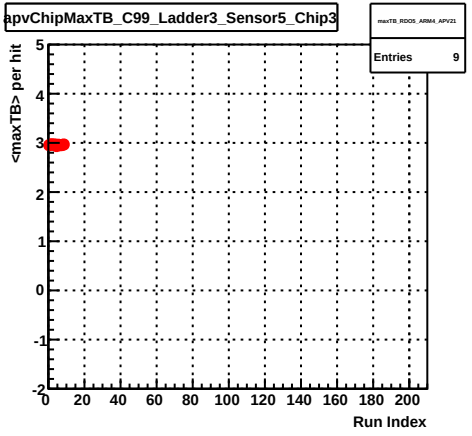

apvChipMaxTB_C94_Ladder3_Sensor4_Chip4
maxTB_RDO5_ARM5_APV2
Entries 9
<maxTB> per hit
Run Index

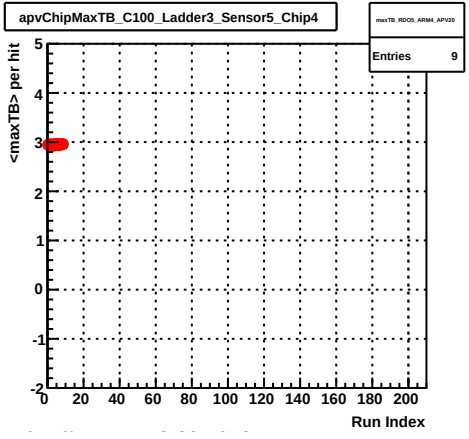

apvChipMaxTB_C95_Ladder3_Sensor4_Chip5
maxTB_RDO5_ARM5_APV1
Entries 9
<maxTB> per hit
Run Index

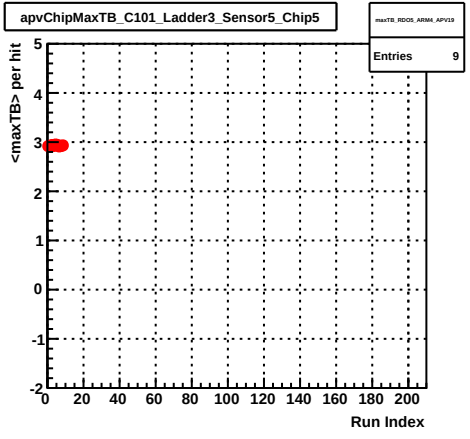

apvChipMaxTB_C96_Ladder3_Sensor4_Chip6
maxTB_RDO5_ARM5_APV0
Entries 9
<maxTB> per hit
Run Index

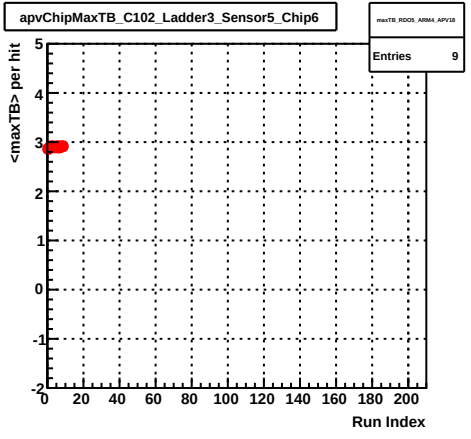

apvChipMaxTB_C97_Ladder3_Sensor5_Chip1
maxTB_RDOS_ARM4_APV23
Entries 9
<maxTB> per hit
Run Index

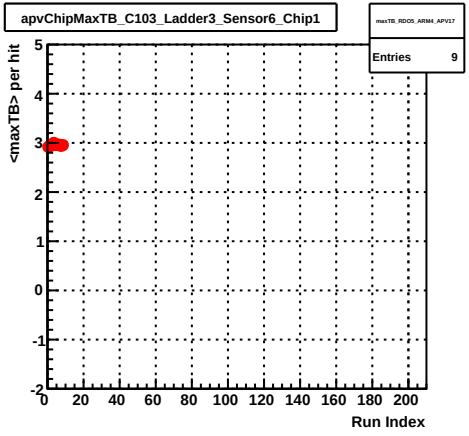

apvChipMaxTB_C98_Ladder3_Sensor5_Chip2
maxTB_RDOS_ARM4_APV22
Entries 9
<maxTB> per hit
Run Index

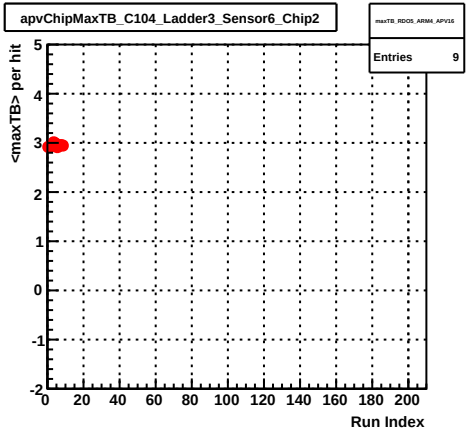

apvChipMaxTB_C99_Ladder3_Sensor5_Chip3
maxTB_RDOS_ARM4_APV21
Entries 9
<maxTB> per hit
Run Index

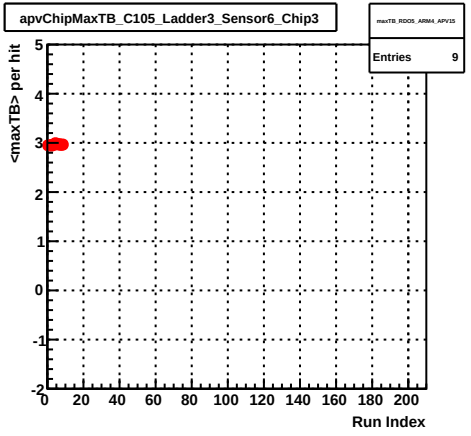

apvChipMaxTB_C100_Ladder3_Sensor5_Chip4
maxTB_RDOS_ARM4_APV20
Entries 9
<maxTB> per hit
Run Index

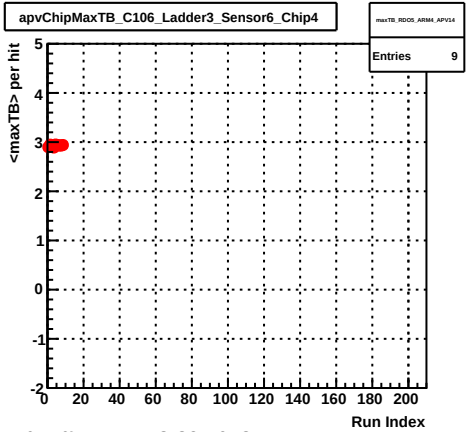

apvChipMaxTB_C101_Ladder3_Sensor5_Chip5
maxTB_RDOS_ARM4_APV19
Entries 9
<maxTB> per hit
Run Index

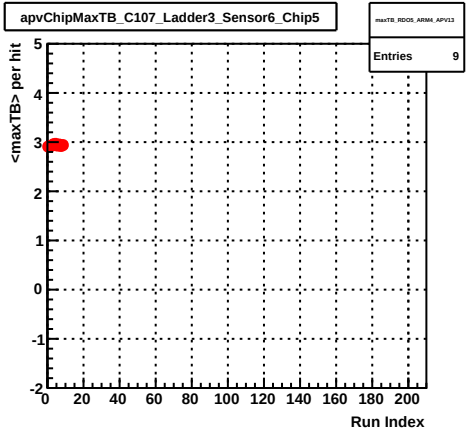

apvChipMaxTB_C102_Ladder3_Sensor5_Chip6
maxTB_RDOS_ARM4_APV18
Entries 9
<maxTB> per hit
Run Index

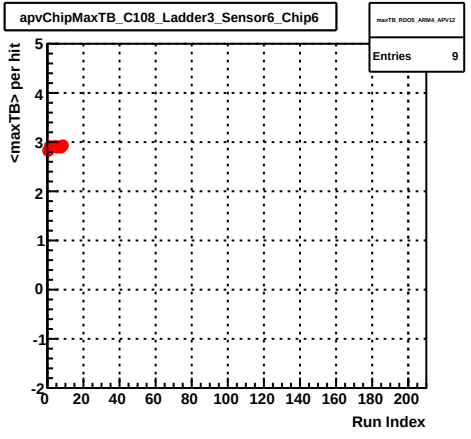

apvChipMaxTB_C103_Ladder3_Sensor6_Chip1
maxTB_RDOS_ARM4_APV17
Entries 9
<maxTB> per hit
Run Index

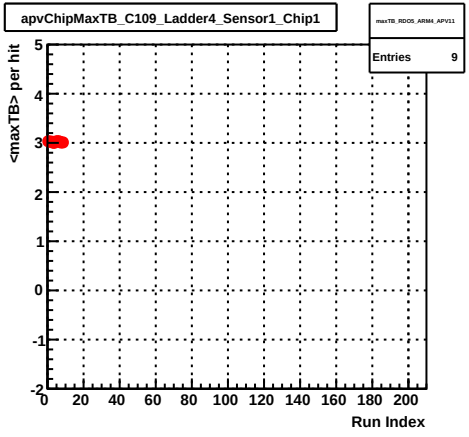

apvChipMaxTB_C104_Ladder3_Sensor6_Chip2
maxTB_RDOS_ARM4_APV16
Entries 9
<maxTB> per hit
Run Index

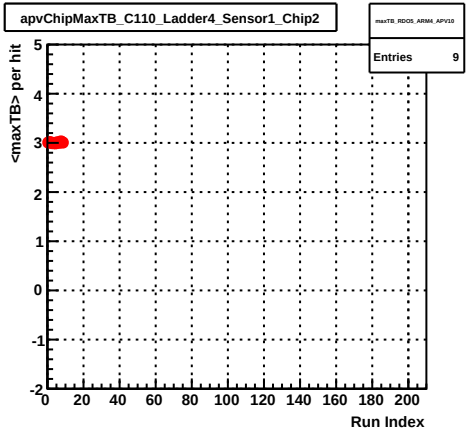

apvChipMaxTB_C105_Ladder3_Sensor6_Chip3
maxTB_RDOS_ARM4_APV15
Entries 9
<maxTB> per hit
Run Index

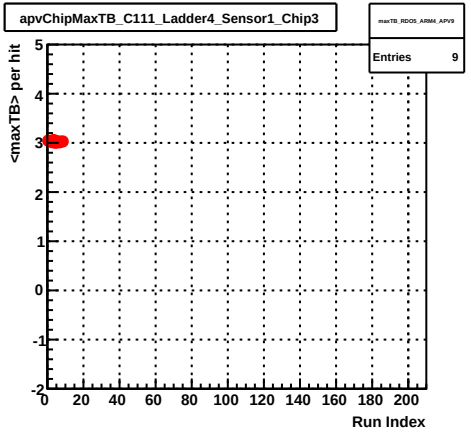

apvChipMaxTB_C106_Ladder3_Sensor6_Chip4
maxTB_RDOS_ARM4_APV14
Entries 9
<maxTB> per hit
Run Index

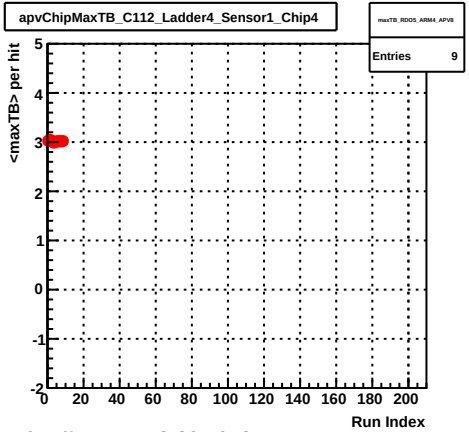

apvChipMaxTB_C107_Ladder3_Sensor6_Chip5
maxTB_RDOS_ARM4_APV13
Entries 9
<maxTB> per hit
Run Index

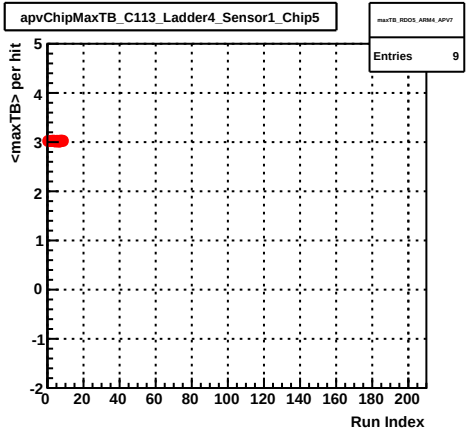

apvChipMaxTB_C108_Ladder3_Sensor6_Chip6
maxTB_RDOS_ARM4_APV12
Entries 9
<maxTB> per hit
Run Index

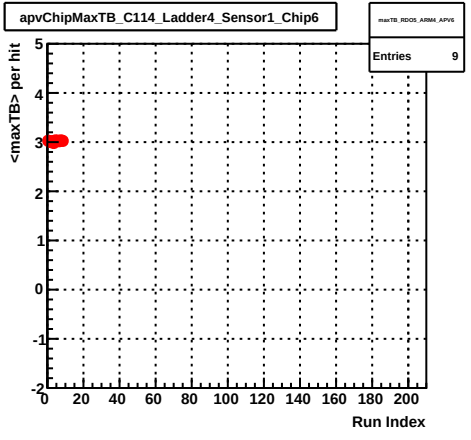

apvChipMaxTB_C109_Ladder4_Sensor1_Chip1
maxTB_RDOS_ARM4_APV11
Entries 9
<maxTB> per hit
Run Index

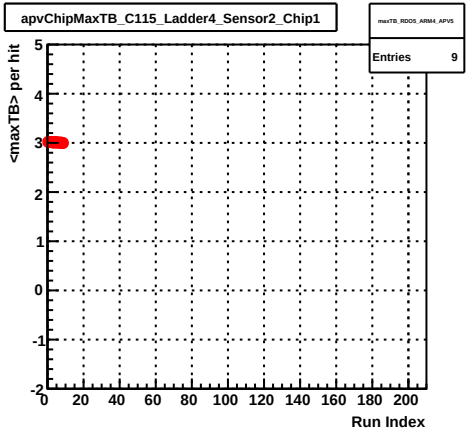

apvChipMaxTB_C110_Ladder4_Sensor1_Chip2
maxTB_RDOS_ARM4_APV10
Entries 9
<maxTB> per hit
Run Index

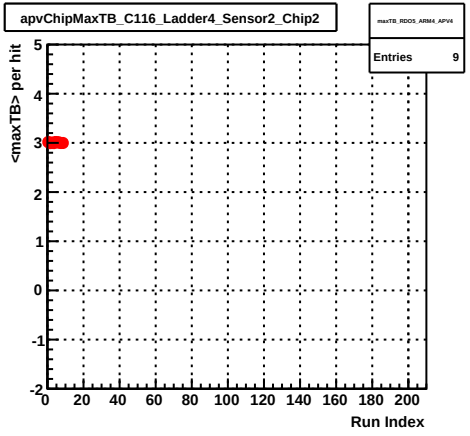

apvChipMaxTB_C111_Ladder4_Sensor1_Chip3
maxTB_RDOS_ARM4_APV9
Entries 9
<maxTB> per hit
Run Index

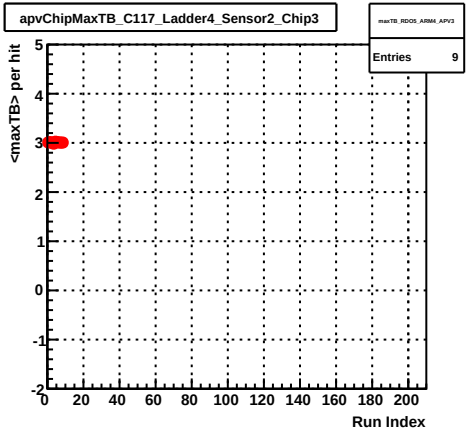

apvChipMaxTB_C112_Ladder4_Sensor1_Chip4
maxTB_RDOS_ARM4_APV8
Entries 9
<maxTB> per hit
Run Index

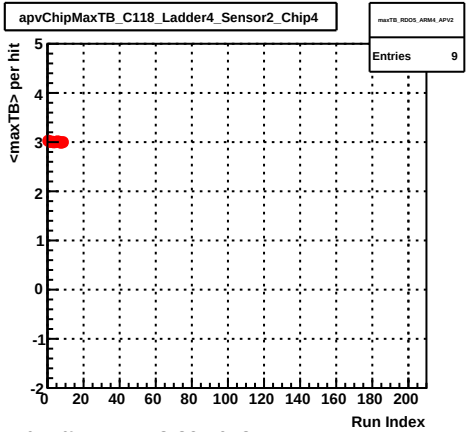

apvChipMaxTB_C113_Ladder4_Sensor1_Chip5
maxTB_RDOS_ARM4_APV7
Entries 9
<maxTB> per hit
Run Index

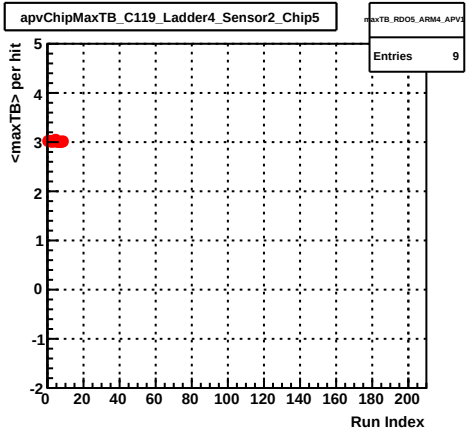

apvChipMaxTB_C114_Ladder4_Sensor1_Chip6
maxTB_RDOS_ARM4_APV6
Entries 9
<maxTB> per hit
Run Index

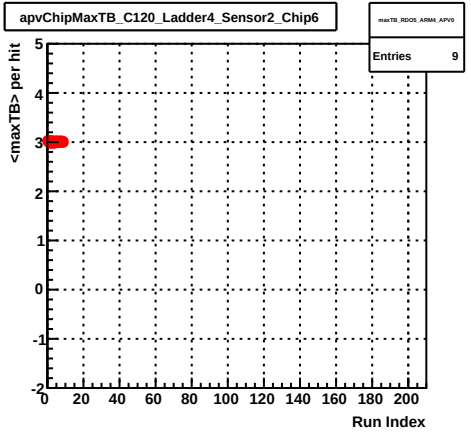

apvChipMaxTB_C115_Ladder4_Sensor2_Chip1
maxTB_RDO5_ARM4_APV5
Entries 9
<maxTB> per hit
Run Index

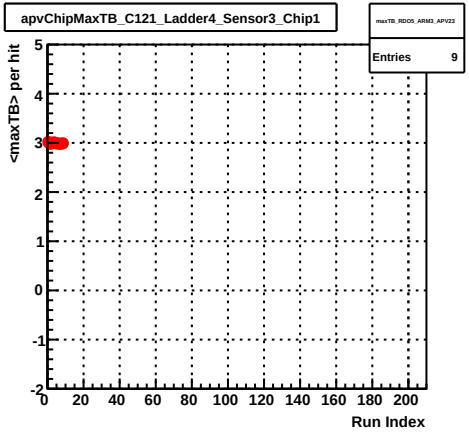

apvChipMaxTB_C116_Ladder4_Sensor2_Chip2
maxTB_RDO5_ARM4_APV4
Entries 9
<maxTB> per hit
Run Index

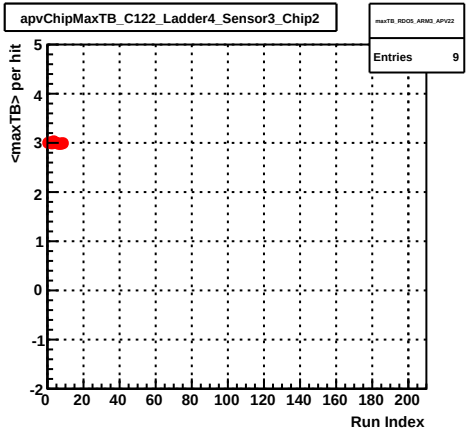

apvChipMaxTB_C117_Ladder4_Sensor2_Chip3
maxTB_RDO5_ARM4_APV3
Entries 9
<maxTB> per hit
Run Index

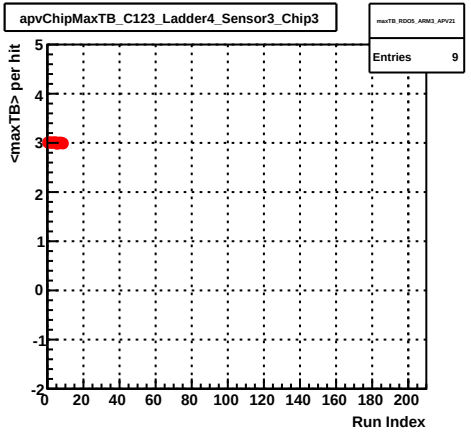

apvChipMaxTB_C118_Ladder4_Sensor2_Chip4
maxTB_RDO5_ARM4_APV2
Entries 9
<maxTB> per hit
Run Index

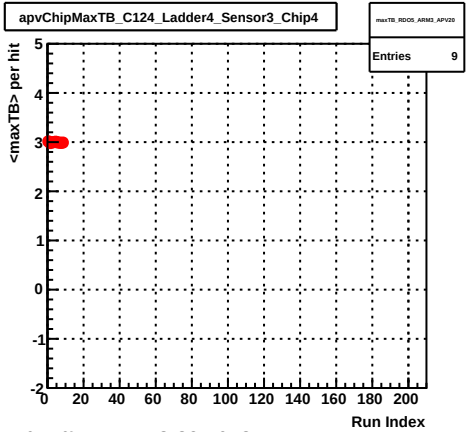

apvChipMaxTB_C119_Ladder4_Sensor2_Chip5
maxTB_RDO5_ARM4_APV1
Entries 9
<maxTB> per hit
Run Index

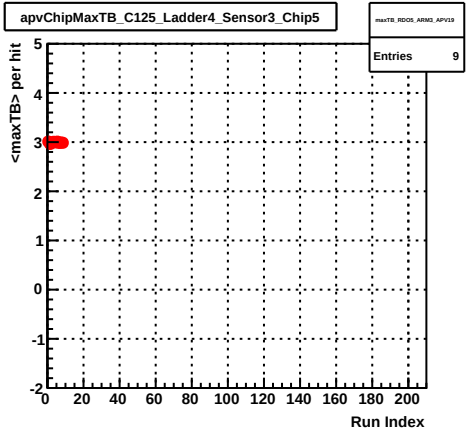

apvChipMaxTB_C120_Ladder4_Sensor2_Chip6
maxTB_RDO5_ARM4_APV0
Entries 9
<maxTB> per hit
Run Index

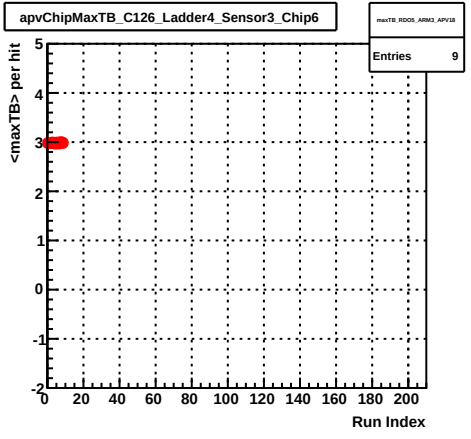

apvChipMaxTB_C121_Ladder4_Sensor3_Chip1
maxTB_RDOS_ARM3_APV23
Entries 9
<maxTB> per hit
Run Index

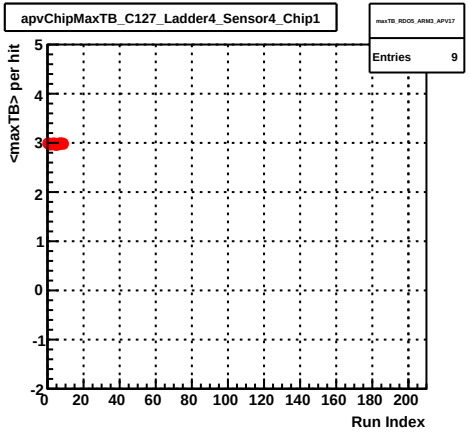

apvChipMaxTB_C122_Ladder4_Sensor3_Chip2
maxTB_RDOS_ARM3_APV22
Entries 9
<maxTB> per hit
Run Index

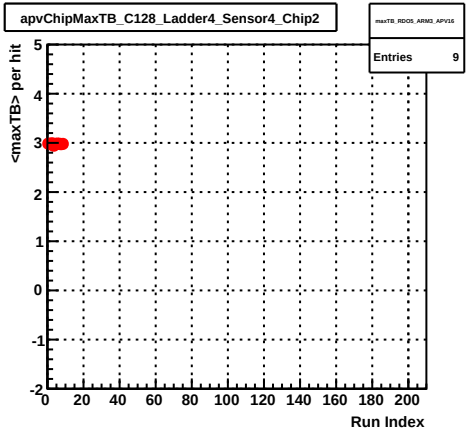

apvChipMaxTB_C123_Ladder4_Sensor3_Chip3
maxTB_RDOS_ARM3_APV21
Entries 9
<maxTB> per hit
Run Index

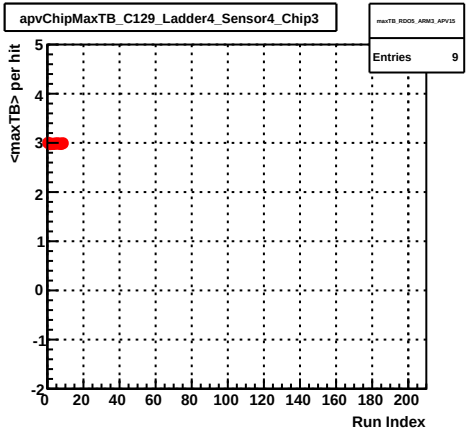

apvChipMaxTB_C124_Ladder4_Sensor3_Chip4
maxTB_RDOS_ARM3_APV20
Entries 9
<maxTB> per hit
Run Index

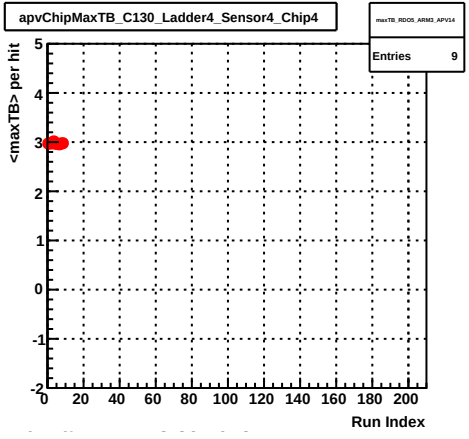

apvChipMaxTB_C125_Ladder4_Sensor3_Chip5
maxTB_RDOS_ARM3_APV19
Entries 9
<maxTB> per hit
Run Index

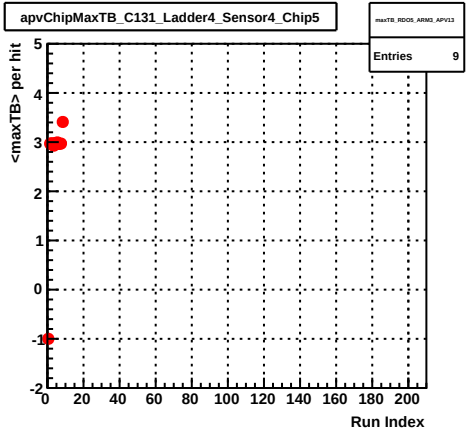

apvChipMaxTB_C126_Ladder4_Sensor3_Chip6
maxTB_RDOS_ARM3_APV18
Entries 9
<maxTB> per hit
Run Index

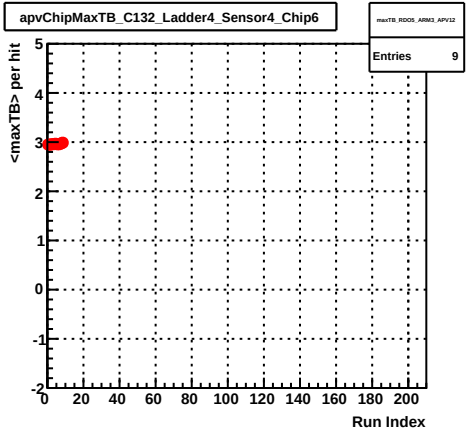

apvChipMaxTB_C127_Ladder4_Sensor4_Chip1
maxTB_RDOS_ARM3_APV17
Entries 9
<maxTB> per hit
Run Index

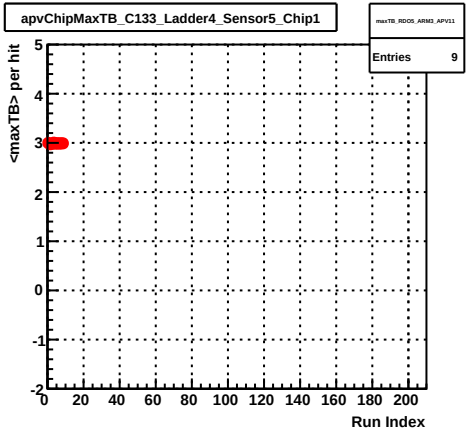

apvChipMaxTB_C128_Ladder4_Sensor4_Chip2
maxTB_RDOS_ARM3_APV16
Entries 9
<maxTB> per hit
Run Index

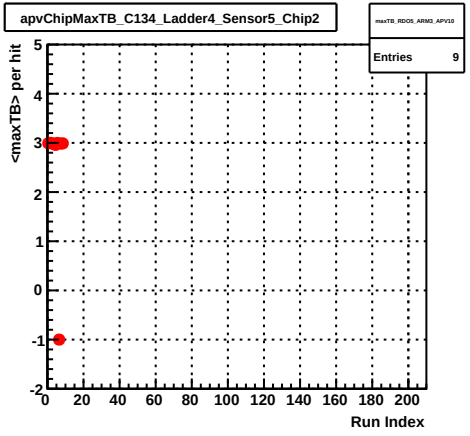

apvChipMaxTB_C129_Ladder4_Sensor4_Chip3
maxTB_RDOS_ARM3_APV15
Entries 9
<maxTB> per hit
Run Index

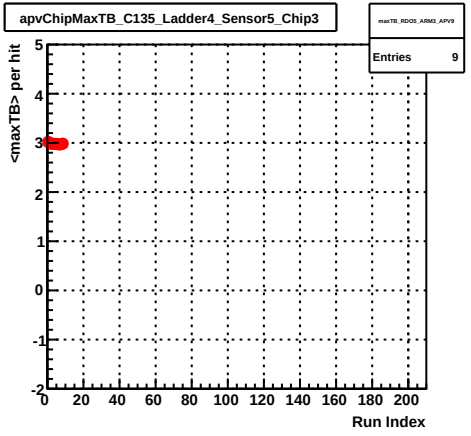

apvChipMaxTB_C130_Ladder4_Sensor4_Chip4
maxTB_RDOS_ARM3_APV14
Entries 9
<maxTB> per hit
Run Index

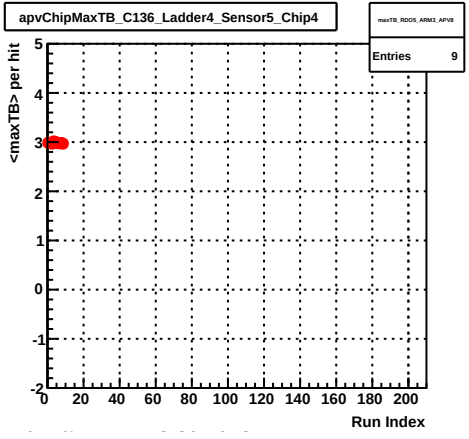

apvChipMaxTB_C131_Ladder4_Sensor4_Chip5
maxTB_RDOS_ARM3_APV13
Entries 9
<maxTB> per hit
Run Index

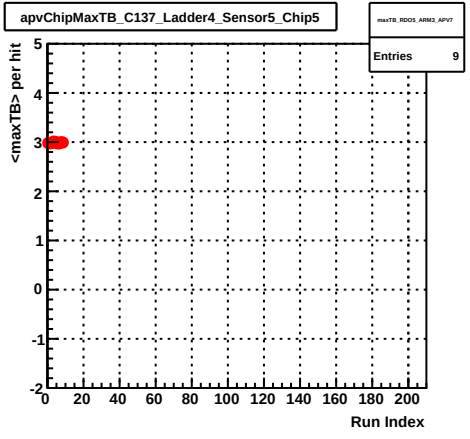

apvChipMaxTB_C132_Ladder4_Sensor4_Chip6
maxTB_RDOS_ARM3_APV12
Entries 9
<maxTB> per hit
Run Index

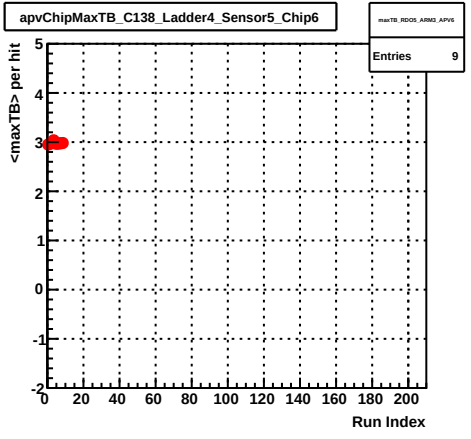

apvChipMaxTB_C133_Ladder4_Sensor5_Chip1
maxTB_RDO5_ARM3_APV11
Entries 9
<maxTB> per hit
Run Index

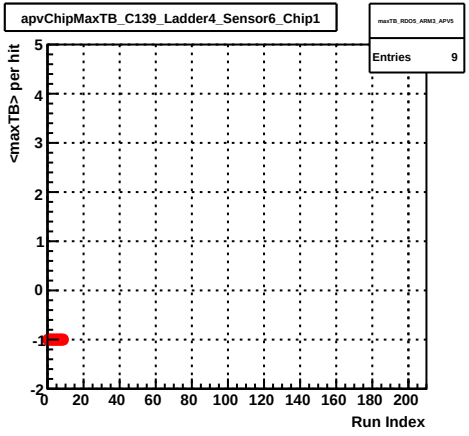

apvChipMaxTB_C134_Ladder4_Sensor5_Chip2
maxTB_RDO5_ARM3_APV10
Entries 9
<maxTB> per hit
Run Index

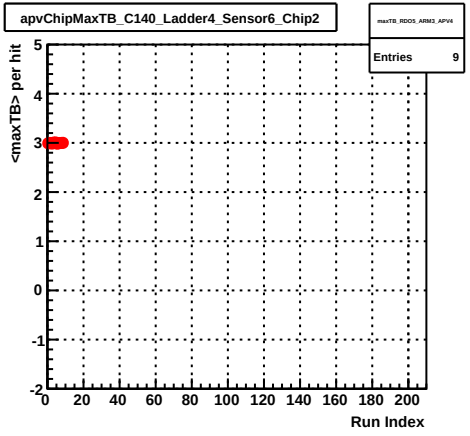

apvChipMaxTB_C135_Ladder4_Sensor5_Chip3
maxTB_RDO5_ARM3_APV9
Entries 9
<maxTB> per hit
Run Index

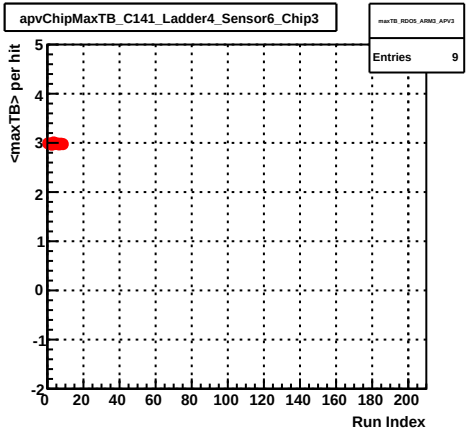

apvChipMaxTB_C136_Ladder4_Sensor5_Chip4
maxTB_RDO5_ARM3_APV8
Entries 9
<maxTB> per hit
Run Index

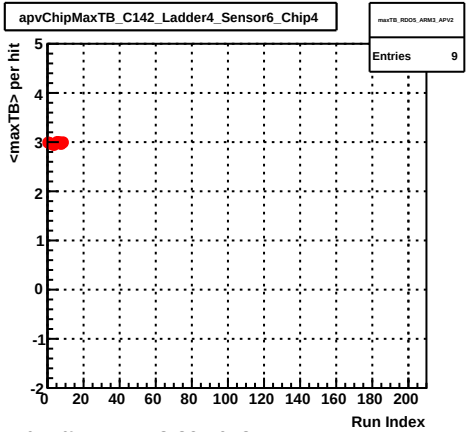

apvChipMaxTB_C137_Ladder4_Sensor5_Chip5
maxTB_RDO5_ARM3_APV7
Entries 9
<maxTB> per hit
Run Index

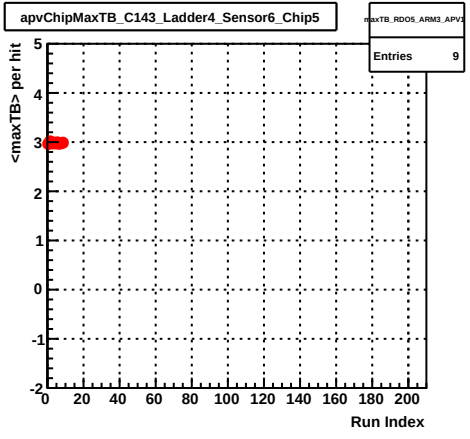

apvChipMaxTB_C138_Ladder4_Sensor5_Chip6
maxTB_RDO5_ARM3_APV6
Entries 9
<maxTB> per hit
Run Index

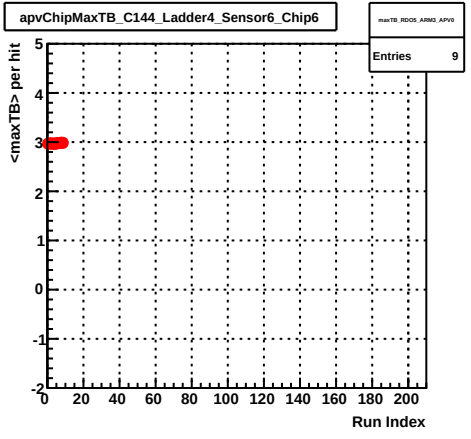

apvChipMaxTB_C139_Ladder4_Sensor6_Chip1
maxTB_RDO5_ARM3_APV5
Entries 9
<maxTB> per hit
Run Index

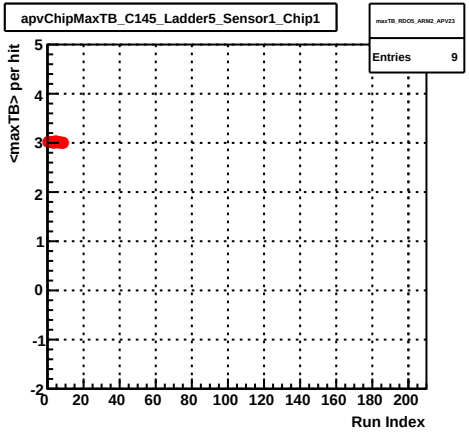

apvChipMaxTB_C140_Ladder4_Sensor6_Chip2
maxTB_RDO5_ARM3_APV4
Entries 9
<maxTB> per hit
Run Index

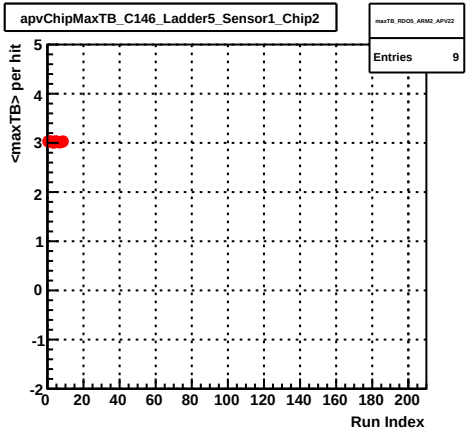

apvChipMaxTB_C141_Ladder4_Sensor6_Chip3
maxTB_RDO5_ARM3_APV3
Entries 9
<maxTB> per hit
Run Index

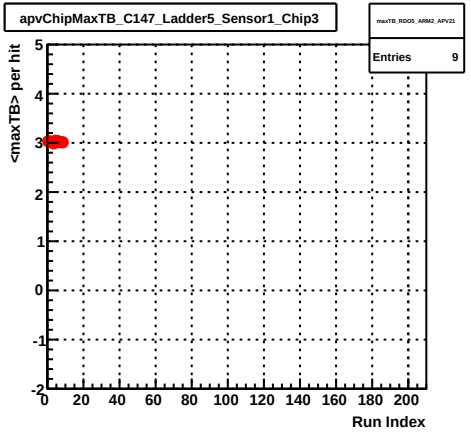

apvChipMaxTB_C142_Ladder4_Sensor6_Chip4
maxTB_RDO5_ARM3_APV2
Entries 9
<maxTB> per hit
Run Index

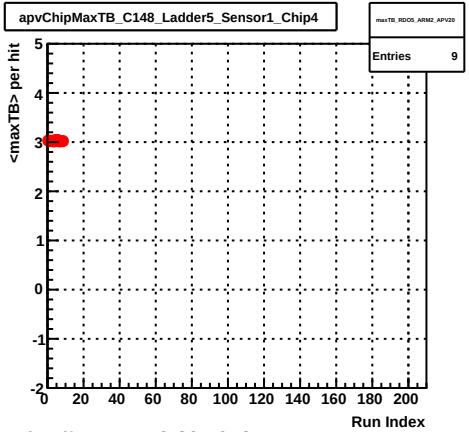

apvChipMaxTB_C143_Ladder4_Sensor6_Chip5
maxTB_RDO5_ARM3_APV1
Entries 9
<maxTB> per hit
Run Index

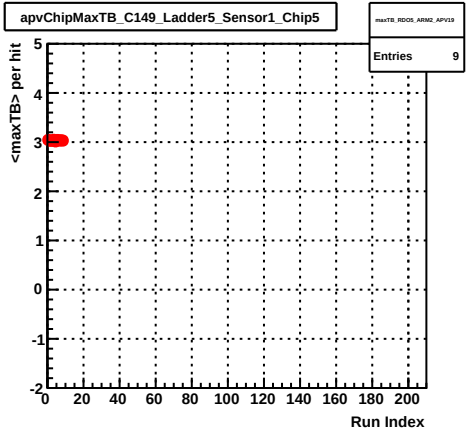

apvChipMaxTB_C144_Ladder4_Sensor6_Chip6
maxTB_RDO5_ARM3_APV0
Entries 9
<maxTB> per hit
Run Index

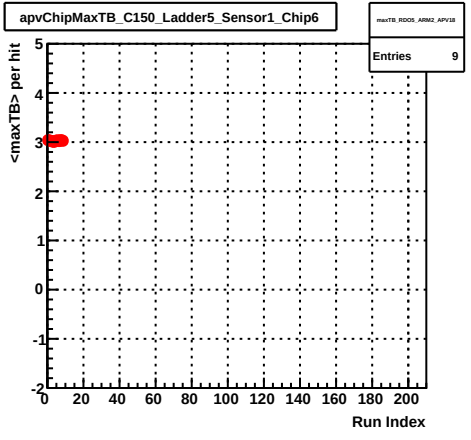

apvChipMaxTB_C145_Ladder5_Sensor1_Chip1
maxTB_RDOS_ARM2_APV23
Entries 9
<maxTB> per hit
Run Index

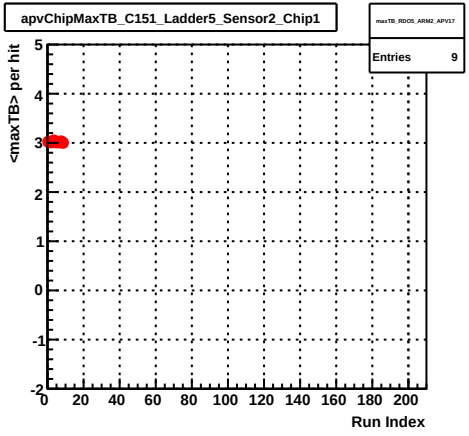

apvChipMaxTB_C146_Ladder5_Sensor1_Chip2
maxTB_RDOS_ARM2_APV22
Entries 9
<maxTB> per hit
Run Index

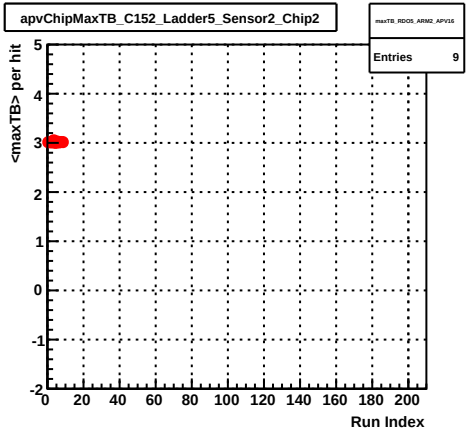

apvChipMaxTB_C147_Ladder5_Sensor1_Chip3
maxTB_RDOS_ARM2_APV21
Entries 9
<maxTB> per hit
Run Index

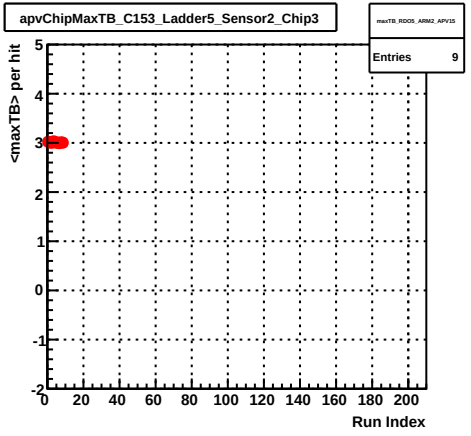

apvChipMaxTB_C148_Ladder5_Sensor1_Chip4
maxTB_RDOS_ARM2_APV20
Entries 9
<maxTB> per hit
Run Index

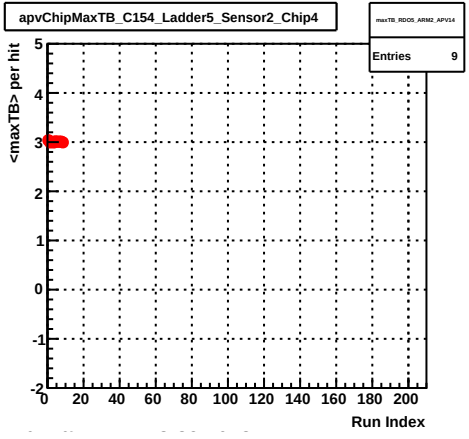

apvChipMaxTB_C149_Ladder5_Sensor1_Chip5
maxTB_RDOS_ARM2_APV19
Entries 9
<maxTB> per hit
Run Index

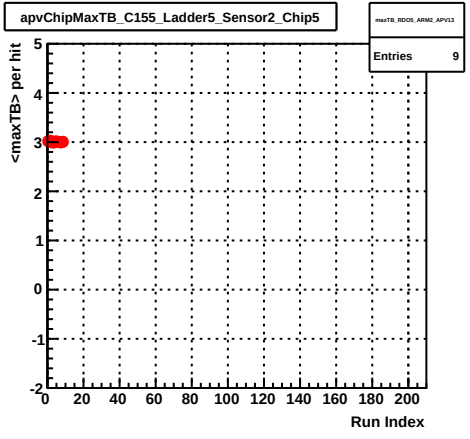

apvChipMaxTB_C150_Ladder5_Sensor1_Chip6
maxTB_RDOS_ARM2_APV18
Entries 9
<maxTB> per hit
Run Index

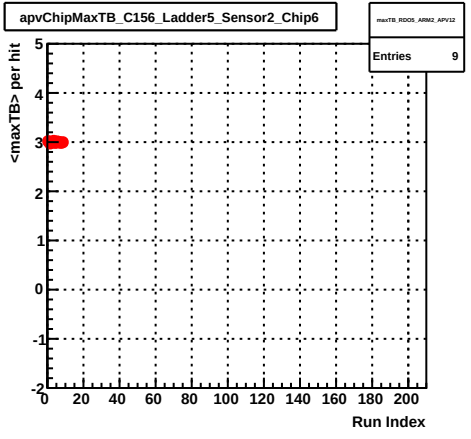

apvChipMaxTB_C151_Ladder5_Sensor2_Chip1
maxTB_RDOS_ARM2_APV17
Entries 9
<maxTB> per hit
Run Index

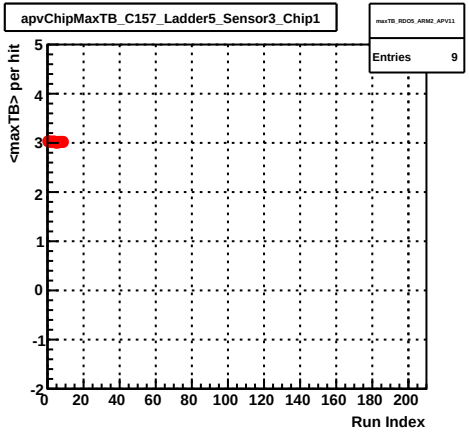

apvChipMaxTB_C152_Ladder5_Sensor2_Chip2
maxTB_RDOS_ARM2_APV16
Entries 9
<maxTB> per hit
Run Index

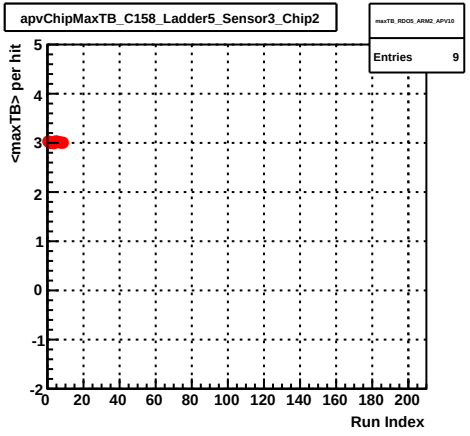

apvChipMaxTB_C153_Ladder5_Sensor2_Chip3
maxTB_RDOS_ARM2_APV15
Entries 9
<maxTB> per hit
Run Index

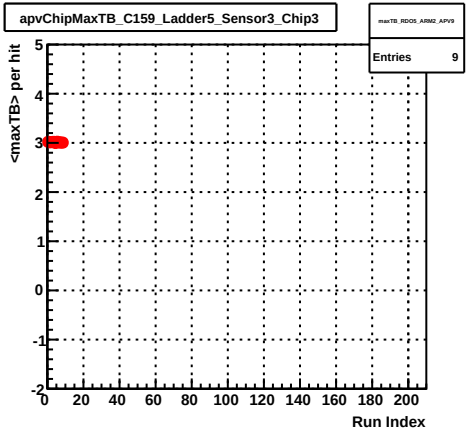

apvChipMaxTB_C154_Ladder5_Sensor2_Chip4
maxTB_RDOS_ARM2_APV14
Entries 9
<maxTB> per hit
Run Index

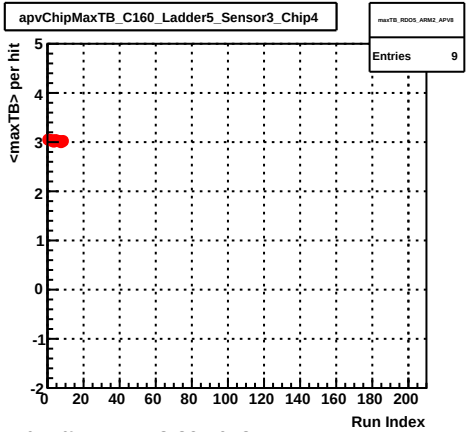

apvChipMaxTB_C155_Ladder5_Sensor2_Chip5
maxTB_RDOS_ARM2_APV13
Entries 9
<maxTB> per hit
Run Index

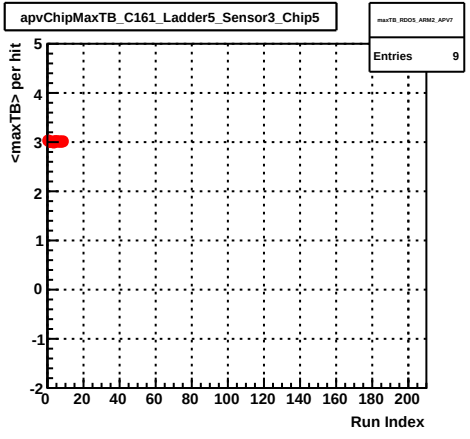

apvChipMaxTB_C156_Ladder5_Sensor2_Chip6
maxTB_RDOS_ARM2_APV12
Entries 9
<maxTB> per hit
Run Index

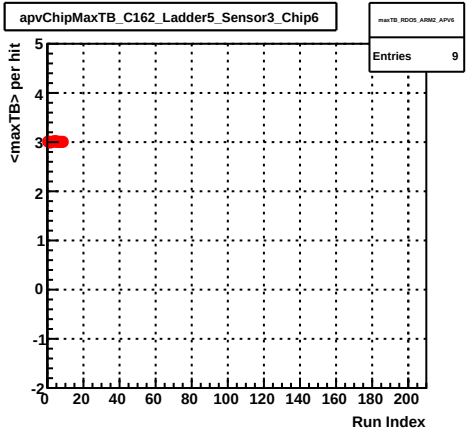

apvChipMaxTB_C157_Ladder5_Sensor3_Chip1
maxTB_RDO5_ARM2_APV11
Entries 9
<maxTB> per hit
Run Index

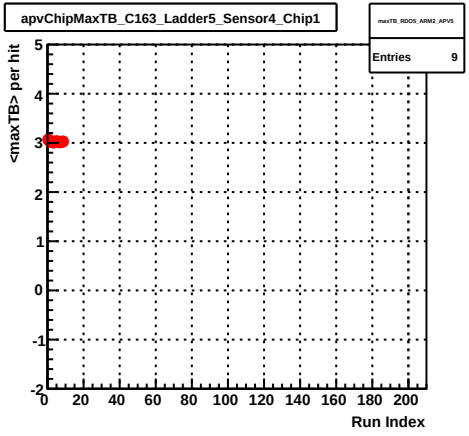

apvChipMaxTB_C158_Ladder5_Sensor3_Chip2
maxTB_RDO5_ARM2_APV10
Entries 9
<maxTB> per hit
Run Index

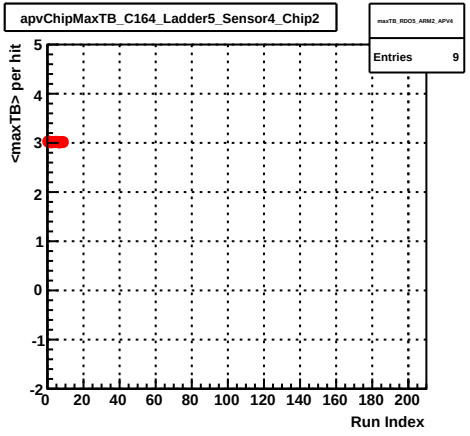

apvChipMaxTB_C159_Ladder5_Sensor3_Chip3
maxTB_RDO5_ARM2_APV9
Entries 9
<maxTB> per hit
Run Index

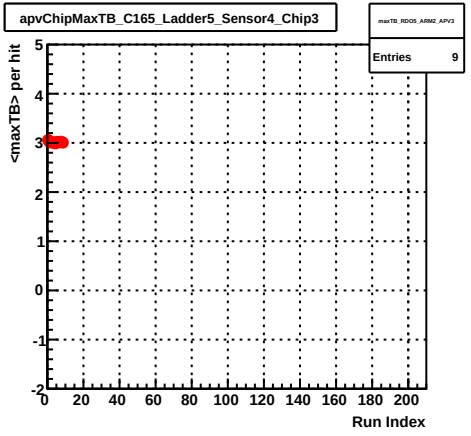

apvChipMaxTB_C160_Ladder5_Sensor3_Chip4
maxTB_RDO5_ARM2_APV8
Entries 9
<maxTB> per hit
Run Index

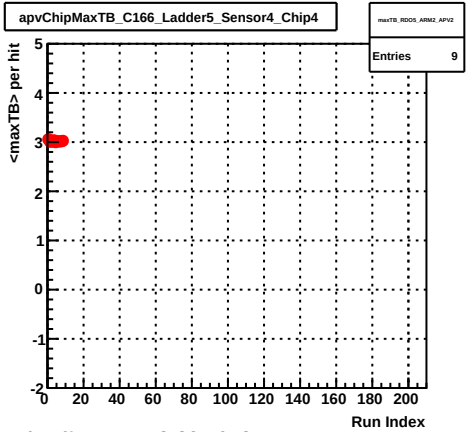

apvChipMaxTB_C161_Ladder5_Sensor3_Chip5
maxTB_RDO5_ARM2_APV7
Entries 9
<maxTB> per hit
Run Index

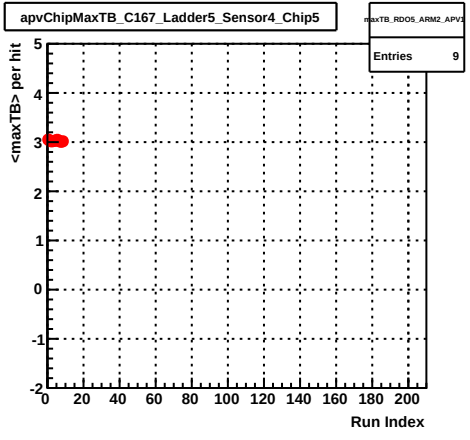

apvChipMaxTB_C162_Ladder5_Sensor3_Chip6
maxTB_RDO5_ARM2_APV6
Entries 9
<maxTB> per hit
Run Index

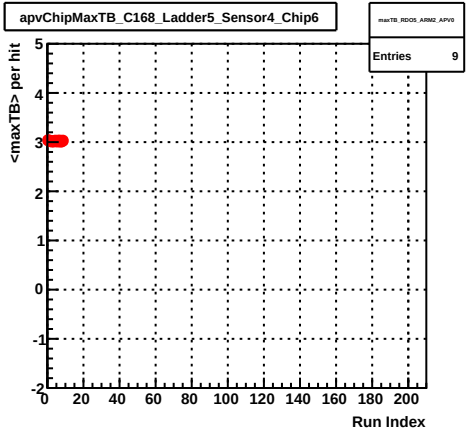

apvChipMaxTB_C163_Ladder5_Sensor4_Chip1
maxTB_RDO5_ARM2_APV5
Entries 9
<maxTB> per hit
Run Index

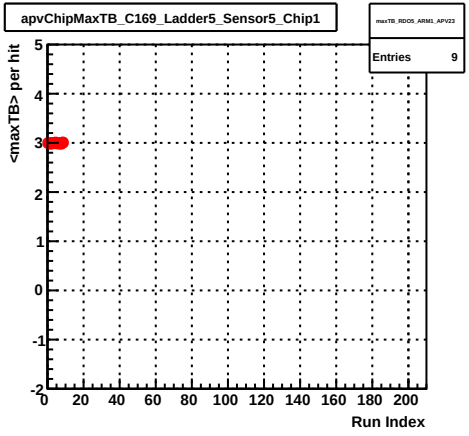

apvChipMaxTB_C164_Ladder5_Sensor4_Chip2
maxTB_RDO5_ARM2_APV4
Entries 9
<maxTB> per hit
Run Index

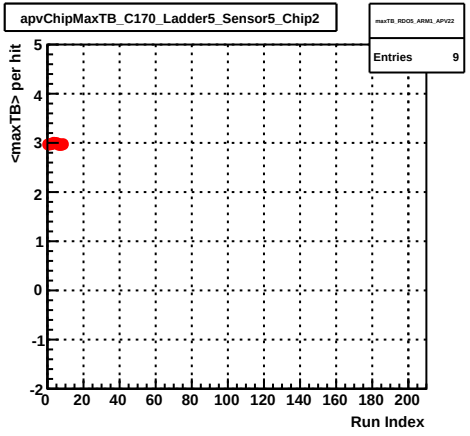

apvChipMaxTB_C165_Ladder5_Sensor4_Chip3
maxTB_RDO5_ARM2_APV3
Entries 9
<maxTB> per hit
Run Index

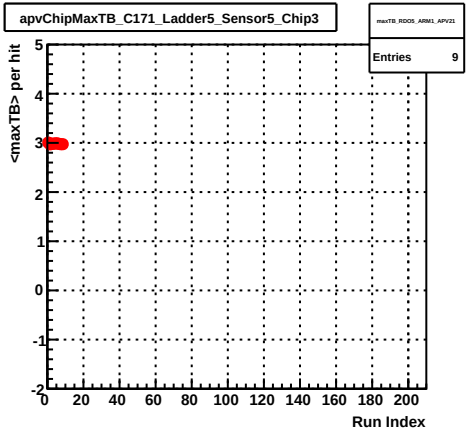

apvChipMaxTB_C166_Ladder5_Sensor4_Chip4
maxTB_RDO5_ARM2_APV2
Entries 9
<maxTB> per hit
Run Index

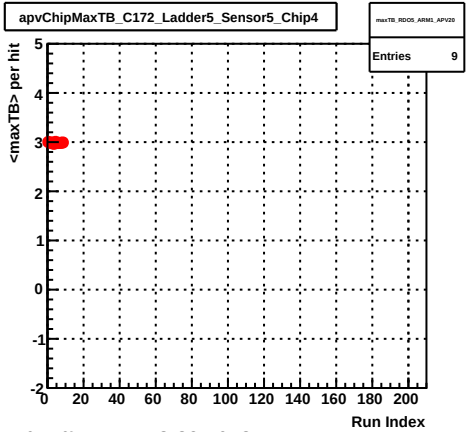

apvChipMaxTB_C167_Ladder5_Sensor4_Chip5
maxTB_RDO5_ARM2_APV1
Entries 9
<maxTB> per hit
Run Index

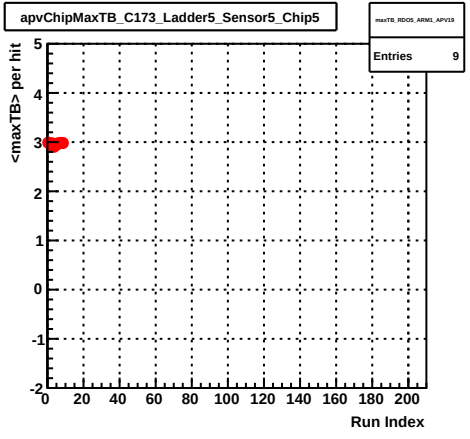

apvChipMaxTB_C168_Ladder5_Sensor4_Chip6
maxTB_RDO5_ARM2_APV0
Entries 9
<maxTB> per hit
Run Index

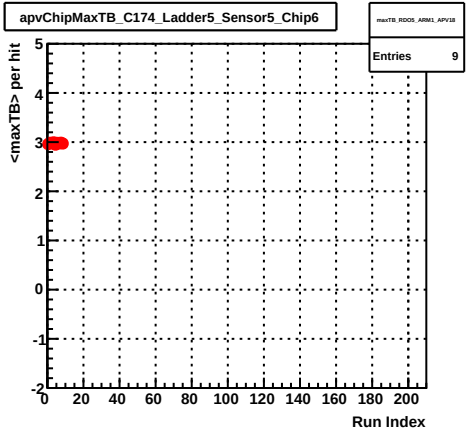

apvChipMaxTB_C169_Ladder5_Sensor5_Chip1
maxTB_RDOS_ARM1_APV23
Entries 9
<maxTB> per hit
Run Index

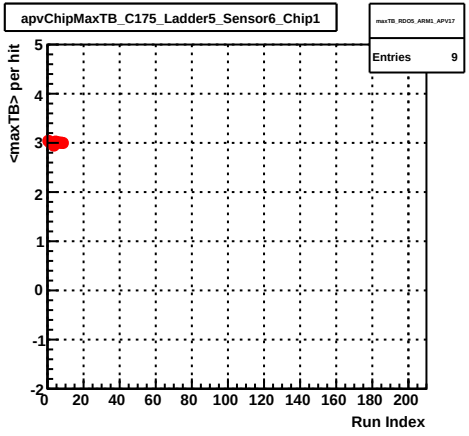

apvChipMaxTB_C170_Ladder5_Sensor5_Chip2
maxTB_RDOS_ARM1_APV22
Entries 9
<maxTB> per hit
Run Index

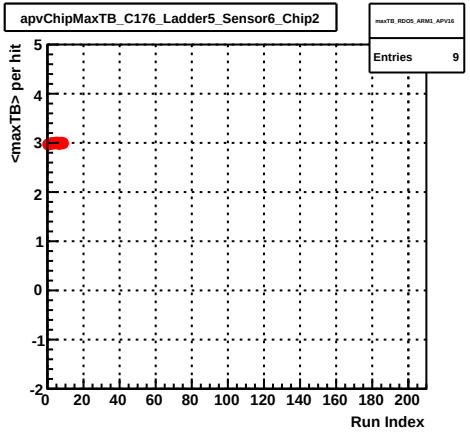

apvChipMaxTB_C171_Ladder5_Sensor5_Chip3
maxTB_RDOS_ARM1_APV21
Entries 9
<maxTB> per hit
Run Index

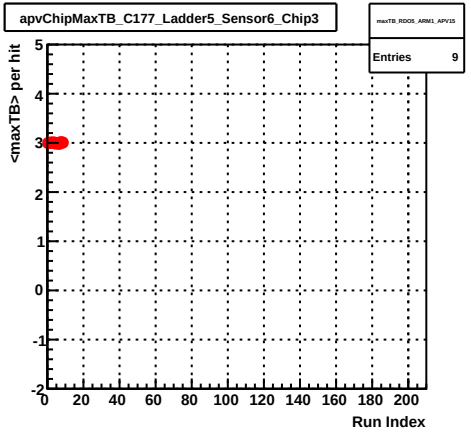

apvChipMaxTB_C172_Ladder5_Sensor5_Chip4
maxTB_RDOS_ARM1_APV20
Entries 9
<maxTB> per hit
Run Index

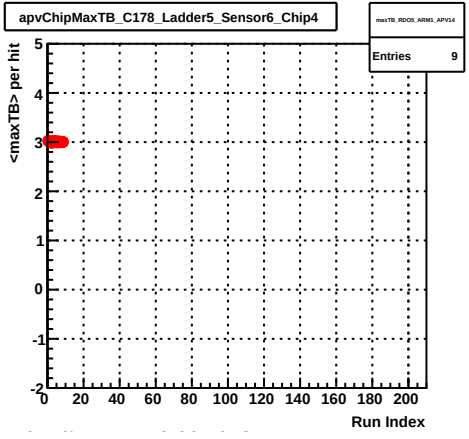

apvChipMaxTB_C173_Ladder5_Sensor5_Chip5
maxTB_RDOS_ARM1_APV19
Entries 9
<maxTB> per hit
Run Index

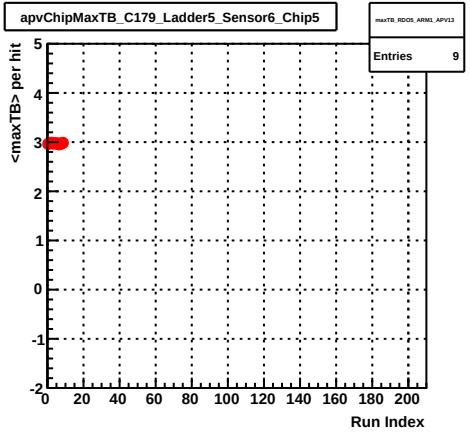

apvChipMaxTB_C174_Ladder5_Sensor5_Chip6
maxTB_RDOS_ARM1_APV18
Entries 9
<maxTB> per hit
Run Index

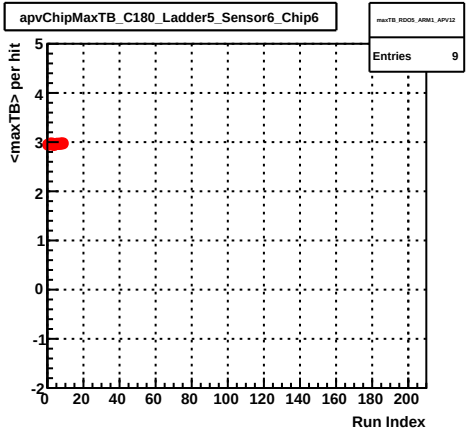

apvChipMaxTB_C175_Ladder5_Sensor6_Chip1
maxTB_RDOS_ARM1_APV17
Entries 9
<maxTB> per hit
Run Index

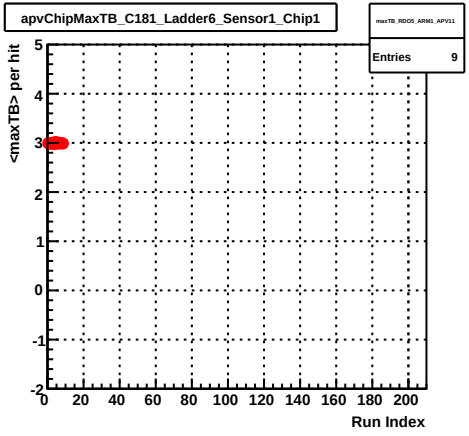

apvChipMaxTB_C176_Ladder5_Sensor6_Chip2
maxTB_RDOS_ARM1_APV16
Entries 9
<maxTB> per hit
Run Index

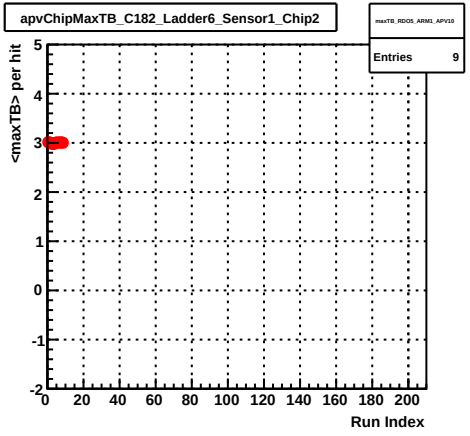

apvChipMaxTB_C177_Ladder5_Sensor6_Chip3
maxTB_RDOS_ARM1_APV15
Entries 9
<maxTB> per hit
Run Index

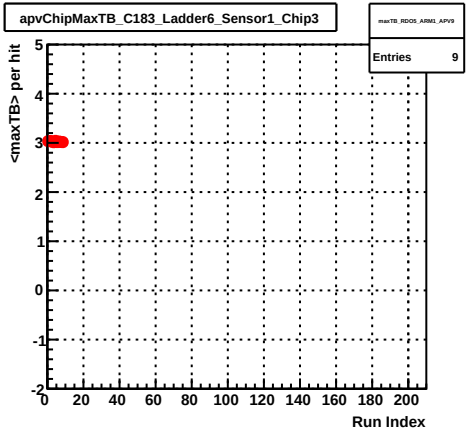

apvChipMaxTB_C178_Ladder5_Sensor6_Chip4
maxTB_RDOS_ARM1_APV14
Entries 9
<maxTB> per hit
Run Index

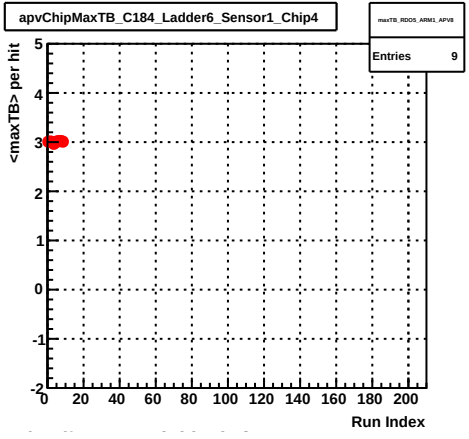

apvChipMaxTB_C179_Ladder5_Sensor6_Chip5
maxTB_RDOS_ARM1_APV13
Entries 9
<maxTB> per hit
Run Index

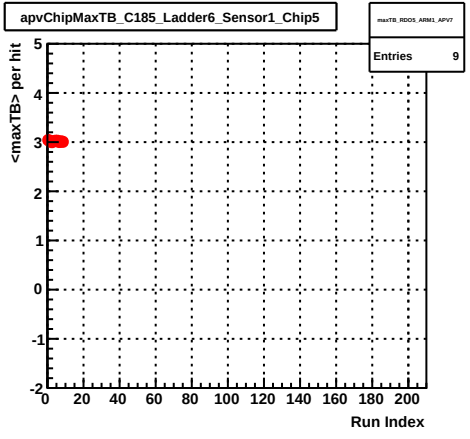

apvChipMaxTB_C180_Ladder5_Sensor6_Chip6
maxTB_RDOS_ARM1_APV12
Entries 9
<maxTB> per hit
Run Index

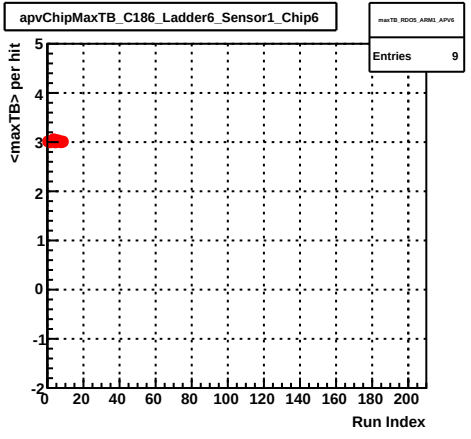

apvChipMaxTB_C181_Ladder6_Sensor1_Chip1
maxTB_RDOS_ARM1_APV11
Entries 9
<maxTB> per hit
Run Index

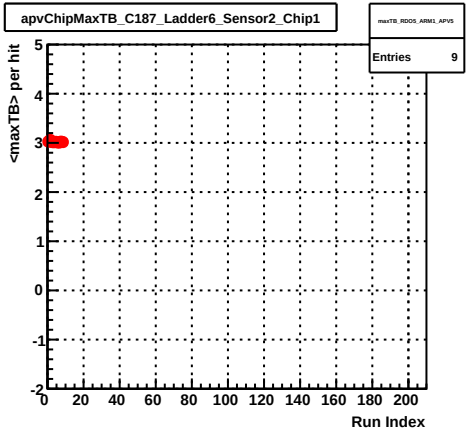

apvChipMaxTB_C182_Ladder6_Sensor1_Chip2
maxTB_RDOS_ARM1_APV10
Entries 9
<maxTB> per hit
Run Index

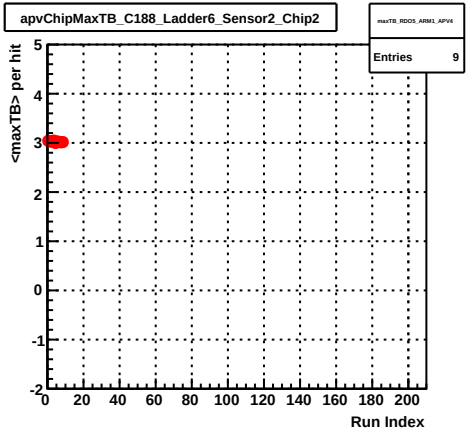

apvChipMaxTB_C183_Ladder6_Sensor1_Chip3
maxTB_RDOS_ARM1_APV9
Entries 9
<maxTB> per hit
Run Index

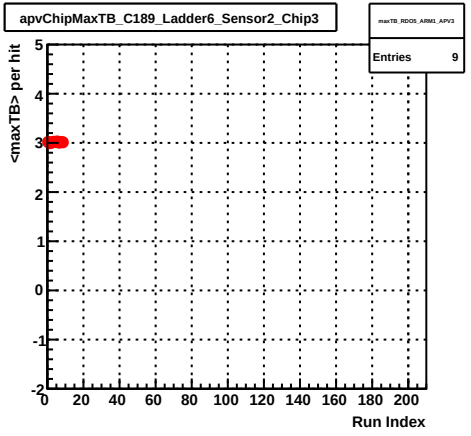

apvChipMaxTB_C184_Ladder6_Sensor1_Chip4
maxTB_RDOS_ARM1_APV8
Entries 9
<maxTB> per hit
Run Index

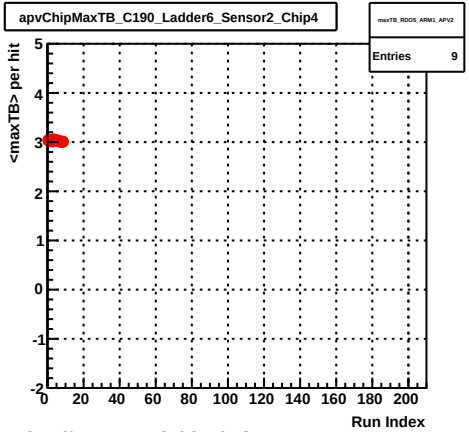

apvChipMaxTB_C185_Ladder6_Sensor1_Chip5
maxTB_RDOS_ARM1_APV7
Entries 9
<maxTB> per hit
Run Index

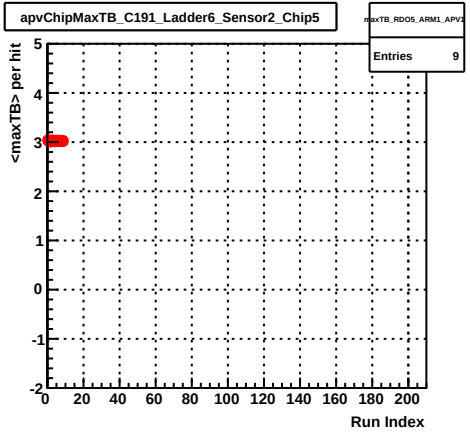

apvChipMaxTB_C186_Ladder6_Sensor1_Chip6
maxTB_RDOS_ARM1_APV6
Entries 9
<maxTB> per hit
Run Index

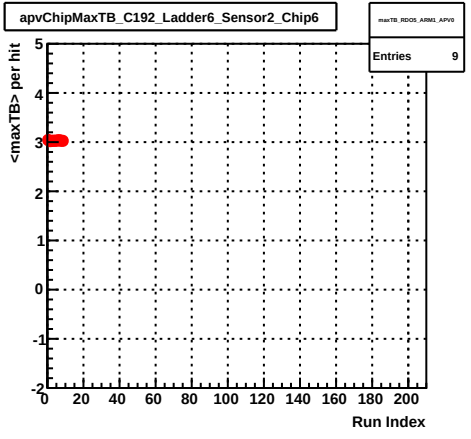

apvChipMaxTB_C187_Ladder6_Sensor2_Chip1
maxTB_RDO5_ARM1_APV5
Entries 9
<maxTB> per hit
Run Index

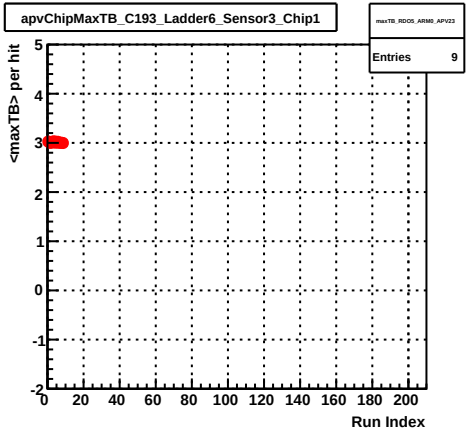

apvChipMaxTB_C188_Ladder6_Sensor2_Chip2
maxTB_RDO5_ARM1_APV4
Entries 9
<maxTB> per hit
Run Index

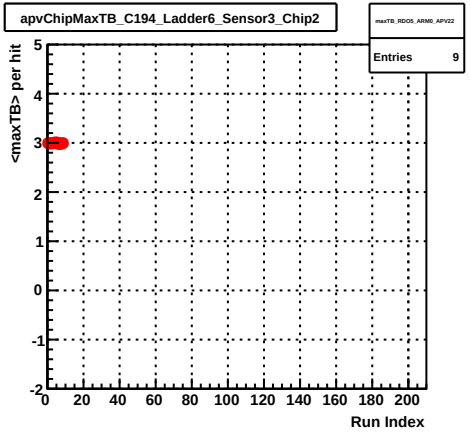

apvChipMaxTB_C189_Ladder6_Sensor2_Chip3
maxTB_RDO5_ARM1_APV3
Entries 9
<maxTB> per hit
Run Index

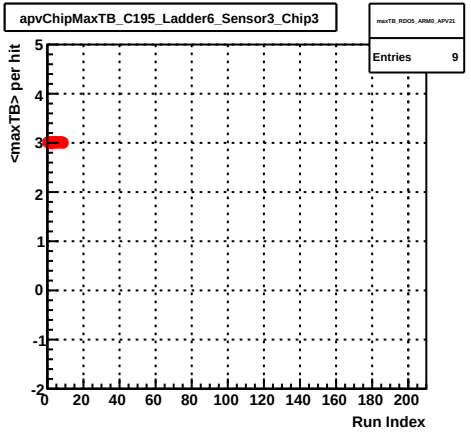

apvChipMaxTB_C190_Ladder6_Sensor2_Chip4
maxTB_RDO5_ARM1_APV2
Entries 9
<maxTB> per hit
Run Index

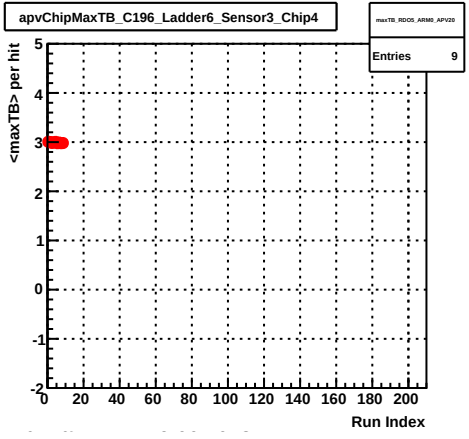

apvChipMaxTB_C191_Ladder6_Sensor2_Chip5
maxTB_RDO5_ARM1_APV1
Entries 9
<maxTB> per hit
Run Index

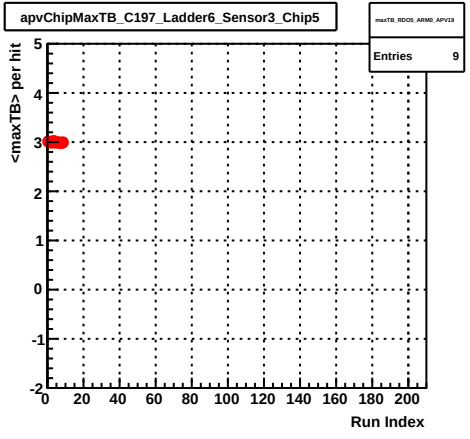

apvChipMaxTB_C192_Ladder6_Sensor2_Chip6
maxTB_RDO5_ARM1_APV0
Entries 9
<maxTB> per hit
Run Index

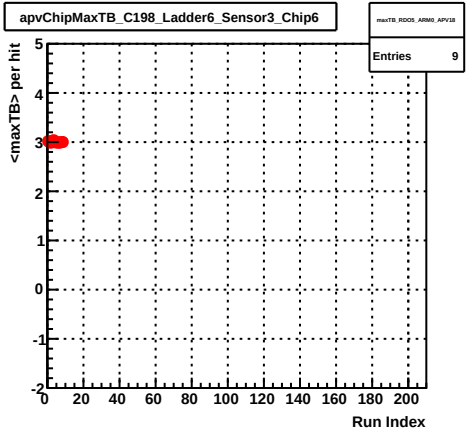

apvChipMaxTB_C193_Ladder6_Sensor3_Chip1
maxTB_RDO5_ARM0_APV23
Entries 9
<maxTB> per hit
Run Index

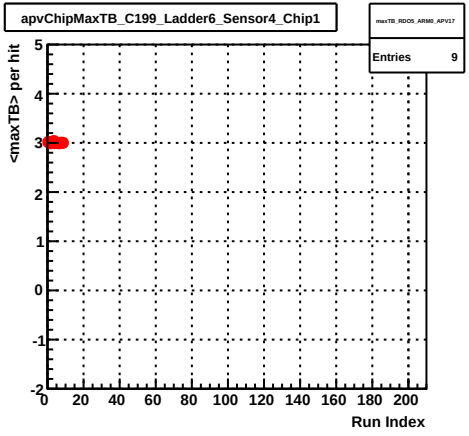

apvChipMaxTB_C194_Ladder6_Sensor3_Chip2
maxTB_RDO5_ARM0_APV22
Entries 9
<maxTB> per hit
Run Index

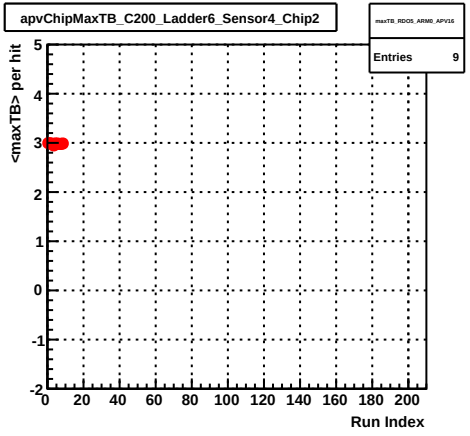

apvChipMaxTB_C195_Ladder6_Sensor3_Chip3
maxTB_RDO5_ARM0_APV21
Entries 9
<maxTB> per hit
Run Index

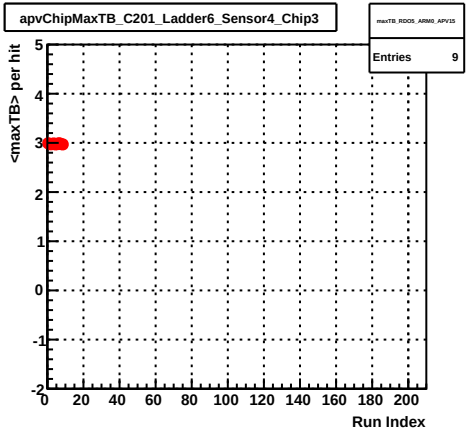

apvChipMaxTB_C196_Ladder6_Sensor3_Chip4
maxTB_RDO5_ARM0_APV20
Entries 9
<maxTB> per hit
Run Index

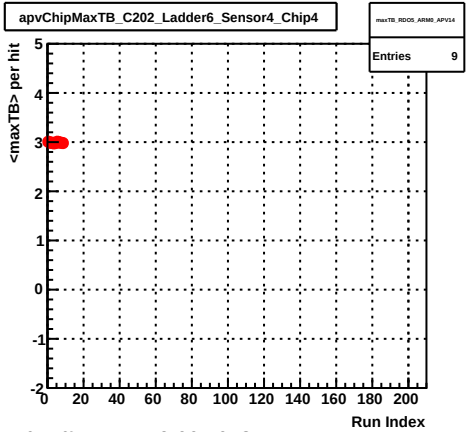

apvChipMaxTB_C197_Ladder6_Sensor3_Chip5
maxTB_RDO5_ARM0_APV19
Entries 9
<maxTB> per hit
Run Index

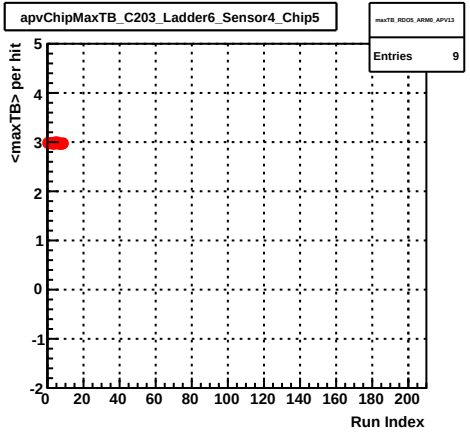

apvChipMaxTB_C198_Ladder6_Sensor3_Chip6
maxTB_RDO5_ARM0_APV18
Entries 9
<maxTB> per hit
Run Index

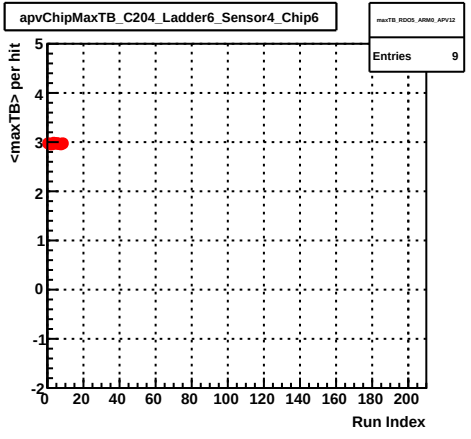

apvChipMaxTB_C199_Ladder6_Sensor4_Chip1
maxTB_RDO5_ARM0_APV17
Entries 9
<maxTB> per hit
Run Index

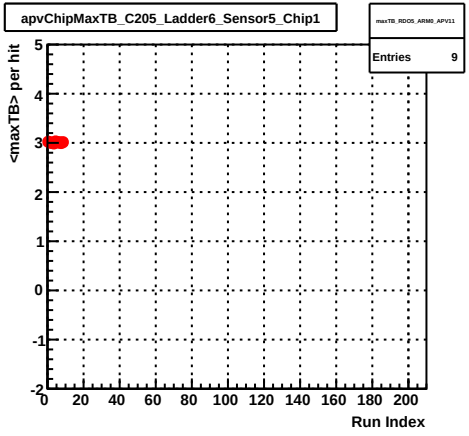

apvChipMaxTB_C200_Ladder6_Sensor4_Chip2
maxTB_RDO5_ARM0_APV16
Entries 9
<maxTB> per hit
Run Index

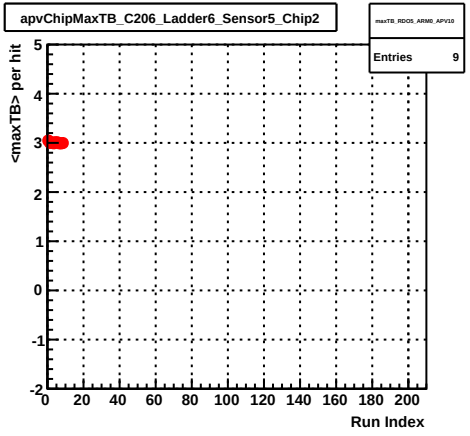

apvChipMaxTB_C201_Ladder6_Sensor4_Chip3
maxTB_RDO5_ARM0_APV15
Entries 9
<maxTB> per hit
Run Index

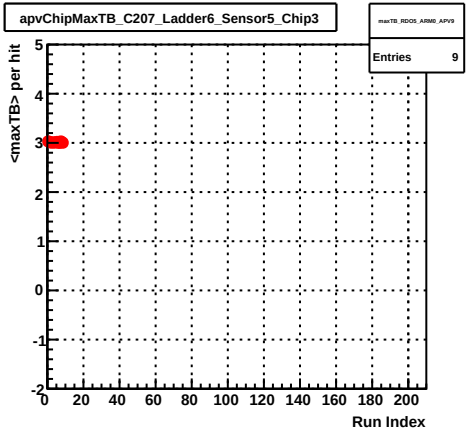

apvChipMaxTB_C202_Ladder6_Sensor4_Chip4
maxTB_RDO5_ARM0_APV14
Entries 9
<maxTB> per hit
Run Index

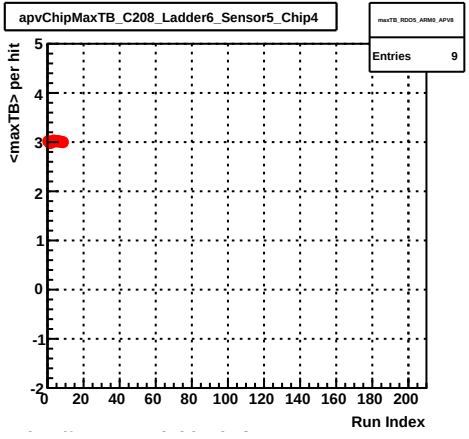

apvChipMaxTB_C203_Ladder6_Sensor4_Chip5
maxTB_RDO5_ARM0_APV13
Entries 9
<maxTB> per hit
Run Index

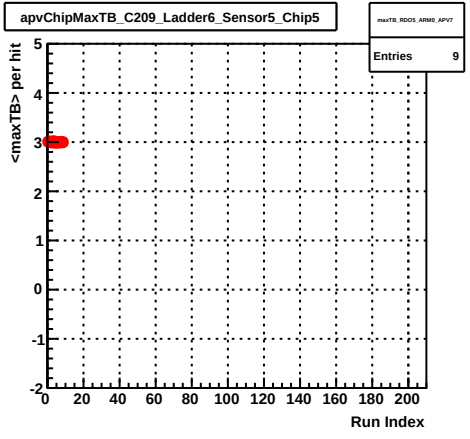

apvChipMaxTB_C204_Ladder6_Sensor4_Chip6
maxTB_RDO5_ARM0_APV12
Entries 9
<maxTB> per hit
Run Index

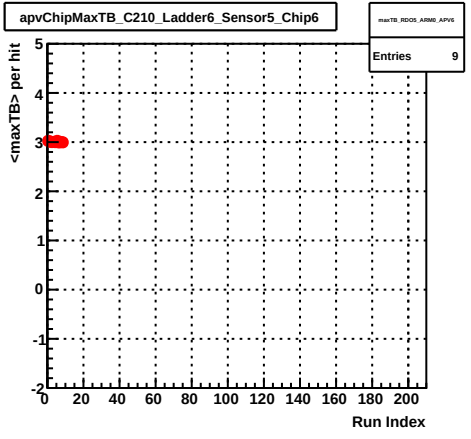

apvChipMaxTB_C205_Ladder6_Sensor5_Chip1
maxTB_RDOS_ARM0_APV11
Entries 9
<maxTB> per hit
Run Index

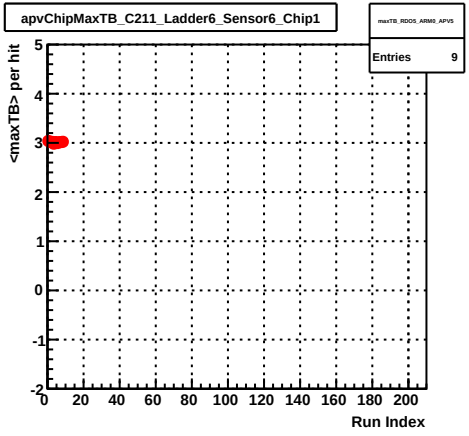

apvChipMaxTB_C206_Ladder6_Sensor5_Chip2
maxTB_RDOS_ARM0_APV10
Entries 9
<maxTB> per hit
Run Index

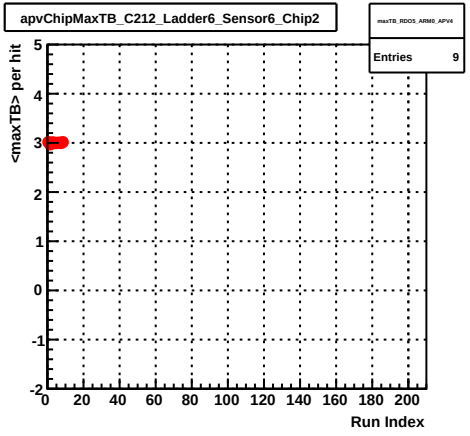

apvChipMaxTB_C207_Ladder6_Sensor5_Chip3
maxTB_RDOS_ARM0_APV9
Entries 9
<maxTB> per hit
Run Index

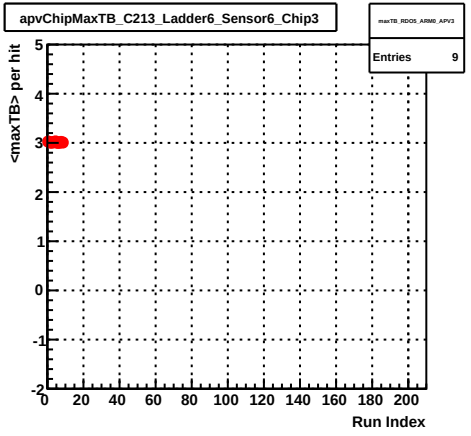

apvChipMaxTB_C208_Ladder6_Sensor5_Chip4
maxTB_RDOS_ARM0_APV8
Entries 9
<maxTB> per hit
Run Index

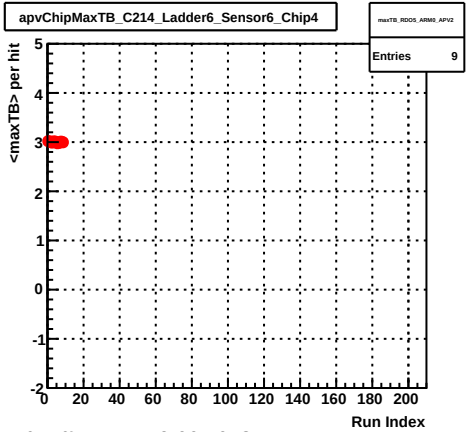

apvChipMaxTB_C209_Ladder6_Sensor5_Chip5
maxTB_RDOS_ARM0_APV7
Entries 9
<maxTB> per hit
Run Index

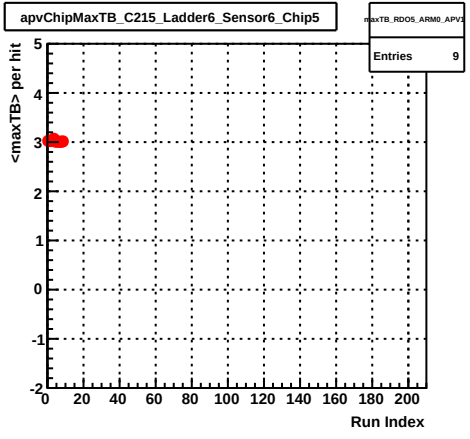

apvChipMaxTB_C210_Ladder6_Sensor5_Chip6
maxTB_RDOS_ARM0_APV6
Entries 9
<maxTB> per hit
Run Index

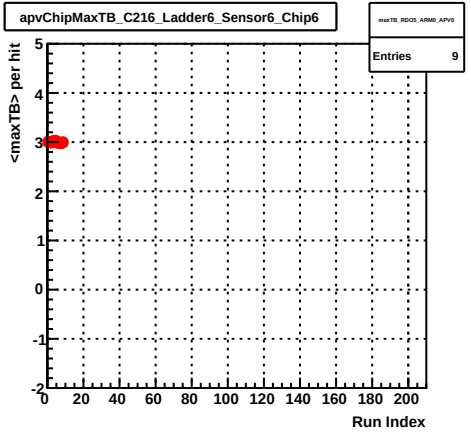

apvChipMaxTB_C211_Ladder6_Sensor6_Chip1
maxTB_RDO5_ARM0_APV5
Entries 9
<maxTB> per hit
Run Index

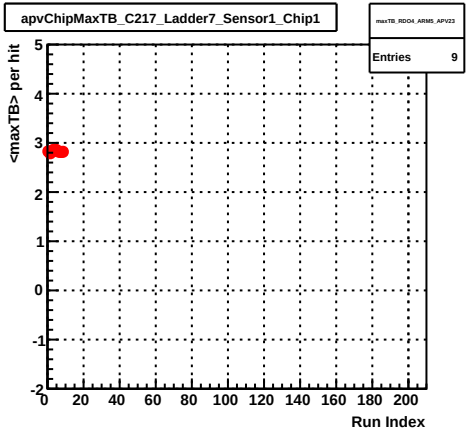

apvChipMaxTB_C212_Ladder6_Sensor6_Chip2
maxTB_RDO5_ARM0_APV4
Entries 9
<maxTB> per hit
Run Index

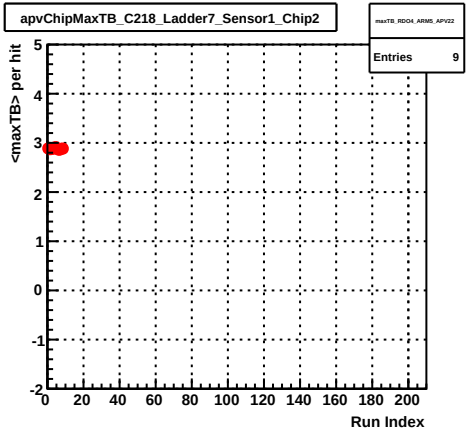

apvChipMaxTB_C213_Ladder6_Sensor6_Chip3
maxTB_RDO5_ARM0_APV3
Entries 9
<maxTB> per hit
Run Index

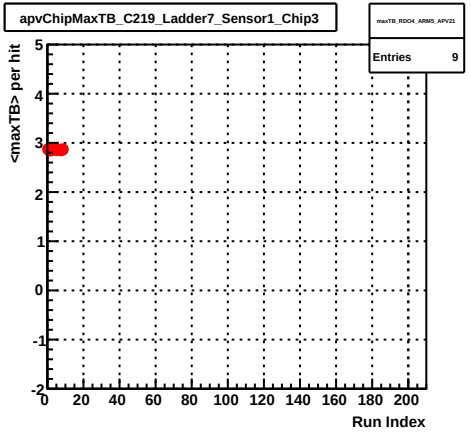

apvChipMaxTB_C214_Ladder6_Sensor6_Chip4
maxTB_RDO5_ARM0_APV2
Entries 9
<maxTB> per hit
Run Index

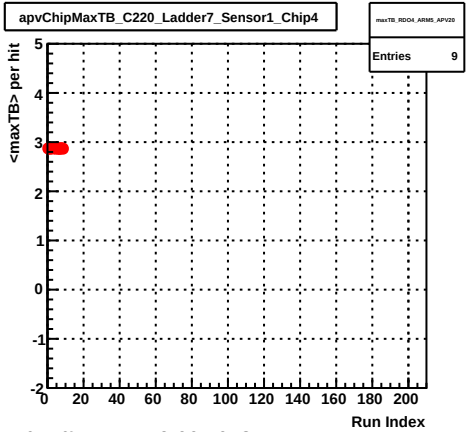

apvChipMaxTB_C215_Ladder6_Sensor6_Chip5
maxTB_RDO5_ARM0_APV1
Entries 9
<maxTB> per hit
Run Index

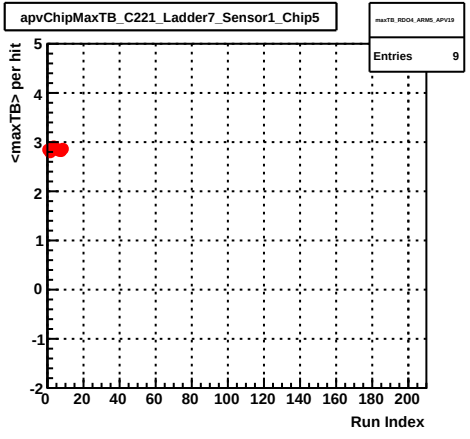

apvChipMaxTB_C216_Ladder6_Sensor6_Chip6
maxTB_RDO5_ARM0_APV0
Entries 9
<maxTB> per hit
Run Index

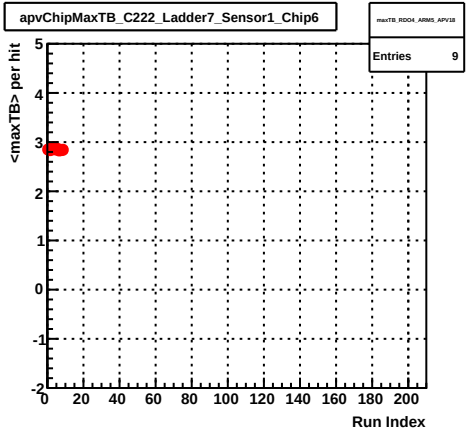

apvChipMaxTB_C217_Ladder7_Sensor1_Chip1
maxTB_RDO4_ARM5_APV23
Entries 9
<maxTB> per hit
Run Index

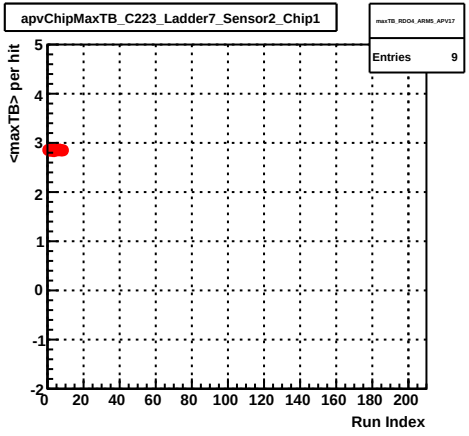

apvChipMaxTB_C218_Ladder7_Sensor1_Chip2
maxTB_RDO4_ARM5_APV22
Entries 9
<maxTB> per hit
Run Index

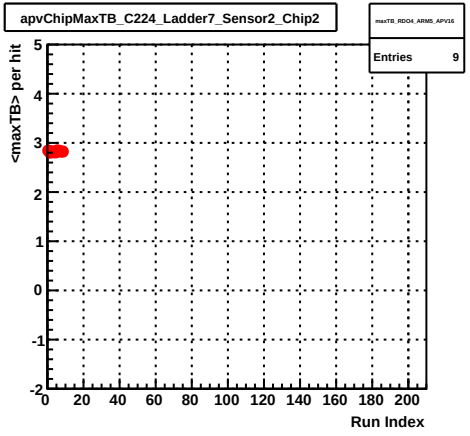

apvChipMaxTB_C219_Ladder7_Sensor1_Chip3
maxTB_RDO4_ARM5_APV21
Entries 9
<maxTB> per hit
Run Index

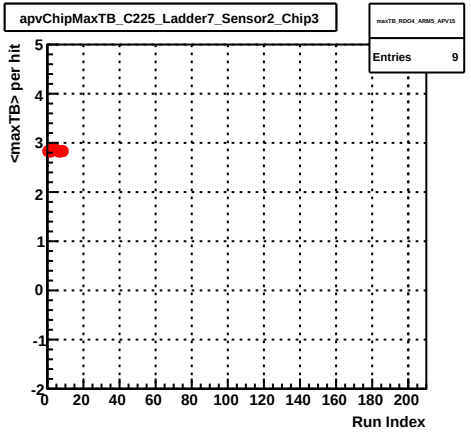

apvChipMaxTB_C220_Ladder7_Sensor1_Chip4
maxTB_RDO4_ARM5_APV20
Entries 9
<maxTB> per hit
Run Index

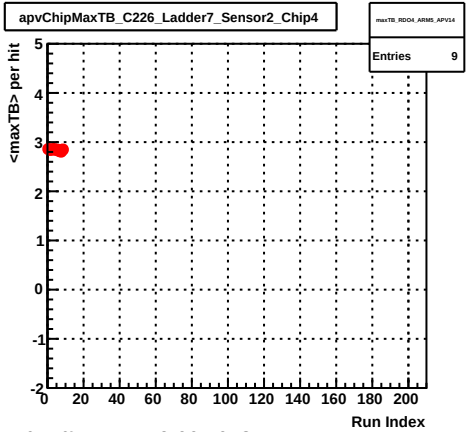

apvChipMaxTB_C221_Ladder7_Sensor1_Chip5
maxTB_RDO4_ARM5_APV19
Entries 9
<maxTB> per hit
Run Index

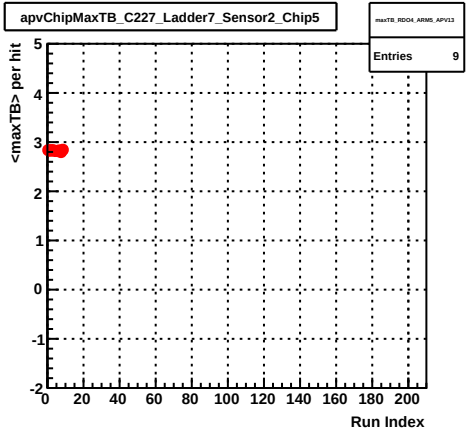

apvChipMaxTB_C222_Ladder7_Sensor1_Chip6
maxTB_RDO4_ARM5_APV18
Entries 9
<maxTB> per hit
Run Index

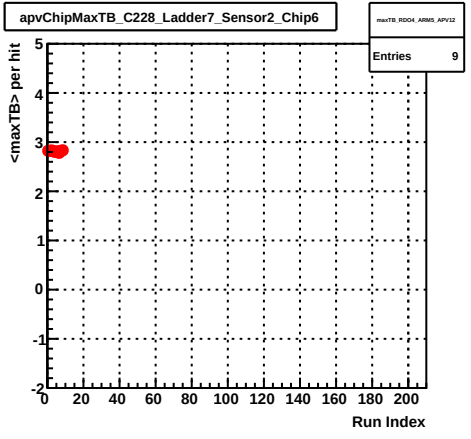

apvChipMaxTB_C223_Ladder7_Sensor2_Chip1
maxTB_RDO4_ARM5_APV17
Entries 9
<maxTB> per hit
Run Index

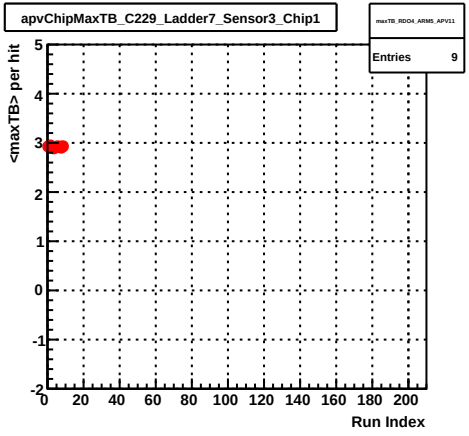

apvChipMaxTB_C224_Ladder7_Sensor2_Chip2
maxTB_RDO4_ARM5_APV16
Entries 9
<maxTB> per hit
Run Index

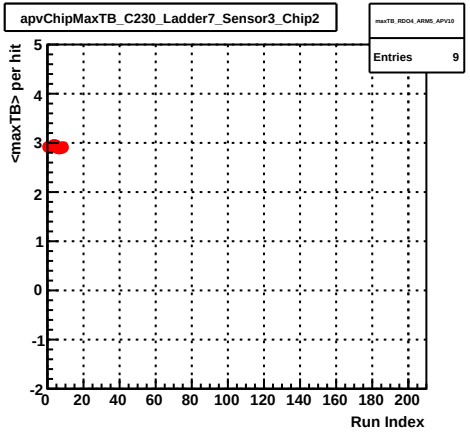

apvChipMaxTB_C225_Ladder7_Sensor2_Chip3
maxTB_RDO4_ARM5_APV15
Entries 9
<maxTB> per hit
Run Index

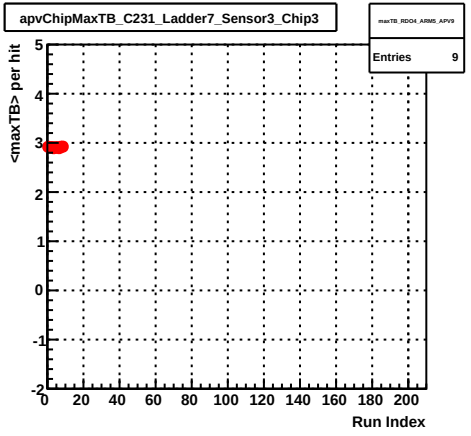

apvChipMaxTB_C226_Ladder7_Sensor2_Chip4
maxTB_RDO4_ARM5_APV14
Entries 9
<maxTB> per hit
Run Index

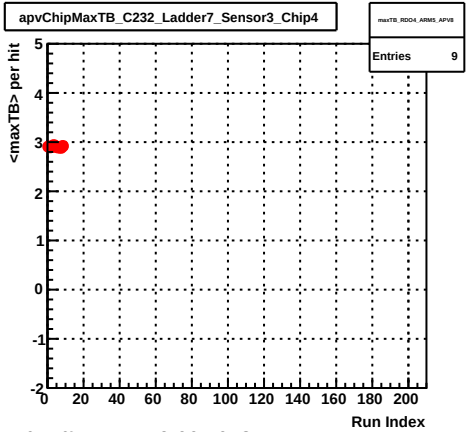

apvChipMaxTB_C227_Ladder7_Sensor2_Chip5
maxTB_RDO4_ARM5_APV13
Entries 9
<maxTB> per hit
Run Index

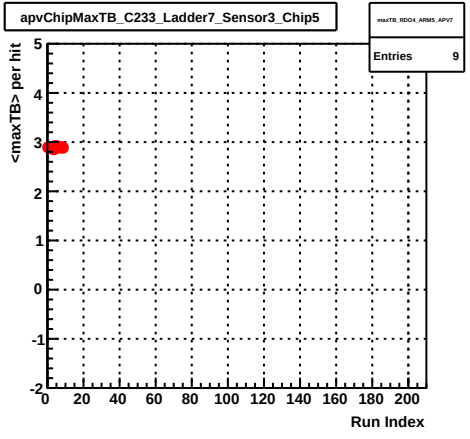

apvChipMaxTB_C228_Ladder7_Sensor2_Chip6
maxTB_RDO4_ARM5_APV12
Entries 9
<maxTB> per hit
Run Index

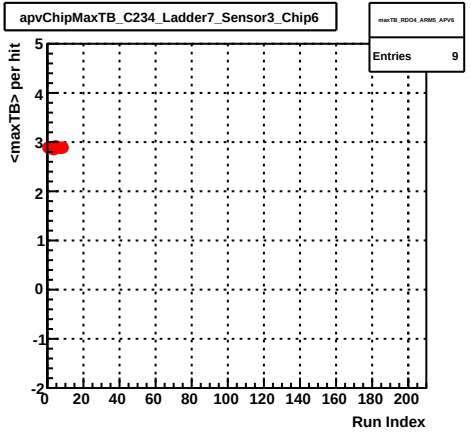

apvChipMaxTB_C229_Ladder7_Sensor3_Chip1
maxTB_RDO4_ARM5_APV11
Entries 9
<maxTB> per hit
Run Index

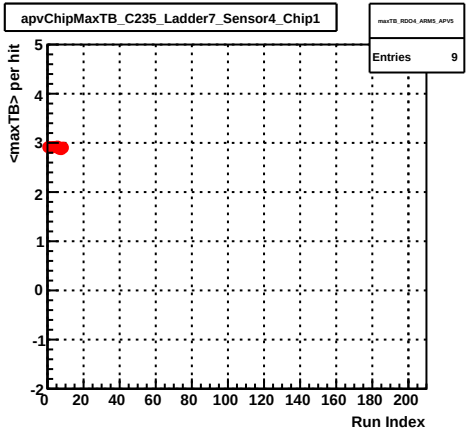

apvChipMaxTB_C230_Ladder7_Sensor3_Chip2
maxTB_RDO4_ARM5_APV10
Entries 9
<maxTB> per hit
Run Index

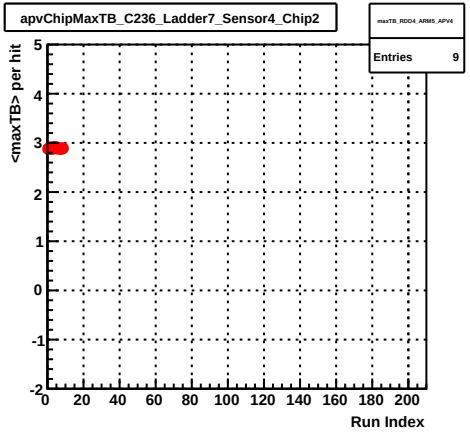

apvChipMaxTB_C231_Ladder7_Sensor3_Chip3
maxTB_RDO4_ARM5_APV9
Entries 9
<maxTB> per hit
Run Index

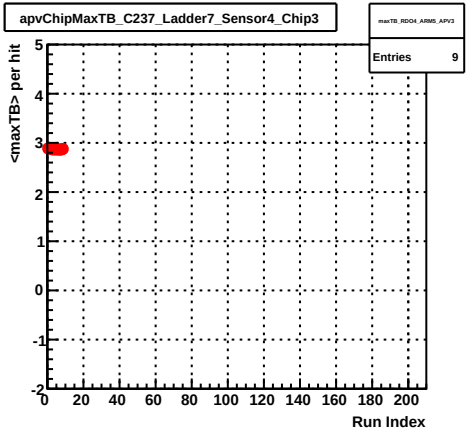

apvChipMaxTB_C232_Ladder7_Sensor3_Chip4
maxTB_RDO4_ARM5_APV8
Entries 9
<maxTB> per hit
Run Index

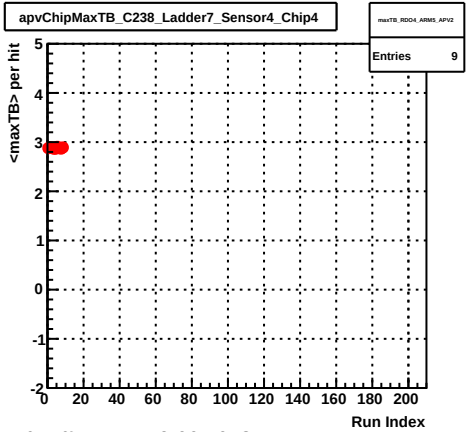

apvChipMaxTB_C233_Ladder7_Sensor3_Chip5
maxTB_RDO4_ARM5_APV7
Entries 9
<maxTB> per hit
Run Index

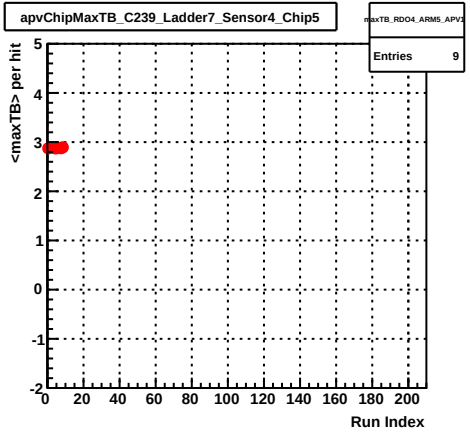

apvChipMaxTB_C234_Ladder7_Sensor3_Chip6
maxTB_RDO4_ARM5_APV6
Entries 9
<maxTB> per hit
Run Index

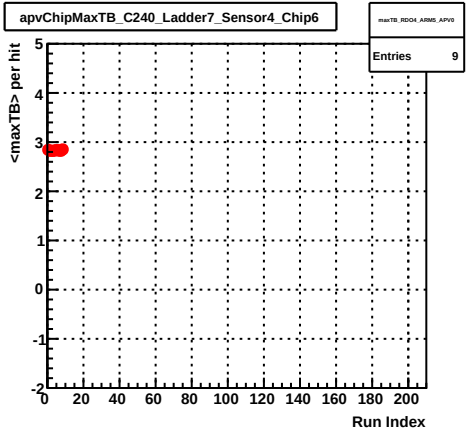

apvChipMaxTB_C235_Ladder7_Sensor4_Chip1
maxTB_RDO4_ARM5_APV5
Entries 9
<maxTB> per hit
Run Index

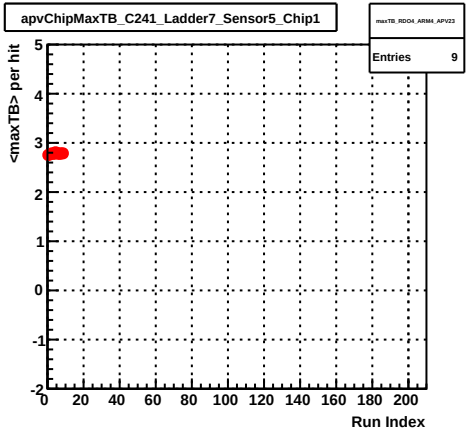

apvChipMaxTB_C236_Ladder7_Sensor4_Chip2
maxTB_RDO4_ARM5_APV4
Entries 9
<maxTB> per hit
Run Index

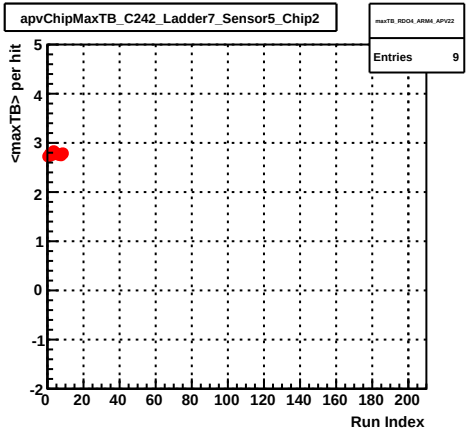

apvChipMaxTB_C237_Ladder7_Sensor4_Chip3
maxTB_RDO4_ARM5_APV3
Entries 9
<maxTB> per hit
Run Index

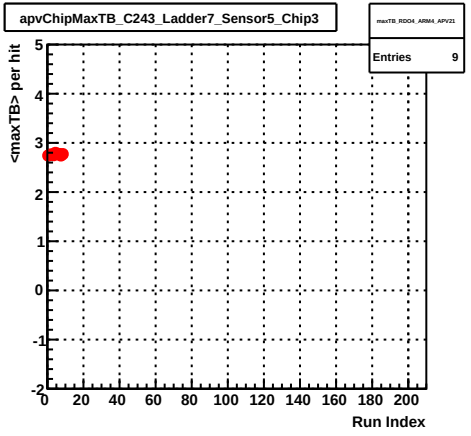

apvChipMaxTB_C238_Ladder7_Sensor4_Chip4
maxTB_RDO4_ARM5_APV2
Entries 9
<maxTB> per hit
Run Index

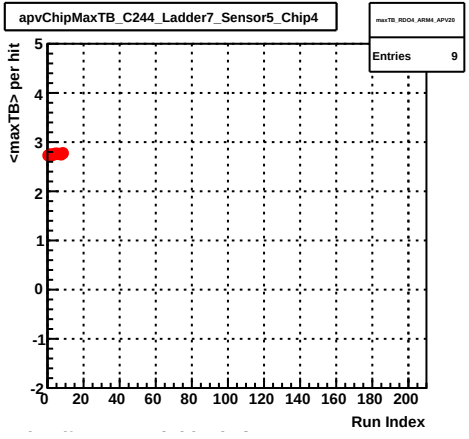

apvChipMaxTB_C239_Ladder7_Sensor4_Chip5
maxTB_RDO4_ARM5_APV1
Entries 9
<maxTB> per hit
Run Index

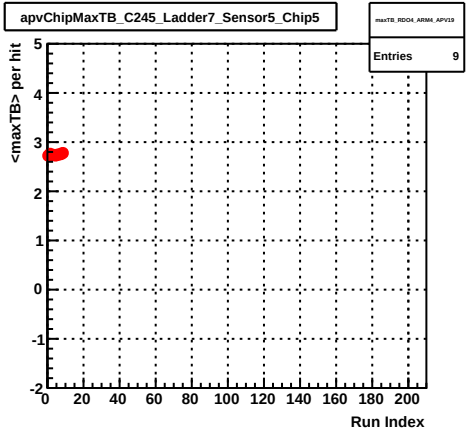

apvChipMaxTB_C240_Ladder7_Sensor4_Chip6
maxTB_RDO4_ARM5_APV0
Entries 9
<maxTB> per hit
Run Index

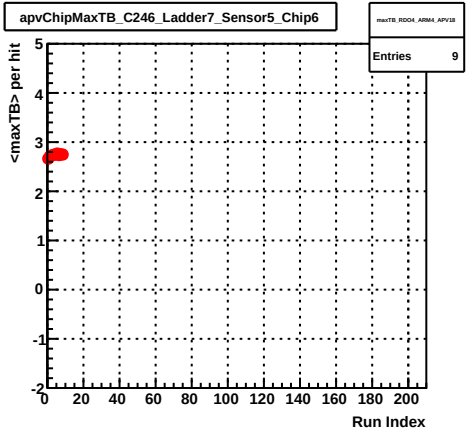

apvChipMaxTB_C241_Ladder7_Sensor5_Chip1
maxTB_RDO4_ARM4_APV23
Entries 9
<maxTB> per hit
Run Index

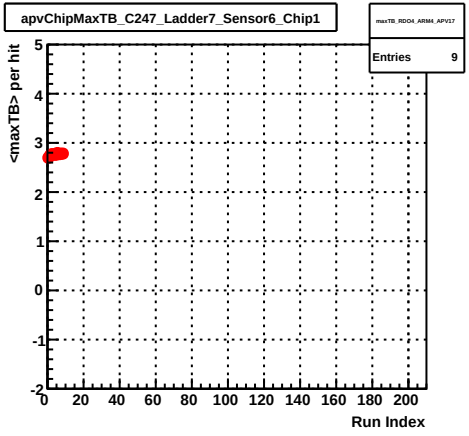

apvChipMaxTB_C242_Ladder7_Sensor5_Chip2
maxTB_RDO4_ARM4_APV22
Entries 9
<maxTB> per hit
Run Index

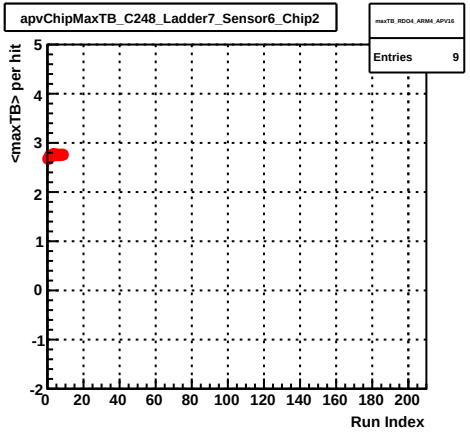

apvChipMaxTB_C243_Ladder7_Sensor5_Chip3
maxTB_RDO4_ARM4_APV21
Entries 9
<maxTB> per hit
Run Index

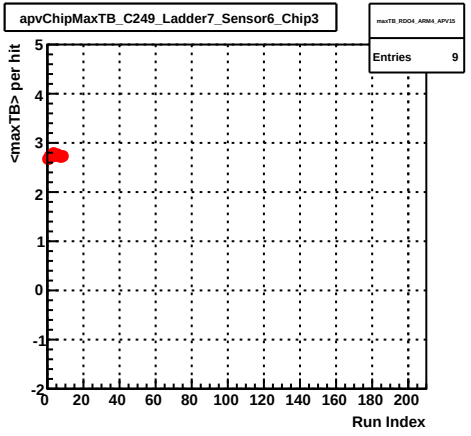

apvChipMaxTB_C244_Ladder7_Sensor5_Chip4
maxTB_RDO4_ARM4_APV20
Entries 9
<maxTB> per hit
Run Index

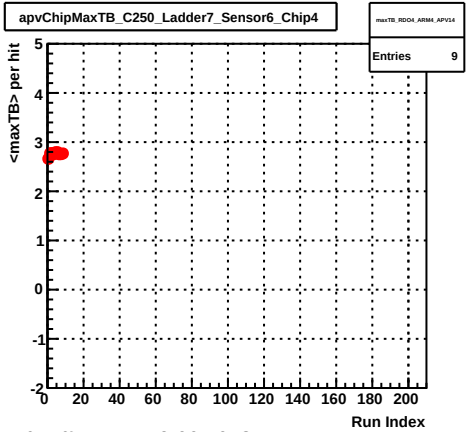

apvChipMaxTB_C245_Ladder7_Sensor5_Chip5
maxTB_RDO4_ARM4_APV19
Entries 9
<maxTB> per hit
Run Index

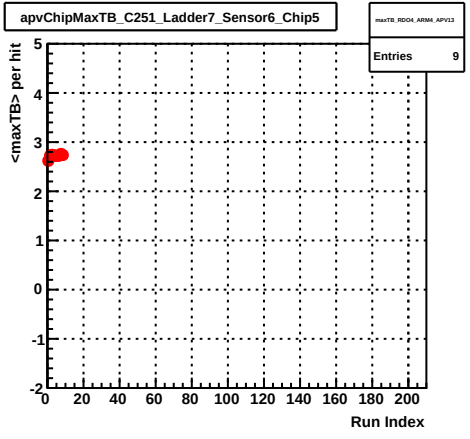

apvChipMaxTB_C246_Ladder7_Sensor5_Chip6
maxTB_RDO4_ARM4_APV18
Entries 9
<maxTB> per hit
Run Index

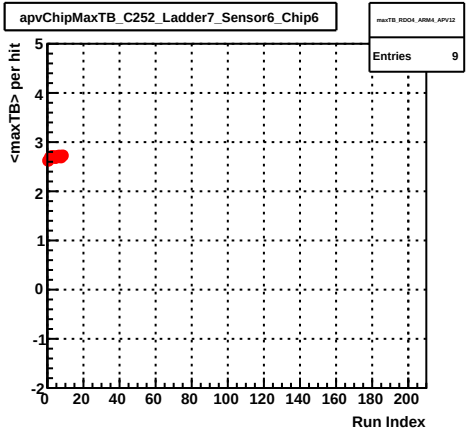

apvChipMaxTB_C247_Ladder7_Sensor6_Chip1
maxTB_RDO4_ARM4_APV17
Entries 9
<maxTB> per hit
Run Index

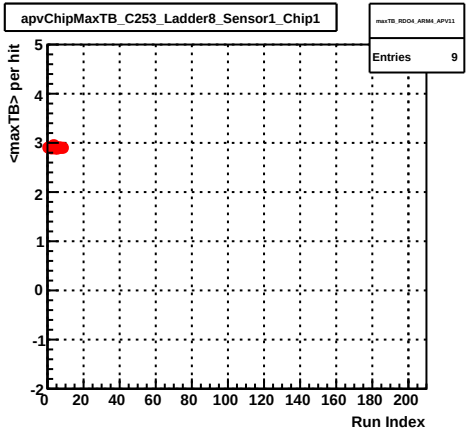

apvChipMaxTB_C248_Ladder7_Sensor6_Chip2
maxTB_RDO4_ARM4_APV16
Entries 9
<maxTB> per hit
Run Index

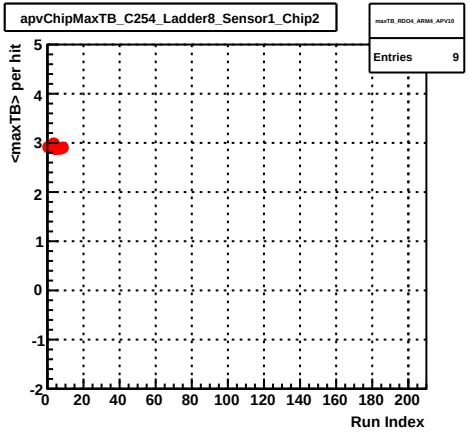

apvChipMaxTB_C249_Ladder7_Sensor6_Chip3
maxTB_RDO4_ARM4_APV15
Entries 9
<maxTB> per hit
Run Index

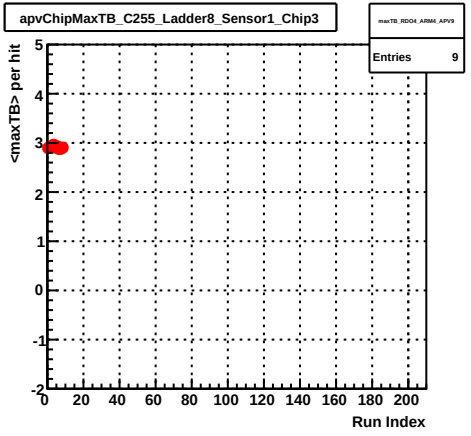

apvChipMaxTB_C250_Ladder7_Sensor6_Chip4
maxTB_RDO4_ARM4_APV14
Entries 9
<maxTB> per hit
Run Index

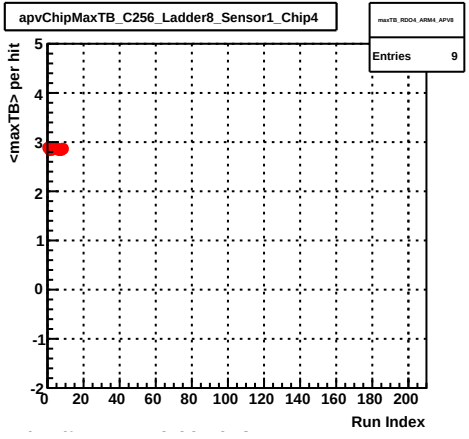

apvChipMaxTB_C251_Ladder7_Sensor6_Chip5
maxTB_RDO4_ARM4_APV13
Entries 9
<maxTB> per hit
Run Index

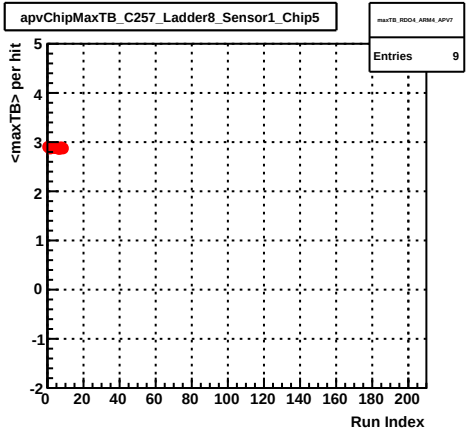

apvChipMaxTB_C252_Ladder7_Sensor6_Chip6
maxTB_RDO4_ARM4_APV12
Entries 9
<maxTB> per hit
Run Index

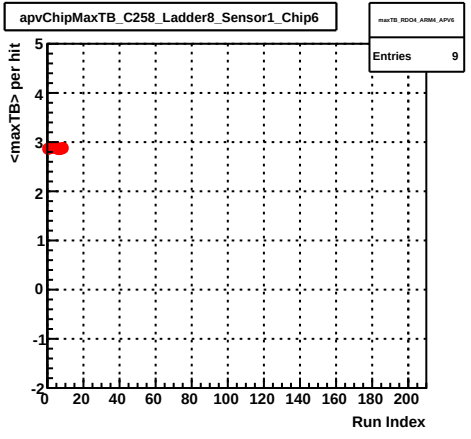

apvChipMaxTB_C253_Ladder8_Sensor1_Chip1
maxTB_RDO4_ARM4_APV11
Entries 9
<maxTB> per hit
Run Index

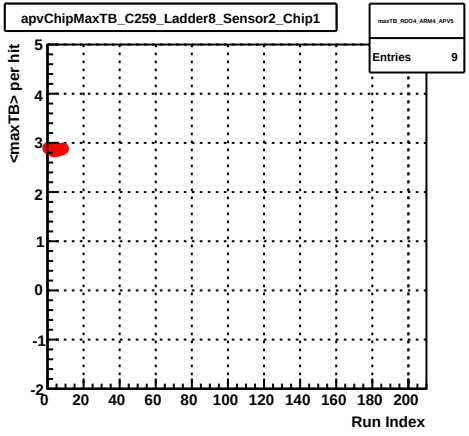

apvChipMaxTB_C254_Ladder8_Sensor1_Chip2
maxTB_RDO4_ARM4_APV10
Entries 9
<maxTB> per hit
Run Index

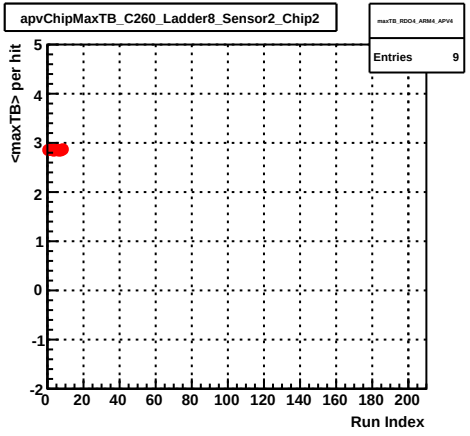

apvChipMaxTB_C255_Ladder8_Sensor1_Chip3
maxTB_RDO4_ARM4_APV9
Entries 9
<maxTB> per hit
Run Index

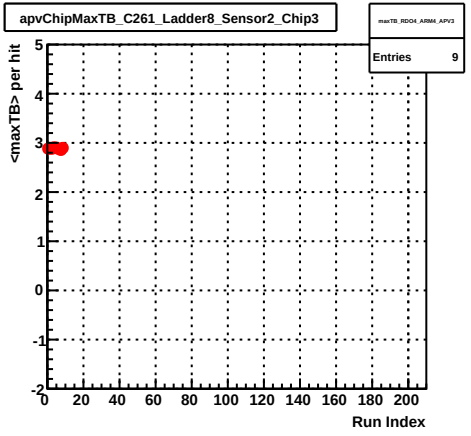

apvChipMaxTB_C256_Ladder8_Sensor1_Chip4
maxTB_RDO4_ARM4_APV8
Entries 9
<maxTB> per hit
Run Index

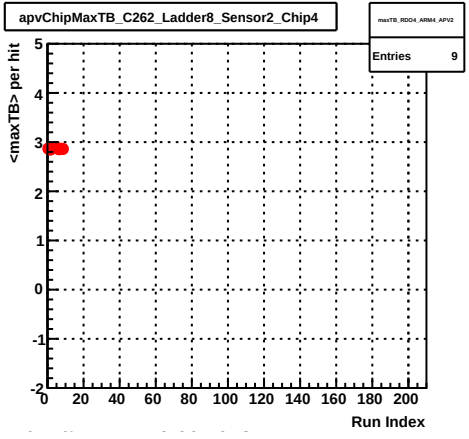

apvChipMaxTB_C257_Ladder8_Sensor1_Chip5
maxTB_RDO4_ARM4_APV7
Entries 9
<maxTB> per hit
Run Index

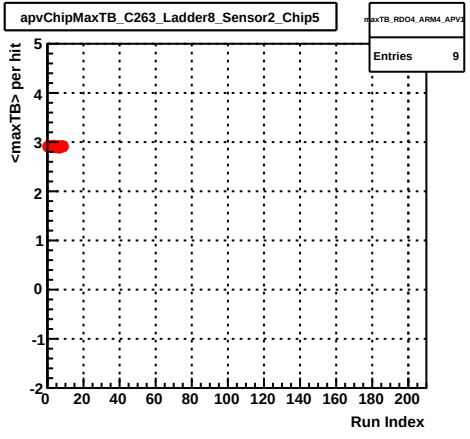

apvChipMaxTB_C258_Ladder8_Sensor1_Chip6
maxTB_RDO4_ARM4_APV6
Entries 9
<maxTB> per hit
Run Index

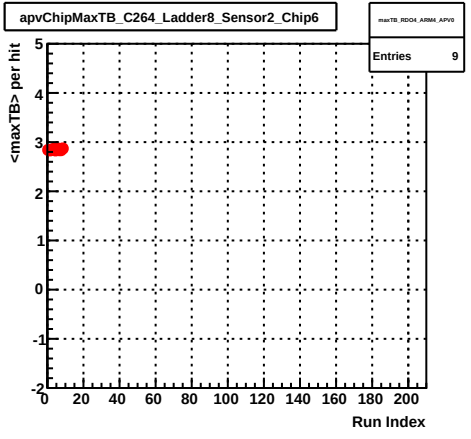

apvChipMaxTB_C259_Ladder8_Sensor2_Chip1
maxTB_RDO4_ARM4_APV5
Entries 9
<maxTB> per hit
Run Index

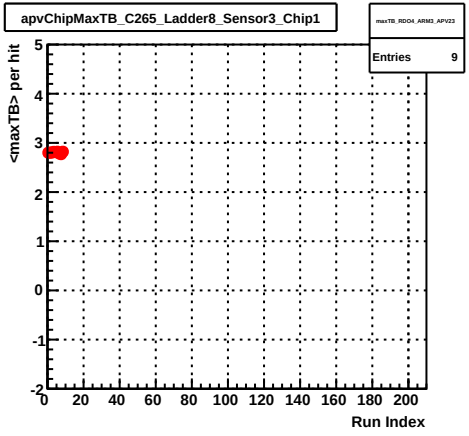

apvChipMaxTB_C260_Ladder8_Sensor2_Chip2
maxTB_RDO4_ARM4_APV4
Entries 9
<maxTB> per hit
Run Index

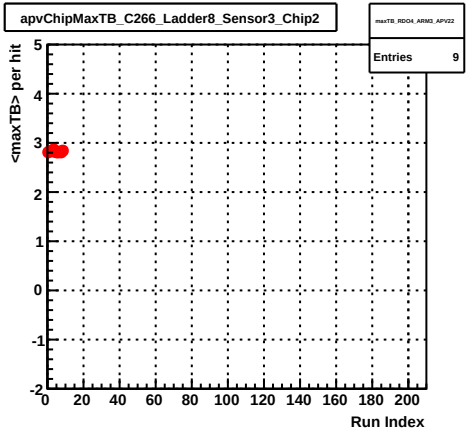

apvChipMaxTB_C261_Ladder8_Sensor2_Chip3
maxTB_RDO4_ARM4_APV3
Entries 9
<maxTB> per hit
Run Index

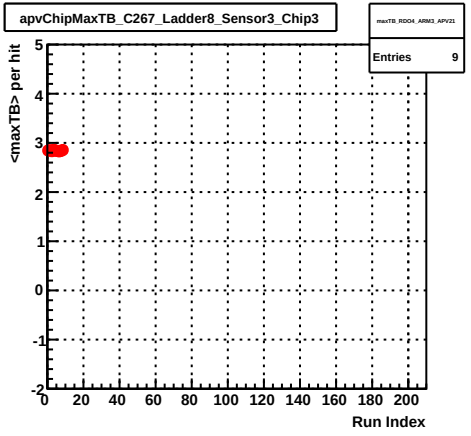

apvChipMaxTB_C262_Ladder8_Sensor2_Chip4
maxTB_RDO4_ARM4_APV2
Entries 9
<maxTB> per hit
Run Index

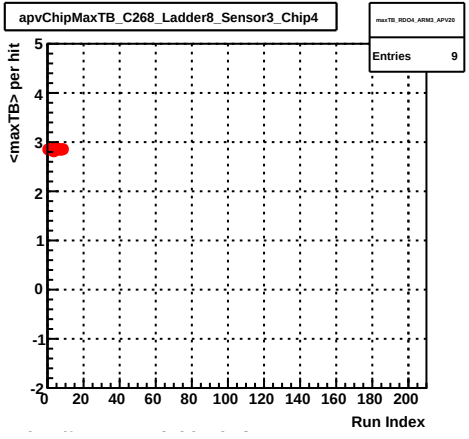

apvChipMaxTB_C263_Ladder8_Sensor2_Chip5
maxTB_RDO4_ARM4_APV1
Entries 9
<maxTB> per hit
Run Index

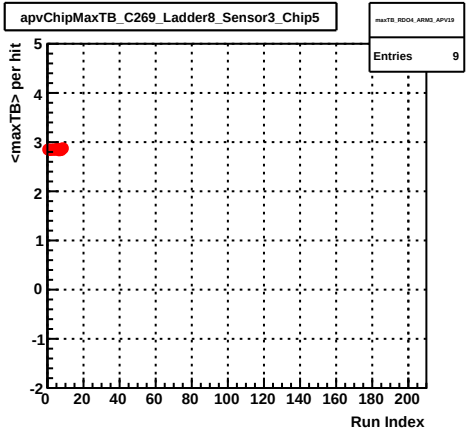

apvChipMaxTB_C264_Ladder8_Sensor2_Chip6
maxTB_RDO4_ARM4_APV0
Entries 9
<maxTB> per hit
Run Index

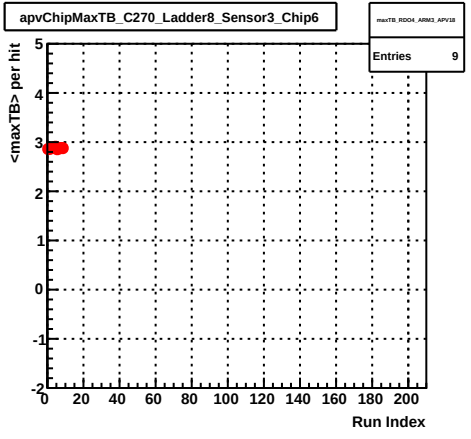

apvChipMaxTB_C265_Ladder8_Sensor3_Chip1
maxTB_RDO4_ARM3_APV23
Entries 9
<maxTB> per hit
Run Index

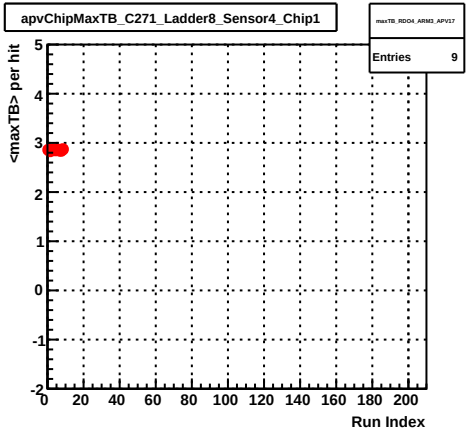

apvChipMaxTB_C266_Ladder8_Sensor3_Chip2
maxTB_RDO4_ARM3_APV22
Entries 9
<maxTB> per hit
Run Index

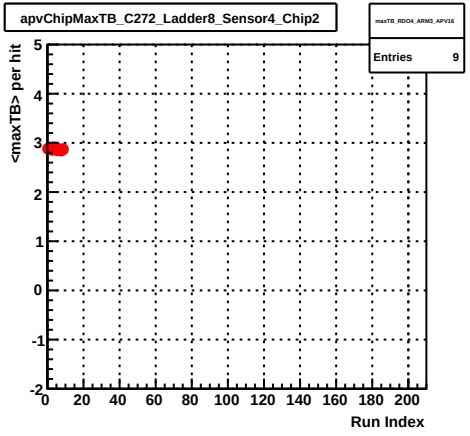

apvChipMaxTB_C267_Ladder8_Sensor3_Chip3
maxTB_RDO4_ARM3_APV21
Entries 9
<maxTB> per hit
Run Index

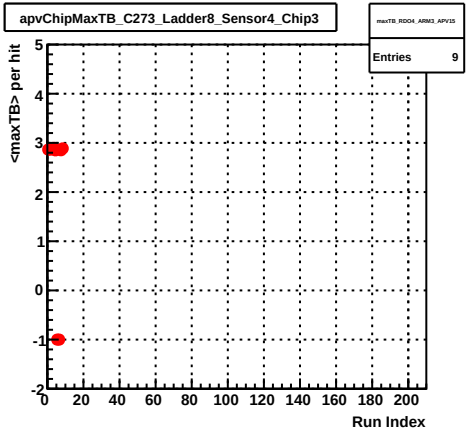

apvChipMaxTB_C268_Ladder8_Sensor3_Chip4
maxTB_RDO4_ARM3_APV20
Entries 9
<maxTB> per hit
Run Index

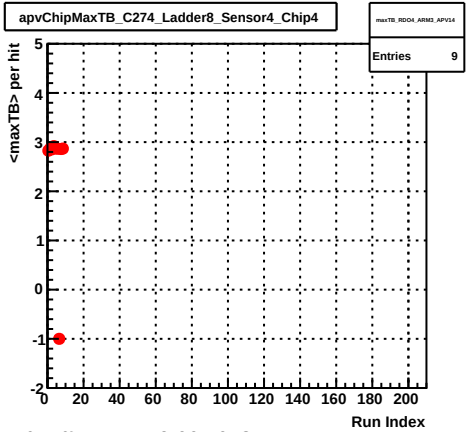

apvChipMaxTB_C269_Ladder8_Sensor3_Chip5
maxTB_RDO4_ARM3_APV19
Entries 9
<maxTB> per hit
Run Index

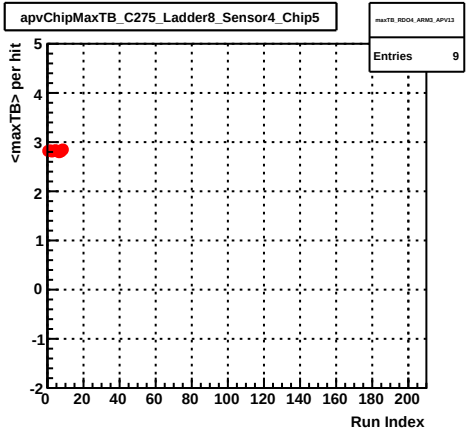

apvChipMaxTB_C270_Ladder8_Sensor3_Chip6
maxTB_RDO4_ARM3_APV18
Entries 9
<maxTB> per hit
Run Index

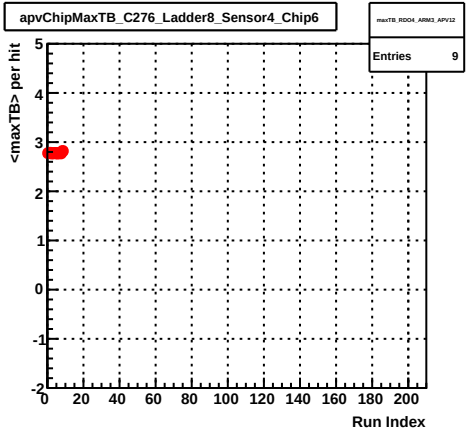

apvChipMaxTB_C271_Ladder8_Sensor4_Chip1
maxTB_RDO4_ARM3_APV17
Entries 9
<maxTB> per hit
Run Index

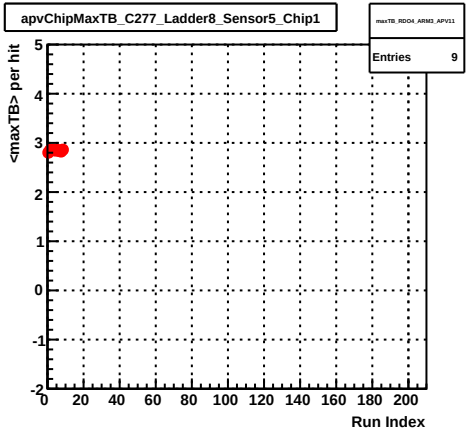

apvChipMaxTB_C272_Ladder8_Sensor4_Chip2
maxTB_RDO4_ARM3_APV16
Entries 9
<maxTB> per hit
Run Index

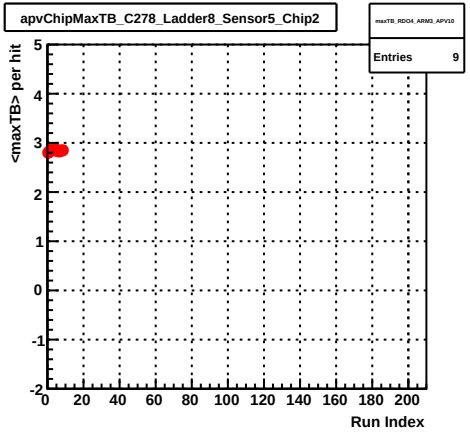

apvChipMaxTB_C273_Ladder8_Sensor4_Chip3
maxTB_RDO4_ARM3_APV15
Entries 9
<maxTB> per hit
Run Index

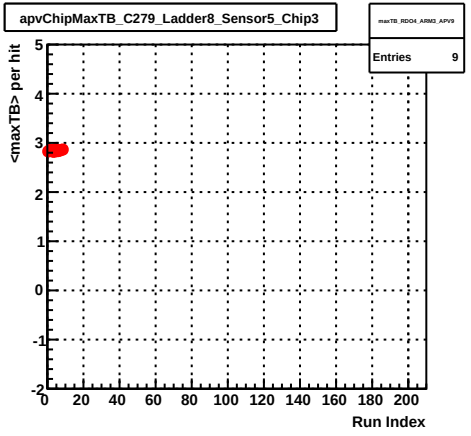

apvChipMaxTB_C274_Ladder8_Sensor4_Chip4
maxTB_RDO4_ARM3_APV14
Entries 9
<maxTB> per hit
Run Index

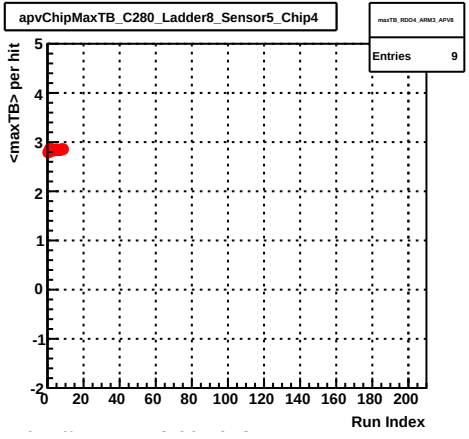

apvChipMaxTB_C275_Ladder8_Sensor4_Chip5
maxTB_RDO4_ARM3_APV13
Entries 9
<maxTB> per hit
Run Index

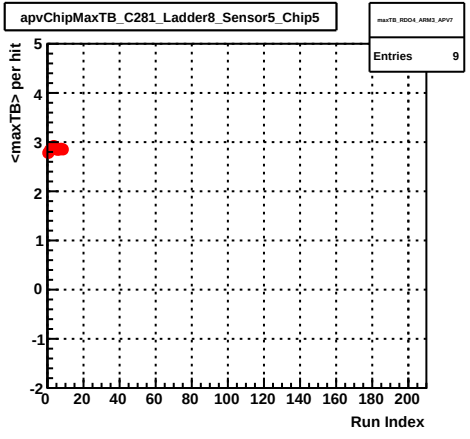

apvChipMaxTB_C276_Ladder8_Sensor4_Chip6
maxTB_RDO4_ARM3_APV12
Entries 9
<maxTB> per hit
Run Index

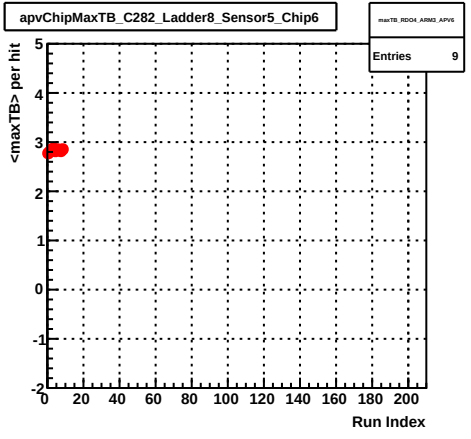

apvChipMaxTB_C277_Ladder8_Sensor5_Chip1
maxTB_RDO4_ARM3_APV11
Entries 9
<maxTB> per hit
Run Index

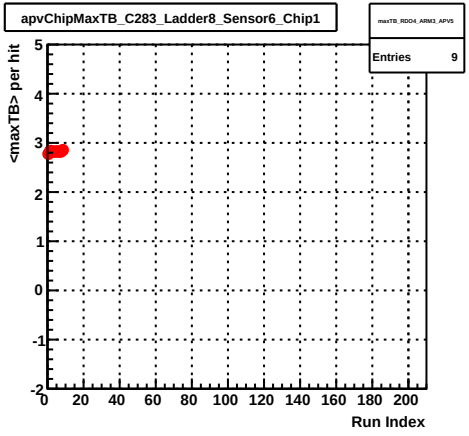

apvChipMaxTB_C278_Ladder8_Sensor5_Chip2
maxTB_RDO4_ARM3_APV10
Entries 9
<maxTB> per hit
Run Index

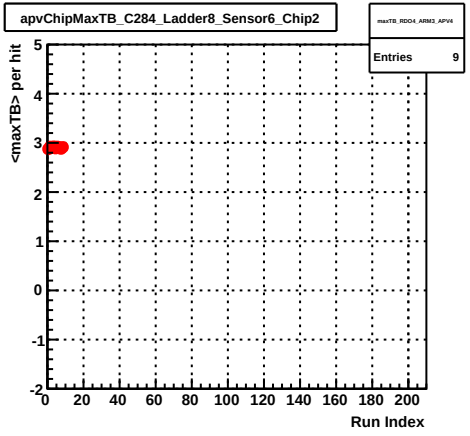

apvChipMaxTB_C279_Ladder8_Sensor5_Chip3
maxTB_RDO4_ARM3_APV9
Entries 9
<maxTB> per hit
Run Index

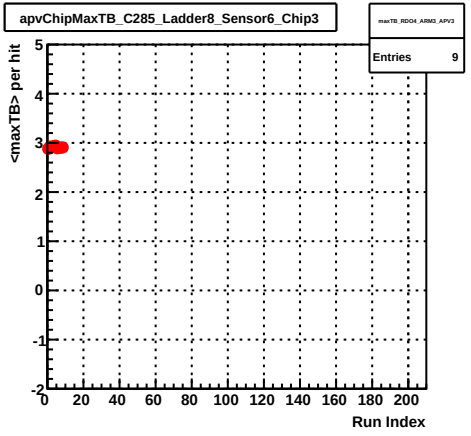

apvChipMaxTB_C280_Ladder8_Sensor5_Chip4
maxTB_RDO4_ARM3_APV8
Entries 9
<maxTB> per hit
Run Index

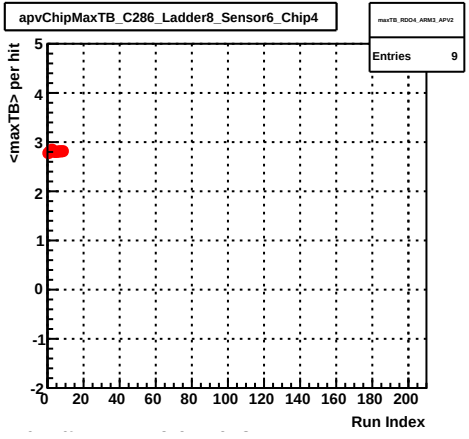

apvChipMaxTB_C281_Ladder8_Sensor5_Chip5
maxTB_RDO4_ARM3_APV7
Entries 9
<maxTB> per hit
Run Index

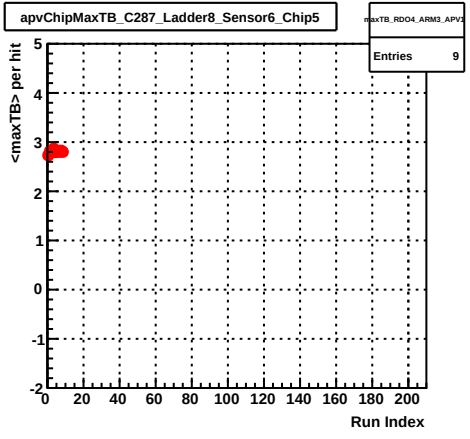

apvChipMaxTB_C282_Ladder8_Sensor5_Chip6
maxTB_RDO4_ARM3_APV6
Entries 9
<maxTB> per hit
Run Index

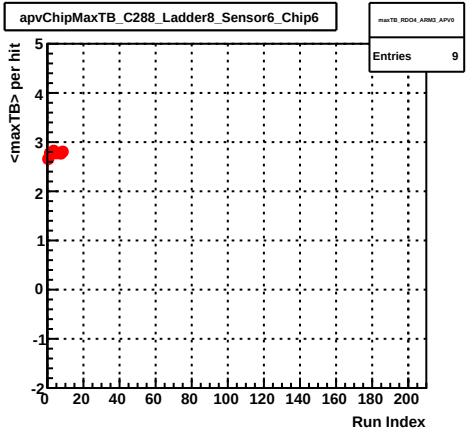

apvChipMaxTB_C283_Ladder8_Sensor6_Chip1
maxTB_RDO4_ARM3_APV5
Entries 9
<maxTB> per hit
Run Index

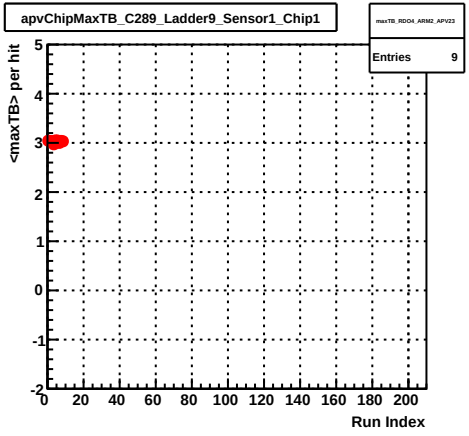

apvChipMaxTB_C284_Ladder8_Sensor6_Chip2
maxTB_RDO4_ARM3_APV4
Entries 9
<maxTB> per hit
Run Index

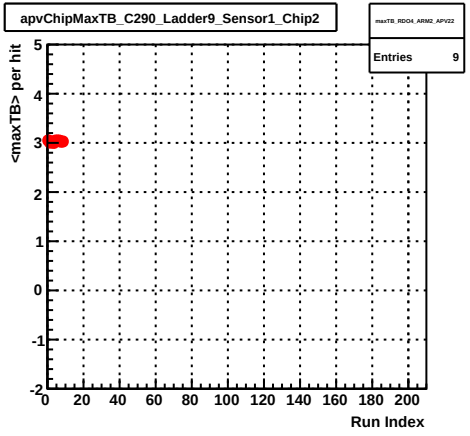

apvChipMaxTB_C285_Ladder8_Sensor6_Chip3
maxTB_RDO4_ARM3_APV3
Entries 9
<maxTB> per hit
Run Index

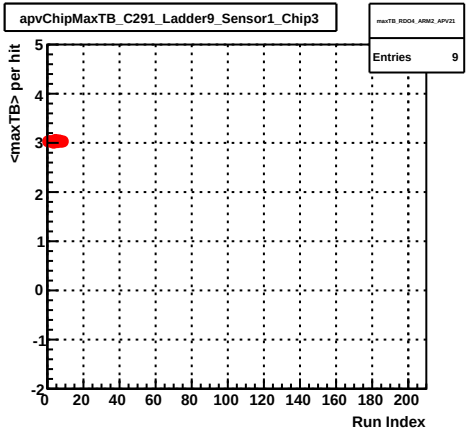

apvChipMaxTB_C286_Ladder8_Sensor6_Chip4
maxTB_RDO4_ARM3_APV2
Entries 9
<maxTB> per hit
Run Index

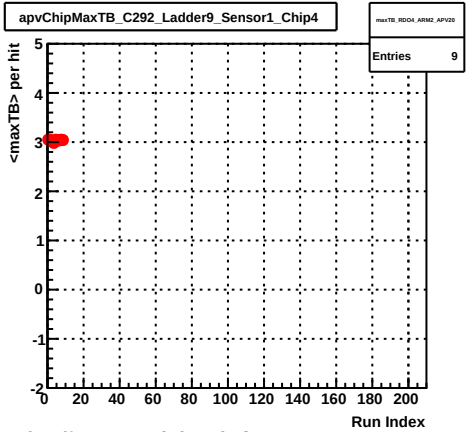

apvChipMaxTB_C287_Ladder8_Sensor6_Chip5
maxTB_RDO4_ARM3_APV1
Entries 9
<maxTB> per hit
Run Index

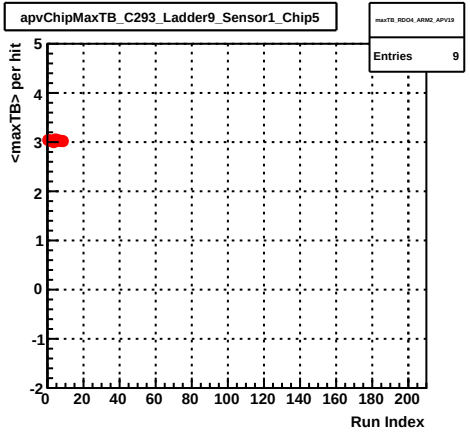

apvChipMaxTB_C288_Ladder8_Sensor6_Chip6
maxTB_RDO4_ARM3_APV0
Entries 9
<maxTB> per hit
Run Index

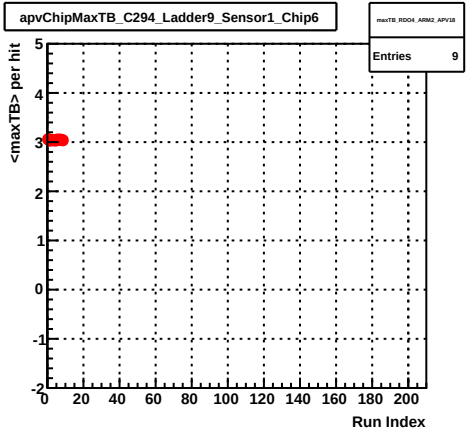

apvChipMaxTB_C289_Ladder9_Sensor1_Chip1
maxTB_RDO4_ARM2_APV23
Entries 9
<maxTB> per hit
Run Index

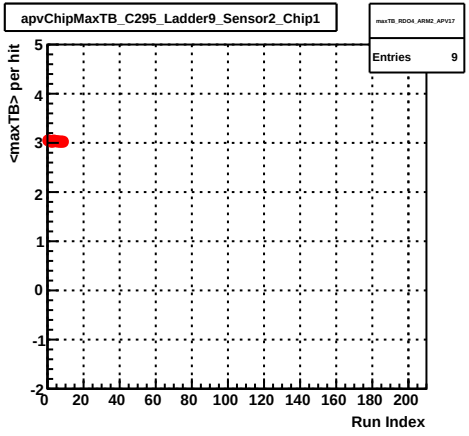

apvChipMaxTB_C290_Ladder9_Sensor1_Chip2
maxTB_RDO4_ARM2_APV22
Entries 9
<maxTB> per hit
Run Index

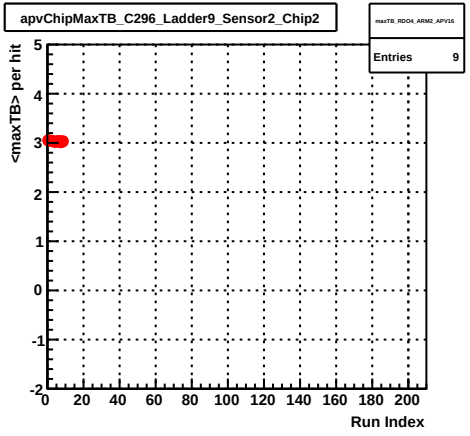

apvChipMaxTB_C291_Ladder9_Sensor1_Chip3
maxTB_RDO4_ARM2_APV21
Entries 9
<maxTB> per hit
Run Index

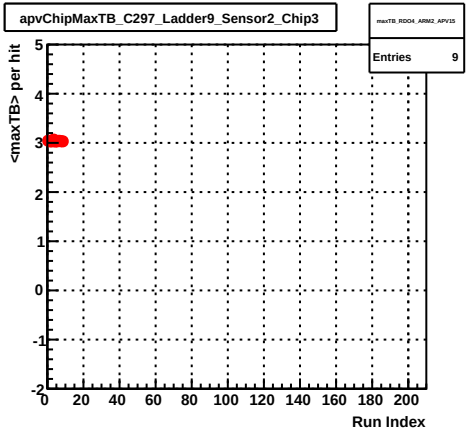

apvChipMaxTB_C292_Ladder9_Sensor1_Chip4
maxTB_RDO4_ARM2_APV20
Entries 9
<maxTB> per hit
Run Index

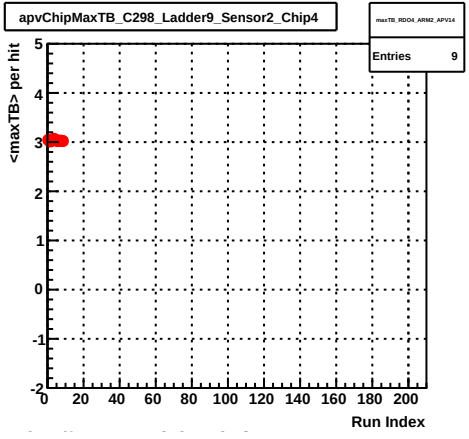

apvChipMaxTB_C293_Ladder9_Sensor1_Chip5
maxTB_RDO4_ARM2_APV19
Entries 9
<maxTB> per hit
Run Index

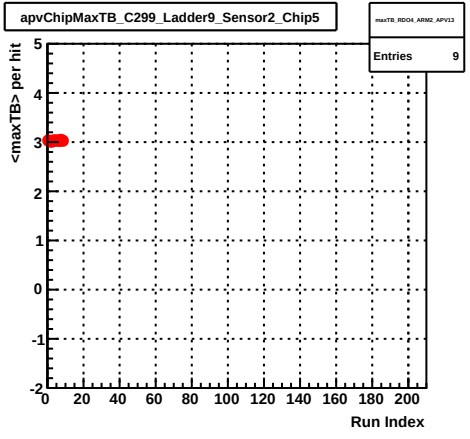

apvChipMaxTB_C294_Ladder9_Sensor1_Chip6
maxTB_RDO4_ARM2_APV18
Entries 9
<maxTB> per hit
Run Index

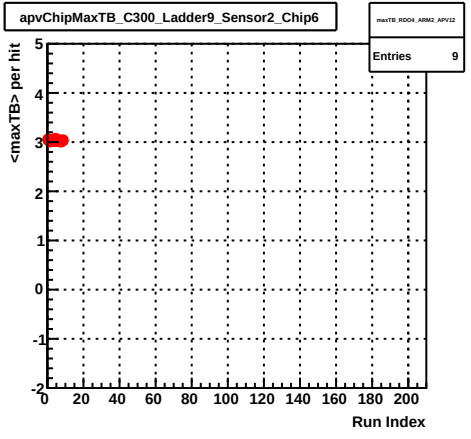

apvChipMaxTB_C295_Ladder9_Sensor2_Chip1
maxTB_RDO4_ARM2_APV17
Entries 9
<maxTB> per hit
Run Index

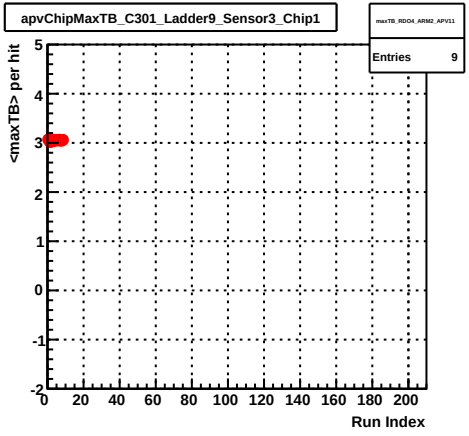

apvChipMaxTB_C296_Ladder9_Sensor2_Chip2
maxTB_RDO4_ARM2_APV16
Entries 9
<maxTB> per hit
Run Index

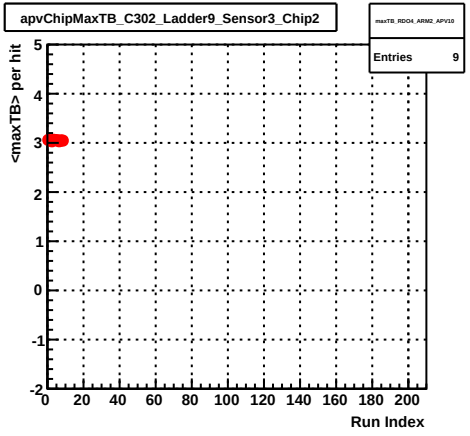

apvChipMaxTB_C297_Ladder9_Sensor2_Chip3
maxTB_RDO4_ARM2_APV15
Entries 9
<maxTB> per hit
Run Index

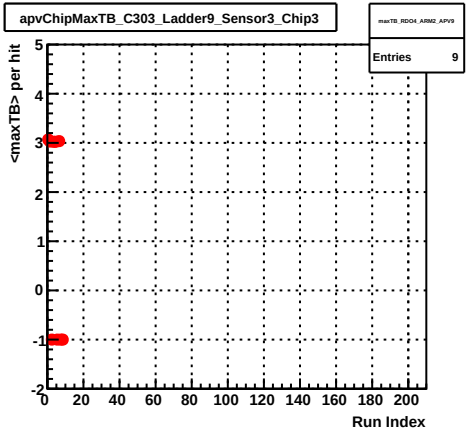

apvChipMaxTB_C298_Ladder9_Sensor2_Chip4
maxTB_RDO4_ARM2_APV14
Entries 9
<maxTB> per hit
Run Index

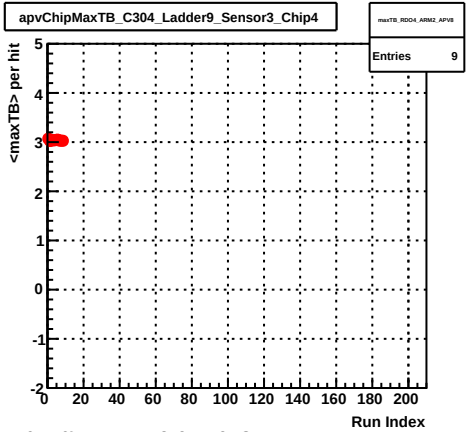

apvChipMaxTB_C299_Ladder9_Sensor2_Chip5
maxTB_RDO4_ARM2_APV13
Entries 9
<maxTB> per hit
Run Index

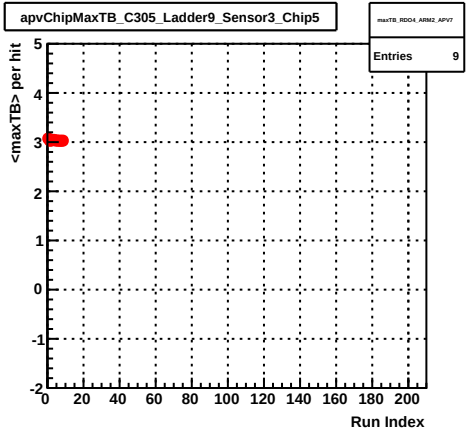

apvChipMaxTB_C300_Ladder9_Sensor2_Chip6
maxTB_RDO4_ARM2_APV12
Entries 9
<maxTB> per hit
Run Index

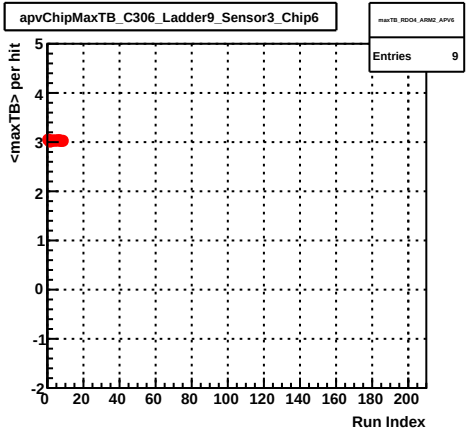

apvChipMaxTB_C301_Ladder9_Sensor3_Chip1
maxTB_RDO4_ARM2_APV11
Entries 9
<maxTB> per hit
Run Index

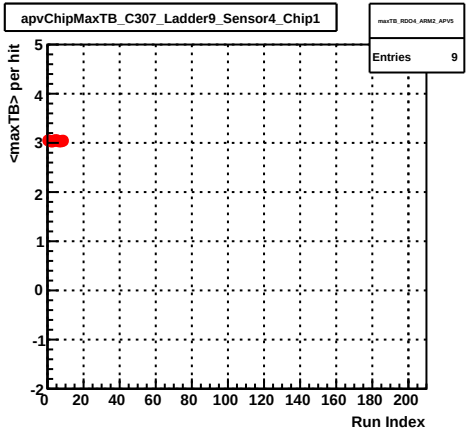

apvChipMaxTB_C302_Ladder9_Sensor3_Chip2
maxTB_RDO4_ARM2_APV10
Entries 9
<maxTB> per hit
Run Index

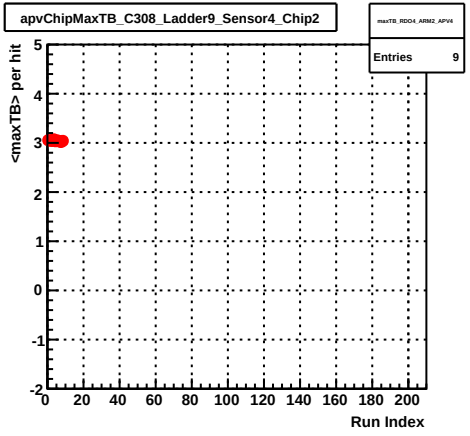

apvChipMaxTB_C303_Ladder9_Sensor3_Chip3
maxTB_RDO4_ARM2_APV9
Entries 9
<maxTB> per hit
Run Index

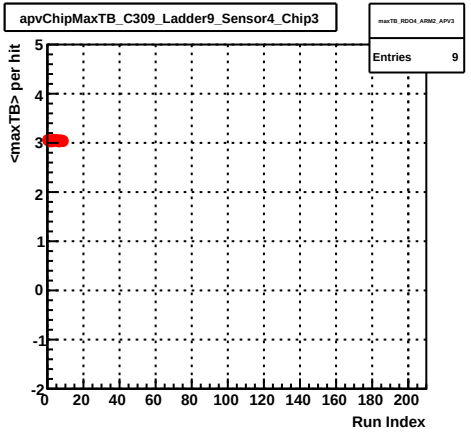

apvChipMaxTB_C304_Ladder9_Sensor3_Chip4
maxTB_RDO4_ARM2_APV8
Entries 9
<maxTB> per hit
Run Index

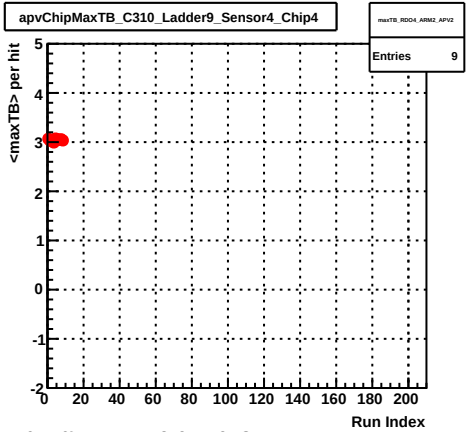

apvChipMaxTB_C305_Ladder9_Sensor3_Chip5
maxTB_RDO4_ARM2_APV7
Entries 9
<maxTB> per hit
Run Index

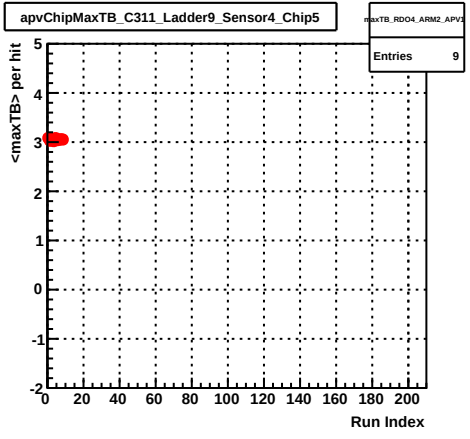

apvChipMaxTB_C306_Ladder9_Sensor3_Chip6
maxTB_RDO4_ARM2_APV6
Entries 9
<maxTB> per hit
Run Index

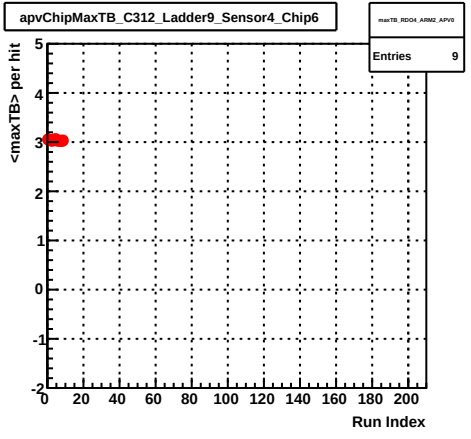

apvChipMaxTB_C307_Ladder9_Sensor4_Chip1
maxTB_RDO4_ARM2_APV5
Entries 9
<maxTB> per hit
Run Index

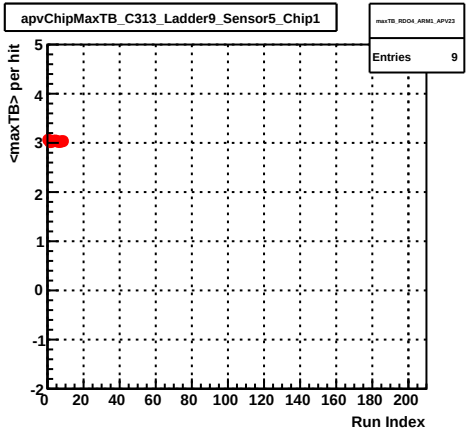

apvChipMaxTB_C308_Ladder9_Sensor4_Chip2
maxTB_RDO4_ARM2_APV4
Entries 9
<maxTB> per hit
Run Index

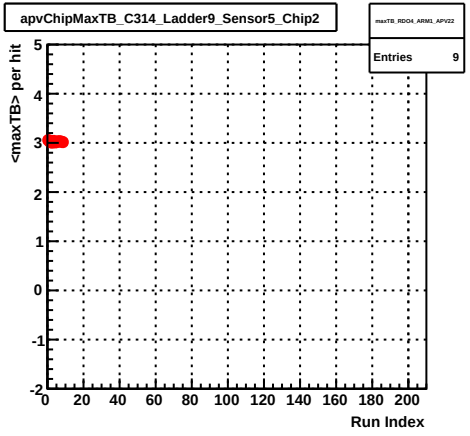

apvChipMaxTB_C309_Ladder9_Sensor4_Chip3
maxTB_RDO4_ARM2_APV3
Entries 9
<maxTB> per hit
Run Index

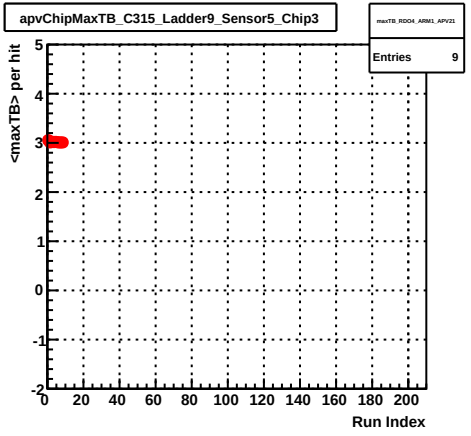

apvChipMaxTB_C310_Ladder9_Sensor4_Chip4
maxTB_RDO4_ARM2_APV2
Entries 9
<maxTB> per hit
Run Index

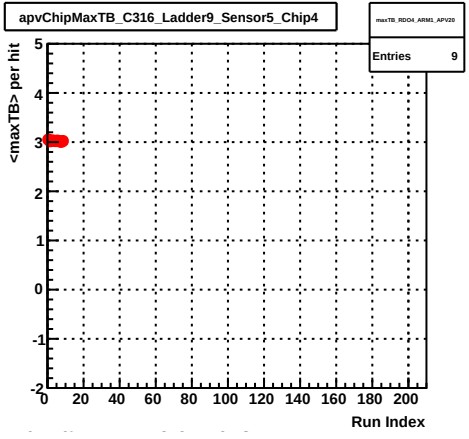

apvChipMaxTB_C311_Ladder9_Sensor4_Chip5
maxTB_RDO4_ARM2_APV1
Entries 9
<maxTB> per hit
Run Index

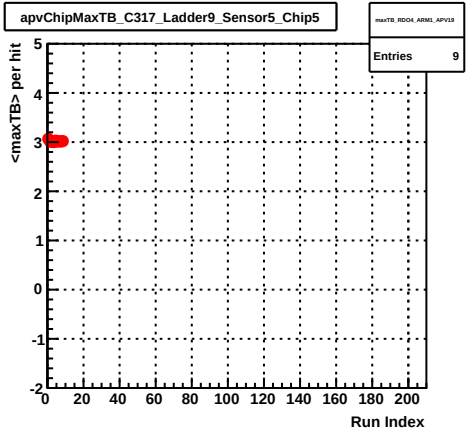

apvChipMaxTB_C312_Ladder9_Sensor4_Chip6
maxTB_RDO4_ARM2_APV0
Entries 9
<maxTB> per hit
Run Index

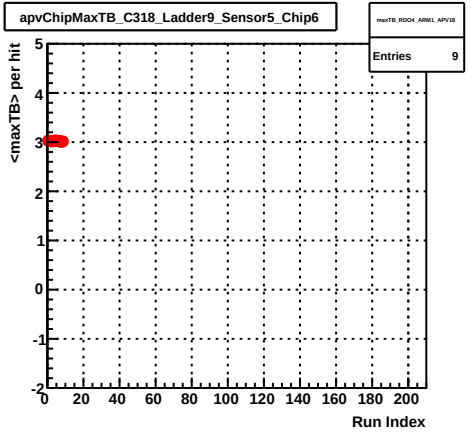

apvChipMaxTB_C313_Ladder9_Sensor5_Chip1
maxTB_RDO4_ARM1_APV23
Entries 9
<maxTB> per hit
Run Index

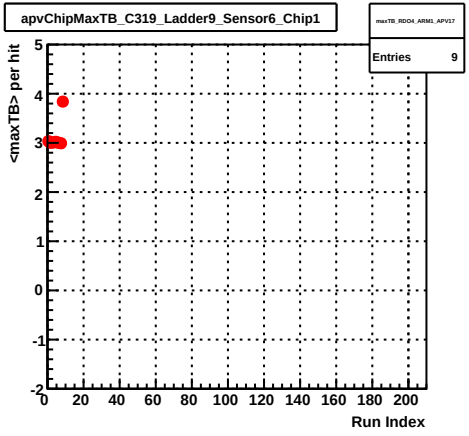

apvChipMaxTB_C314_Ladder9_Sensor5_Chip2
maxTB_RDO4_ARM1_APV22
Entries 9
<maxTB> per hit
Run Index

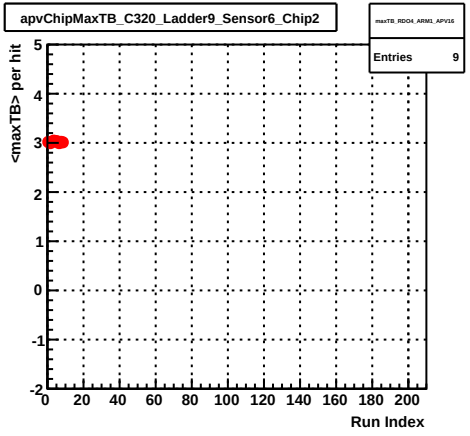

apvChipMaxTB_C315_Ladder9_Sensor5_Chip3
maxTB_RDO4_ARM1_APV21
Entries 9
<maxTB> per hit
Run Index

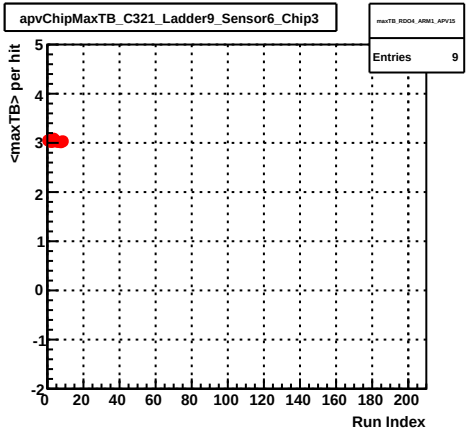

apvChipMaxTB_C316_Ladder9_Sensor5_Chip4
maxTB_RDO4_ARM1_APV20
Entries 9
<maxTB> per hit
Run Index

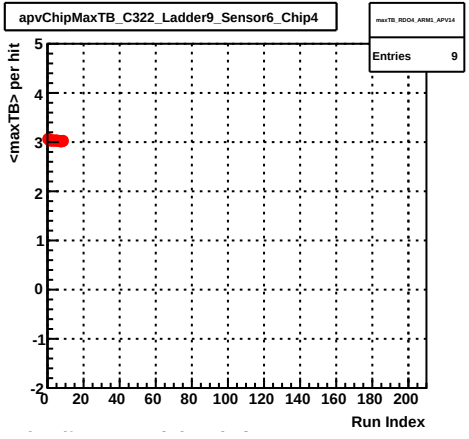

apvChipMaxTB_C317_Ladder9_Sensor5_Chip5
maxTB_RDO4_ARM1_APV19
Entries 9
<maxTB> per hit
Run Index

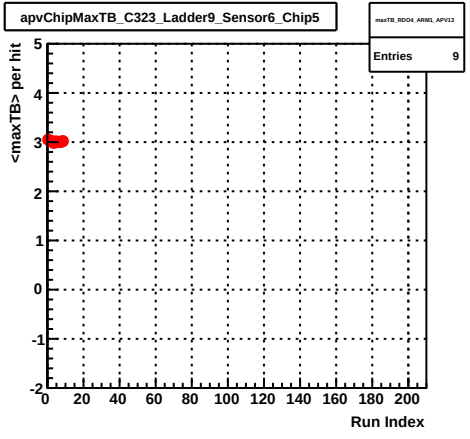

apvChipMaxTB_C318_Ladder9_Sensor5_Chip6
maxTB_RDO4_ARM1_APV18
Entries 9
<maxTB> per hit
Run Index

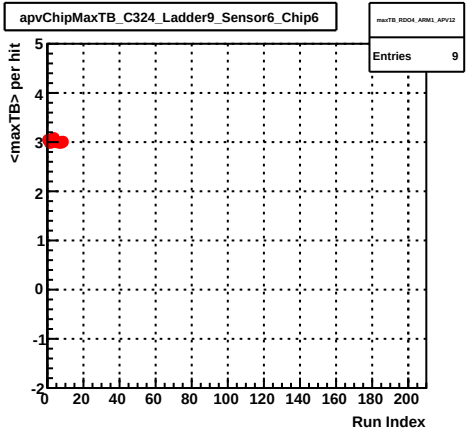

apvChipMaxTB_C319_Ladder9_Sensor6_Chip1
maxTB_RDO4_ARM1_APV17
Entries 9
<maxTB> per hit
Run Index

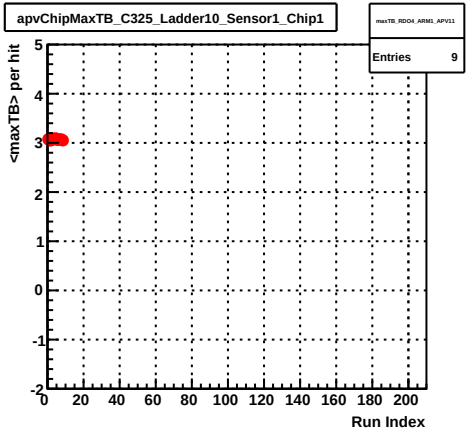

apvChipMaxTB_C320_Ladder9_Sensor6_Chip2
maxTB_RDO4_ARM1_APV16
Entries 9
<maxTB> per hit
Run Index

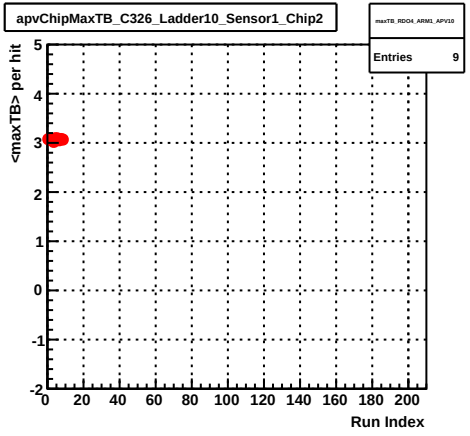

apvChipMaxTB_C321_Ladder9_Sensor6_Chip3
maxTB_RDO4_ARM1_APV15
Entries 9
<maxTB> per hit
Run Index

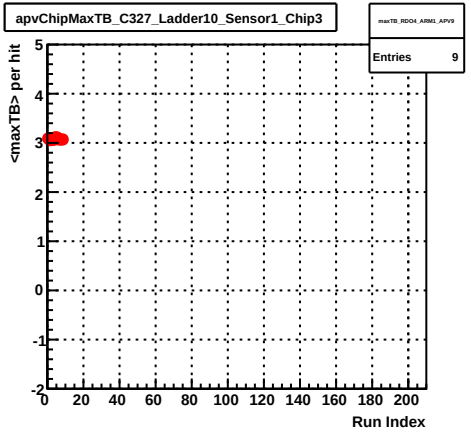

apvChipMaxTB_C322_Ladder9_Sensor6_Chip4
maxTB_RDO4_ARM1_APV14
Entries 9
<maxTB> per hit
Run Index

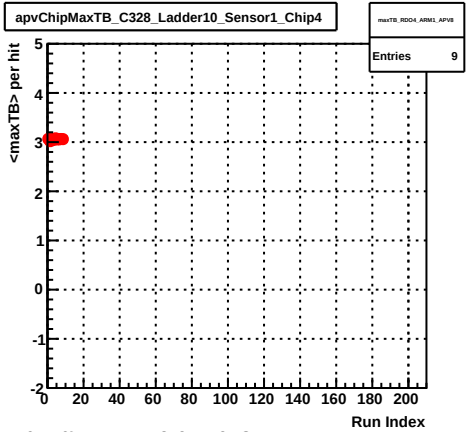

apvChipMaxTB_C323_Ladder9_Sensor6_Chip5
maxTB_RDO4_ARM1_APV13
Entries 9
<maxTB> per hit
Run Index

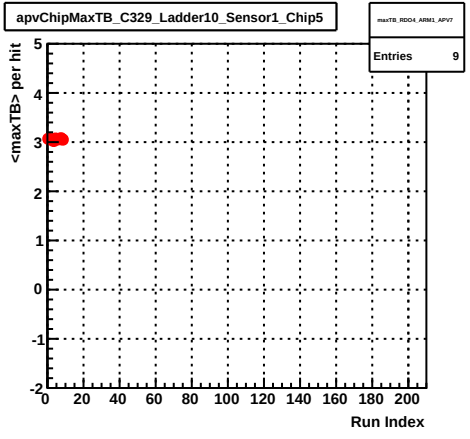

apvChipMaxTB_C324_Ladder9_Sensor6_Chip6
maxTB_RDO4_ARM1_APV12
Entries 9
<maxTB> per hit
Run Index

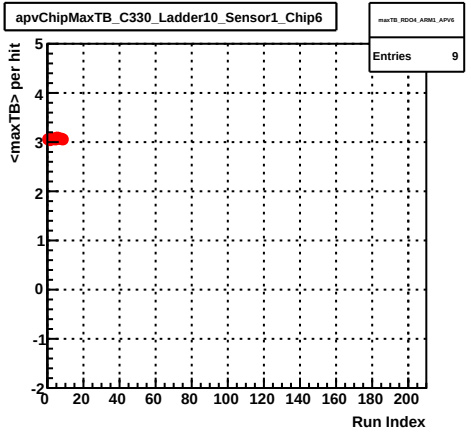

apvChipMaxTB_C325_Ladder10_Sensor1_Chip1
maxTB_RDO4_ARM1_APV11
Entries 9
<maxTB> per hit
Run Index

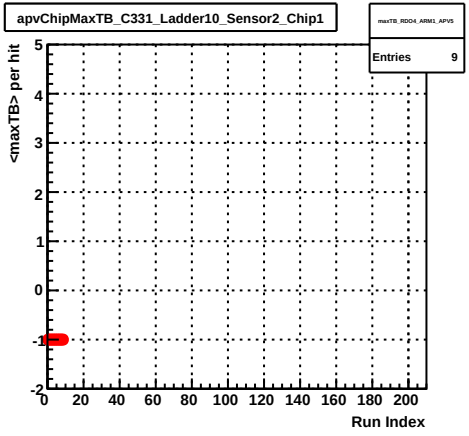

apvChipMaxTB_C326_Ladder10_Sensor1_Chip2
maxTB_RDO4_ARM1_APV10
Entries 9
<maxTB> per hit
Run Index

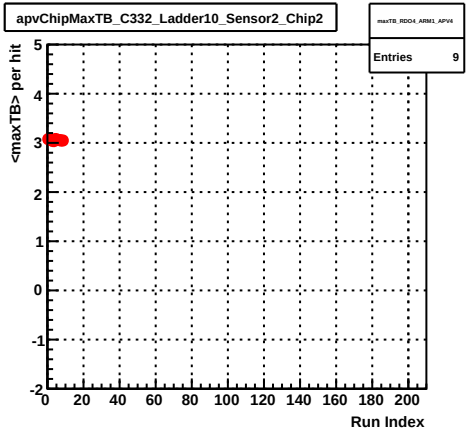

apvChipMaxTB_C327_Ladder10_Sensor1_Chip3
maxTB_RDO4_ARM1_APV9
Entries 9
<maxTB> per hit
Run Index

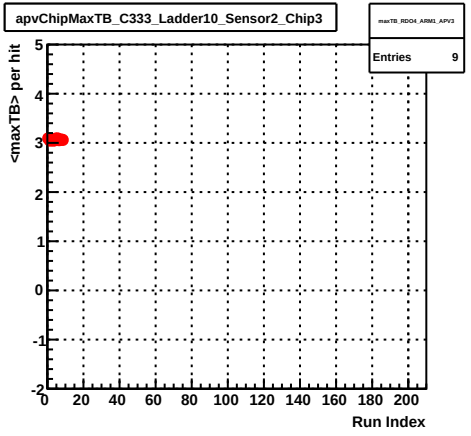

apvChipMaxTB_C328_Ladder10_Sensor1_Chip4
maxTB_RDO4_ARM1_APV8
Entries 9
<maxTB> per hit
Run Index

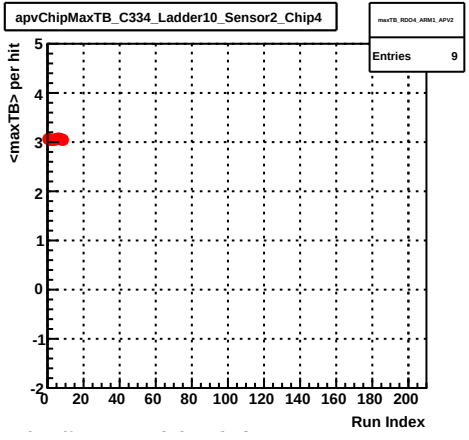

apvChipMaxTB_C329_Ladder10_Sensor1_Chip5
maxTB_RDO4_ARM1_APV7
Entries 9
<maxTB> per hit
Run Index

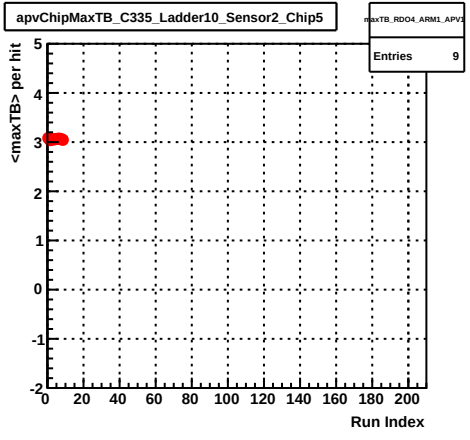

apvChipMaxTB_C330_Ladder10_Sensor1_Chip6
maxTB_RDO4_ARM1_APV6
Entries 9
<maxTB> per hit
Run Index

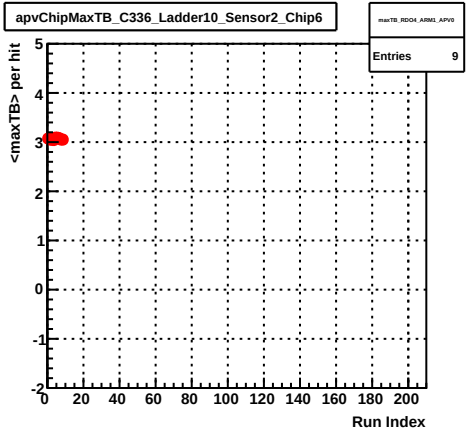

apvChipMaxTB_C331_Ladder10_Sensor2_Chip1
maxTB_RDO4_ARM1_APV5
Entries 9
<maxTB> per hit
Run Index

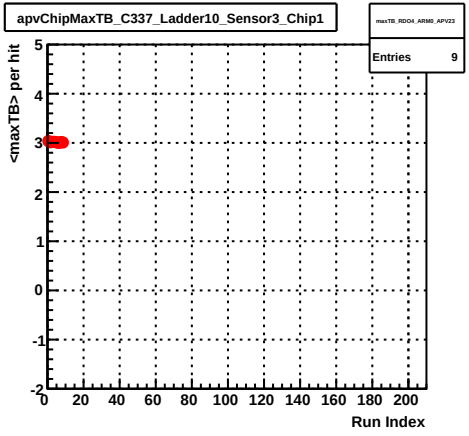

apvChipMaxTB_C332_Ladder10_Sensor2_Chip2
maxTB_RDO4_ARM1_APV4
Entries 9
<maxTB> per hit
Run Index

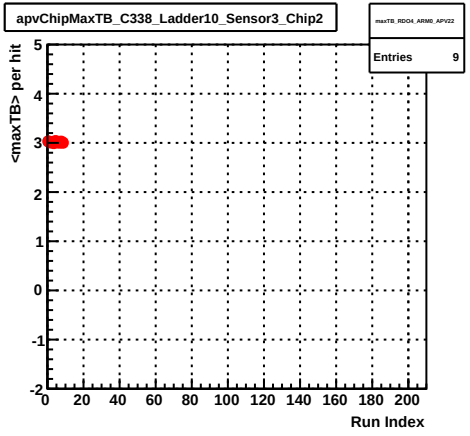

apvChipMaxTB_C333_Ladder10_Sensor2_Chip3
maxTB_RDO4_ARM1_APV3
Entries 9
<maxTB> per hit
Run Index

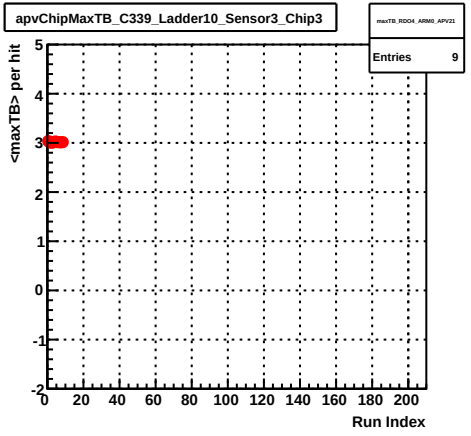

apvChipMaxTB_C334_Ladder10_Sensor2_Chip4
maxTB_RDO4_ARM1_APV2
Entries 9
<maxTB> per hit
Run Index

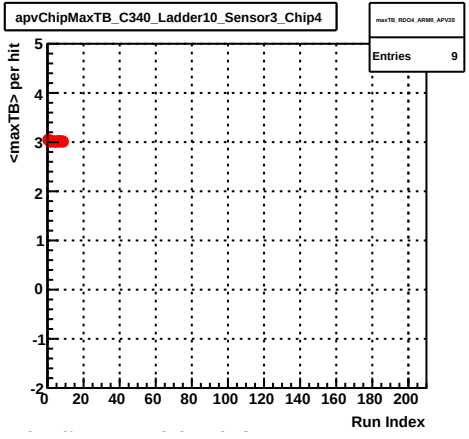

apvChipMaxTB_C335_Ladder10_Sensor2_Chip5
maxTB_RDO4_ARM1_APV1
Entries 9
<maxTB> per hit
Run Index

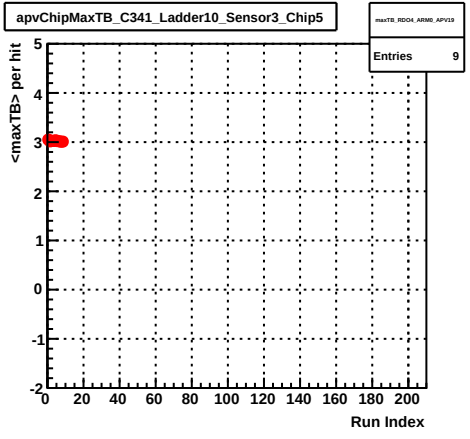

apvChipMaxTB_C336_Ladder10_Sensor2_Chip6
maxTB_RDO4_ARM1_APV0
Entries 9
<maxTB> per hit
Run Index

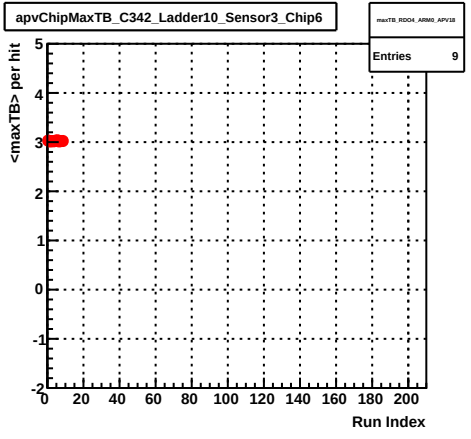

apvChipMaxTB_C337_Ladder10_Sensor3_Chip1
maxTB_RDO4_ARM0_APV23
Entries 9
<maxTB> per hit
Run Index

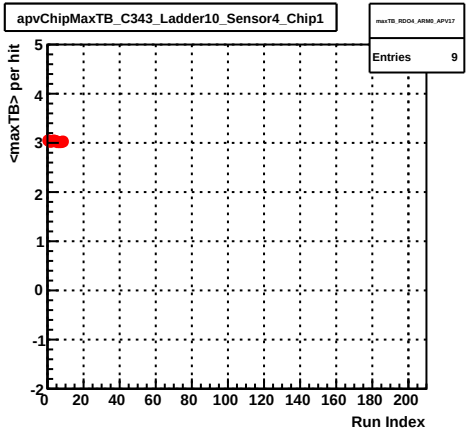

apvChipMaxTB_C338_Ladder10_Sensor3_Chip2
maxTB_RDO4_ARM0_APV22
Entries 9
<maxTB> per hit
Run Index

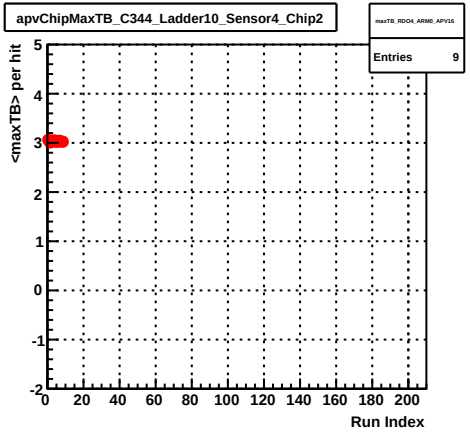

apvChipMaxTB_C339_Ladder10_Sensor3_Chip3
maxTB_RDO4_ARM0_APV21
Entries 9
<maxTB> per hit
Run Index

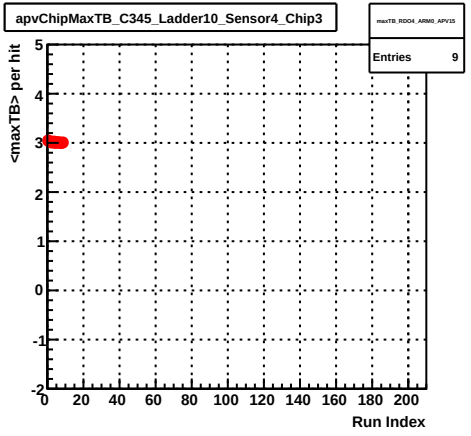

apvChipMaxTB_C340_Ladder10_Sensor3_Chip4
maxTB_RDO4_ARM0_APV20
Entries 9
<maxTB> per hit
Run Index

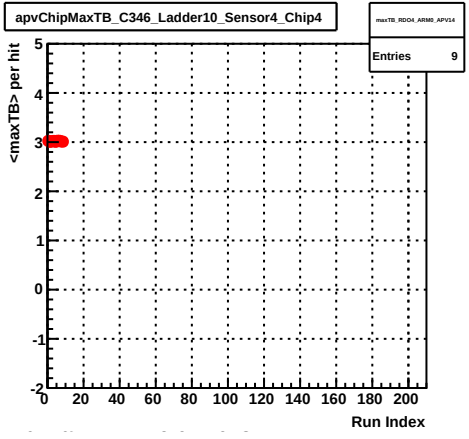

apvChipMaxTB_C341_Ladder10_Sensor3_Chip5
maxTB_RDO4_ARM0_APV19
Entries 9
<maxTB> per hit
Run Index

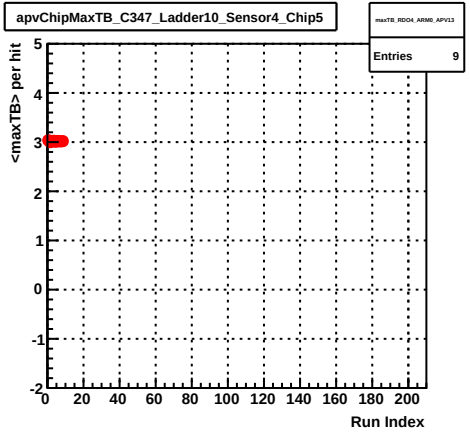

apvChipMaxTB_C342_Ladder10_Sensor3_Chip6
maxTB_RDO4_ARM0_APV18
Entries 9
<maxTB> per hit
Run Index

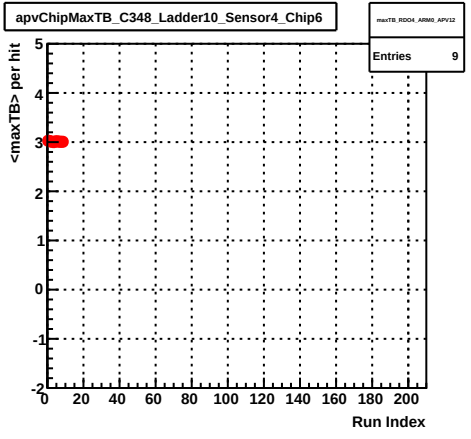

apvChipMaxTB_C343_Ladder10_Sensor4_Chip1
maxTB_RDO4_ARM0_APV17
Entries 9
<maxTB> per hit
Run Index

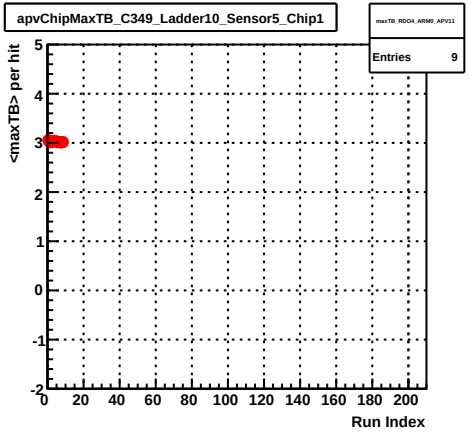

apvChipMaxTB_C344_Ladder10_Sensor4_Chip2
maxTB_RDO4_ARM0_APV16
Entries 9
<maxTB> per hit
Run Index

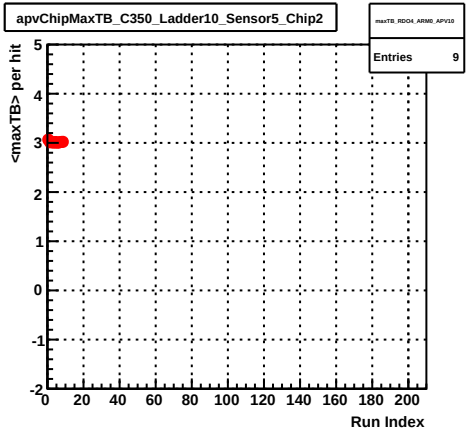

apvChipMaxTB_C345_Ladder10_Sensor4_Chip3
maxTB_RDO4_ARM0_APV15
Entries 9
<maxTB> per hit
Run Index

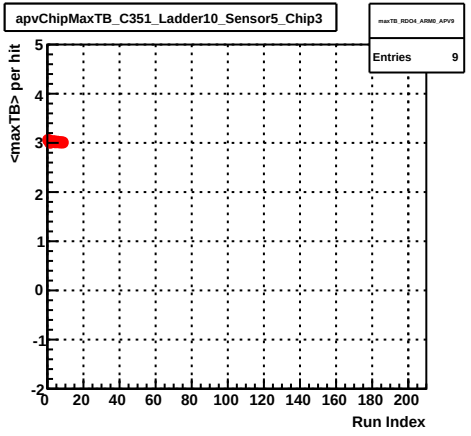

apvChipMaxTB_C346_Ladder10_Sensor4_Chip4
maxTB_RDO4_ARM0_APV14
Entries 9
<maxTB> per hit
Run Index

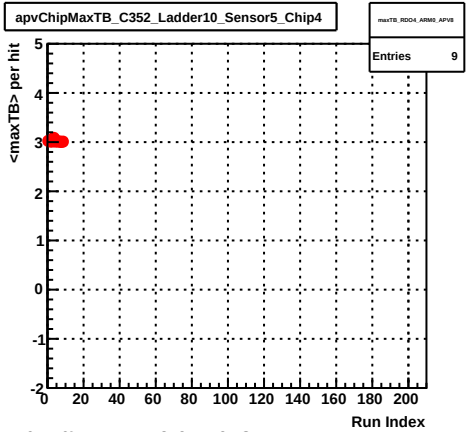

apvChipMaxTB_C347_Ladder10_Sensor4_Chip5
maxTB_RDO4_ARM0_APV13
Entries 9
<maxTB> per hit
Run Index

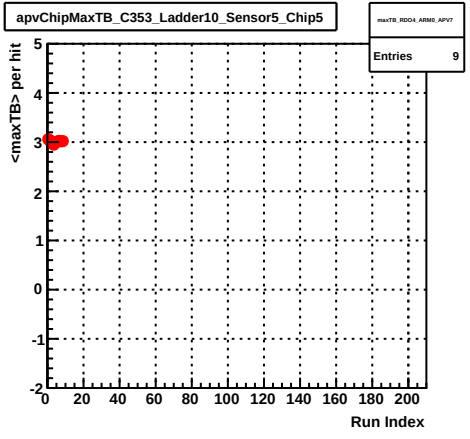

apvChipMaxTB_C348_Ladder10_Sensor4_Chip6
maxTB_RDO4_ARM0_APV12
Entries 9
<maxTB> per hit
Run Index

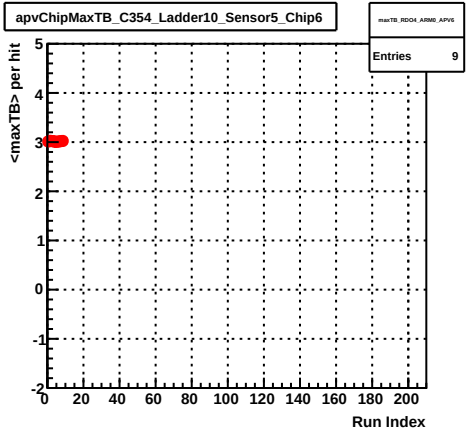

apvChipMaxTB_C349_Ladder10_Sensor5_Chip1
maxTB_RDO4_ARM0_APV11
Entries 9
<maxTB> per hit
Run Index

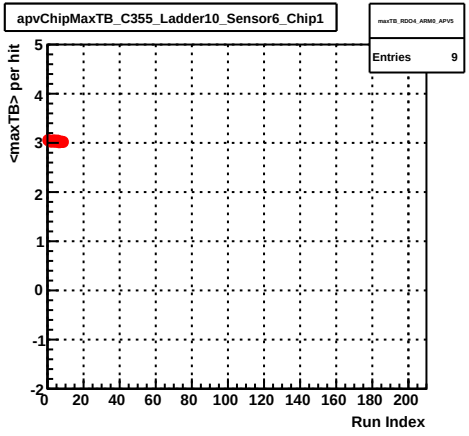

apvChipMaxTB_C350_Ladder10_Sensor5_Chip2
maxTB_RDO4_ARM0_APV10
Entries 9
<maxTB> per hit
Run Index

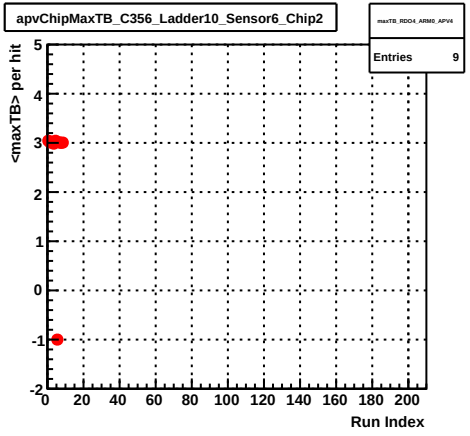

apvChipMaxTB_C351_Ladder10_Sensor5_Chip3
maxTB_RDO4_ARM0_APV9
Entries 9
<maxTB> per hit
Run Index

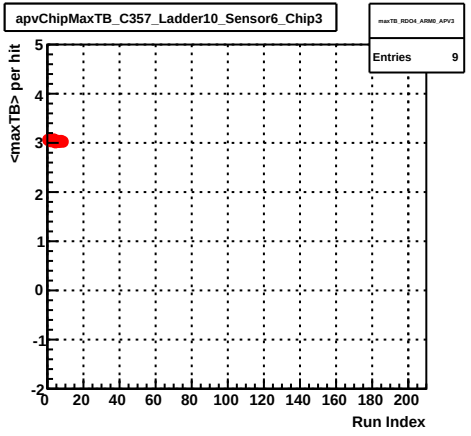

apvChipMaxTB_C352_Ladder10_Sensor5_Chip4
maxTB_RDO4_ARM0_APV8
Entries 9
<maxTB> per hit
Run Index

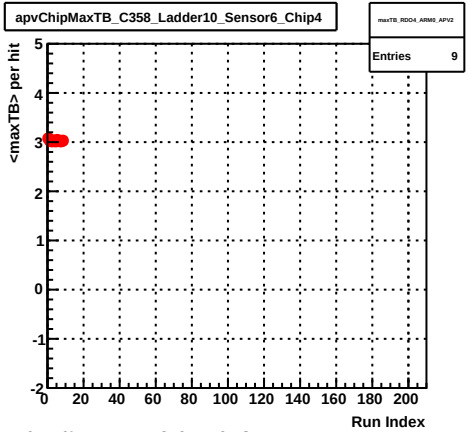

apvChipMaxTB_C353_Ladder10_Sensor5_Chip5
maxTB_RDO4_ARM0_APV7
Entries 9
<maxTB> per hit
Run Index

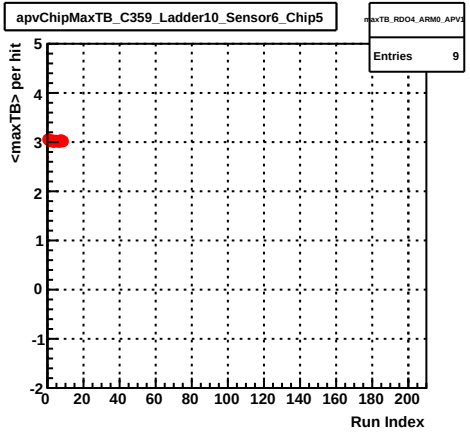

apvChipMaxTB_C354_Ladder10_Sensor5_Chip6
maxTB_RDO4_ARM0_APV6
Entries 9
<maxTB> per hit
Run Index

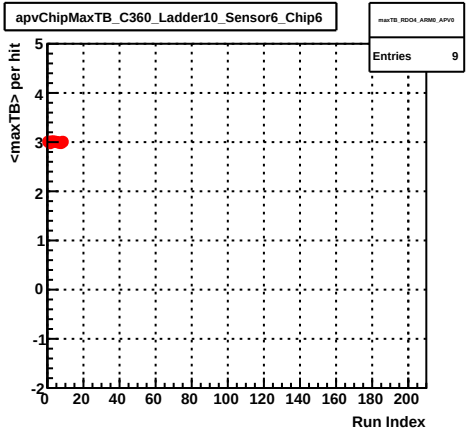

apvChipMaxTB_C355_Ladder10_Sensor6_Chip1
maxTB_RDO4_ARM0_APV5
Entries 9
<maxTB> per hit
Run Index

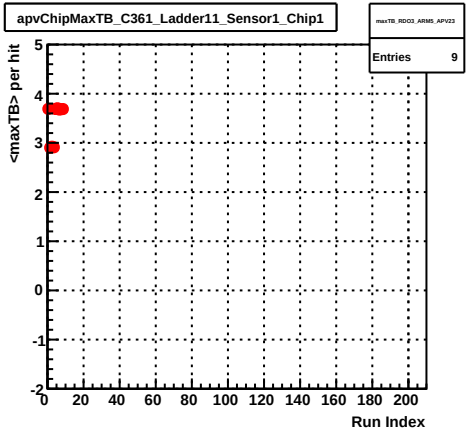

apvChipMaxTB_C356_Ladder10_Sensor6_Chip2
maxTB_RDO4_ARM0_APV4
Entries 9
<maxTB> per hit
Run Index

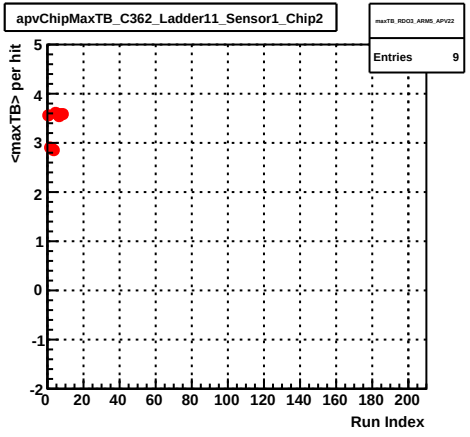

apvChipMaxTB_C357_Ladder10_Sensor6_Chip3
maxTB_RDO4_ARM0_APV3
Entries 9
<maxTB> per hit
Run Index

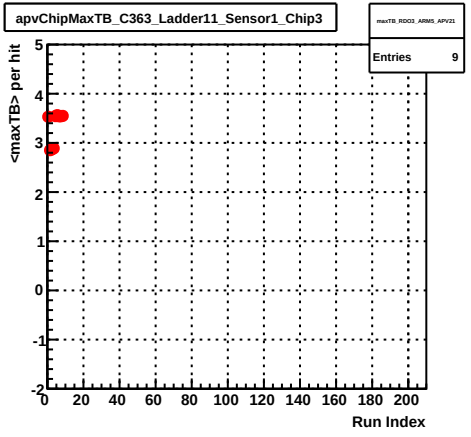

apvChipMaxTB_C358_Ladder10_Sensor6_Chip4
maxTB_RDO4_ARM0_APV2
Entries 9
<maxTB> per hit
Run Index

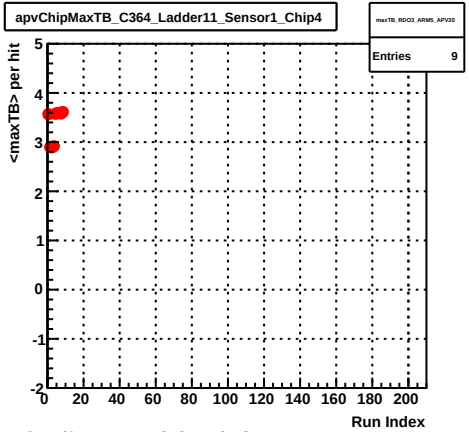

apvChipMaxTB_C359_Ladder10_Sensor6_Chip5
maxTB_RDO4_ARM0_APV1
Entries 9
<maxTB> per hit
Run Index

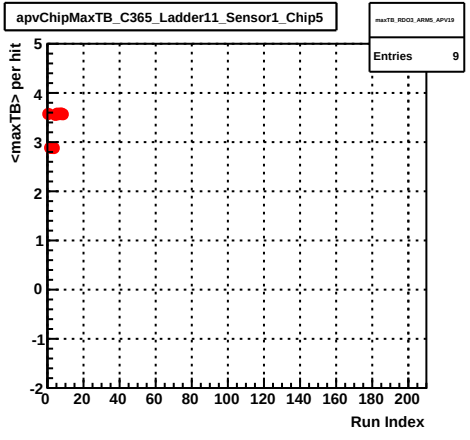

apvChipMaxTB_C360_Ladder10_Sensor6_Chip6
maxTB_RDO4_ARM0_APV0
Entries 9
<maxTB> per hit
Run Index

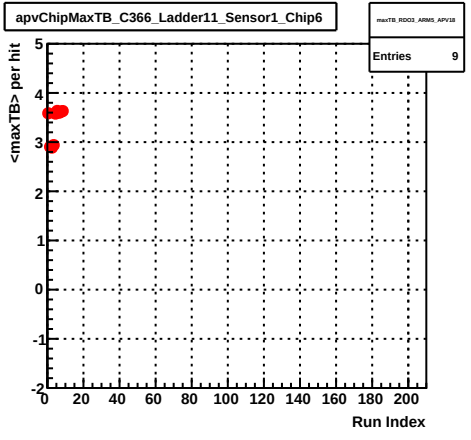

apvChipMaxTB_C361_Ladder11_Sensor1_Chip1
maxTB_RDO3_ARM5_APV23
Entries 9
<maxTB> per hit
Run Index

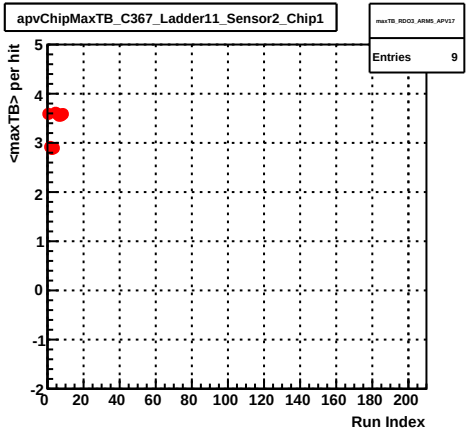

apvChipMaxTB_C362_Ladder11_Sensor1_Chip2
maxTB_RDO3_ARM5_APV22
Entries 9
<maxTB> per hit
Run Index

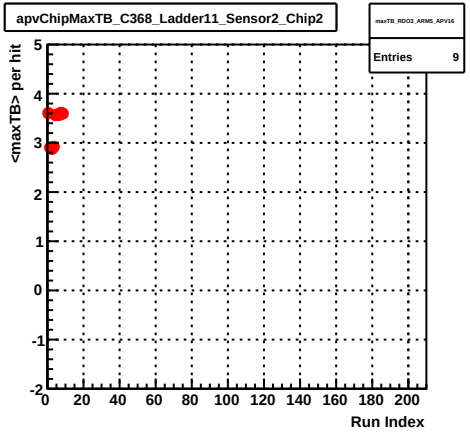

apvChipMaxTB_C363_Ladder11_Sensor1_Chip3
maxTB_RDO3_ARM5_APV21
Entries 9
<maxTB> per hit
Run Index

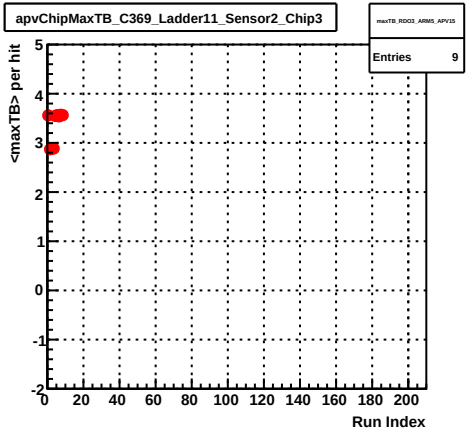

apvChipMaxTB_C364_Ladder11_Sensor1_Chip4
maxTB_RDO3_ARM5_APV20
Entries 9
<maxTB> per hit
Run Index

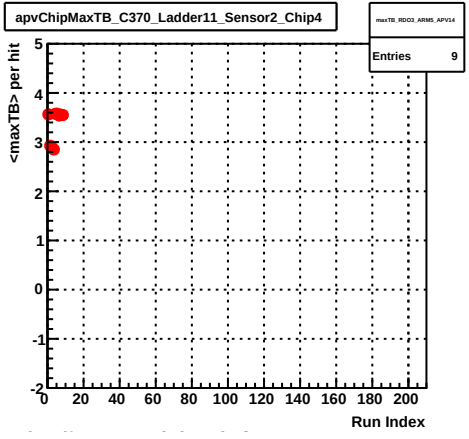

apvChipMaxTB_C365_Ladder11_Sensor1_Chip5
maxTB_RDO3_ARM5_APV19
Entries 9
<maxTB> per hit
Run Index

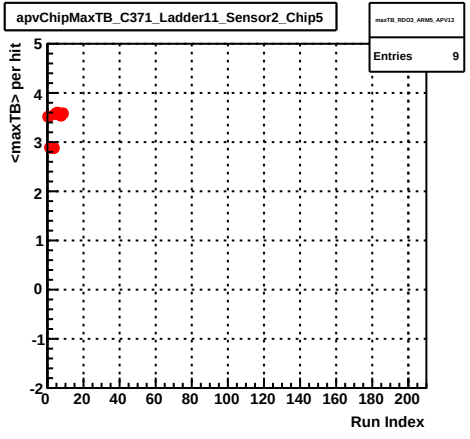

apvChipMaxTB_C366_Ladder11_Sensor1_Chip6
maxTB_RDO3_ARM5_APV18
Entries 9
<maxTB> per hit
Run Index

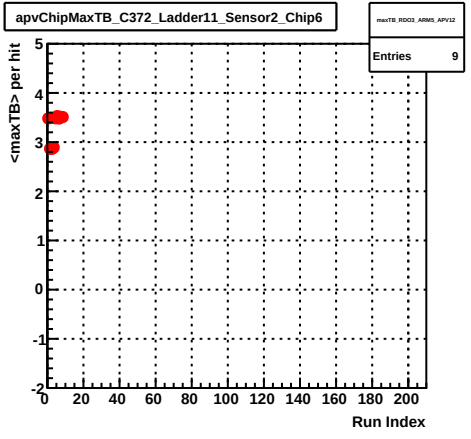

apvChipMaxTB_C367_Ladder11_Sensor2_Chip1
maxTB_RDO3_ARM5_APV17
Entries 9
<maxTB> per hit
Run Index

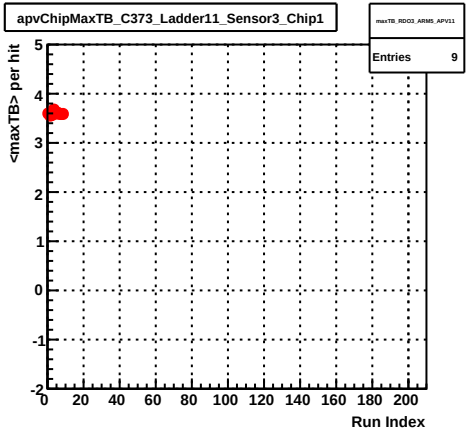

apvChipMaxTB_C368_Ladder11_Sensor2_Chip2
maxTB_RDO3_ARM5_APV16
Entries 9
<maxTB> per hit
Run Index

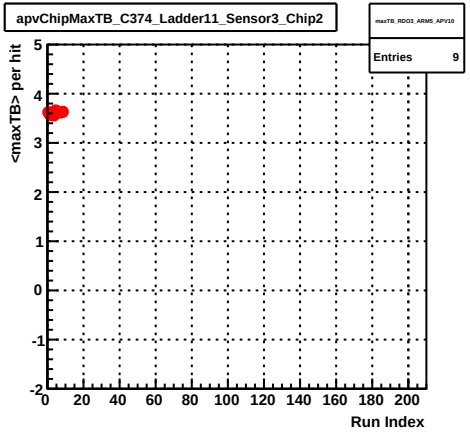

apvChipMaxTB_C369_Ladder11_Sensor2_Chip3
maxTB_RDO3_ARM5_APV15
Entries 9
<maxTB> per hit
Run Index

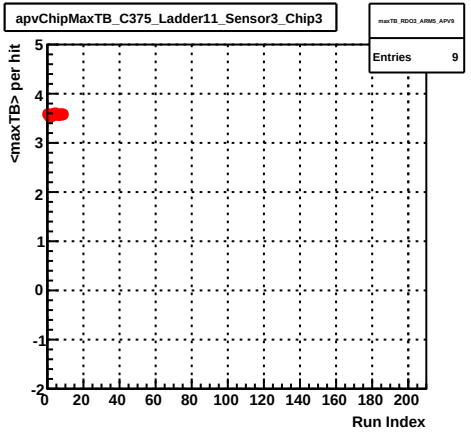

apvChipMaxTB_C370_Ladder11_Sensor2_Chip4
maxTB_RDO3_ARM5_APV14
Entries 9
<maxTB> per hit
Run Index

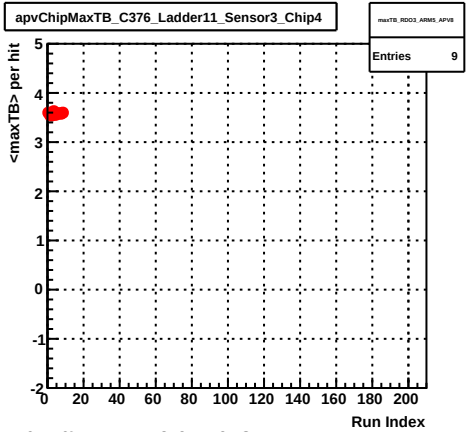

apvChipMaxTB_C371_Ladder11_Sensor2_Chip5
maxTB_RDO3_ARM5_APV13
Entries 9
<maxTB> per hit
Run Index

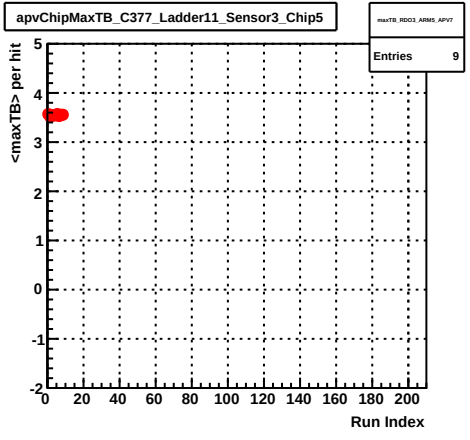

apvChipMaxTB_C372_Ladder11_Sensor2_Chip6
maxTB_RDO3_ARM5_APV12
Entries 9
<maxTB> per hit
Run Index

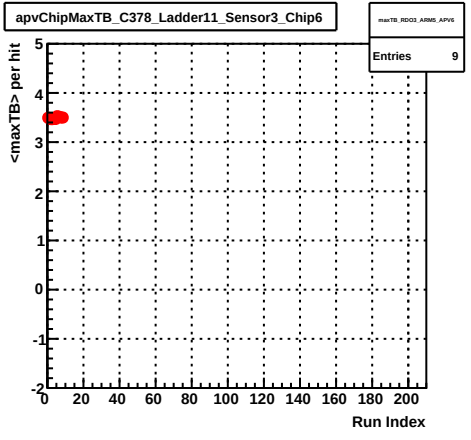

apvChipMaxTB_C373_Ladder11_Sensor3_Chip1
maxTB_RDO3_ARM5_APV11
Entries 9
<maxTB> per hit
Run Index

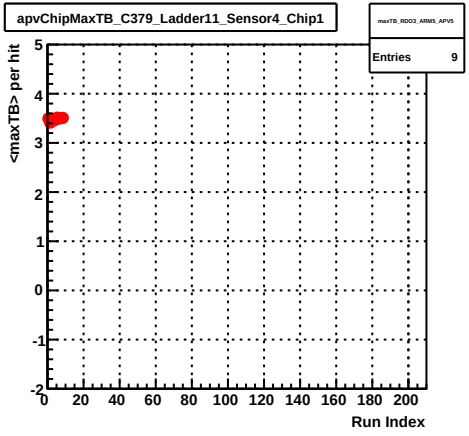

apvChipMaxTB_C374_Ladder11_Sensor3_Chip2
maxTB_RDO3_ARM5_APV10
Entries 9
<maxTB> per hit
Run Index

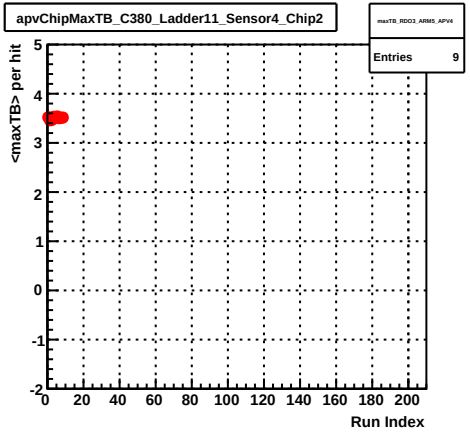

apvChipMaxTB_C375_Ladder11_Sensor3_Chip3
maxTB_RDO3_ARM5_APV9
Entries 9
<maxTB> per hit
Run Index

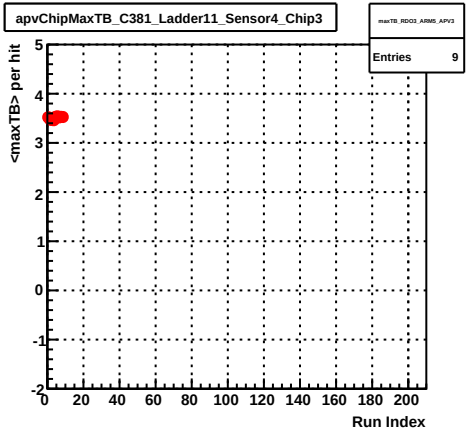

apvChipMaxTB_C376_Ladder11_Sensor3_Chip4
maxTB_RDO3_ARM5_APV8
Entries 9
<maxTB> per hit
Run Index

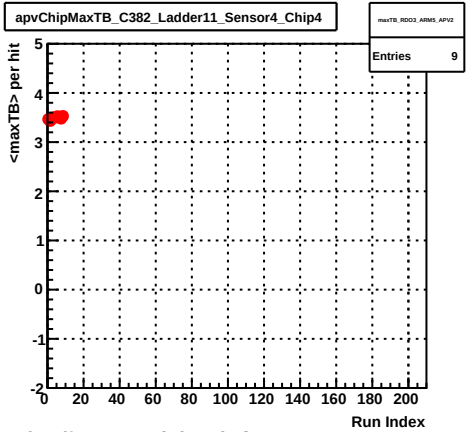

apvChipMaxTB_C377_Ladder11_Sensor3_Chip5
maxTB_RDO3_ARM5_APV7
Entries 9
<maxTB> per hit
Run Index

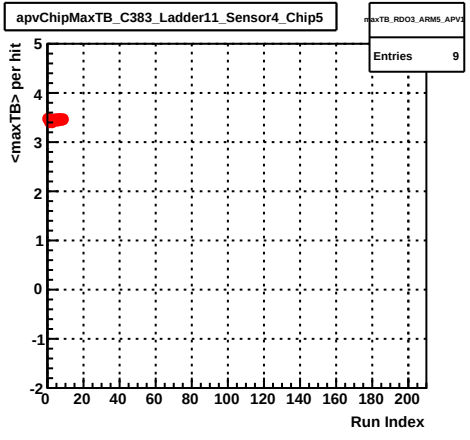

apvChipMaxTB_C378_Ladder11_Sensor3_Chip6
maxTB_RDO3_ARM5_APV6
Entries 9
<maxTB> per hit
Run Index

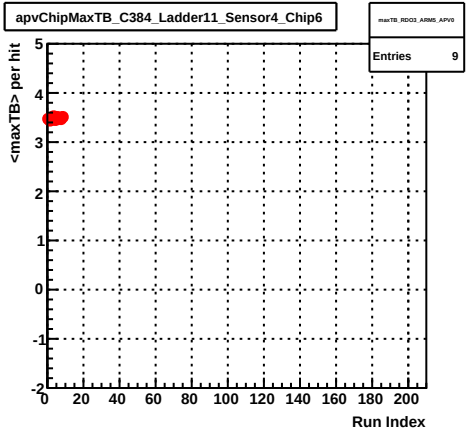

apvChipMaxTB_C379_Ladder11_Sensor4_Chip1
maxTB_RDO3_ARM5_APV5
Entries 9
<maxTB> per hit
Run Index

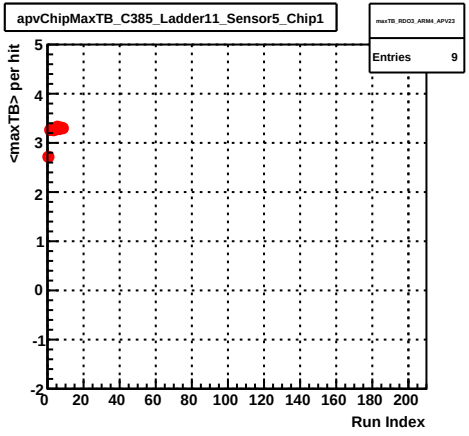

apvChipMaxTB_C380_Ladder11_Sensor4_Chip2
maxTB_RDO3_ARM5_APV4
Entries 9
<maxTB> per hit
Run Index

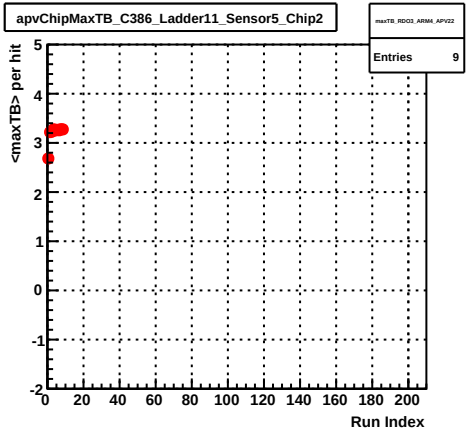

apvChipMaxTB_C381_Ladder11_Sensor4_Chip3
maxTB_RDO3_ARM5_APV3
Entries 9
<maxTB> per hit
Run Index

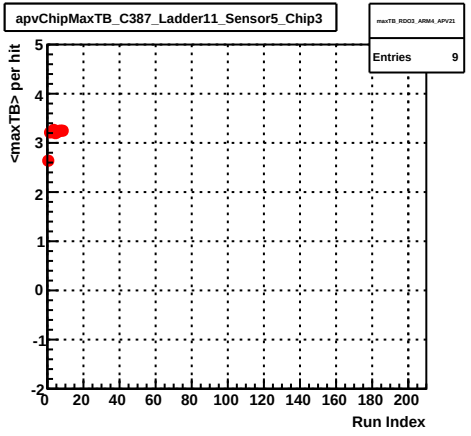

apvChipMaxTB_C382_Ladder11_Sensor4_Chip4
maxTB_RDO3_ARM5_APV2
Entries 9
<maxTB> per hit
Run Index

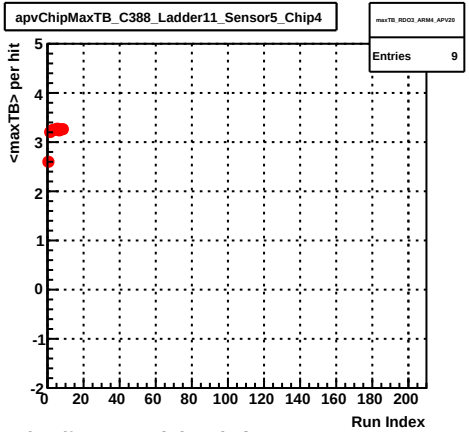

apvChipMaxTB_C383_Ladder11_Sensor4_Chip5
maxTB_RDO3_ARM5_APV1
Entries 9
<maxTB> per hit
Run Index

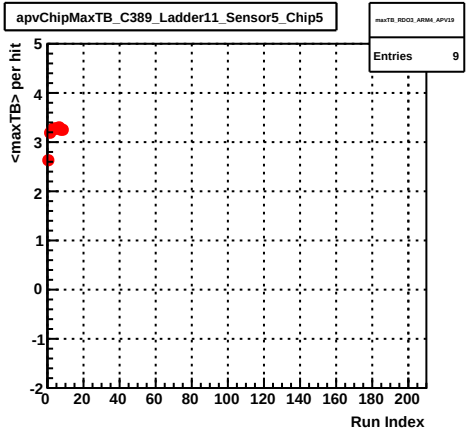

apvChipMaxTB_C384_Ladder11_Sensor4_Chip6
maxTB_RDO3_ARM5_APV0
Entries 9
<maxTB> per hit
Run Index

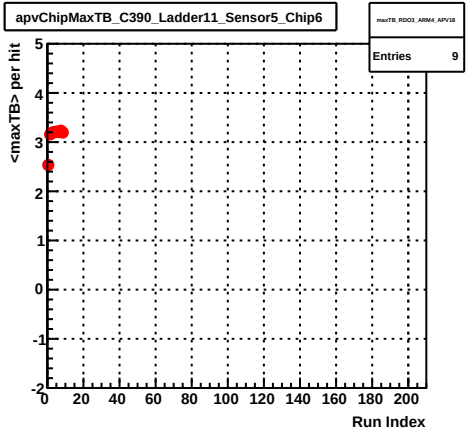

apvChipMaxTB_C385_Ladder11_Sensor5_Chip1
maxTB_RDO3_ARM4_APV23
Entries 9
<maxTB> per hit
Run Index

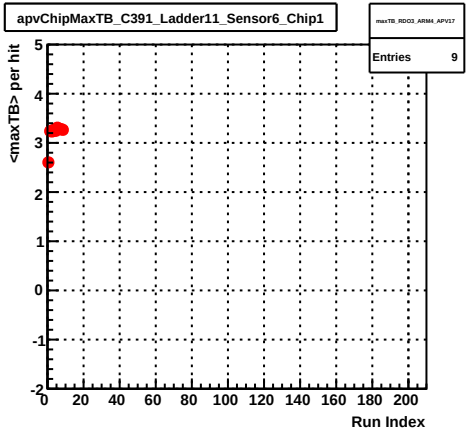

apvChipMaxTB_C386_Ladder11_Sensor5_Chip2
maxTB_RDO3_ARM4_APV22
Entries 9
<maxTB> per hit
Run Index

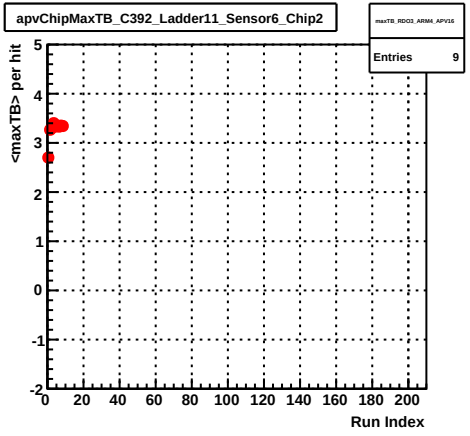

apvChipMaxTB_C387_Ladder11_Sensor5_Chip3
maxTB_RDO3_ARM4_APV21
Entries 9
<maxTB> per hit
Run Index

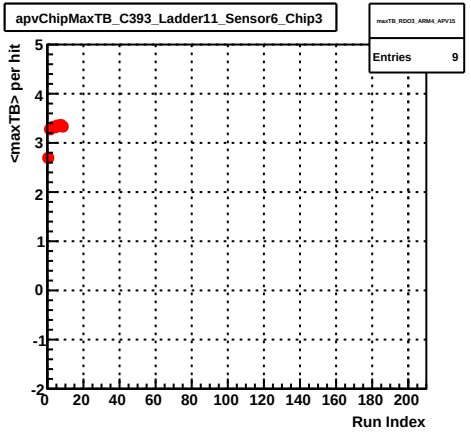

apvChipMaxTB_C388_Ladder11_Sensor5_Chip4
maxTB_RDO3_ARM4_APV20
Entries 9
<maxTB> per hit
Run Index

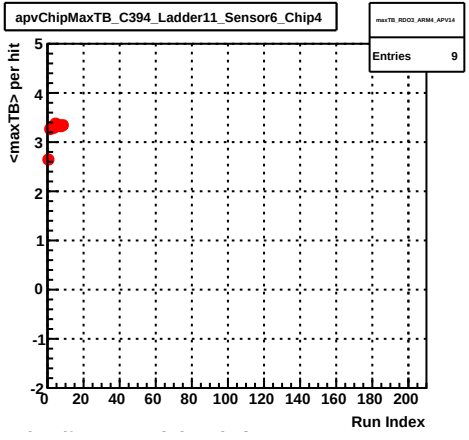

apvChipMaxTB_C389_Ladder11_Sensor5_Chip5
maxTB_RDO3_ARM4_APV19
Entries 9
<maxTB> per hit
Run Index

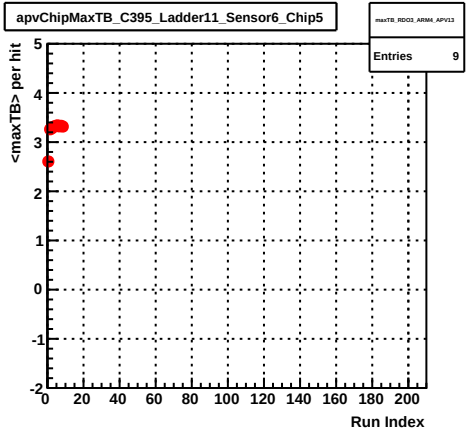

apvChipMaxTB_C390_Ladder11_Sensor5_Chip6
maxTB_RDO3_ARM4_APV18
Entries 9
<maxTB> per hit
Run Index

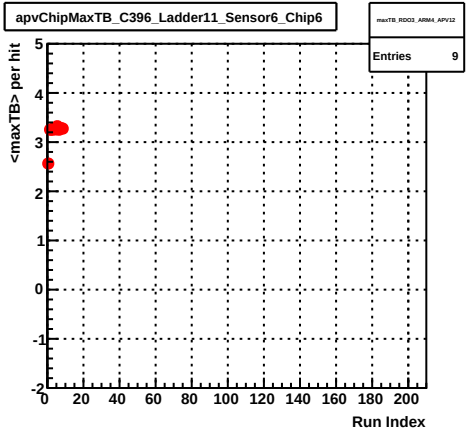

apvChipMaxTB_C391_Ladder11_Sensor6_Chip1
maxTB_RDO3_ARM4_APV17
Entries 9
<maxTB> per hit
Run Index

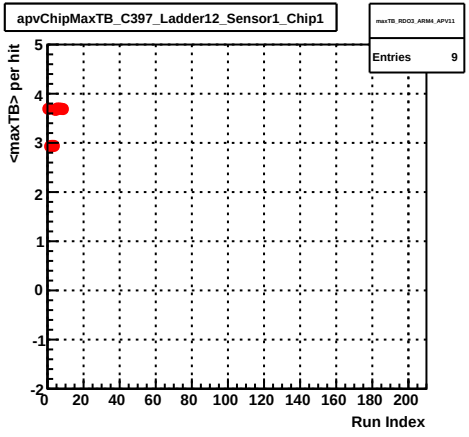

apvChipMaxTB_C392_Ladder11_Sensor6_Chip2
maxTB_RDO3_ARM4_APV16
Entries 9
<maxTB> per hit
Run Index

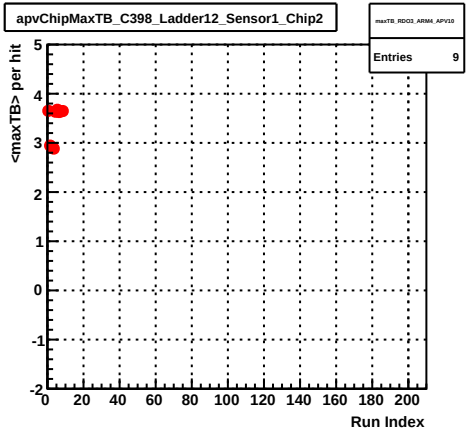

apvChipMaxTB_C393_Ladder11_Sensor6_Chip3
maxTB_RDO3_ARM4_APV15
Entries 9
<maxTB> per hit
Run Index

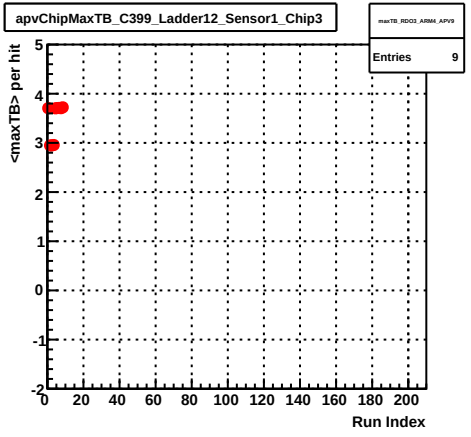

apvChipMaxTB_C394_Ladder11_Sensor6_Chip4
maxTB_RDO3_ARM4_APV14
Entries 9
<maxTB> per hit
Run Index

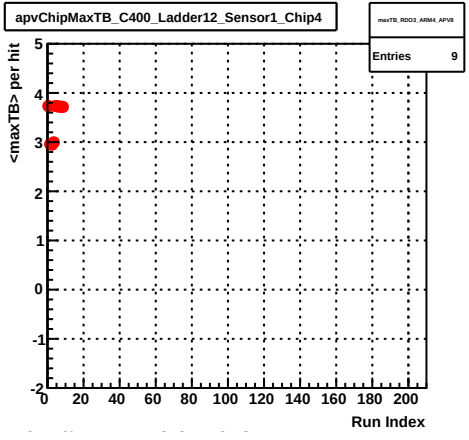

apvChipMaxTB_C395_Ladder11_Sensor6_Chip5
maxTB_RDO3_ARM4_APV13
Entries 9
<maxTB> per hit
Run Index

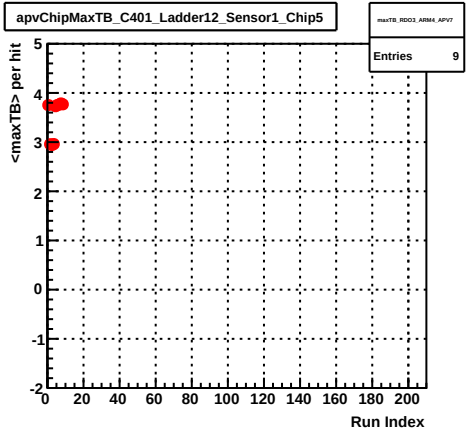

apvChipMaxTB_C396_Ladder11_Sensor6_Chip6
maxTB_RDO3_ARM4_APV12
Entries 9
<maxTB> per hit
Run Index

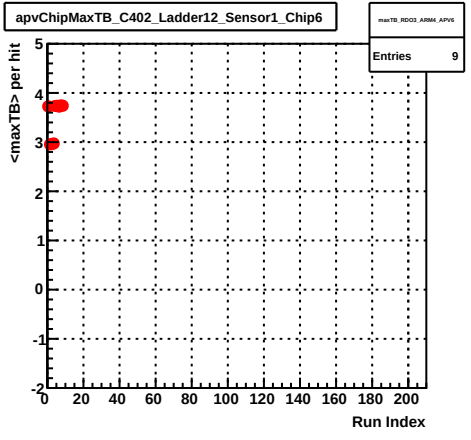

apvChipMaxTB_C397_Ladder12_Sensor1_Chip1
maxTB_RDO3_ARM4_APV11
Entries 9
<maxTB> per hit
Run Index

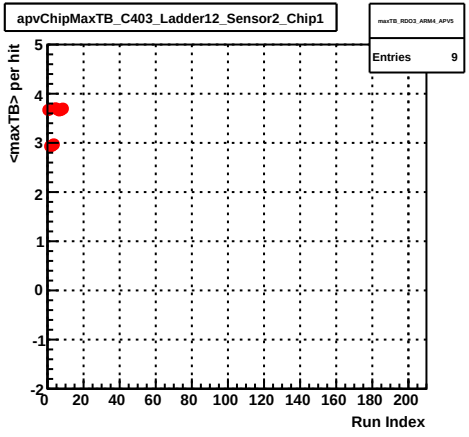

apvChipMaxTB_C398_Ladder12_Sensor1_Chip2
maxTB_RDO3_ARM4_APV10
Entries 9
<maxTB> per hit
Run Index

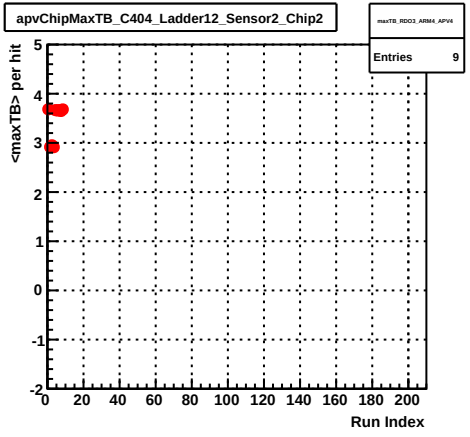

apvChipMaxTB_C399_Ladder12_Sensor1_Chip3
maxTB_RDO3_ARM4_APV9
Entries 9
<maxTB> per hit
Run Index

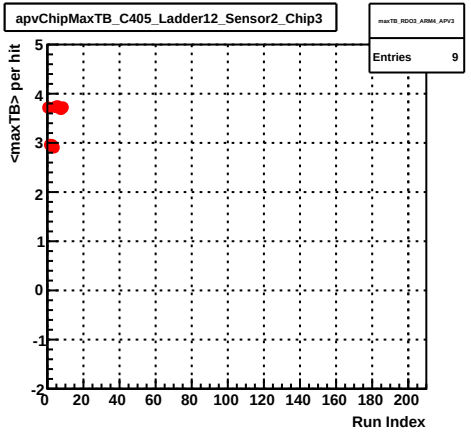

apvChipMaxTB_C400_Ladder12_Sensor1_Chip4
maxTB_RDO3_ARM4_APV8
Entries 9
<maxTB> per hit
Run Index

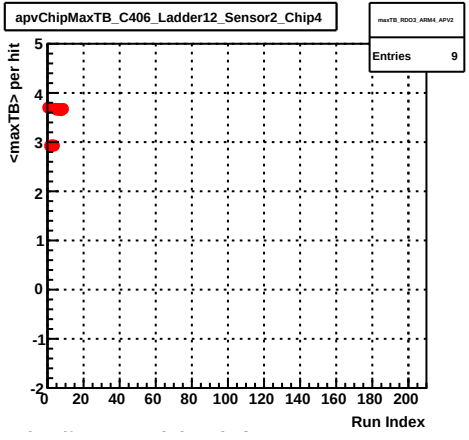

apvChipMaxTB_C401_Ladder12_Sensor1_Chip5
maxTB_RDO3_ARM4_APV7
Entries 9
<maxTB> per hit
Run Index

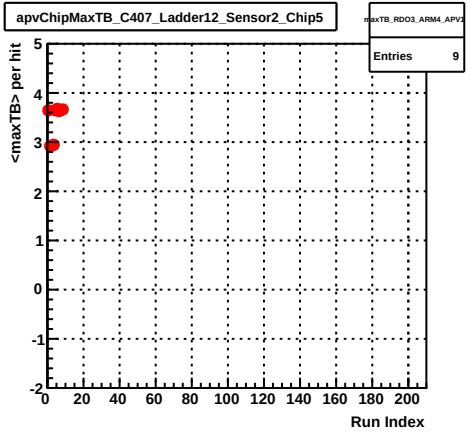

apvChipMaxTB_C402_Ladder12_Sensor1_Chip6
maxTB_RDO3_ARM4_APV6
Entries 9
<maxTB> per hit
Run Index

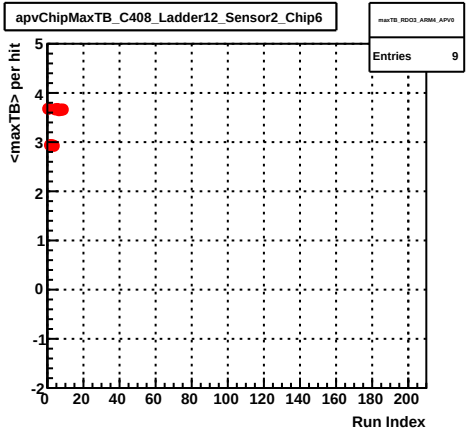

apvChipMaxTB_C403_Ladder12_Sensor2_Chip1
maxTB_RDO3_ARM4_APV5
Entries 9
<maxTB> per hit
Run Index

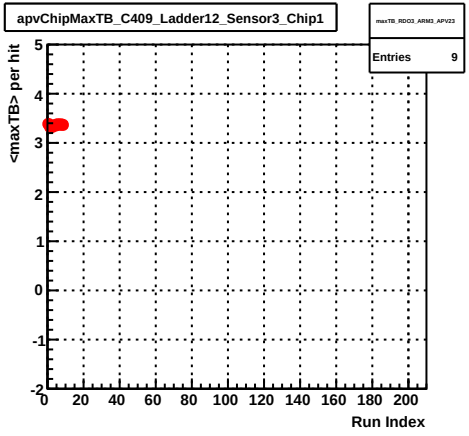

apvChipMaxTB_C404_Ladder12_Sensor2_Chip2
maxTB_RDO3_ARM4_APV4
Entries 9
<maxTB> per hit
Run Index

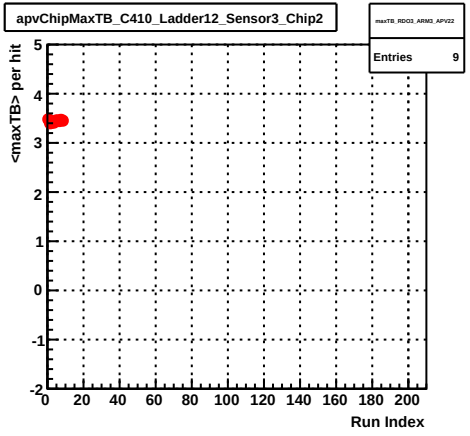

apvChipMaxTB_C405_Ladder12_Sensor2_Chip3
maxTB_RDO3_ARM4_APV3
Entries 9
<maxTB> per hit
Run Index

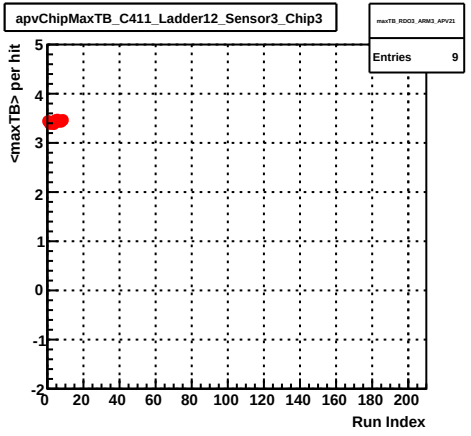

apvChipMaxTB_C406_Ladder12_Sensor2_Chip4
maxTB_RDO3_ARM4_APV2
Entries 9
<maxTB> per hit
Run Index

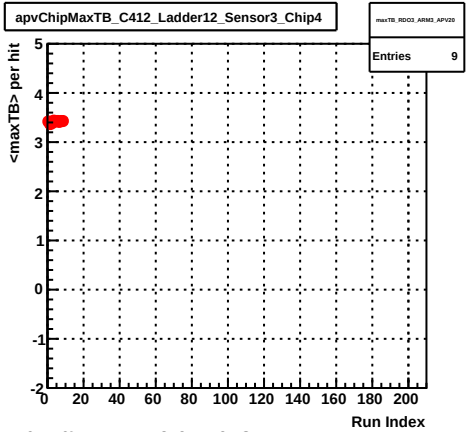

apvChipMaxTB_C407_Ladder12_Sensor2_Chip5
maxTB_RDO3_ARM4_APV1
Entries 9
<maxTB> per hit
Run Index

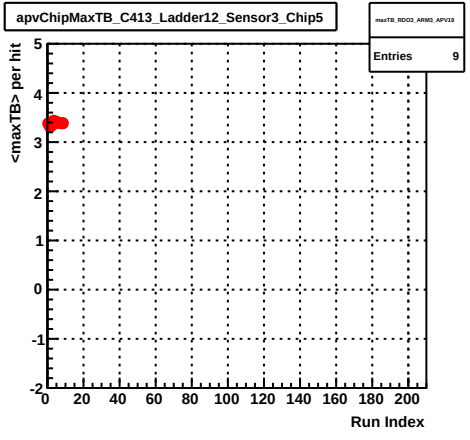

apvChipMaxTB_C408_Ladder12_Sensor2_Chip6
maxTB_RDO3_ARM4_APV0
Entries 9
<maxTB> per hit
Run Index

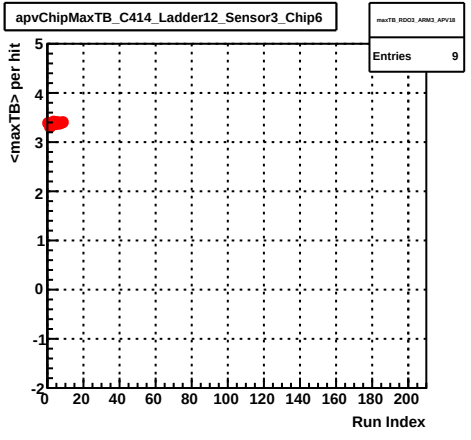

apvChipMaxTB_C409_Ladder12_Sensor3_Chip1
maxTB_RDO3_ARM3_APV23
Entries 9
<maxTB> per hit
Run Index

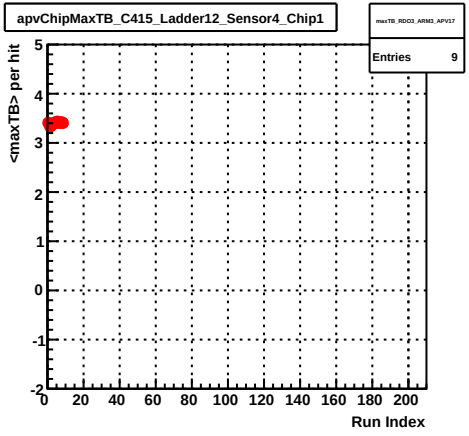

apvChipMaxTB_C410_Ladder12_Sensor3_Chip2
maxTB_RDO3_ARM3_APV22
Entries 9
<maxTB> per hit
Run Index

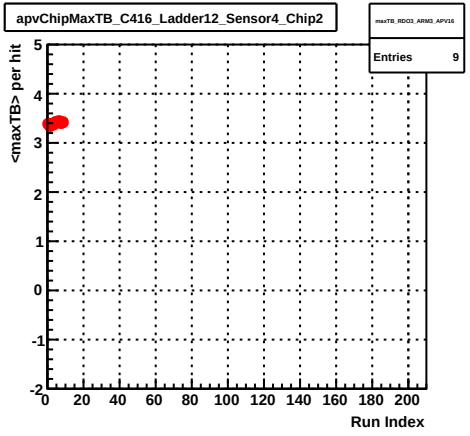

apvChipMaxTB_C411_Ladder12_Sensor3_Chip3
maxTB_RDO3_ARM3_APV21
Entries 9
<maxTB> per hit
Run Index

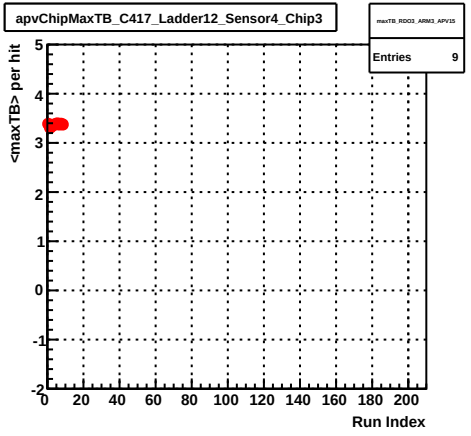

apvChipMaxTB_C412_Ladder12_Sensor3_Chip4
maxTB_RDO3_ARM3_APV20
Entries 9
<maxTB> per hit
Run Index

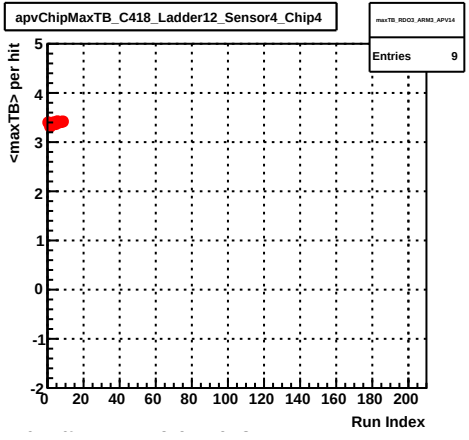

apvChipMaxTB_C413_Ladder12_Sensor3_Chip5
maxTB_RDO3_ARM3_APV19
Entries 9
<maxTB> per hit
Run Index

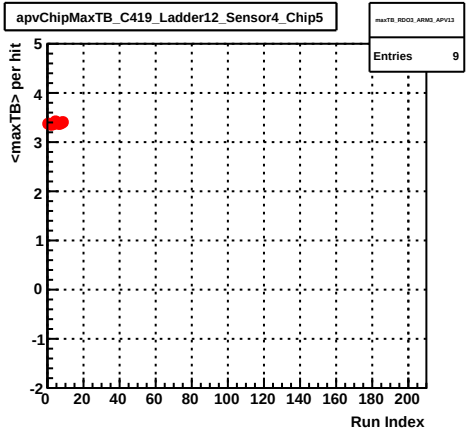

apvChipMaxTB_C414_Ladder12_Sensor3_Chip6
maxTB_RDO3_ARM3_APV18
Entries 9
<maxTB> per hit
Run Index

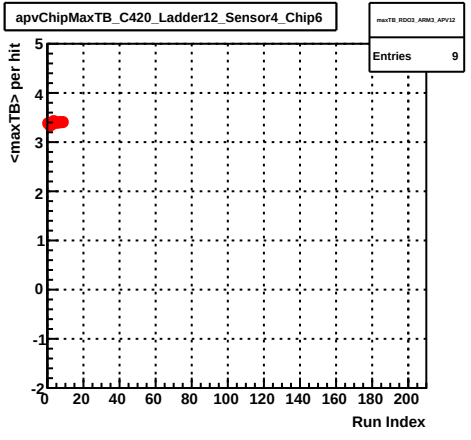

apvChipMaxTB_C415_Ladder12_Sensor4_Chip1
maxTB_RDO3_ARM3_APV17
Entries 9
<maxTB> per hit
Run Index

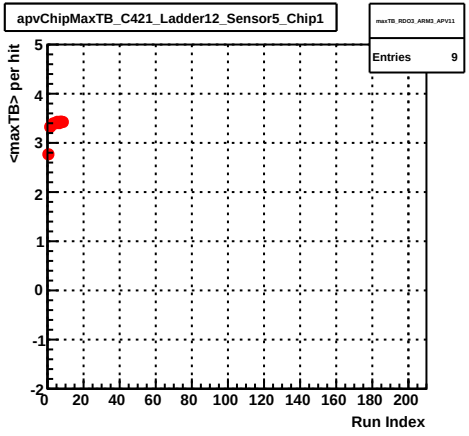

apvChipMaxTB_C416_Ladder12_Sensor4_Chip2
maxTB_RDO3_ARM3_APV16
Entries 9
<maxTB> per hit
Run Index

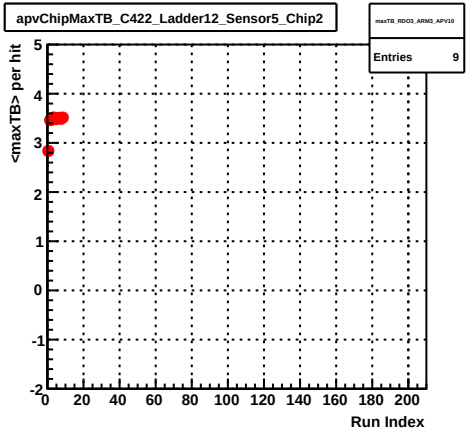

apvChipMaxTB_C417_Ladder12_Sensor4_Chip3
maxTB_RDO3_ARM3_APV15
Entries 9
<maxTB> per hit
Run Index

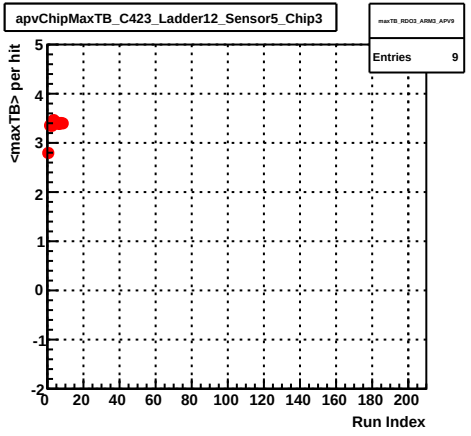

apvChipMaxTB_C418_Ladder12_Sensor4_Chip4
maxTB_RDO3_ARM3_APV14
Entries 9
<maxTB> per hit
Run Index

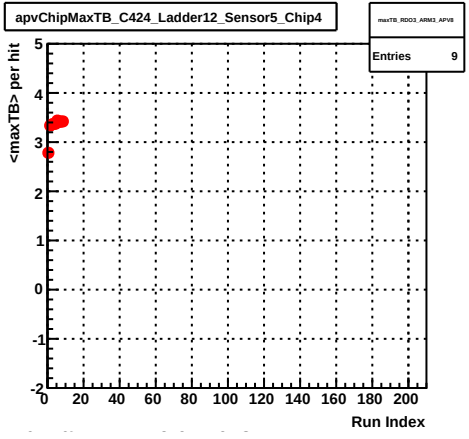

apvChipMaxTB_C419_Ladder12_Sensor4_Chip5
maxTB_RDO3_ARM3_APV13
Entries 9
<maxTB> per hit
Run Index

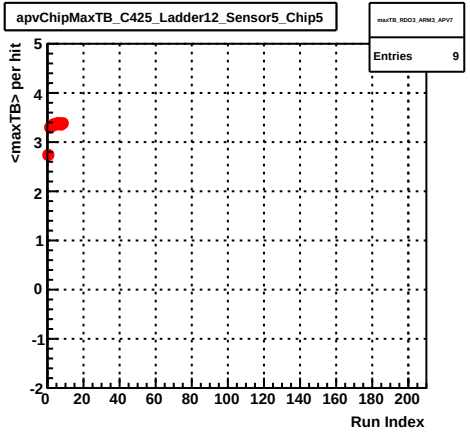

apvChipMaxTB_C420_Ladder12_Sensor4_Chip6
maxTB_RDO3_ARM3_APV12
Entries 9
<maxTB> per hit
Run Index

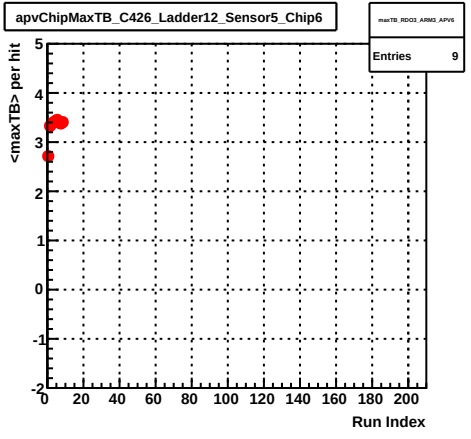

apvChipMaxTB_C421_Ladder12_Sensor5_Chip1
maxTB_RDO3_ARM3_APV11
Entries 9
<maxTB> per hit
Run Index

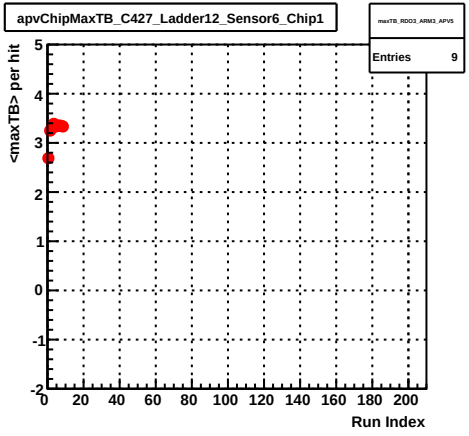

apvChipMaxTB_C422_Ladder12_Sensor5_Chip2
maxTB_RDO3_ARM3_APV10
Entries 9
<maxTB> per hit
Run Index

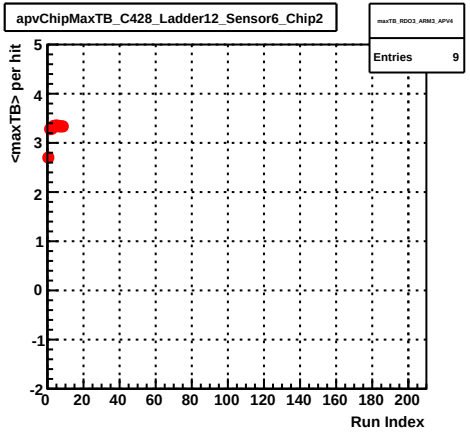

apvChipMaxTB_C423_Ladder12_Sensor5_Chip3
maxTB_RDO3_ARM3_APV9
Entries 9
<maxTB> per hit
Run Index

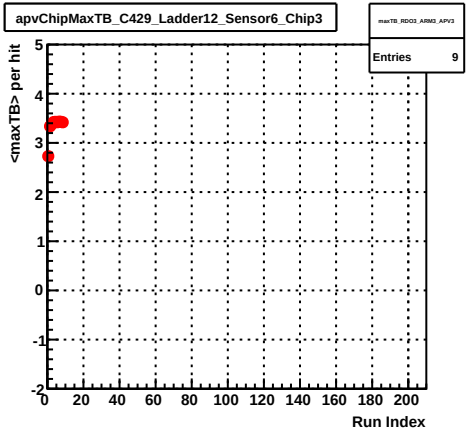

apvChipMaxTB_C424_Ladder12_Sensor5_Chip4
maxTB_RDO3_ARM3_APV8
Entries 9
<maxTB> per hit
Run Index

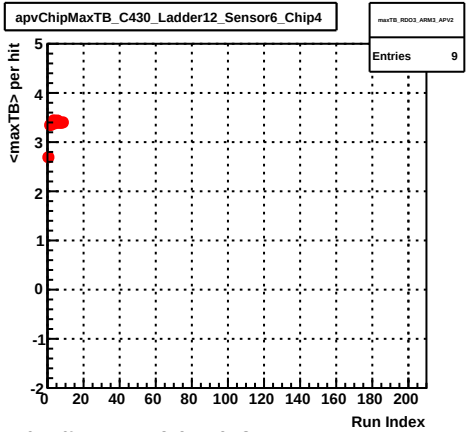

apvChipMaxTB_C425_Ladder12_Sensor5_Chip5
maxTB_RDO3_ARM3_APV7
Entries 9
<maxTB> per hit
Run Index

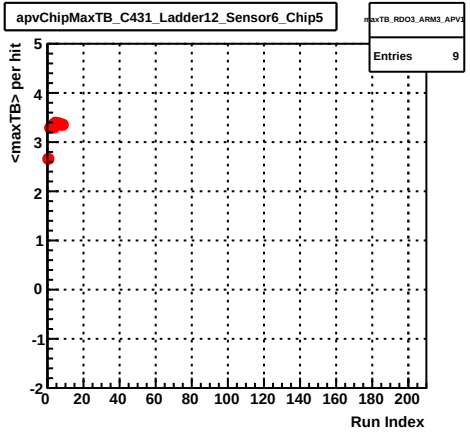

apvChipMaxTB_C426_Ladder12_Sensor5_Chip6
maxTB_RDO3_ARM3_APV6
Entries 9
<maxTB> per hit
Run Index

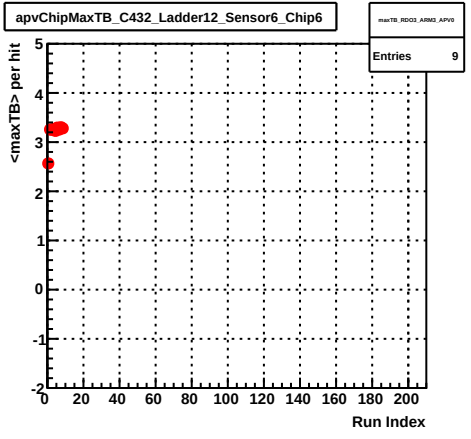

apvChipMaxTB_C427_Ladder12_Sensor6_Chip1
maxTB_RDO3_ARM3_APV5
Entries 9
<maxTB> per hit
Run Index

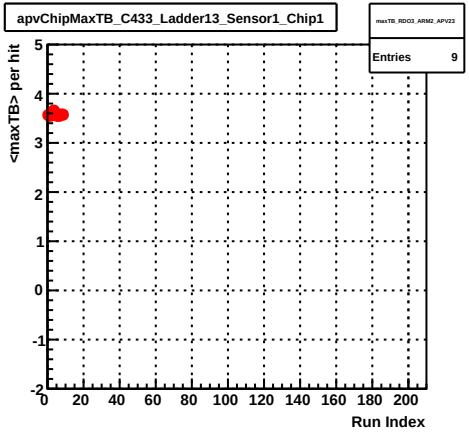

apvChipMaxTB_C428_Ladder12_Sensor6_Chip2
maxTB_RDO3_ARM3_APV4
Entries 9
<maxTB> per hit
Run Index

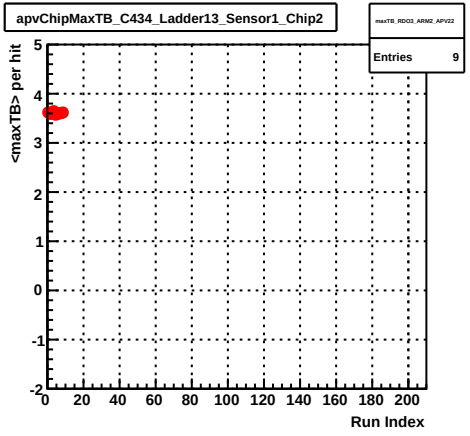

apvChipMaxTB_C429_Ladder12_Sensor6_Chip3
maxTB_RDO3_ARM3_APV3
Entries 9
<maxTB> per hit
Run Index

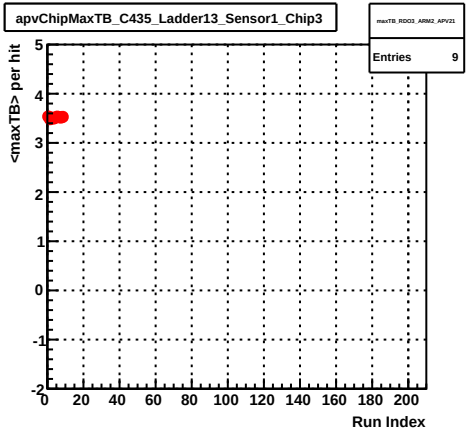

apvChipMaxTB_C430_Ladder12_Sensor6_Chip4
maxTB_RDO3_ARM3_APV2
Entries 9
<maxTB> per hit
Run Index

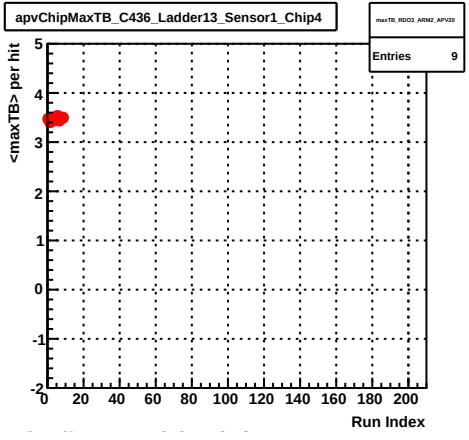

apvChipMaxTB_C431_Ladder12_Sensor6_Chip5
maxTB_RDO3_ARM3_APV1
Entries 9
<maxTB> per hit
Run Index

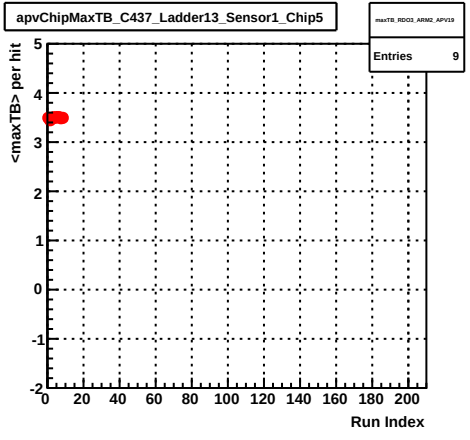

apvChipMaxTB_C432_Ladder12_Sensor6_Chip6
maxTB_RDO3_ARM3_APV0
Entries 9
<maxTB> per hit
Run Index

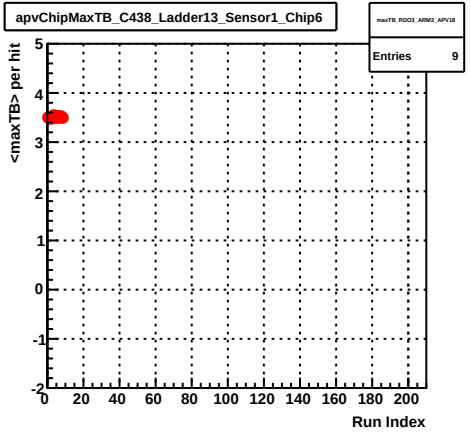

apvChipMaxTB_C433_Ladder13_Sensor1_Chip1
maxTB_RDO3_ARM2_APV23
Entries 9
<maxTB> per hit
Run Index

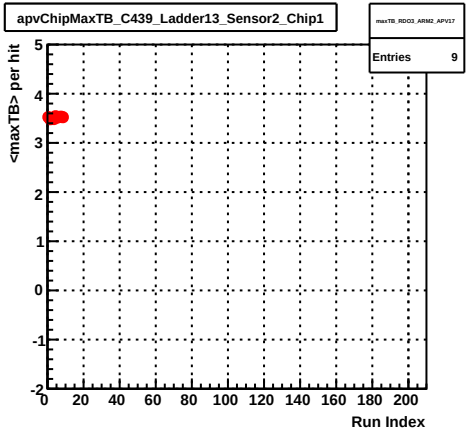

apvChipMaxTB_C434_Ladder13_Sensor1_Chip2
maxTB_RDO3_ARM2_APV22
Entries 9
<maxTB> per hit
Run Index

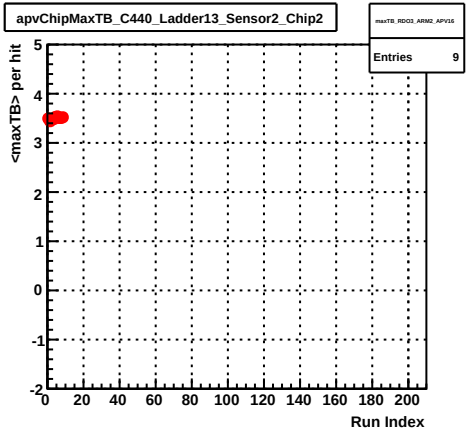

apvChipMaxTB_C435_Ladder13_Sensor1_Chip3
maxTB_RDO3_ARM2_APV21
Entries 9
<maxTB> per hit
Run Index

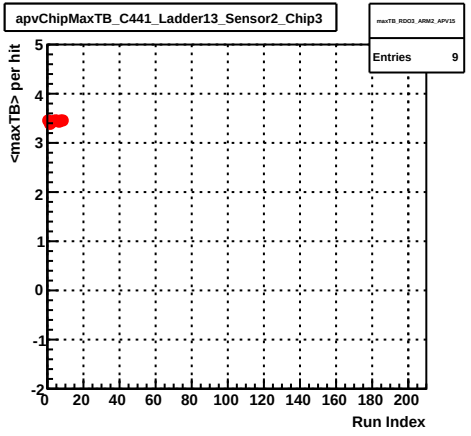

apvChipMaxTB_C436_Ladder13_Sensor1_Chip4
maxTB_RDO3_ARM2_APV20
Entries 9
<maxTB> per hit
Run Index

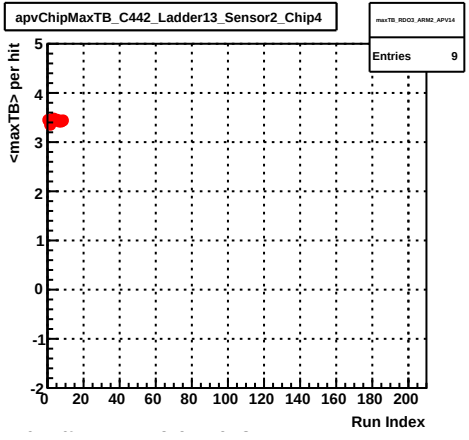

apvChipMaxTB_C437_Ladder13_Sensor1_Chip5
maxTB_RDO3_ARM2_APV19
Entries 9
<maxTB> per hit
Run Index

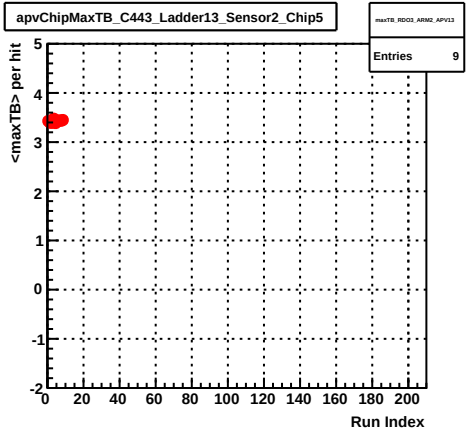

apvChipMaxTB_C438_Ladder13_Sensor1_Chip6
maxTB_RDO3_ARM2_APV18
Entries 9
<maxTB> per hit
Run Index

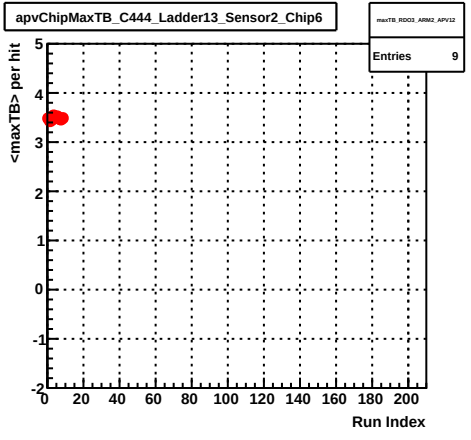

apvChipMaxTB_C439_Ladder13_Sensor2_Chip1
maxTB_RDO3_ARM2_APV17
Entries 9
<maxTB> per hit
Run Index

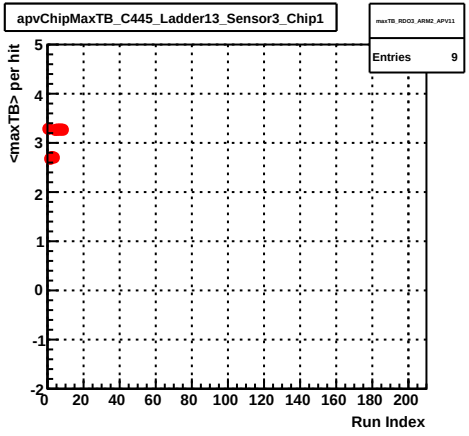

apvChipMaxTB_C440_Ladder13_Sensor2_Chip2
maxTB_RDO3_ARM2_APV16
Entries 9
<maxTB> per hit
Run Index

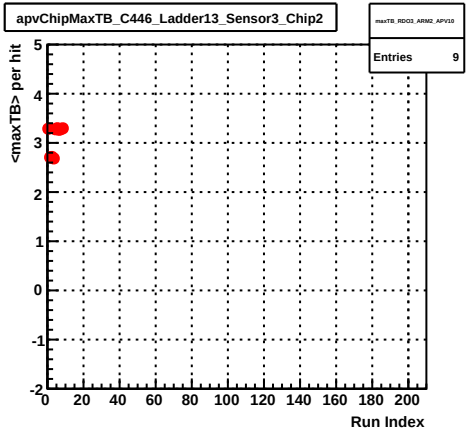

apvChipMaxTB_C441_Ladder13_Sensor2_Chip3
maxTB_RDO3_ARM2_APV15
Entries 9
<maxTB> per hit
Run Index

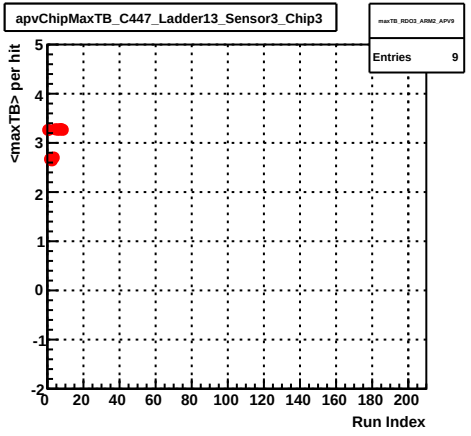

apvChipMaxTB_C442_Ladder13_Sensor2_Chip4
maxTB_RDO3_ARM2_APV14
Entries 9
<maxTB> per hit
Run Index

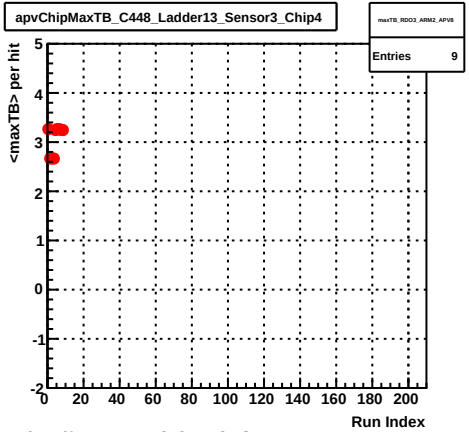

apvChipMaxTB_C443_Ladder13_Sensor2_Chip5
maxTB_RDO3_ARM2_APV13
Entries 9
<maxTB> per hit
Run Index

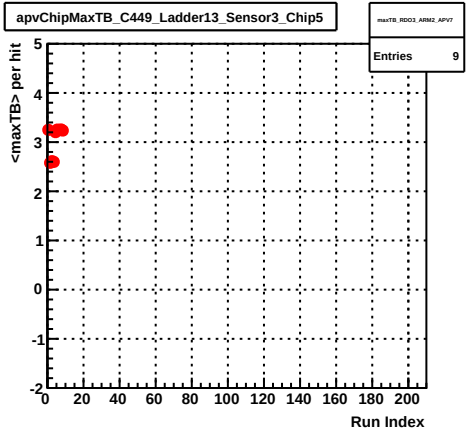

apvChipMaxTB_C444_Ladder13_Sensor2_Chip6
maxTB_RDO3_ARM2_APV12
Entries 9
<maxTB> per hit
Run Index

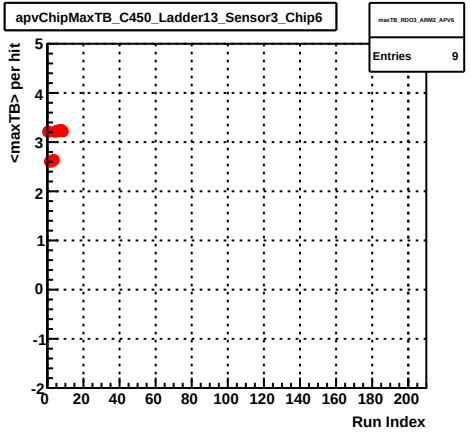

apvChipMaxTB_C445_Ladder13_Sensor3_Chip1
maxTB_RDO3_ARM2_APV11
Entries 9
<maxTB> per hit
Run Index

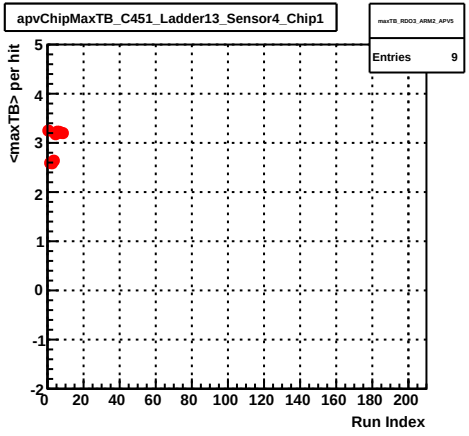

apvChipMaxTB_C446_Ladder13_Sensor3_Chip2
maxTB_RDO3_ARM2_APV10
Entries 9
<maxTB> per hit
Run Index

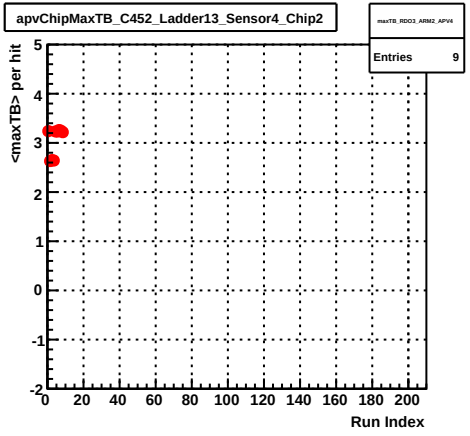

apvChipMaxTB_C447_Ladder13_Sensor3_Chip3
maxTB_RDO3_ARM2_APV9
Entries 9
<maxTB> per hit
Run Index

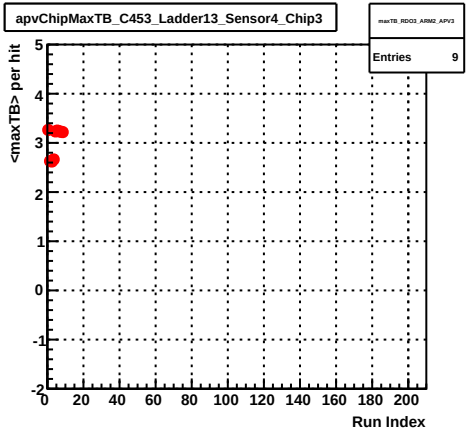

apvChipMaxTB_C448_Ladder13_Sensor3_Chip4
maxTB_RDO3_ARM2_APV8
Entries 9
<maxTB> per hit
Run Index

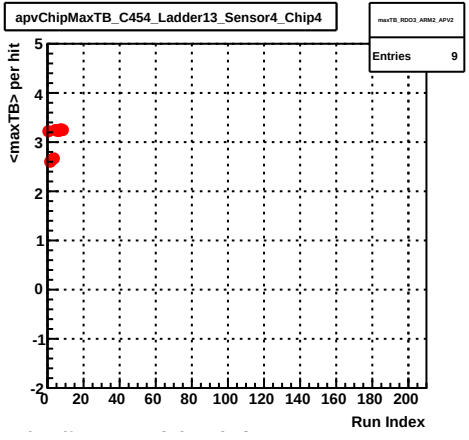

apvChipMaxTB_C449_Ladder13_Sensor3_Chip5
maxTB_RDO3_ARM2_APV7
Entries 9
<maxTB> per hit
Run Index

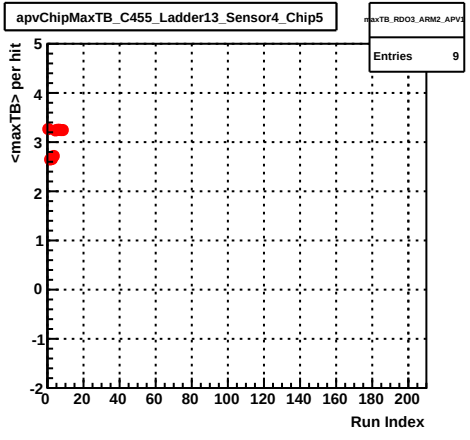

apvChipMaxTB_C450_Ladder13_Sensor3_Chip6
maxTB_RDO3_ARM2_APV6
Entries 9
<maxTB> per hit
Run Index

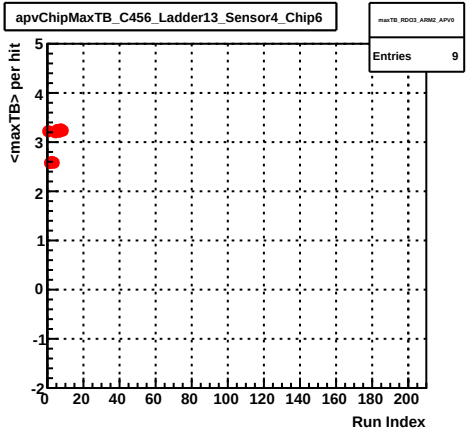

apvChipMaxTB_C451_Ladder13_Sensor4_Chip1
maxTB_RDO3_ARM2_APV5
Entries 9
<maxTB> per hit
Run Index

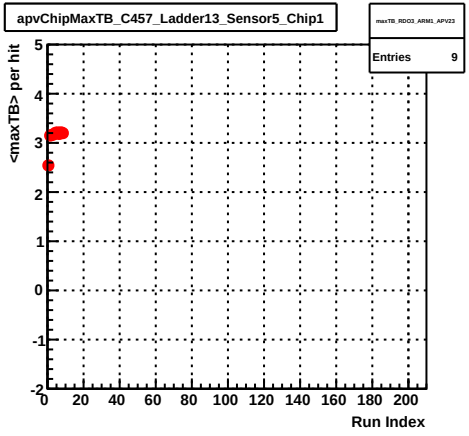

apvChipMaxTB_C452_Ladder13_Sensor4_Chip2
maxTB_RDO3_ARM2_APV4
Entries 9
<maxTB> per hit
Run Index

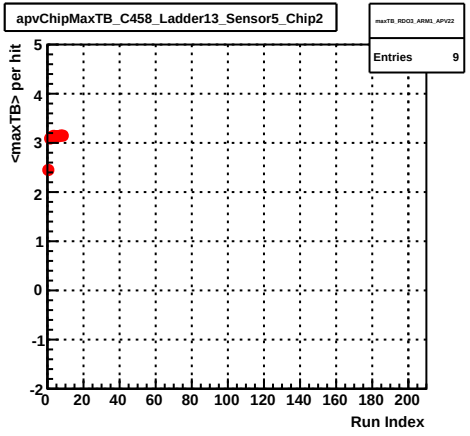

apvChipMaxTB_C453_Ladder13_Sensor4_Chip3
maxTB_RDO3_ARM2_APV3
Entries 9
<maxTB> per hit
Run Index

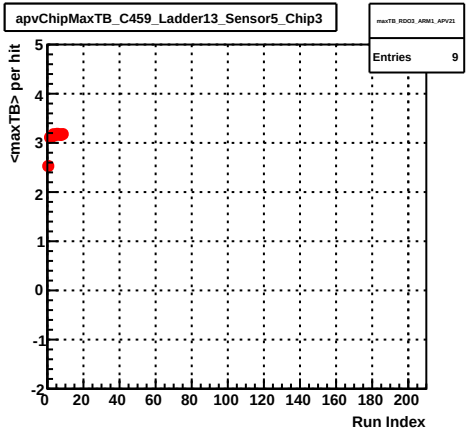

apvChipMaxTB_C454_Ladder13_Sensor4_Chip4
maxTB_RDO3_ARM2_APV2
Entries 9
<maxTB> per hit
Run Index

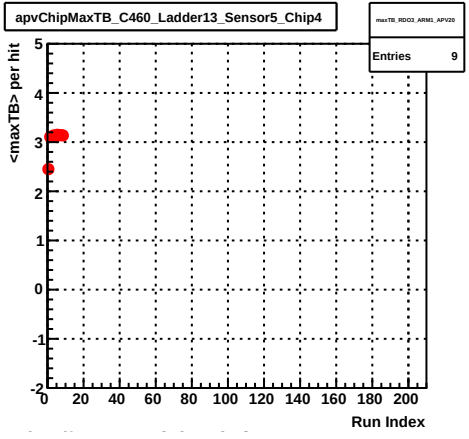

apvChipMaxTB_C455_Ladder13_Sensor4_Chip5
maxTB_RDO3_ARM2_APV1
Entries 9
<maxTB> per hit
Run Index

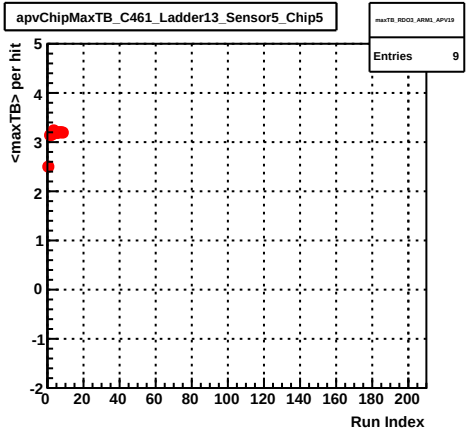

apvChipMaxTB_C456_Ladder13_Sensor4_Chip6
maxTB_RDO3_ARM2_APV0
Entries 9
<maxTB> per hit
Run Index

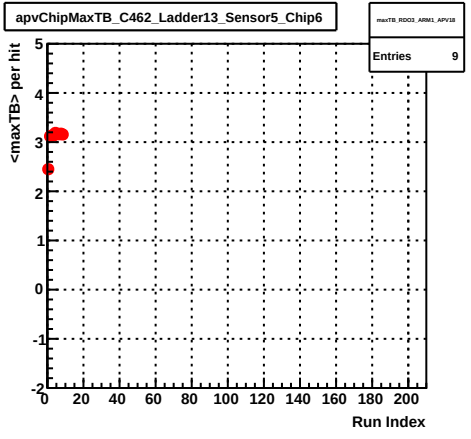

apvChipMaxTB_C457_Ladder13_Sensor5_Chip1
maxTB_RDO3_ARM1_APV23
Entries 9
<maxTB> per hit
Run Index

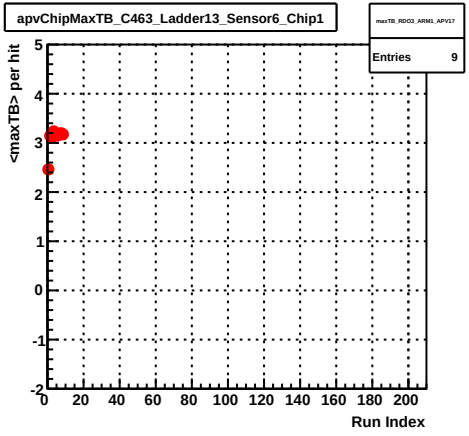

apvChipMaxTB_C458_Ladder13_Sensor5_Chip2
maxTB_RDO3_ARM1_APV22
Entries 9
<maxTB> per hit
Run Index

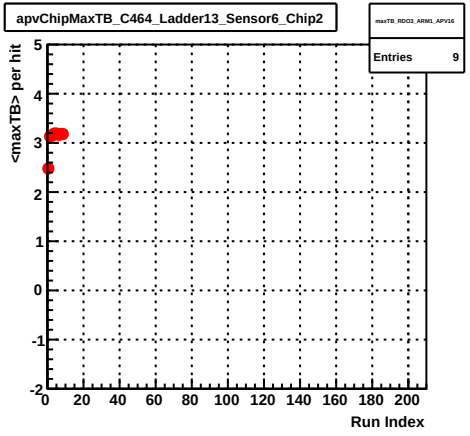

apvChipMaxTB_C459_Ladder13_Sensor5_Chip3
maxTB_RDO3_ARM1_APV21
Entries 9
<maxTB> per hit
Run Index

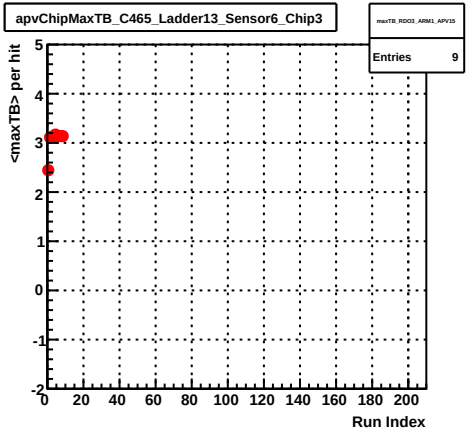

apvChipMaxTB_C460_Ladder13_Sensor5_Chip4
maxTB_RDO3_ARM1_APV20
Entries 9
<maxTB> per hit
Run Index

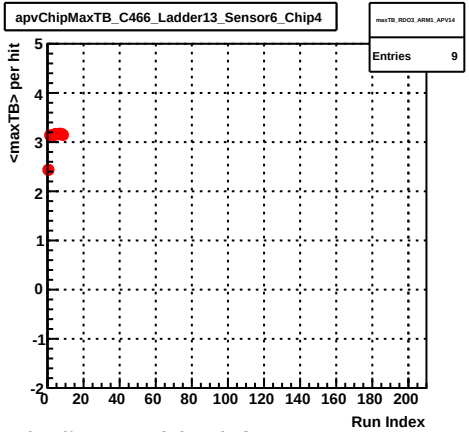

apvChipMaxTB_C461_Ladder13_Sensor5_Chip5
maxTB_RDO3_ARM1_APV19
Entries 9
<maxTB> per hit
Run Index

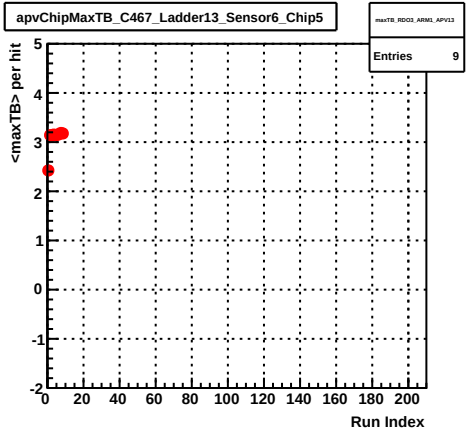

apvChipMaxTB_C462_Ladder13_Sensor5_Chip6
maxTB_RDO3_ARM1_APV18
Entries 9
<maxTB> per hit
Run Index

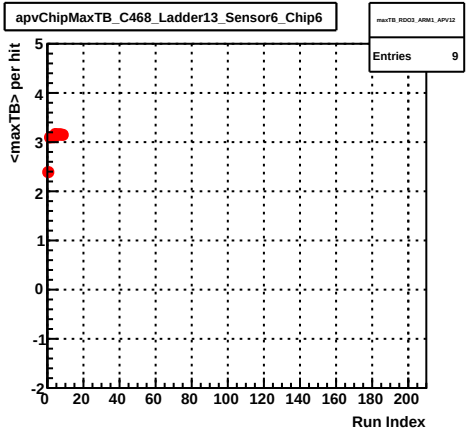

apvChipMaxTB_C463_Ladder13_Sensor6_Chip1
maxTB_RDO3_ARM1_APV17
Entries 9
<maxTB> per hit
Run Index

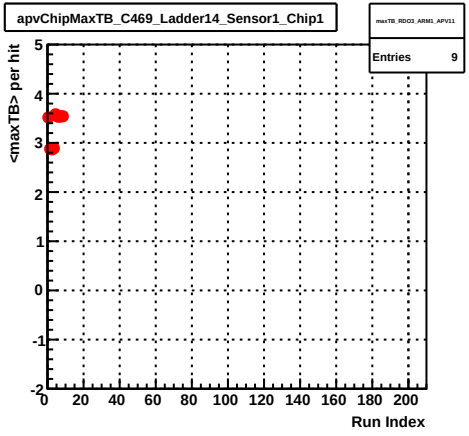

apvChipMaxTB_C464_Ladder13_Sensor6_Chip2
maxTB_RDO3_ARM1_APV16
Entries 9
<maxTB> per hit
Run Index

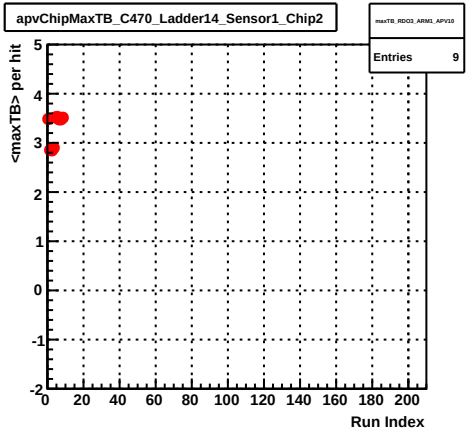

apvChipMaxTB_C465_Ladder13_Sensor6_Chip3
maxTB_RDO3_ARM1_APV15
Entries 9
<maxTB> per hit
Run Index

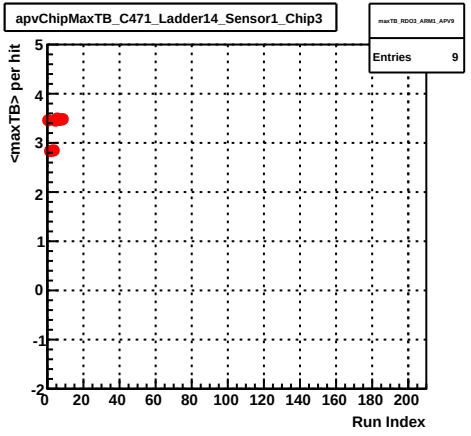

apvChipMaxTB_C466_Ladder13_Sensor6_Chip4
maxTB_RDO3_ARM1_APV14
Entries 9
<maxTB> per hit
Run Index

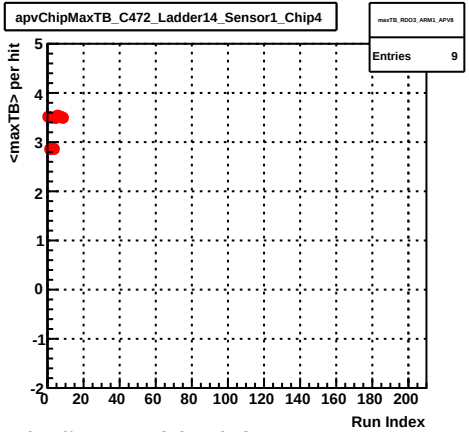

apvChipMaxTB_C467_Ladder13_Sensor6_Chip5
maxTB_RDO3_ARM1_APV13
Entries 9
<maxTB> per hit
Run Index

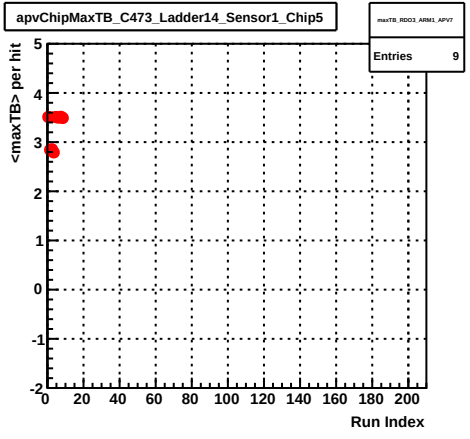

apvChipMaxTB_C468_Ladder13_Sensor6_Chip6
maxTB_RDO3_ARM1_APV12
Entries 9
<maxTB> per hit
Run Index

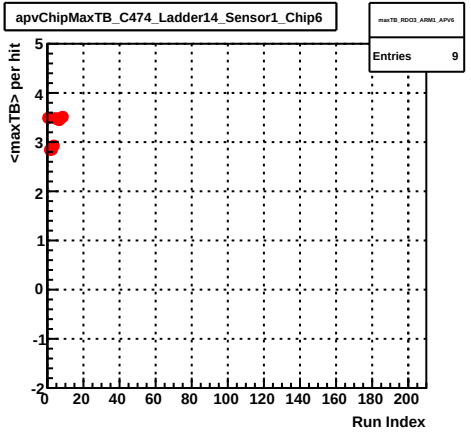

apvChipMaxTB_C469_Ladder14_Sensor1_Chip1
maxTB_RDO3_ARM1_APV11
Entries 9
<maxTB> per hit
Run Index

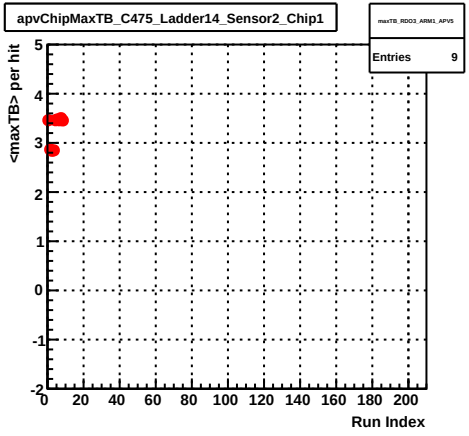

apvChipMaxTB_C470_Ladder14_Sensor1_Chip2
maxTB_RDO3_ARM1_APV10
Entries 9
<maxTB> per hit
Run Index

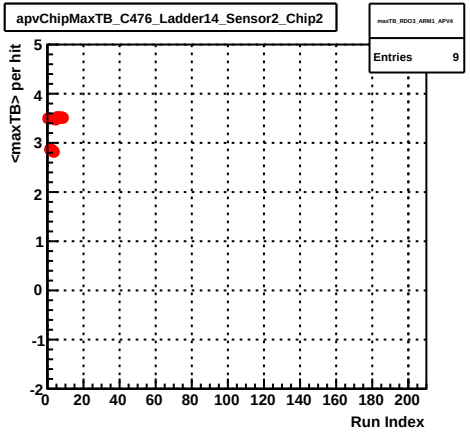

apvChipMaxTB_C471_Ladder14_Sensor1_Chip3
maxTB_RDO3_ARM1_APV9
Entries 9
<maxTB> per hit
Run Index

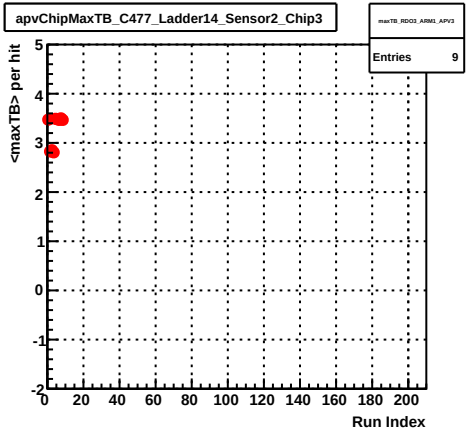

apvChipMaxTB_C472_Ladder14_Sensor1_Chip4
maxTB_RDO3_ARM1_APV8
Entries 9
<maxTB> per hit
Run Index

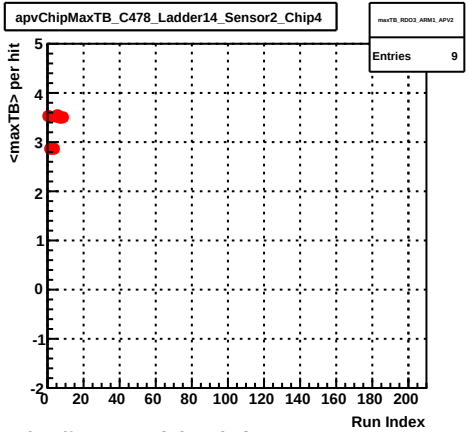

apvChipMaxTB_C473_Ladder14_Sensor1_Chip5
maxTB_RDO3_ARM1_APV7
Entries 9
<maxTB> per hit
Run Index

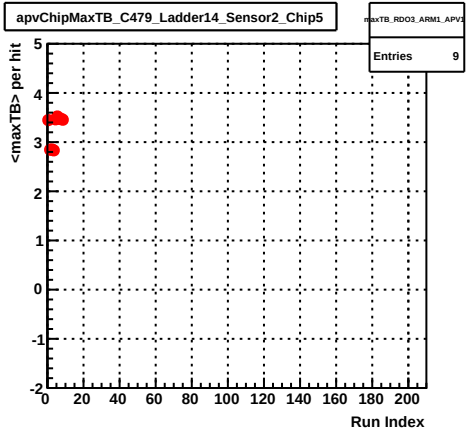

apvChipMaxTB_C474_Ladder14_Sensor1_Chip6
maxTB_RDO3_ARM1_APV6
Entries 9
<maxTB> per hit
Run Index

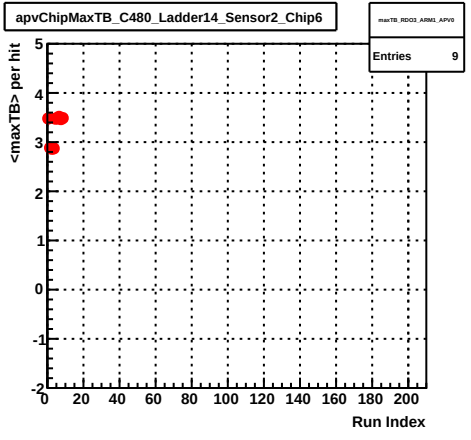

apvChipMaxTB_C475_Ladder14_Sensor2_Chip1
maxTB_RDO3_ARM1_APV5
Entries 9
<maxTB> per hit
Run Index

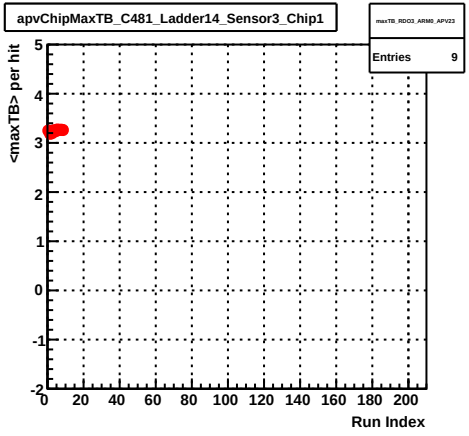

apvChipMaxTB_C476_Ladder14_Sensor2_Chip2
maxTB_RDO3_ARM1_APV4
Entries 9
<maxTB> per hit
Run Index

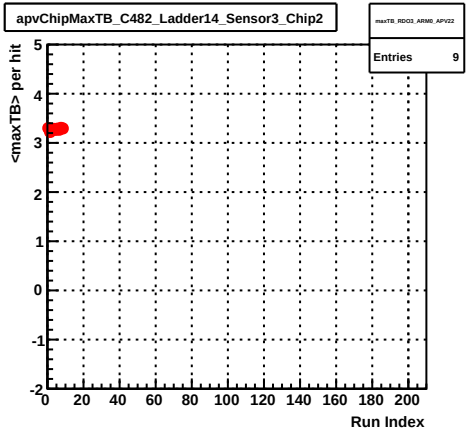

apvChipMaxTB_C477_Ladder14_Sensor2_Chip3
maxTB_RDO3_ARM1_APV3
Entries 9
<maxTB> per hit
Run Index

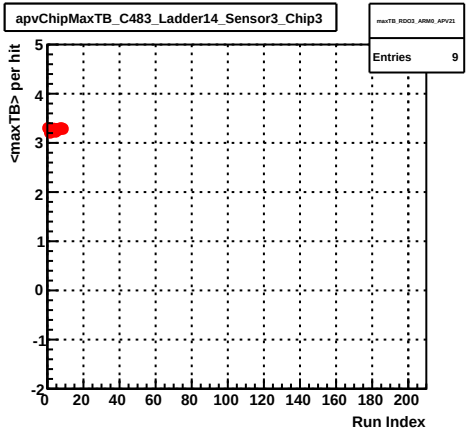

apvChipMaxTB_C478_Ladder14_Sensor2_Chip4
maxTB_RDO3_ARM1_APV2
Entries 9
<maxTB> per hit
Run Index

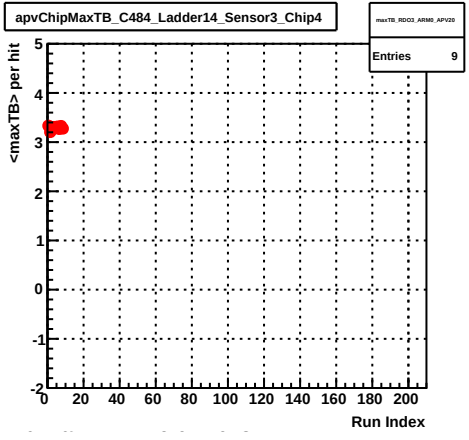

apvChipMaxTB_C479_Ladder14_Sensor2_Chip5
maxTB_RDO3_ARM1_APV1
Entries 9
<maxTB> per hit
Run Index

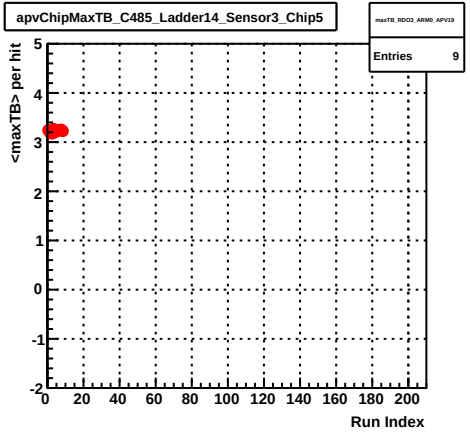

apvChipMaxTB_C480_Ladder14_Sensor2_Chip6
maxTB_RDO3_ARM1_APV0
Entries 9
<maxTB> per hit
Run Index

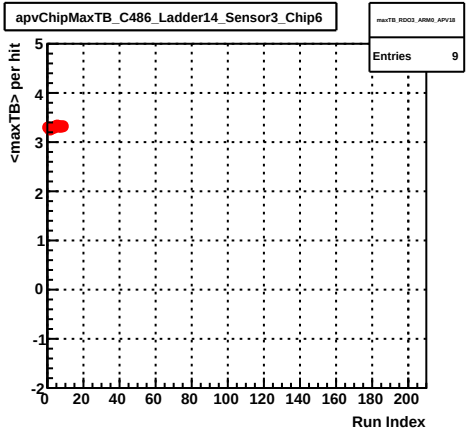

apvChipMaxTB_C481_Ladder14_Sensor3_Chip1
maxTB_RDO3_ARM0_APV23
Entries 9
<maxTB> per hit
Run Index

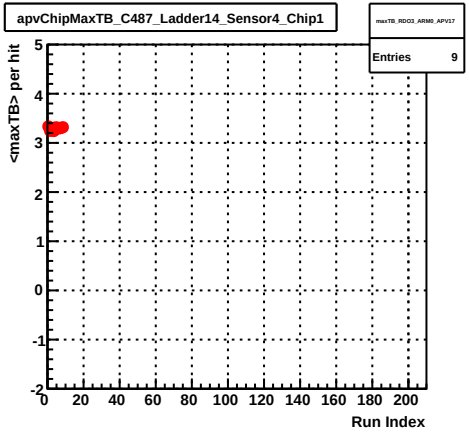

apvChipMaxTB_C482_Ladder14_Sensor3_Chip2
maxTB_RDO3_ARM0_APV22
Entries 9
<maxTB> per hit
Run Index

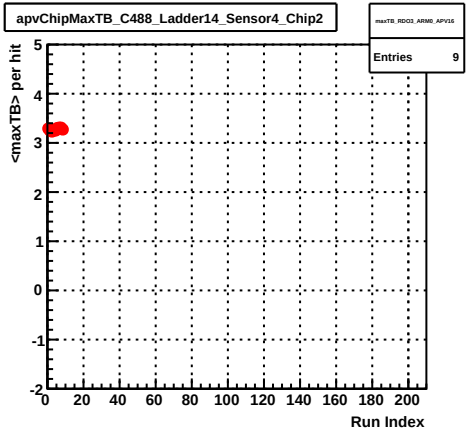

apvChipMaxTB_C483_Ladder14_Sensor3_Chip3
maxTB_RDO3_ARM0_APV21
Entries 9
<maxTB> per hit
Run Index

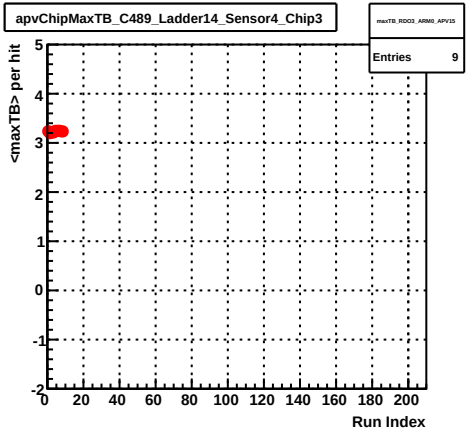

apvChipMaxTB_C484_Ladder14_Sensor3_Chip4
maxTB_RDO3_ARM0_APV20
Entries 9
<maxTB> per hit
Run Index

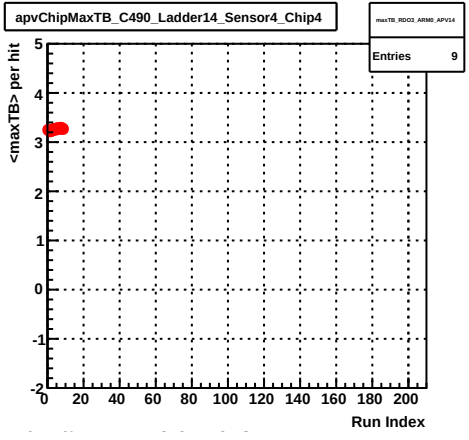

apvChipMaxTB_C485_Ladder14_Sensor3_Chip5
maxTB_RDO3_ARM0_APV19
Entries 9
<maxTB> per hit
Run Index

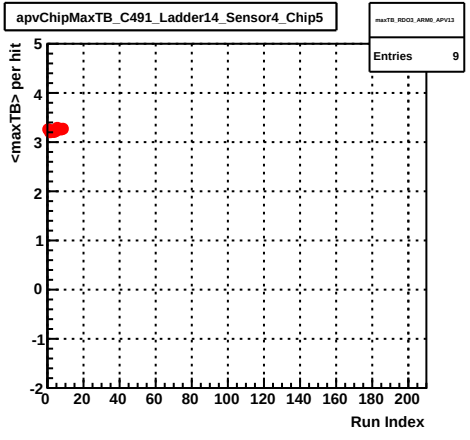

apvChipMaxTB_C486_Ladder14_Sensor3_Chip6
maxTB_RDO3_ARM0_APV18
Entries 9
<maxTB> per hit
Run Index

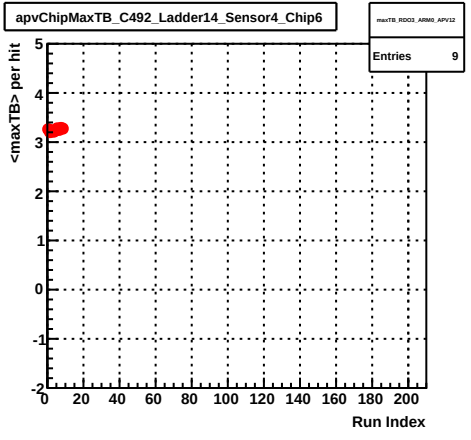

apvChipMaxTB_C487_Ladder14_Sensor4_Chip1
maxTB_RDO3_ARM0_APV17
Entries 9
<maxTB> per hit
Run Index

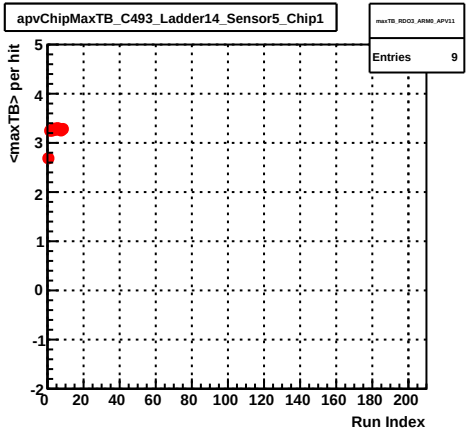

apvChipMaxTB_C488_Ladder14_Sensor4_Chip2
maxTB_RDO3_ARM0_APV16
Entries 9
<maxTB> per hit
Run Index

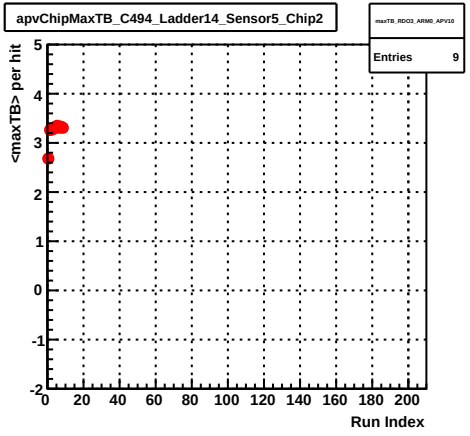

apvChipMaxTB_C489_Ladder14_Sensor4_Chip3
maxTB_RDO3_ARM0_APV15
Entries 9
<maxTB> per hit
Run Index

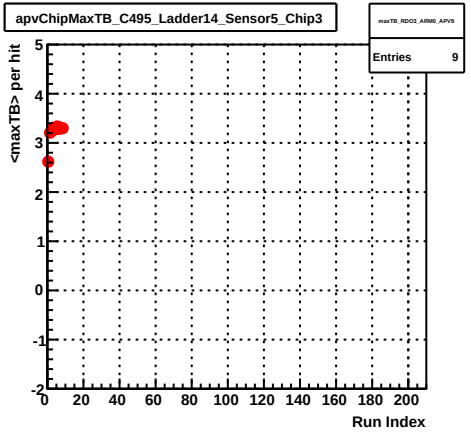

apvChipMaxTB_C490_Ladder14_Sensor4_Chip4
maxTB_RDO3_ARM0_APV14
Entries 9
<maxTB> per hit
Run Index

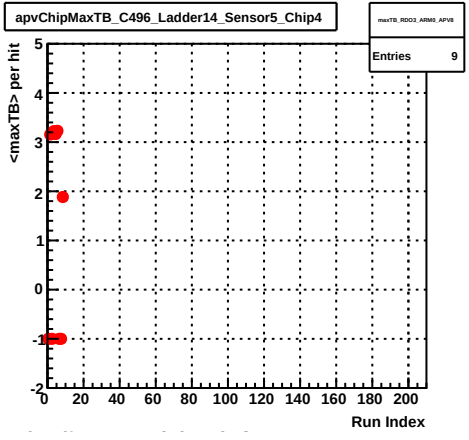

apvChipMaxTB_C491_Ladder14_Sensor4_Chip5
maxTB_RDO3_ARM0_APV13
Entries 9
<maxTB> per hit
Run Index

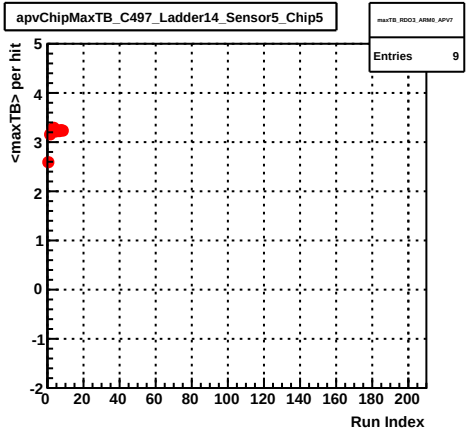

apvChipMaxTB_C492_Ladder14_Sensor4_Chip6
maxTB_RDO3_ARM0_APV12
Entries 9
<maxTB> per hit
Run Index

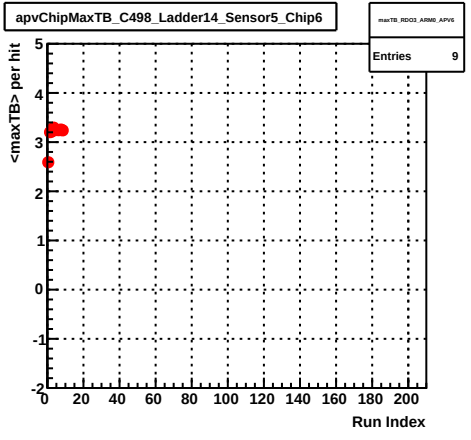

apvChipMaxTB_C493_Ladder14_Sensor5_Chip1
maxTB_RDO3_ARM0_APV11
Entries 9
<maxTB> per hit
Run Index

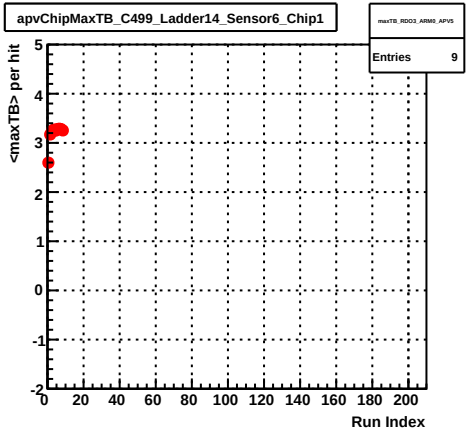

apvChipMaxTB_C494_Ladder14_Sensor5_Chip2
maxTB_RDO3_ARM0_APV10
Entries 9
<maxTB> per hit
Run Index

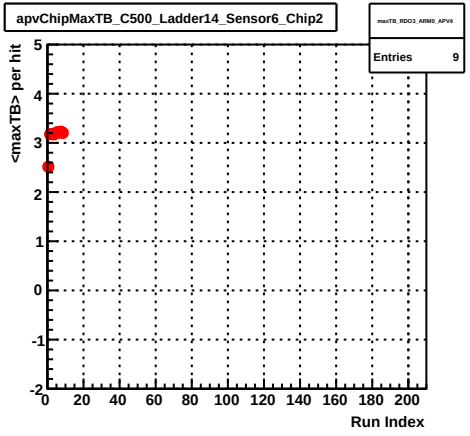

apvChipMaxTB_C495_Ladder14_Sensor5_Chip3
maxTB_RDO3_ARM0_APV9
Entries 9
<maxTB> per hit
Run Index

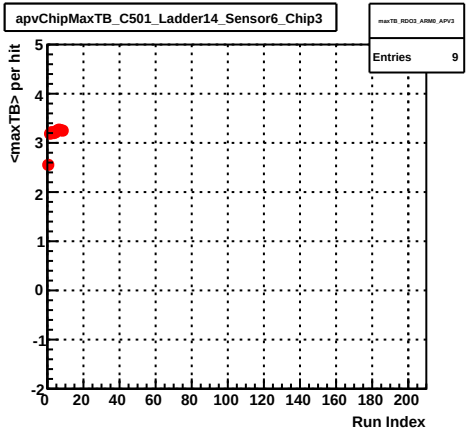

apvChipMaxTB_C496_Ladder14_Sensor5_Chip4
maxTB_RDO3_ARM0_APV8
Entries 9
<maxTB> per hit
Run Index

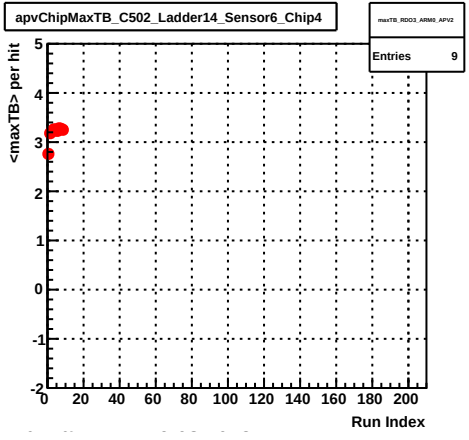

apvChipMaxTB_C497_Ladder14_Sensor5_Chip5
maxTB_RDO3_ARM0_APV7
Entries 9
<maxTB> per hit
Run Index

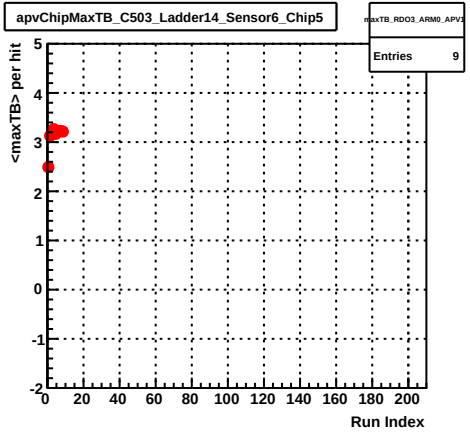

apvChipMaxTB_C498_Ladder14_Sensor5_Chip6
maxTB_RDO3_ARM0_APV6
Entries 9
<maxTB> per hit
Run Index

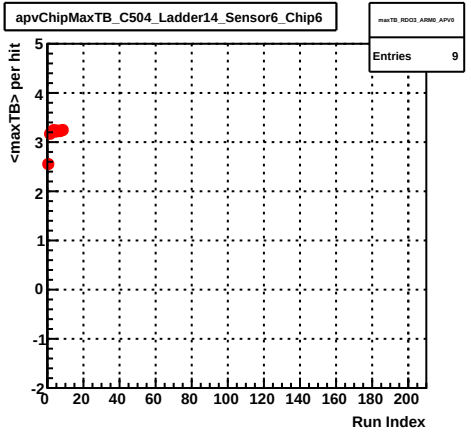

apvChipMaxTB_C499_Ladder14_Sensor6_Chip1
maxTB_RDO3_ARM0_APV5
Entries 9
<maxTB> per hit
Run Index

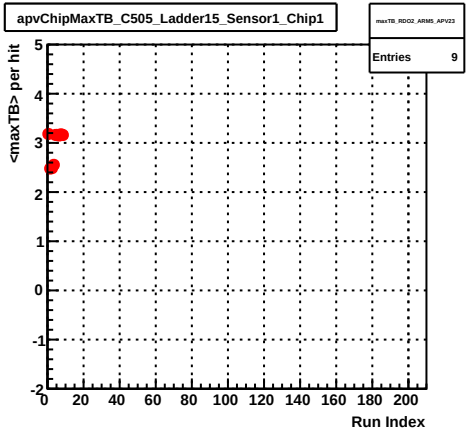

apvChipMaxTB_C500_Ladder14_Sensor6_Chip2
maxTB_RDO3_ARM0_APV4
Entries 9
<maxTB> per hit
Run Index

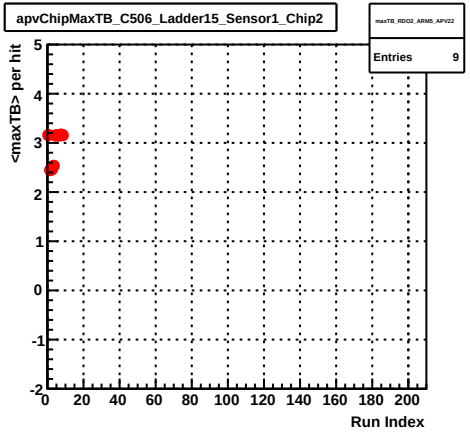

apvChipMaxTB_C501_Ladder14_Sensor6_Chip3
maxTB_RDO3_ARM0_APV3
Entries 9
<maxTB> per hit
Run Index

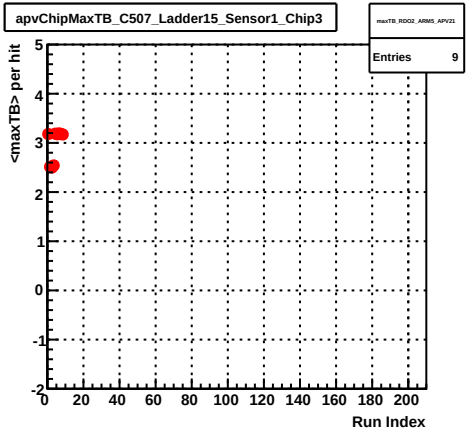

apvChipMaxTB_C502_Ladder14_Sensor6_Chip4
maxTB_RDO3_ARM0_APV2
Entries 9
<maxTB> per hit
Run Index

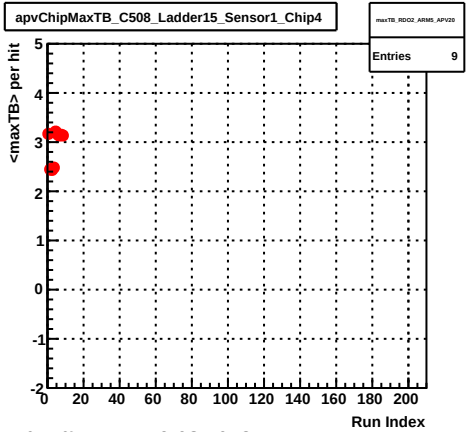

apvChipMaxTB_C503_Ladder14_Sensor6_Chip5
maxTB_RDO3_ARM0_APV1
Entries 9
<maxTB> per hit
Run Index

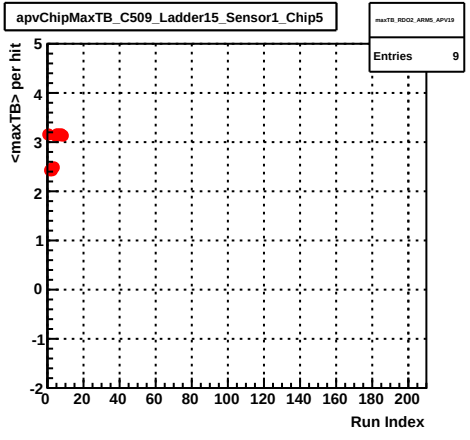

apvChipMaxTB_C504_Ladder14_Sensor6_Chip6
maxTB_RDO3_ARM0_APV0
Entries 9
<maxTB> per hit
Run Index

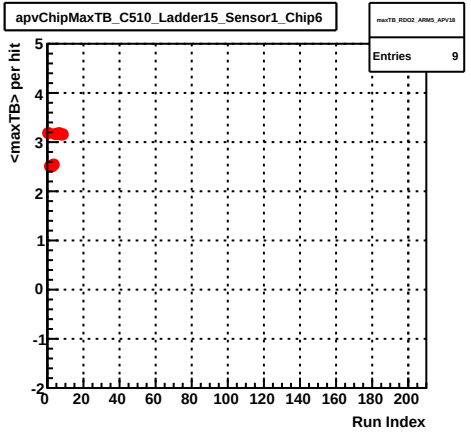

apvChipMaxTB_C505_Ladder15_Sensor1_Chip1
maxTB_RDO2_ARM5_APV23
Entries 9
<maxTB> per hit
Run Index

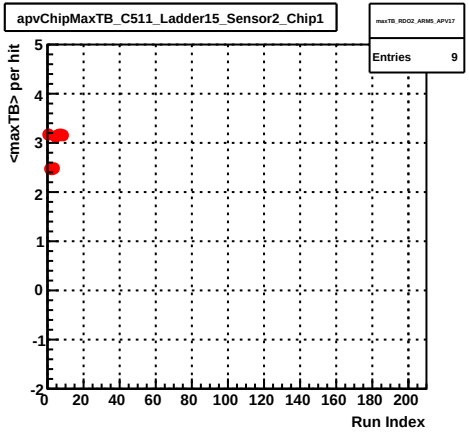

apvChipMaxTB_C506_Ladder15_Sensor1_Chip2
maxTB_RDO2_ARM5_APV22
Entries 9
<maxTB> per hit
Run Index

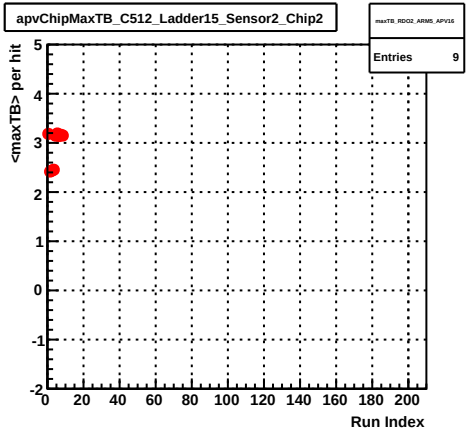

apvChipMaxTB_C507_Ladder15_Sensor1_Chip3
maxTB_RDO2_ARM5_APV21
Entries 9
<maxTB> per hit
Run Index

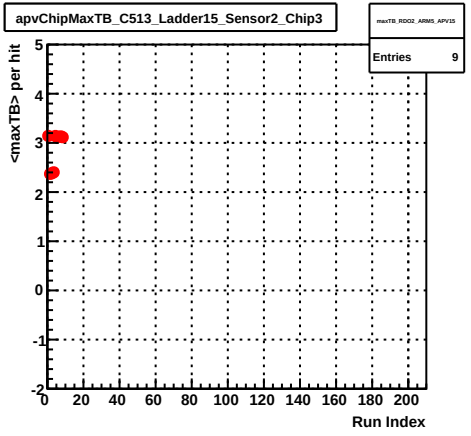

apvChipMaxTB_C508_Ladder15_Sensor1_Chip4
maxTB_RDO2_ARM5_APV20
Entries 9
<maxTB> per hit
Run Index

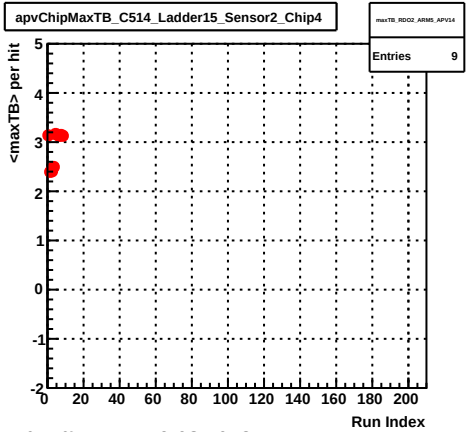

apvChipMaxTB_C509_Ladder15_Sensor1_Chip5
maxTB_RDO2_ARM5_APV19
Entries 9
<maxTB> per hit
Run Index

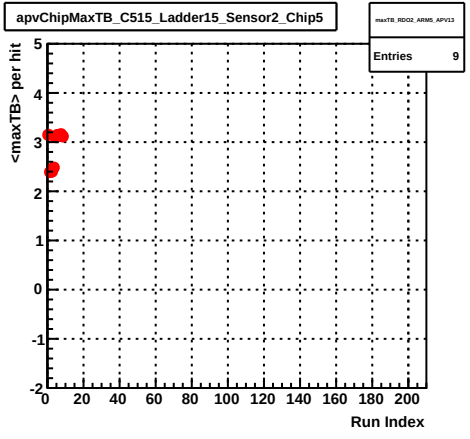

apvChipMaxTB_C510_Ladder15_Sensor1_Chip6
maxTB_RDO2_ARM5_APV18
Entries 9
<maxTB> per hit
Run Index

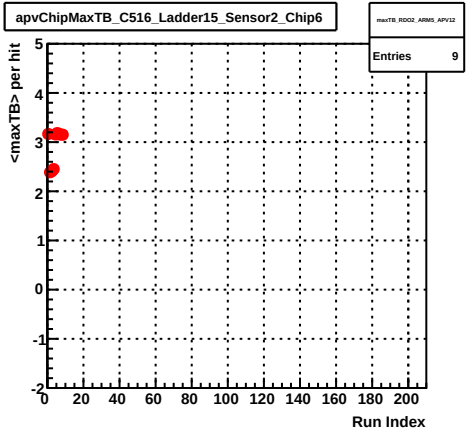

apvChipMaxTB_C511_Ladder15_Sensor2_Chip1
maxTB_RDO2_ARM5_APV17
Entries 9
<maxTB> per hit
Run Index

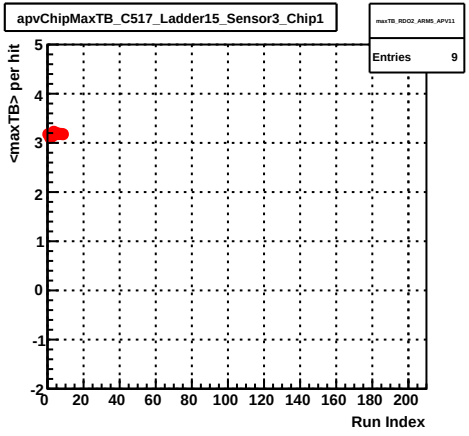

apvChipMaxTB_C512_Ladder15_Sensor2_Chip2
maxTB_RDO2_ARM5_APV16
Entries 9
<maxTB> per hit
Run Index

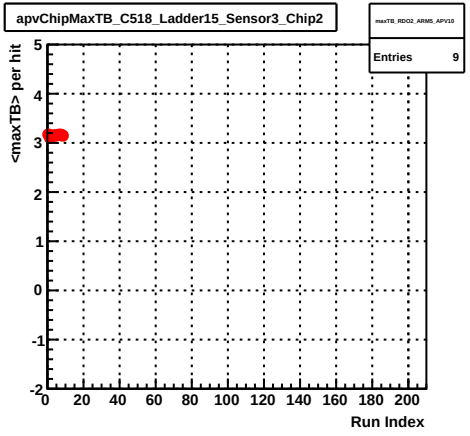

apvChipMaxTB_C513_Ladder15_Sensor2_Chip3
maxTB_RDO2_ARM5_APV15
Entries 9
<maxTB> per hit
Run Index

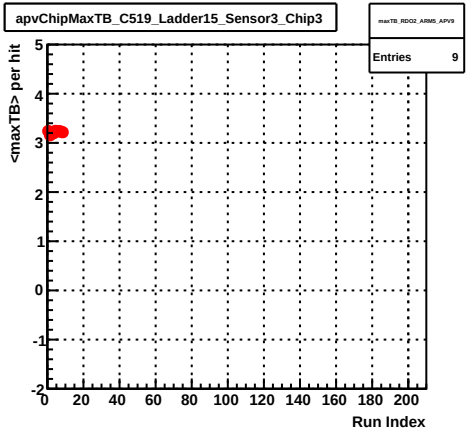

apvChipMaxTB_C514_Ladder15_Sensor2_Chip4
maxTB_RDO2_ARM5_APV14
Entries 9
<maxTB> per hit
Run Index

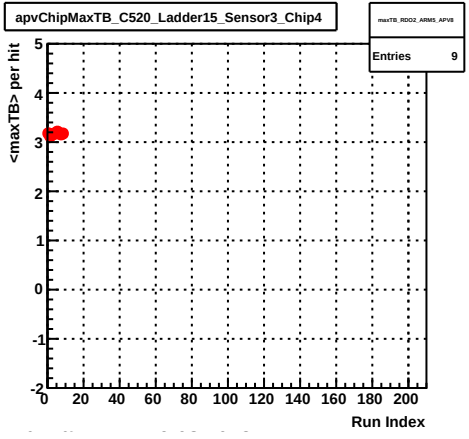

apvChipMaxTB_C515_Ladder15_Sensor2_Chip5
maxTB_RDO2_ARM5_APV13
Entries 9
<maxTB> per hit
Run Index

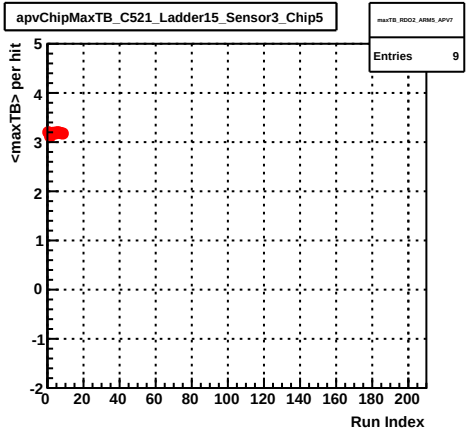

apvChipMaxTB_C516_Ladder15_Sensor2_Chip6
maxTB_RDO2_ARM5_APV12
Entries 9
<maxTB> per hit
Run Index

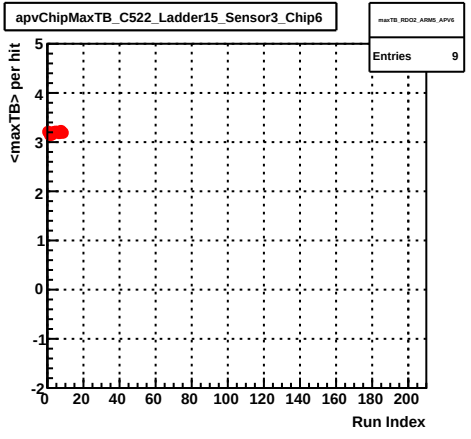

apvChipMaxTB_C517_Ladder15_Sensor3_Chip1
maxTB_RDO2_ARM5_APV11
Entries 9
<maxTB> per hit
Run Index

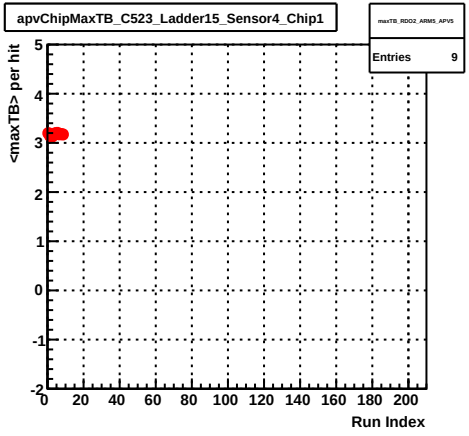

apvChipMaxTB_C518_Ladder15_Sensor3_Chip2
maxTB_RDO2_ARM5_APV10
Entries 9
<maxTB> per hit
Run Index

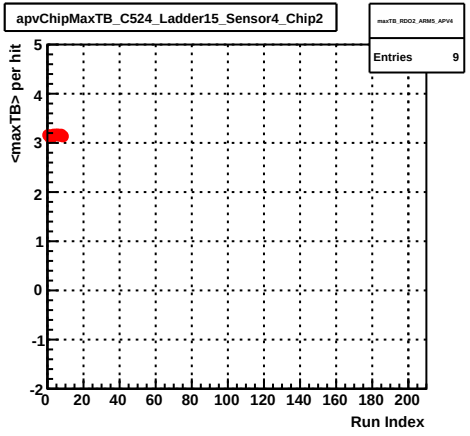

apvChipMaxTB_C519_Ladder15_Sensor3_Chip3
maxTB_RDO2_ARM5_APV9
Entries 9
<maxTB> per hit
Run Index

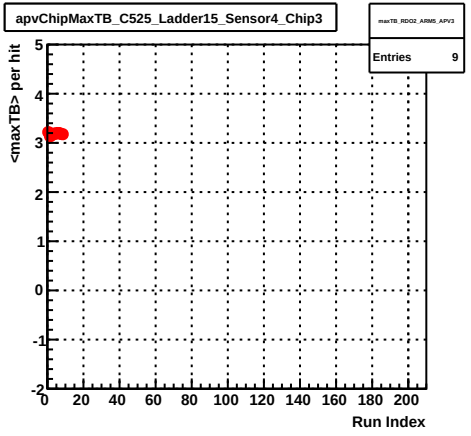

apvChipMaxTB_C520_Ladder15_Sensor3_Chip4
maxTB_RDO2_ARM5_APV8
Entries 9
<maxTB> per hit
Run Index

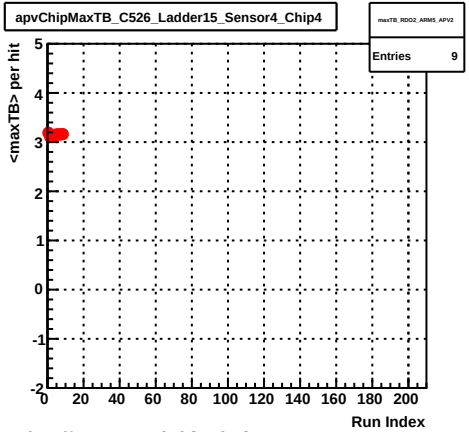

apvChipMaxTB_C521_Ladder15_Sensor3_Chip5
maxTB_RDO2_ARM5_APV7
Entries 9
<maxTB> per hit
Run Index

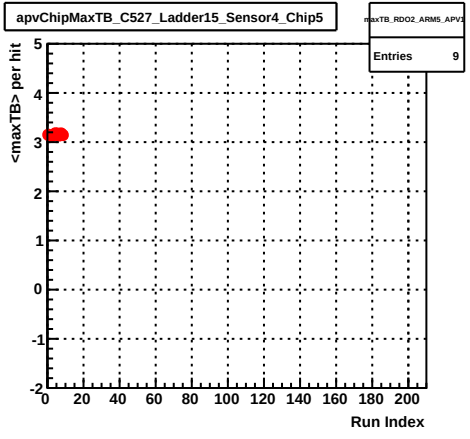

apvChipMaxTB_C522_Ladder15_Sensor3_Chip6
maxTB_RDO2_ARM5_APV6
Entries 9
<maxTB> per hit
Run Index

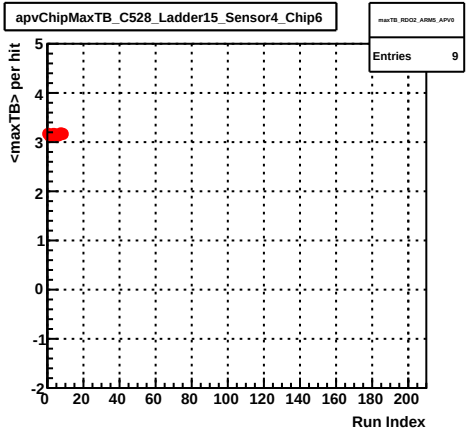

apvChipMaxTB_C523_Ladder15_Sensor4_Chip1
maxTB_RDO2_ARM5_APV5
Entries 9
<maxTB> per hit
Run Index

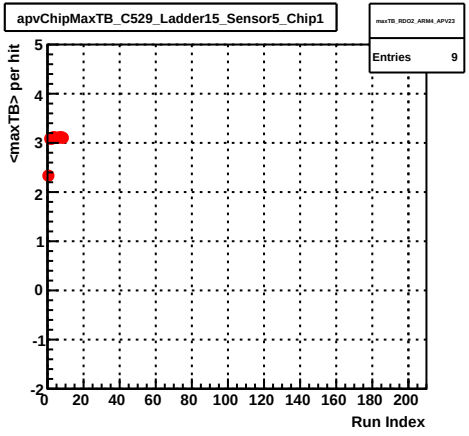

apvChipMaxTB_C524_Ladder15_Sensor4_Chip2
maxTB_RDO2_ARM5_APV4
Entries 9
<maxTB> per hit
Run Index

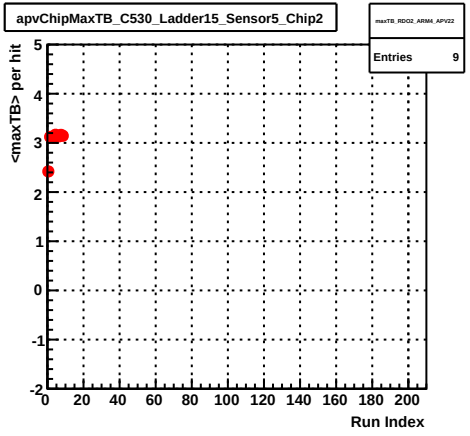

apvChipMaxTB_C525_Ladder15_Sensor4_Chip3
maxTB_RDO2_ARM5_APV3
Entries 9
<maxTB> per hit
Run Index

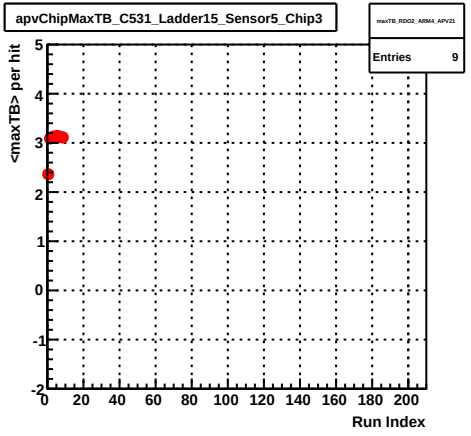

apvChipMaxTB_C526_Ladder15_Sensor4_Chip4
maxTB_RDO2_ARM5_APV2
Entries 9
<maxTB> per hit
Run Index

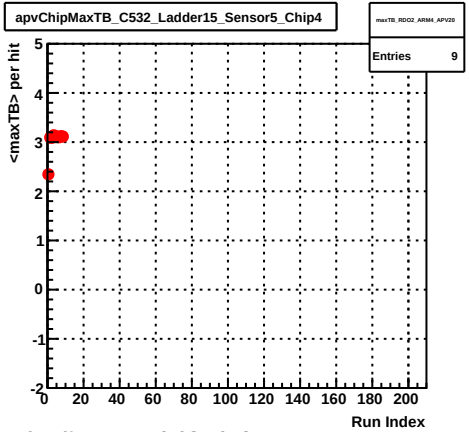

apvChipMaxTB_C527_Ladder15_Sensor4_Chip5
maxTB_RDO2_ARM5_APV1
Entries 9
<maxTB> per hit
Run Index

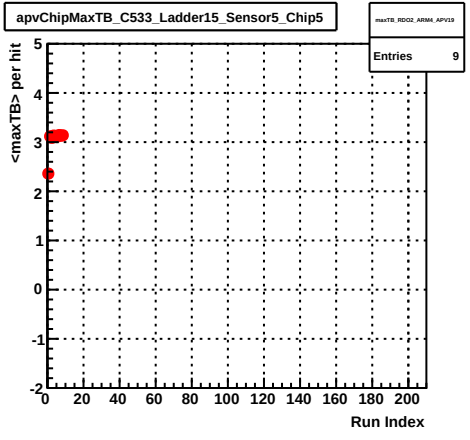

apvChipMaxTB_C528_Ladder15_Sensor4_Chip6
maxTB_RDO2_ARM5_APV0
Entries 9
<maxTB> per hit
Run Index

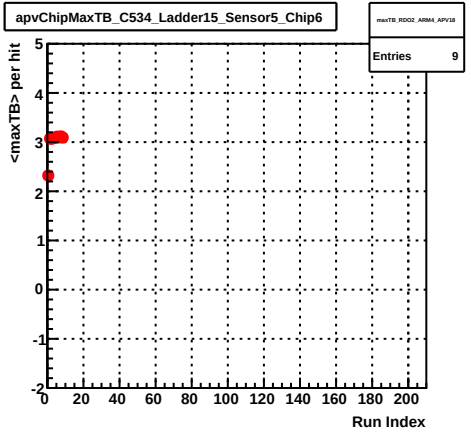

apvChipMaxTB_C529_Ladder15_Sensor5_Chip1
maxTB_RDO2_ARM4_APV23
Entries 9
<maxTB> per hit
Run Index

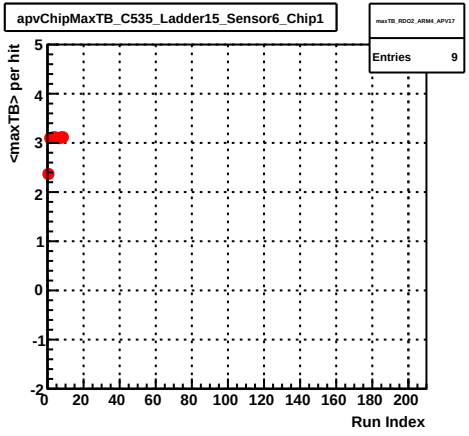

apvChipMaxTB_C530_Ladder15_Sensor5_Chip2
maxTB_RDO2_ARM4_APV22
Entries 9
<maxTB> per hit
Run Index

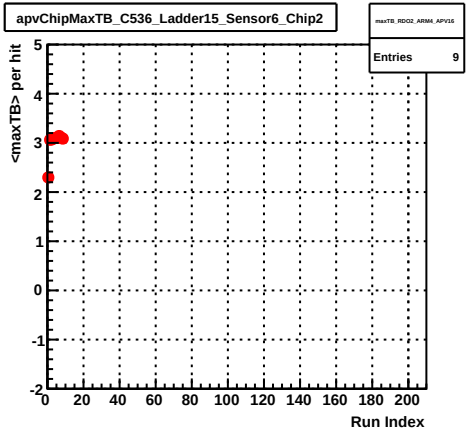

apvChipMaxTB_C531_Ladder15_Sensor5_Chip3
maxTB_RDO2_ARM4_APV21
Entries 9
<maxTB> per hit
Run Index

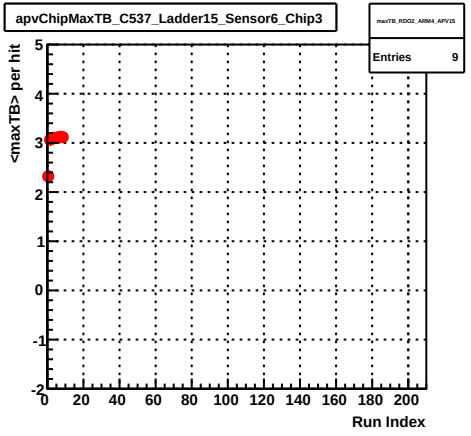

apvChipMaxTB_C532_Ladder15_Sensor5_Chip4
maxTB_RDO2_ARM4_APV20
Entries 9
<maxTB> per hit
Run Index

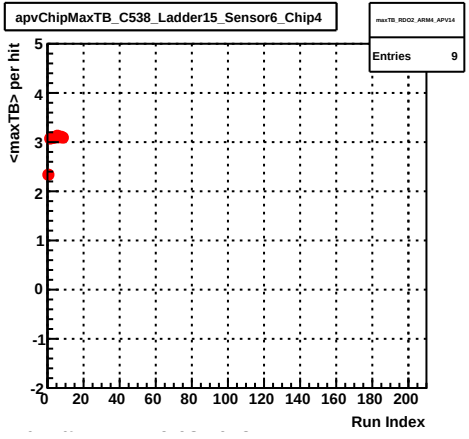

apvChipMaxTB_C533_Ladder15_Sensor5_Chip5
maxTB_RDO2_ARM4_APV19
Entries 9
<maxTB> per hit
Run Index

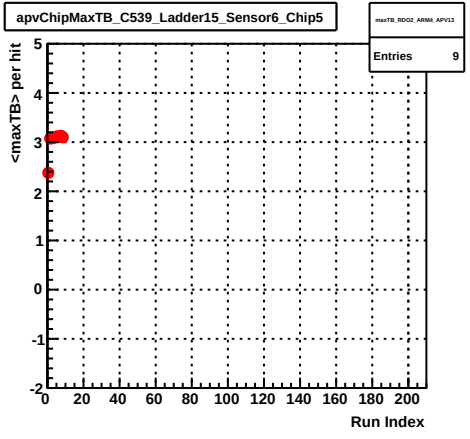

apvChipMaxTB_C534_Ladder15_Sensor5_Chip6
maxTB_RDO2_ARM4_APV18
Entries 9
<maxTB> per hit
Run Index

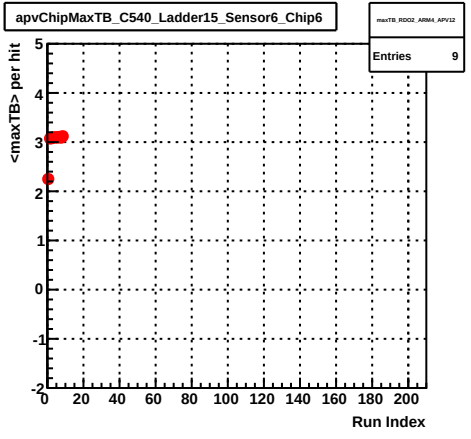

apvChipMaxTB_C535_Ladder15_Sensor6_Chip1
maxTB_RDO2_ARM4_APV17
Entries 9
<maxTB> per hit
Run Index

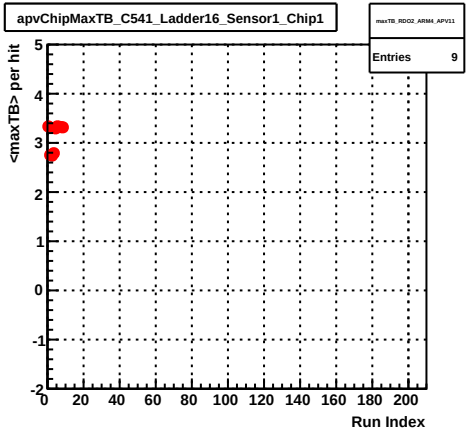

apvChipMaxTB_C536_Ladder15_Sensor6_Chip2
maxTB_RDO2_ARM4_APV16
Entries 9
<maxTB> per hit
Run Index

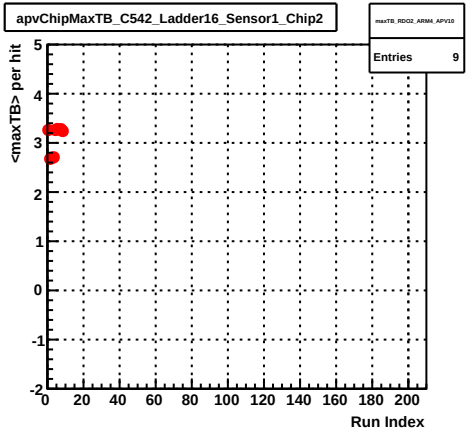

apvChipMaxTB_C537_Ladder15_Sensor6_Chip3
maxTB_RDO2_ARM4_APV15
Entries 9
<maxTB> per hit
Run Index

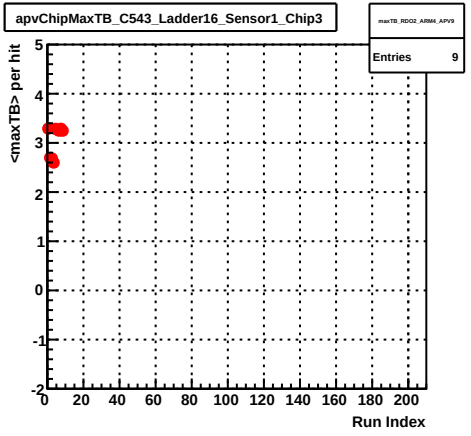

apvChipMaxTB_C538_Ladder15_Sensor6_Chip4
maxTB_RDO2_ARM4_APV14
Entries 9
<maxTB> per hit
Run Index

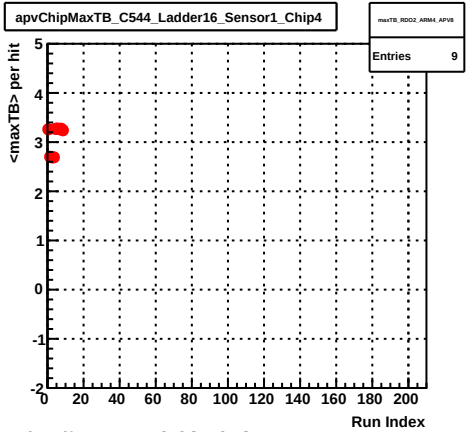

apvChipMaxTB_C539_Ladder15_Sensor6_Chip5
maxTB_RDO2_ARM4_APV13
Entries 9
<maxTB> per hit
Run Index

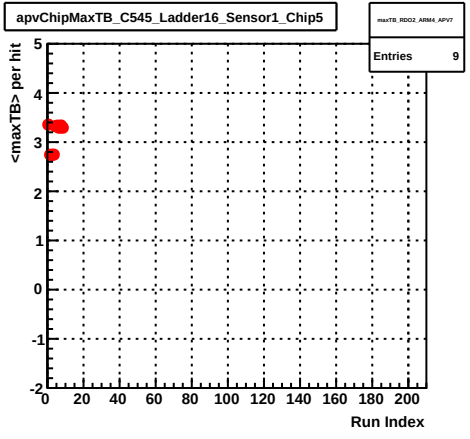

apvChipMaxTB_C540_Ladder15_Sensor6_Chip6
maxTB_RDO2_ARM4_APV12
Entries 9
<maxTB> per hit
Run Index

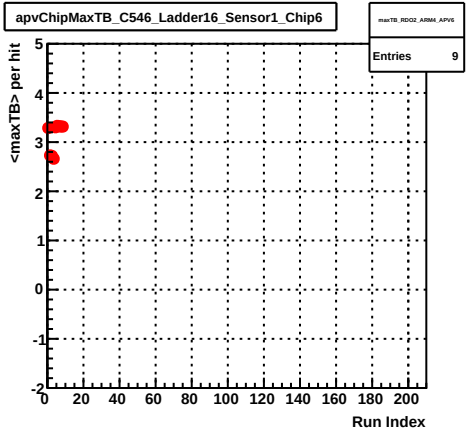

apvChipMaxTB_C541_Ladder16_Sensor1_Chip1
maxTB_RDO2_ARM4_APV11
Entries 9
<maxTB> per hit
Run Index

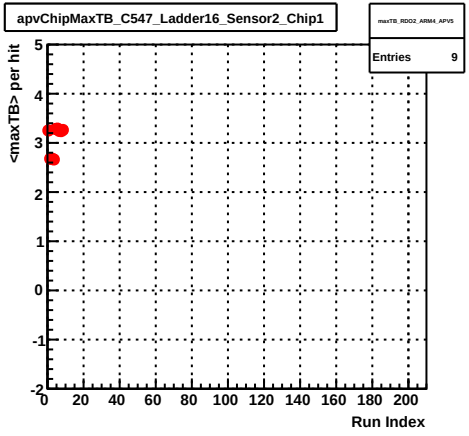

apvChipMaxTB_C542_Ladder16_Sensor1_Chip2
maxTB_RDO2_ARM4_APV10
Entries 9
<maxTB> per hit
Run Index

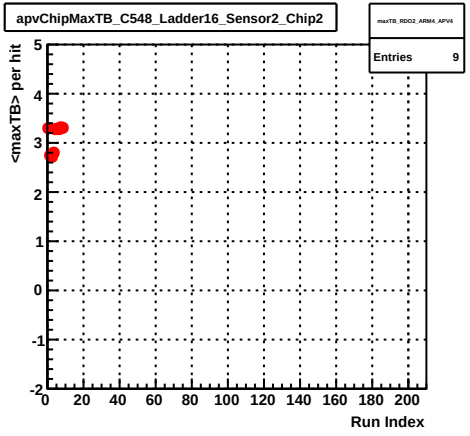

apvChipMaxTB_C543_Ladder16_Sensor1_Chip3
maxTB_RDO2_ARM4_APV9
Entries 9
<maxTB> per hit
Run Index

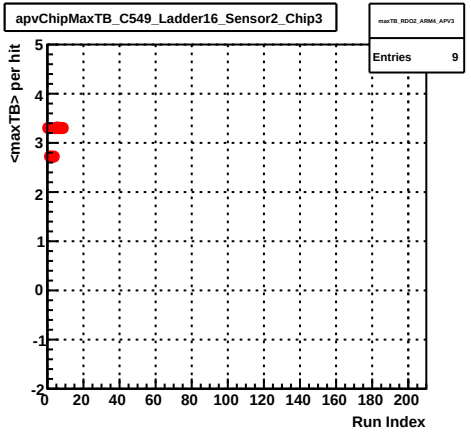

apvChipMaxTB_C544_Ladder16_Sensor1_Chip4
maxTB_RDO2_ARM4_APV8
Entries 9
<maxTB> per hit
Run Index

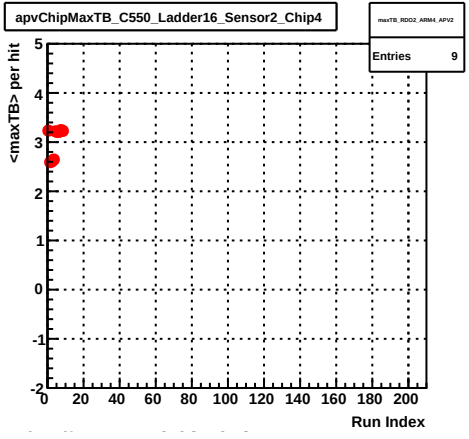

apvChipMaxTB_C545_Ladder16_Sensor1_Chip5
maxTB_RDO2_ARM4_APV7
Entries 9
<maxTB> per hit
Run Index

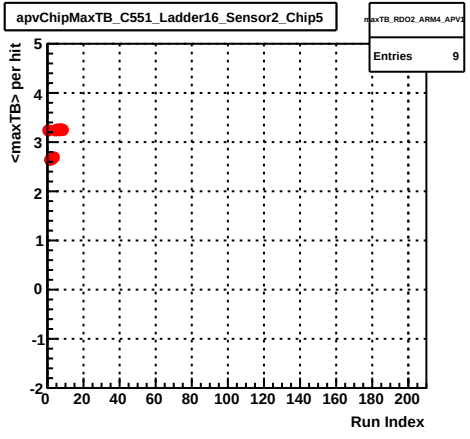

apvChipMaxTB_C546_Ladder16_Sensor1_Chip6
maxTB_RDO2_ARM4_APV6
Entries 9
<maxTB> per hit
Run Index

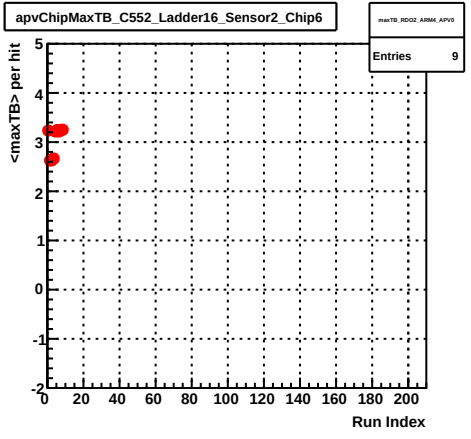

apvChipMaxTB_C547_Ladder16_Sensor2_Chip1
maxTB_RDO2_ARM4_APV5
Entries 9
<maxTB> per hit
Run Index

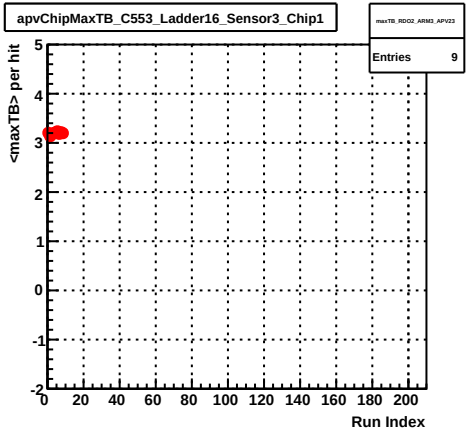

apvChipMaxTB_C548_Ladder16_Sensor2_Chip2
maxTB_RDO2_ARM4_APV4
Entries 9
<maxTB> per hit
Run Index

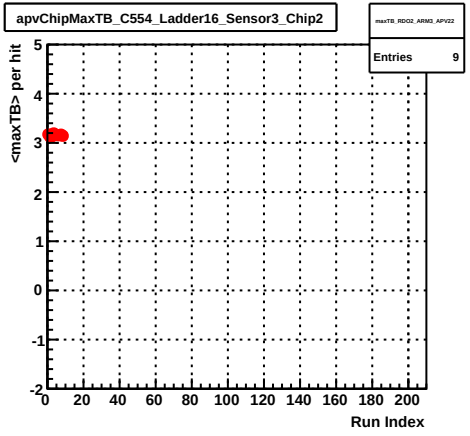

apvChipMaxTB_C549_Ladder16_Sensor2_Chip3
maxTB_RDO2_ARM4_APV3
Entries 9
<maxTB> per hit
Run Index

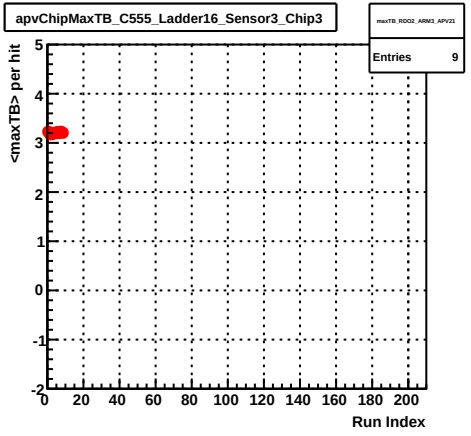

apvChipMaxTB_C550_Ladder16_Sensor2_Chip4
maxTB_RDO2_ARM4_APV2
Entries 9
<maxTB> per hit
Run Index

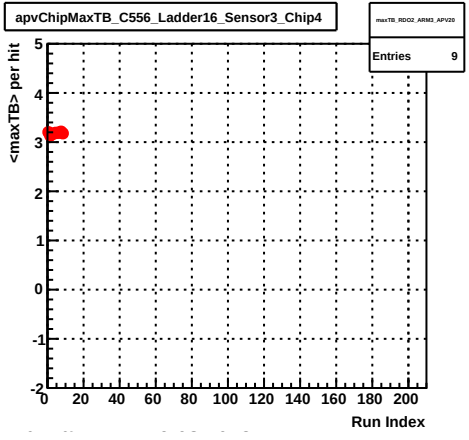

apvChipMaxTB_C551_Ladder16_Sensor2_Chip5
maxTB_RDO2_ARM4_APV1
Entries 9
<maxTB> per hit
Run Index

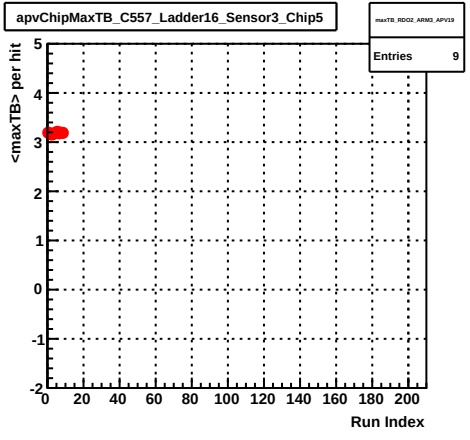

apvChipMaxTB_C552_Ladder16_Sensor2_Chip6
maxTB_RDO2_ARM4_APV0
Entries 9
<maxTB> per hit
Run Index

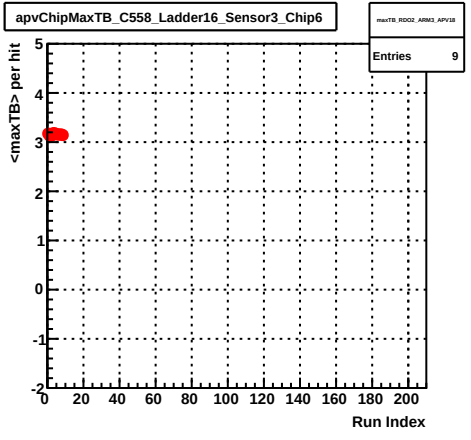

apvChipMaxTB_C553_Ladder16_Sensor3_Chip1
maxTB_RDO2_ARM3_APV23
Entries 9
<maxTB> per hit
Run Index

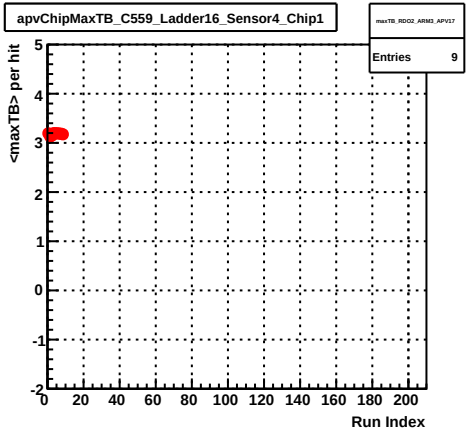

apvChipMaxTB_C554_Ladder16_Sensor3_Chip2
maxTB_RDO2_ARM3_APV22
Entries 9
<maxTB> per hit
Run Index

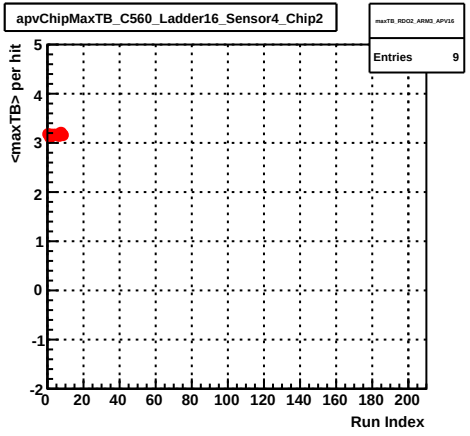

apvChipMaxTB_C555_Ladder16_Sensor3_Chip3
maxTB_RDO2_ARM3_APV21
Entries 9
<maxTB> per hit
Run Index

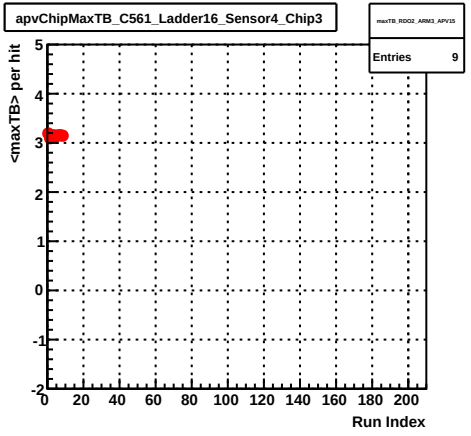

apvChipMaxTB_C556_Ladder16_Sensor3_Chip4
maxTB_RDO2_ARM3_APV20
Entries 9
<maxTB> per hit
Run Index

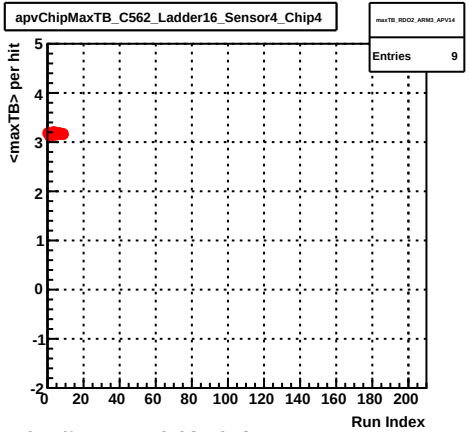

apvChipMaxTB_C557_Ladder16_Sensor3_Chip5
maxTB_RDO2_ARM3_APV19
Entries 9
<maxTB> per hit
Run Index

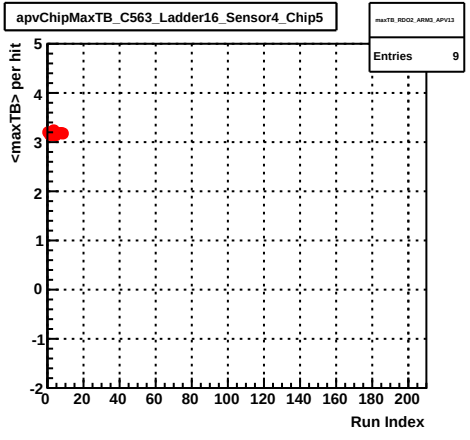

apvChipMaxTB_C558_Ladder16_Sensor3_Chip6
maxTB_RDO2_ARM3_APV18
Entries 9
<maxTB> per hit
Run Index

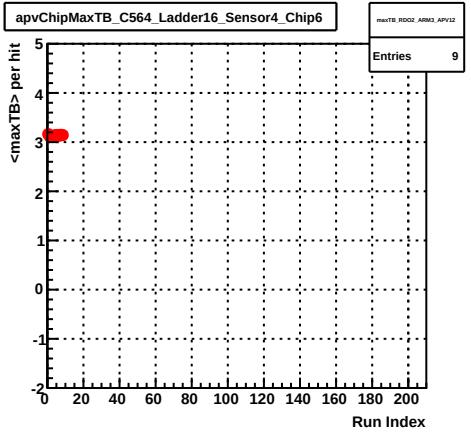

apvChipMaxTB_C559_Ladder16_Sensor4_Chip1
maxTB_RDO2_ARM3_APV17
Entries 9
<maxTB> per hit
Run Index

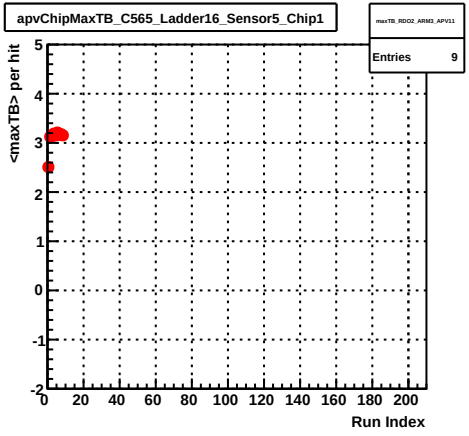

apvChipMaxTB_C560_Ladder16_Sensor4_Chip2
maxTB_RDO2_ARM3_APV16
Entries 9
<maxTB> per hit
Run Index

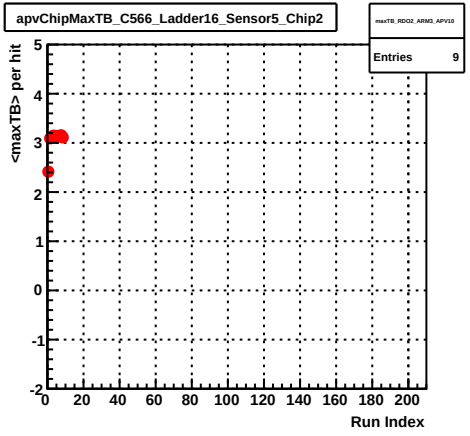

apvChipMaxTB_C561_Ladder16_Sensor4_Chip3
maxTB_RDO2_ARM3_APV15
Entries 9
<maxTB> per hit
Run Index

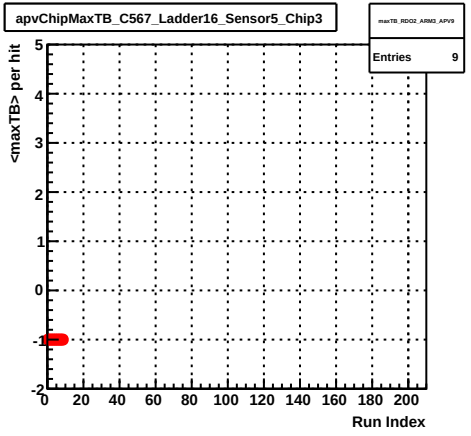

apvChipMaxTB_C562_Ladder16_Sensor4_Chip4
maxTB_RDO2_ARM3_APV14
Entries 9
<maxTB> per hit
Run Index

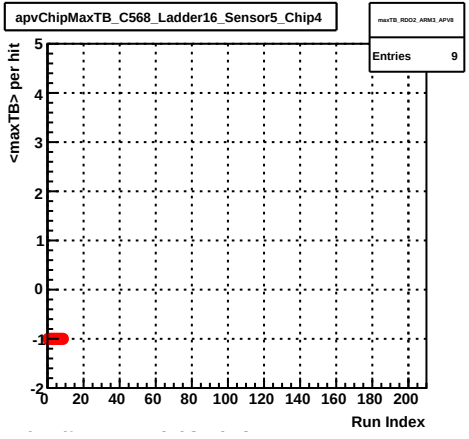

apvChipMaxTB_C563_Ladder16_Sensor4_Chip5
maxTB_RDO2_ARM3_APV13
Entries 9
<maxTB> per hit
Run Index

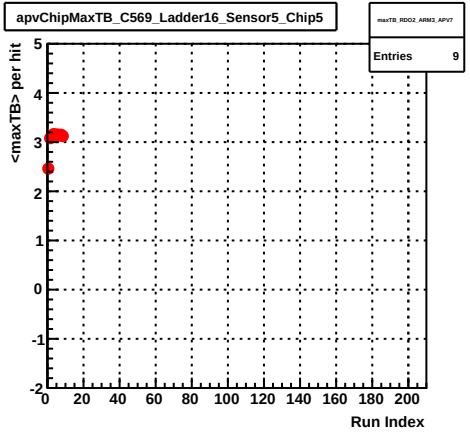

apvChipMaxTB_C564_Ladder16_Sensor4_Chip6
maxTB_RDO2_ARM3_APV12
Entries 9
<maxTB> per hit
Run Index

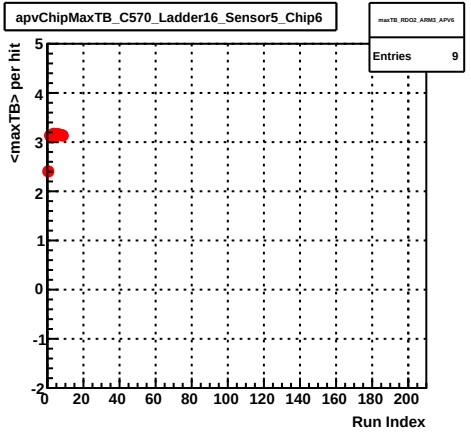

apvChipMaxTB_C565_Ladder16_Sensor5_Chip1
maxTB_RDO2_ARM3_APV11
Entries 9
<maxTB> per hit
Run Index

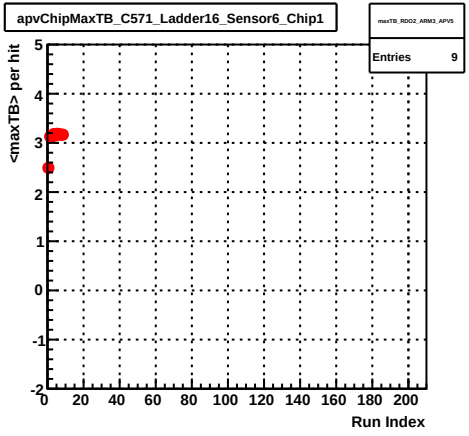

apvChipMaxTB_C566_Ladder16_Sensor5_Chip2
maxTB_RDO2_ARM3_APV10
Entries 9
<maxTB> per hit
Run Index

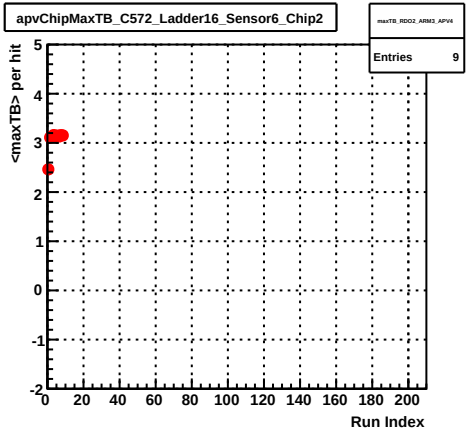

apvChipMaxTB_C567_Ladder16_Sensor5_Chip3
maxTB_RDO2_ARM3_APV9
Entries 9
<maxTB> per hit
Run Index

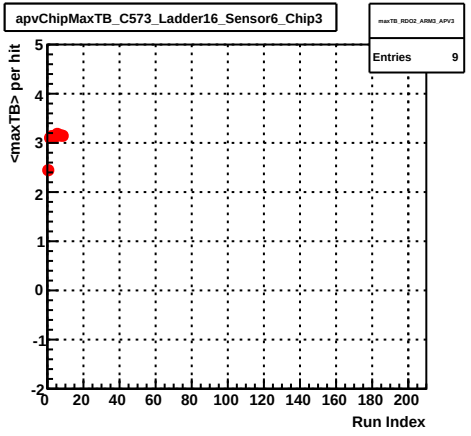

apvChipMaxTB_C568_Ladder16_Sensor5_Chip4
maxTB_RDO2_ARM3_APV8
Entries 9
<maxTB> per hit
Run Index

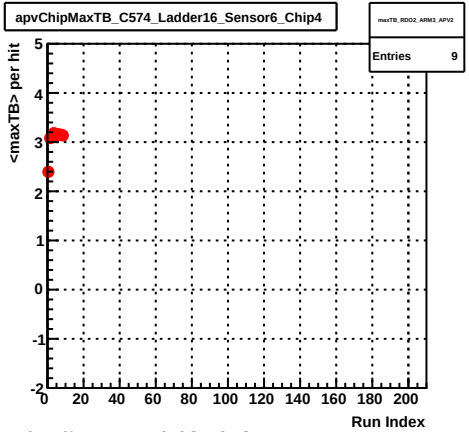

apvChipMaxTB_C569_Ladder16_Sensor5_Chip5
maxTB_RDO2_ARM3_APV7
Entries 9
<maxTB> per hit
Run Index

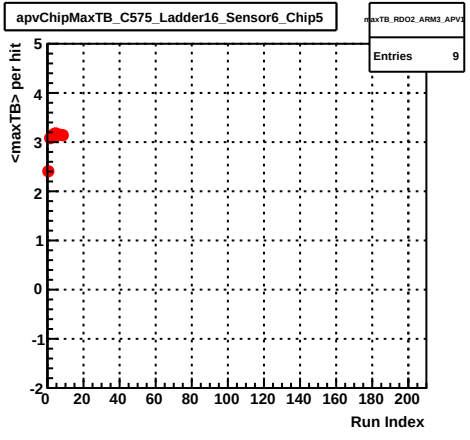

apvChipMaxTB_C570_Ladder16_Sensor5_Chip6
maxTB_RDO2_ARM3_APV6
Entries 9
<maxTB> per hit
Run Index

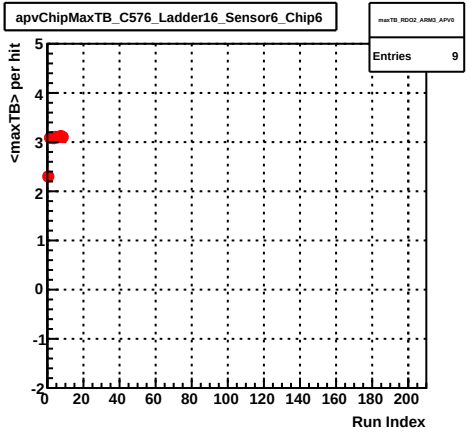

apvChipMaxTB_C571_Ladder16_Sensor6_Chip1
maxTB_RDO2_ARM3_APV5
Entries 9
<maxTB> per hit
Run Index

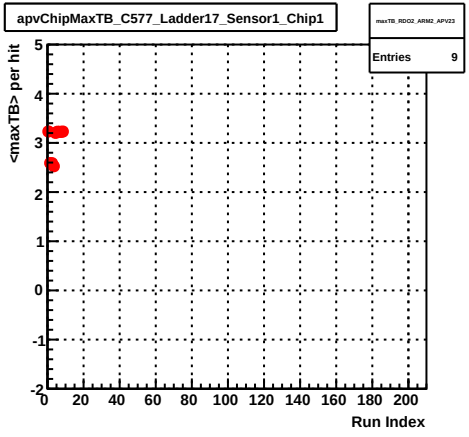

apvChipMaxTB_C572_Ladder16_Sensor6_Chip2
maxTB_RDO2_ARM3_APV4
Entries 9
<maxTB> per hit
Run Index

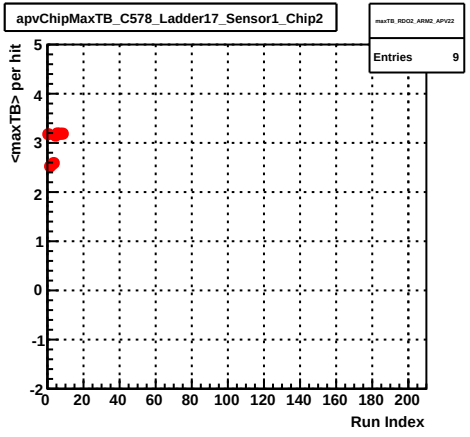

apvChipMaxTB_C573_Ladder16_Sensor6_Chip3
maxTB_RDO2_ARM3_APV3
Entries 9
<maxTB> per hit
Run Index

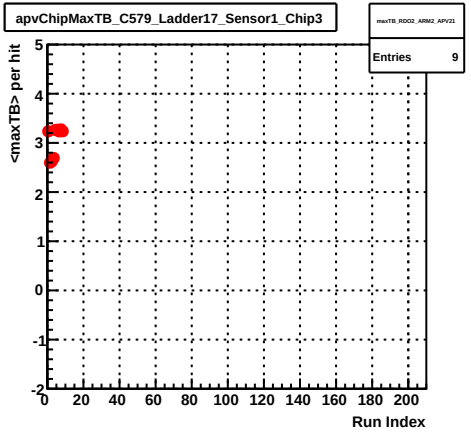

apvChipMaxTB_C574_Ladder16_Sensor6_Chip4
maxTB_RDO2_ARM3_APV2
Entries 9
<maxTB> per hit
Run Index

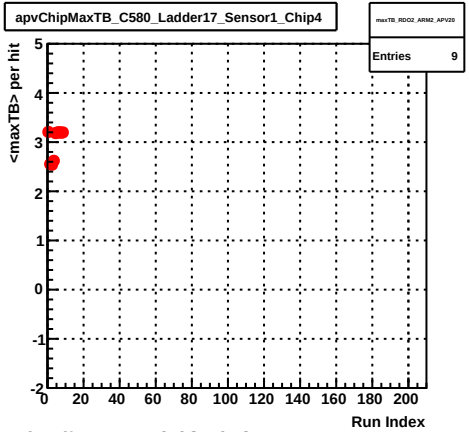

apvChipMaxTB_C575_Ladder16_Sensor6_Chip5
maxTB_RDO2_ARM3_APV1
Entries 9
<maxTB> per hit
Run Index

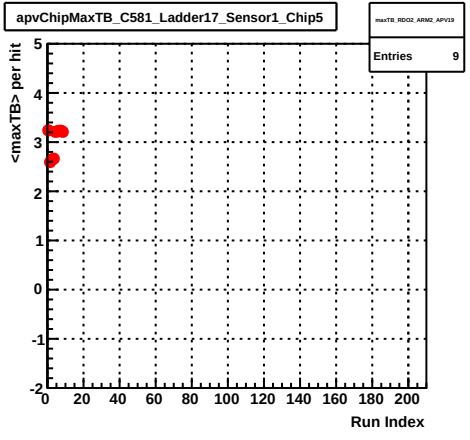

apvChipMaxTB_C576_Ladder16_Sensor6_Chip6
maxTB_RDO2_ARM3_APV0
Entries 9
<maxTB> per hit
Run Index

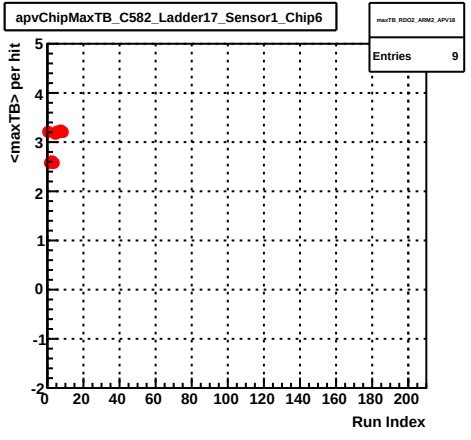

apvChipMaxTB_C577_Ladder17_Sensor1_Chip1
maxTB_RDO2_ARM2_APV23
Entries 9
<maxTB> per hit
Run Index

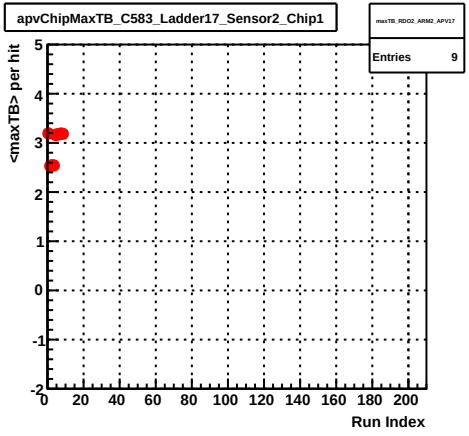

apvChipMaxTB_C578_Ladder17_Sensor1_Chip2
maxTB_RDO2_ARM2_APV22
Entries 9
<maxTB> per hit
Run Index

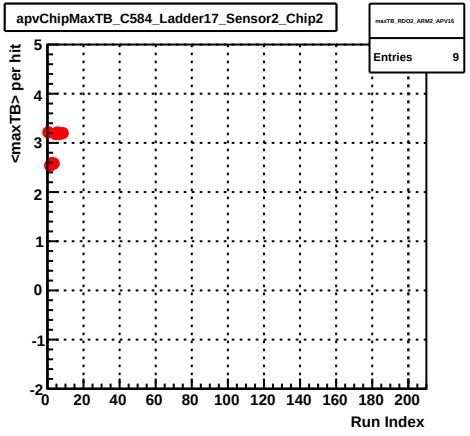

apvChipMaxTB_C579_Ladder17_Sensor1_Chip3
maxTB_RDO2_ARM2_APV21
Entries 9
<maxTB> per hit
Run Index

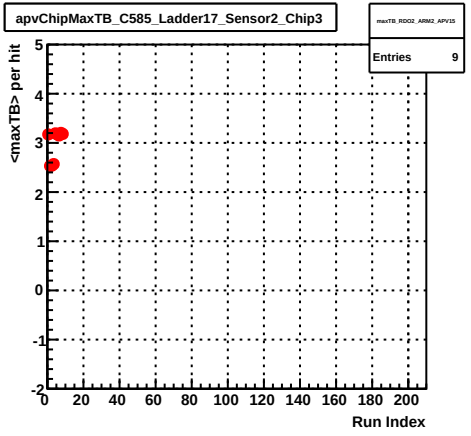

apvChipMaxTB_C580_Ladder17_Sensor1_Chip4
maxTB_RDO2_ARM2_APV20
Entries 9
<maxTB> per hit
Run Index

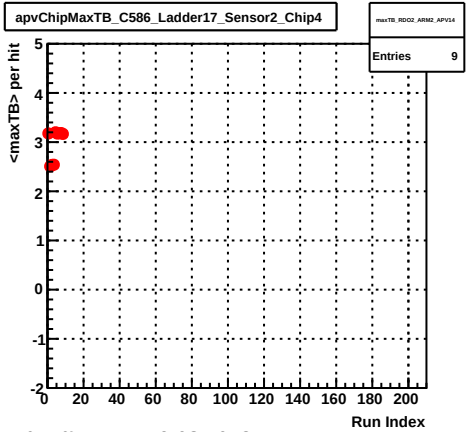

apvChipMaxTB_C581_Ladder17_Sensor1_Chip5
maxTB_RDO2_ARM2_APV19
Entries 9
<maxTB> per hit
Run Index

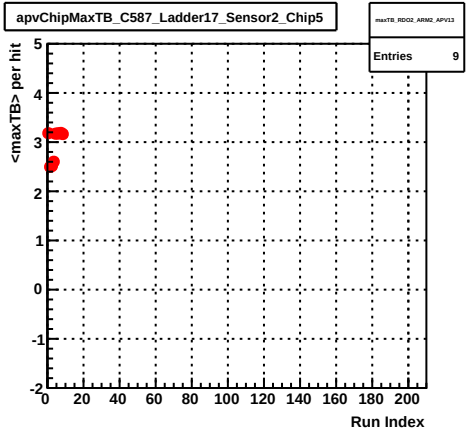

apvChipMaxTB_C582_Ladder17_Sensor1_Chip6
maxTB_RDO2_ARM2_APV18
Entries 9
<maxTB> per hit
Run Index

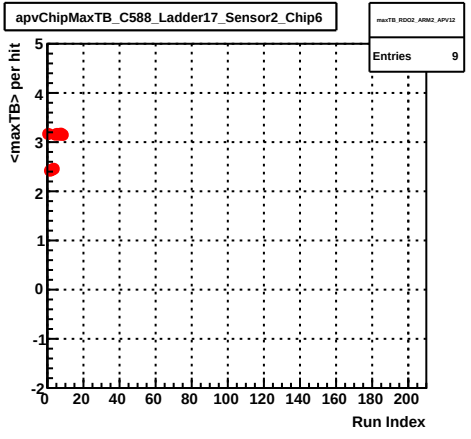

apvChipMaxTB_C583_Ladder17_Sensor2_Chip1
maxTB_RDO2_ARM2_APV17
Entries 9
<maxTB> per hit
Run Index

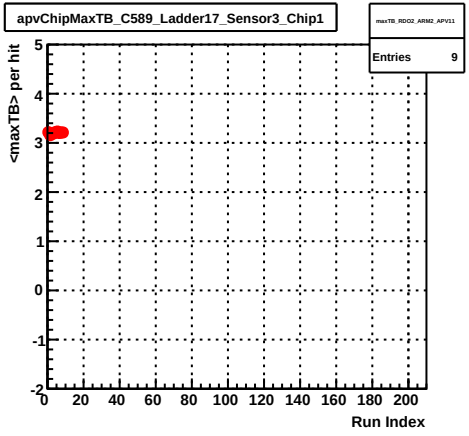

apvChipMaxTB_C584_Ladder17_Sensor2_Chip2
maxTB_RDO2_ARM2_APV16
Entries 9
<maxTB> per hit
Run Index

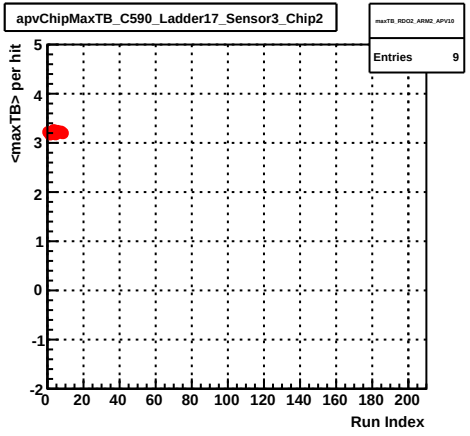

apvChipMaxTB_C585_Ladder17_Sensor2_Chip3
maxTB_RDO2_ARM2_APV15
Entries 9
<maxTB> per hit
Run Index

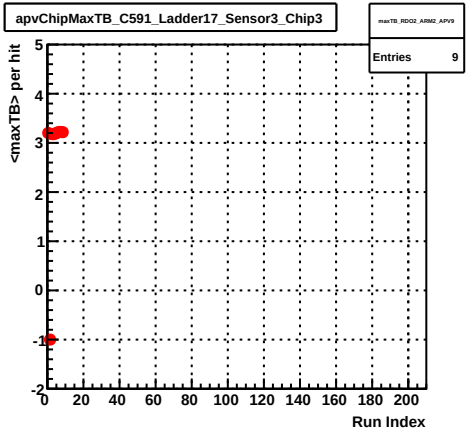

apvChipMaxTB_C586_Ladder17_Sensor2_Chip4
maxTB_RDO2_ARM2_APV14
Entries 9
<maxTB> per hit
Run Index

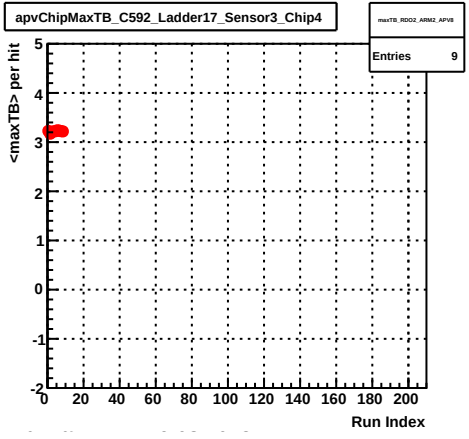

apvChipMaxTB_C587_Ladder17_Sensor2_Chip5
maxTB_RDO2_ARM2_APV13
Entries 9
<maxTB> per hit
Run Index

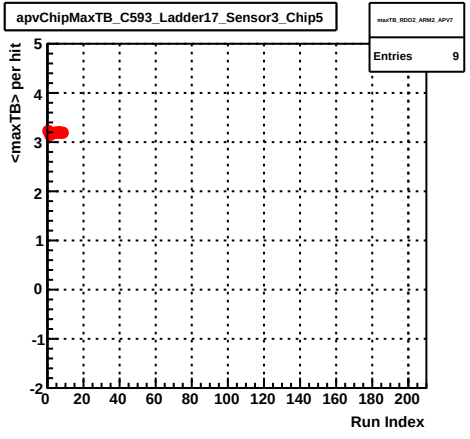

apvChipMaxTB_C588_Ladder17_Sensor2_Chip6
maxTB_RDO2_ARM2_APV12
Entries 9
<maxTB> per hit
Run Index

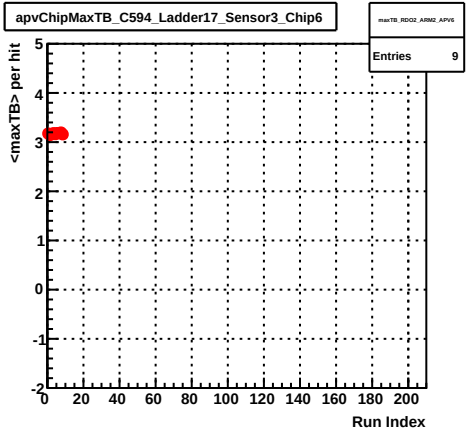

apvChipMaxTB_C589_Ladder17_Sensor3_Chip1
maxTB_RDO2_ARM2_APV11
Entries 9
<maxTB> per hit
Run Index

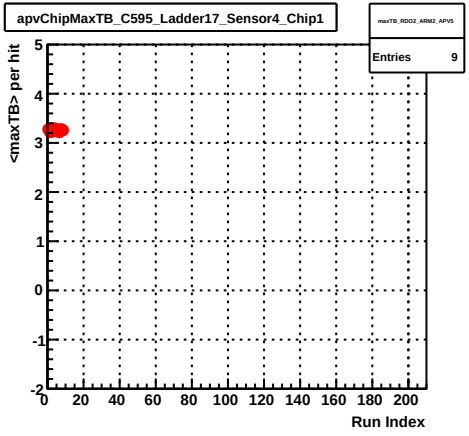

apvChipMaxTB_C590_Ladder17_Sensor3_Chip2
maxTB_RDO2_ARM2_APV10
Entries 9
<maxTB> per hit
Run Index

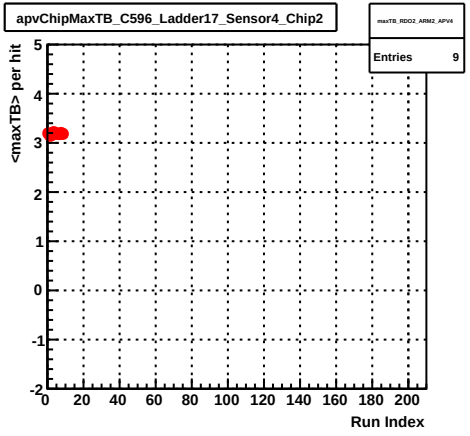

apvChipMaxTB_C591_Ladder17_Sensor3_Chip3
maxTB_RDO2_ARM2_APV9
Entries 9
<maxTB> per hit
Run Index

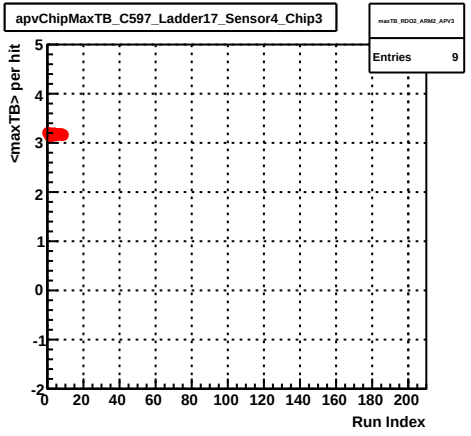

apvChipMaxTB_C592_Ladder17_Sensor3_Chip4
maxTB_RDO2_ARM2_APV8
Entries 9
<maxTB> per hit
Run Index

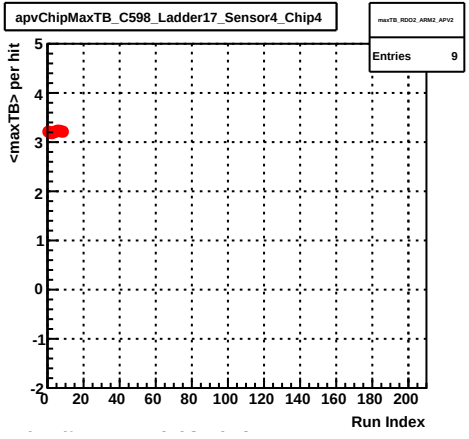

apvChipMaxTB_C593_Ladder17_Sensor3_Chip5
maxTB_RDO2_ARM2_APV7
Entries 9
<maxTB> per hit
Run Index

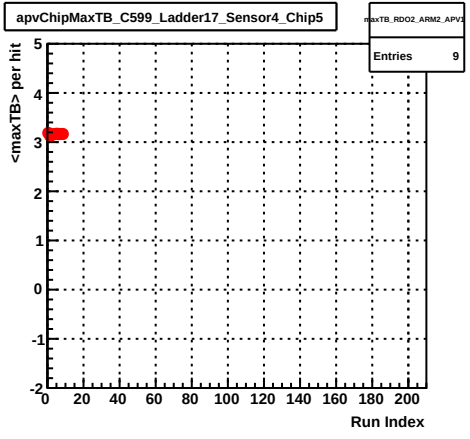

apvChipMaxTB_C594_Ladder17_Sensor3_Chip6
maxTB_RDO2_ARM2_APV6
Entries 9
<maxTB> per hit
Run Index

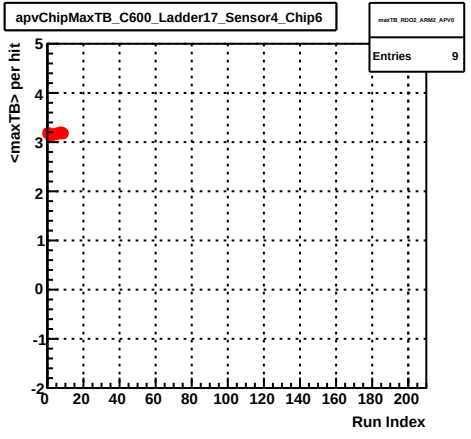

apvChipMaxTB_C595_Ladder17_Sensor4_Chip1
maxTB_RDO2_ARM2_APV5
Entries 9
<maxTB> per hit
Run Index

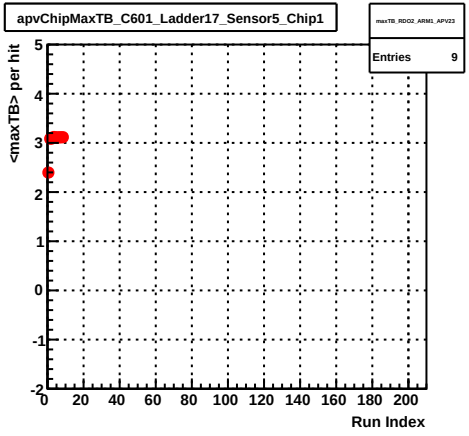

apvChipMaxTB_C596_Ladder17_Sensor4_Chip2
maxTB_RDO2_ARM2_APV4
Entries 9
<maxTB> per hit
Run Index

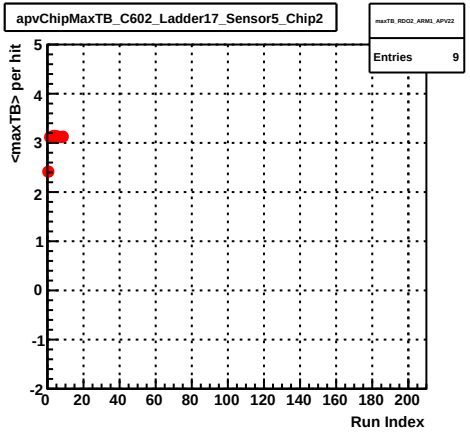

apvChipMaxTB_C597_Ladder17_Sensor4_Chip3
maxTB_RDO2_ARM2_APV3
Entries 9
<maxTB> per hit
Run Index

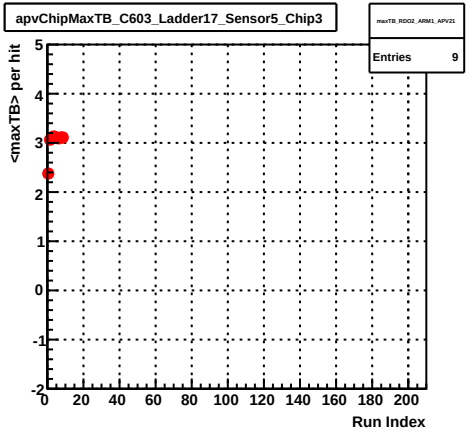

apvChipMaxTB_C598_Ladder17_Sensor4_Chip4
maxTB_RDO2_ARM2_APV2
Entries 9
<maxTB> per hit
Run Index

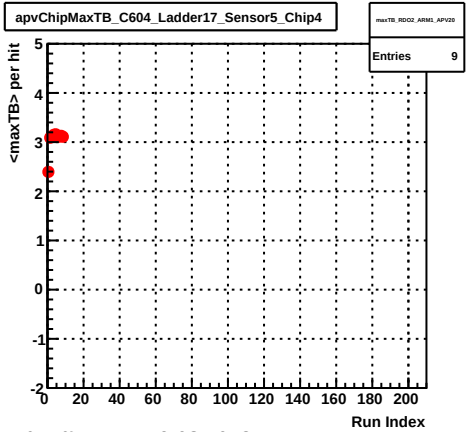

apvChipMaxTB_C599_Ladder17_Sensor4_Chip5
maxTB_RDO2_ARM2_APV1
Entries 9
<maxTB> per hit
Run Index

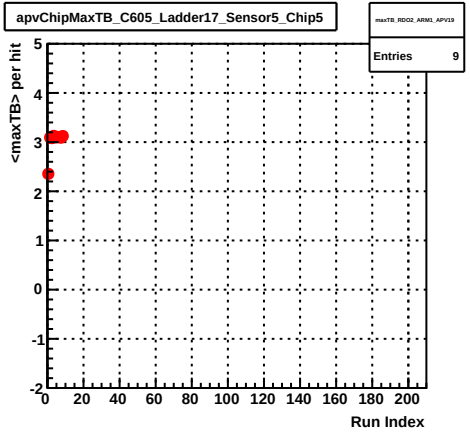

apvChipMaxTB_C600_Ladder17_Sensor4_Chip6
maxTB_RDO2_ARM2_APV0
Entries 9
<maxTB> per hit
Run Index

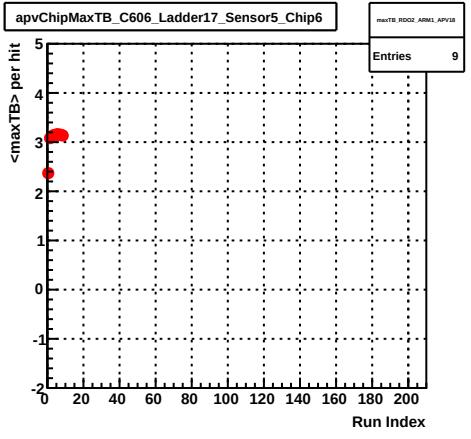

apvChipMaxTB_C601_Ladder17_Sensor5_Chip1
maxTB_RDO2_ARM1_APV23
Entries 9
<maxTB> per hit
Run Index

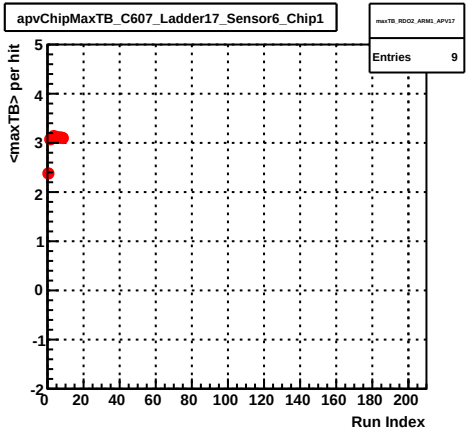

apvChipMaxTB_C602_Ladder17_Sensor5_Chip2
maxTB_RDO2_ARM1_APV22
Entries 9
<maxTB> per hit
Run Index

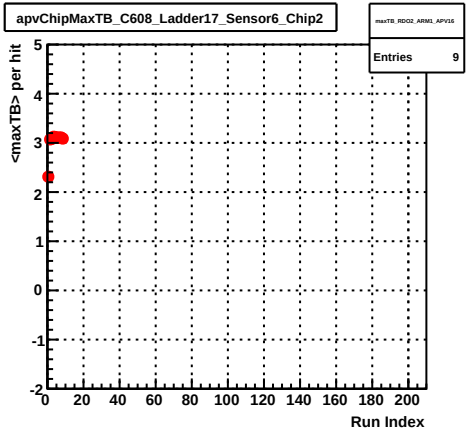

apvChipMaxTB_C603_Ladder17_Sensor5_Chip3
maxTB_RDO2_ARM1_APV21
Entries 9
<maxTB> per hit
Run Index

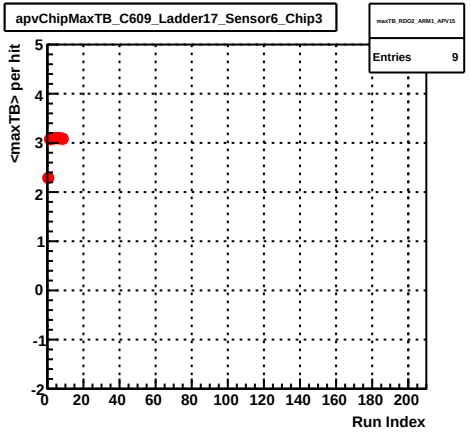

apvChipMaxTB_C604_Ladder17_Sensor5_Chip4
maxTB_RDO2_ARM1_APV20
Entries 9
<maxTB> per hit
Run Index

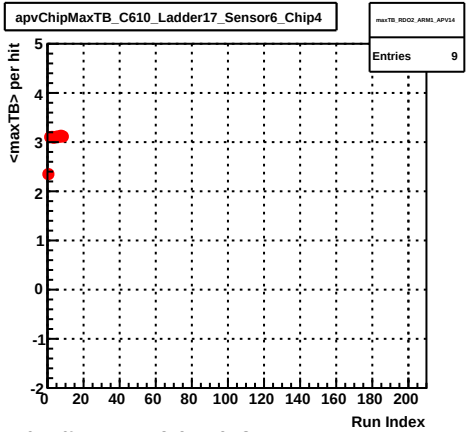

apvChipMaxTB_C605_Ladder17_Sensor5_Chip5
maxTB_RDO2_ARM1_APV19
Entries 9
<maxTB> per hit
Run Index

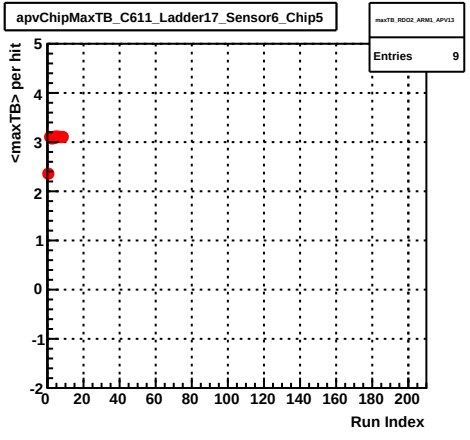

apvChipMaxTB_C606_Ladder17_Sensor5_Chip6
maxTB_RDO2_ARM1_APV18
Entries 9
<maxTB> per hit
Run Index

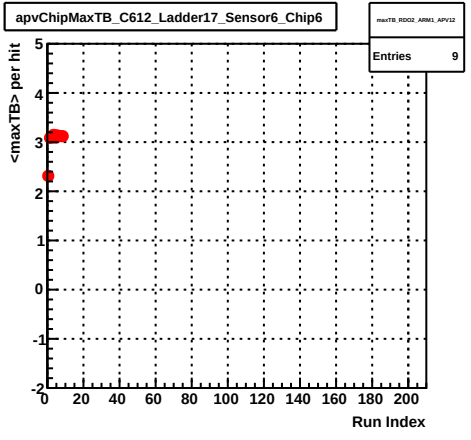

apvChipMaxTB_C607_Ladder17_Sensor6_Chip1
maxTB_RDO2_ARM1_APV17
Entries 9
<maxTB> per hit
Run Index

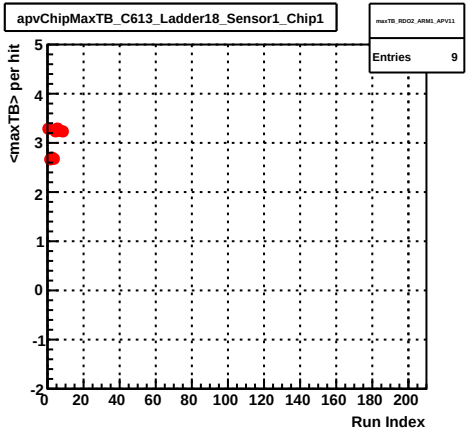

apvChipMaxTB_C608_Ladder17_Sensor6_Chip2
maxTB_RDO2_ARM1_APV16
Entries 9
<maxTB> per hit
Run Index

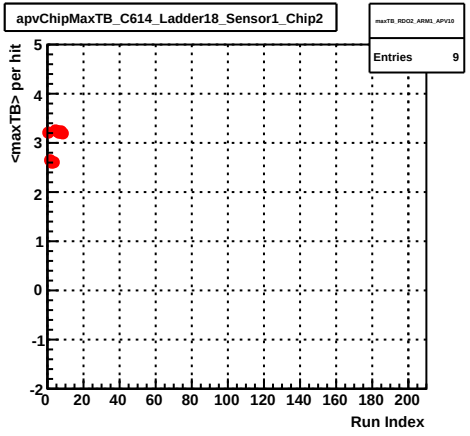

apvChipMaxTB_C609_Ladder17_Sensor6_Chip3
maxTB_RDO2_ARM1_APV15
Entries 9
<maxTB> per hit
Run Index

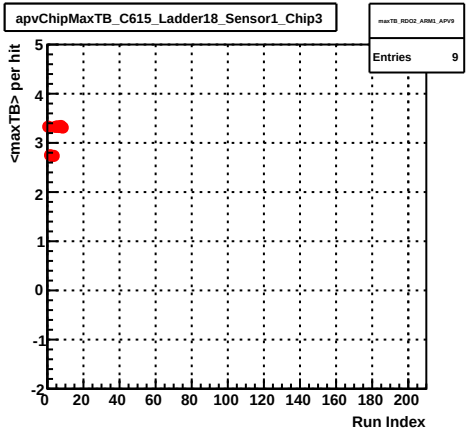

apvChipMaxTB_C610_Ladder17_Sensor6_Chip4
maxTB_RDO2_ARM1_APV14
Entries 9
<maxTB> per hit
Run Index

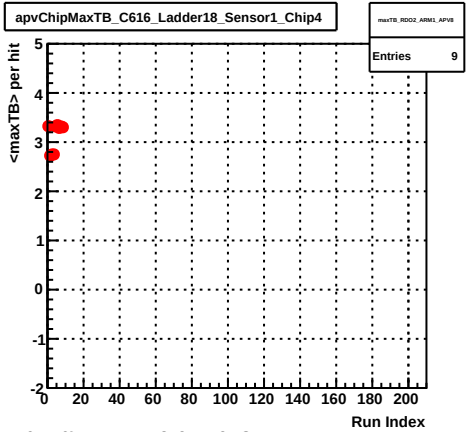

apvChipMaxTB_C611_Ladder17_Sensor6_Chip5
maxTB_RDO2_ARM1_APV13
Entries 9
<maxTB> per hit
Run Index

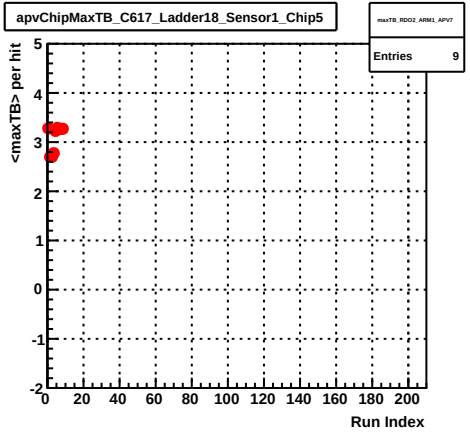

apvChipMaxTB_C612_Ladder17_Sensor6_Chip6
maxTB_RDO2_ARM1_APV12
Entries 9
<maxTB> per hit
Run Index

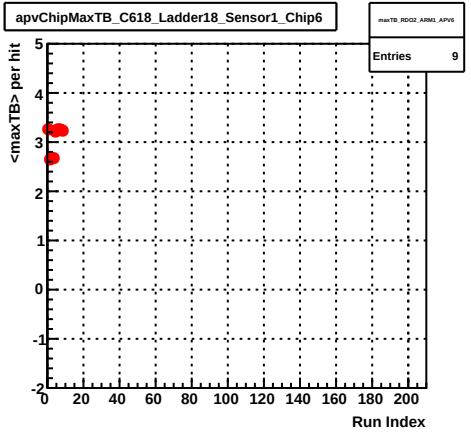

apvChipMaxTB_C613_Ladder18_Sensor1_Chip1
maxTB_RDO2_ARM1_APV11
Entries 9
<maxTB> per hit
Run Index

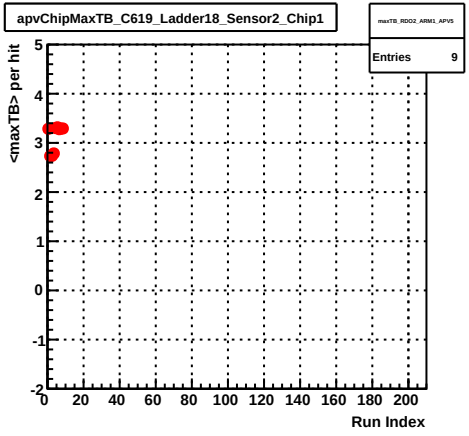

apvChipMaxTB_C614_Ladder18_Sensor1_Chip2
maxTB_RDO2_ARM1_APV10
Entries 9
<maxTB> per hit
Run Index

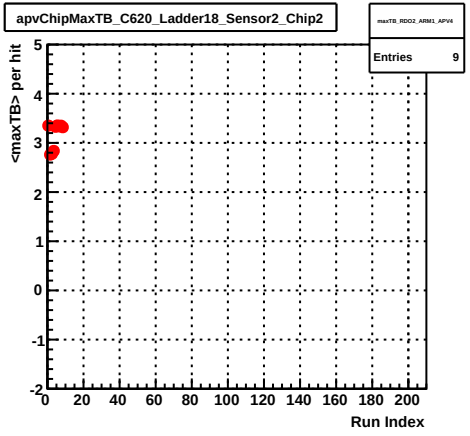

apvChipMaxTB_C615_Ladder18_Sensor1_Chip3
maxTB_RDO2_ARM1_APV9
Entries 9
<maxTB> per hit
Run Index

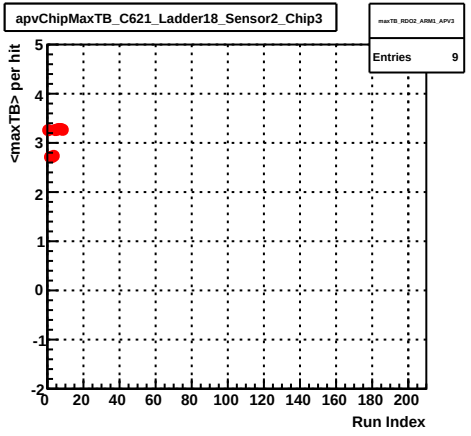

apvChipMaxTB_C616_Ladder18_Sensor1_Chip4
maxTB_RDO2_ARM1_APV8
Entries 9
<maxTB> per hit
Run Index

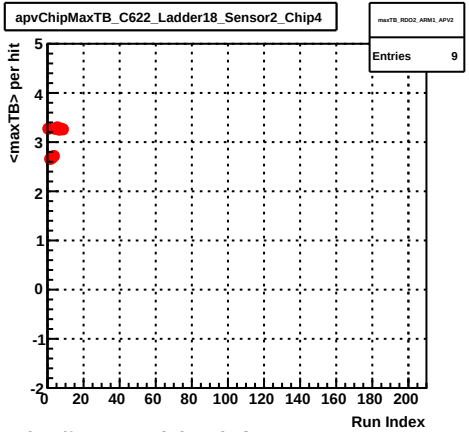

apvChipMaxTB_C617_Ladder18_Sensor1_Chip5
maxTB_RDO2_ARM1_APV7
Entries 9
<maxTB> per hit
Run Index

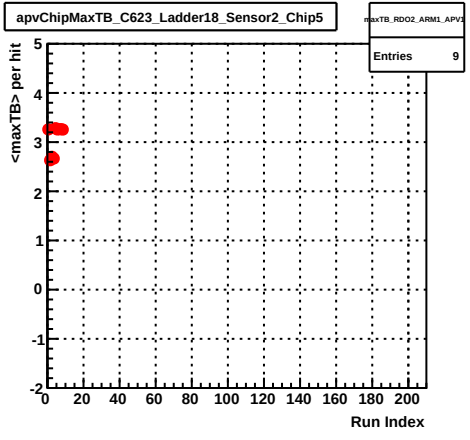

apvChipMaxTB_C618_Ladder18_Sensor1_Chip6
maxTB_RDO2_ARM1_APV6
Entries 9
<maxTB> per hit
Run Index

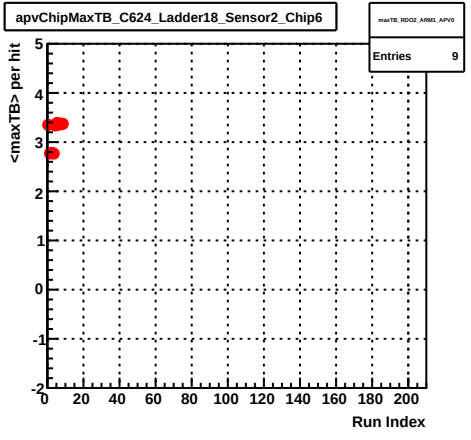

apvChipMaxTB_C619_Ladder18_Sensor2_Chip1
maxTB_RDO2_ARM1_APV5
Entries 9
<maxTB> per hit
Run Index

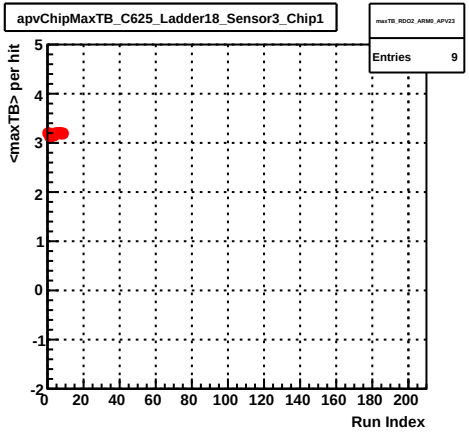

apvChipMaxTB_C620_Ladder18_Sensor2_Chip2
maxTB_RDO2_ARM1_APV4
Entries 9
<maxTB> per hit
Run Index

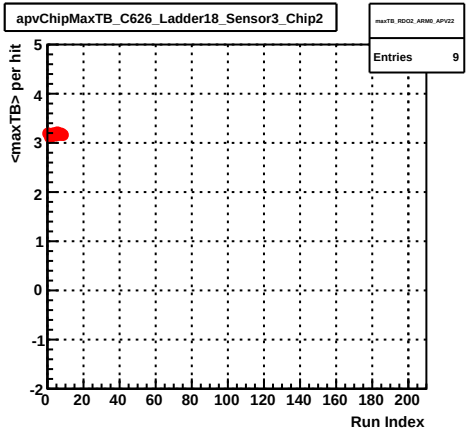

apvChipMaxTB_C621_Ladder18_Sensor2_Chip3
maxTB_RDO2_ARM1_APV3
Entries 9
<maxTB> per hit
Run Index

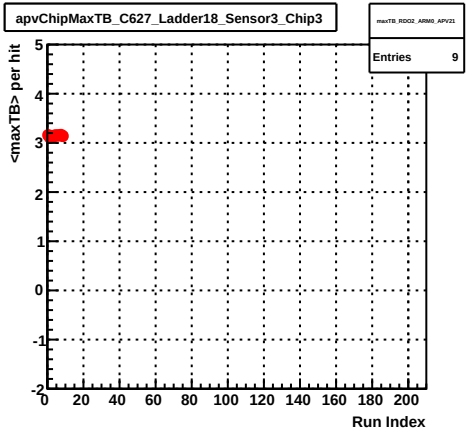

apvChipMaxTB_C622_Ladder18_Sensor2_Chip4
maxTB_RDO2_ARM1_APV2
Entries 9
<maxTB> per hit
Run Index

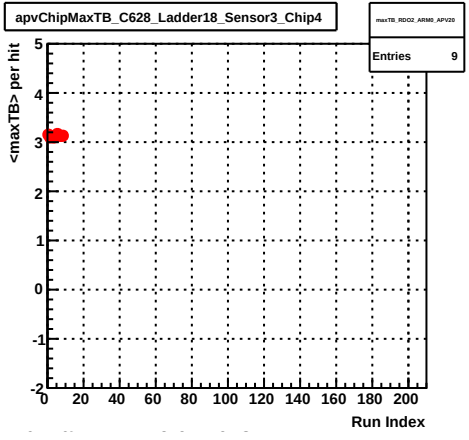

apvChipMaxTB_C623_Ladder18_Sensor2_Chip5
maxTB_RDO2_ARM1_APV1
Entries 9
<maxTB> per hit
Run Index

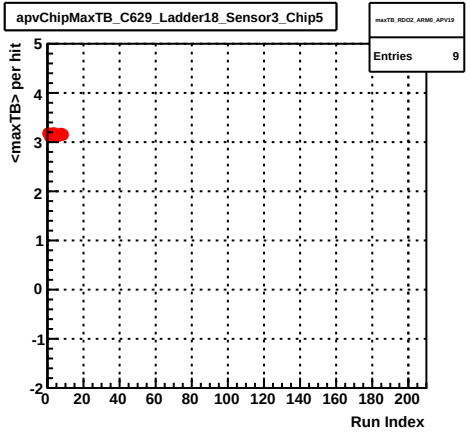

apvChipMaxTB_C624_Ladder18_Sensor2_Chip6
maxTB_RDO2_ARM1_APV0
Entries 9
<maxTB> per hit
Run Index

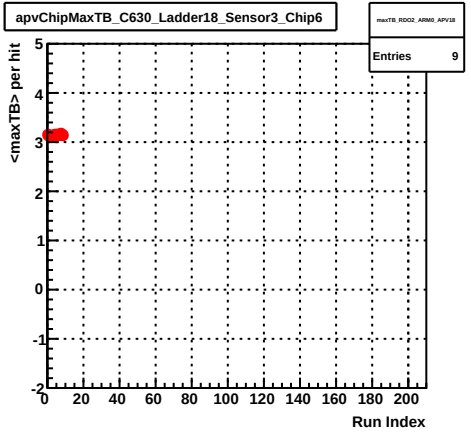

apvChipMaxTB_C625_Ladder18_Sensor3_Chip1
maxTB_RDO2_ARM0_APV23
Entries 9
<maxTB> per hit
Run Index

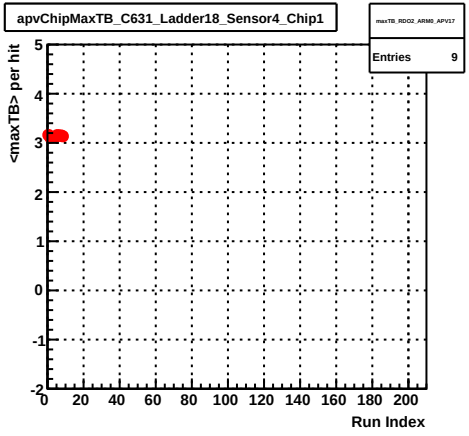

apvChipMaxTB_C626_Ladder18_Sensor3_Chip2
maxTB_RDO2_ARM0_APV22
Entries 9
<maxTB> per hit
Run Index

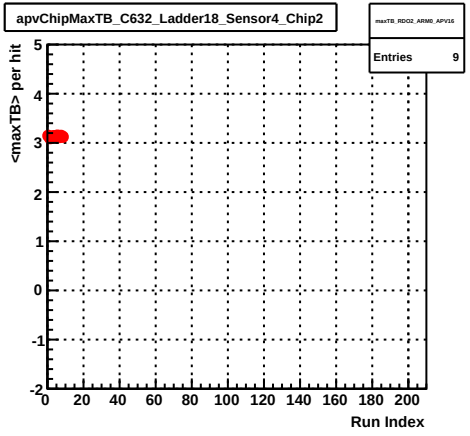

apvChipMaxTB_C627_Ladder18_Sensor3_Chip3
maxTB_RDO2_ARM0_APV21
Entries 9
<maxTB> per hit
Run Index

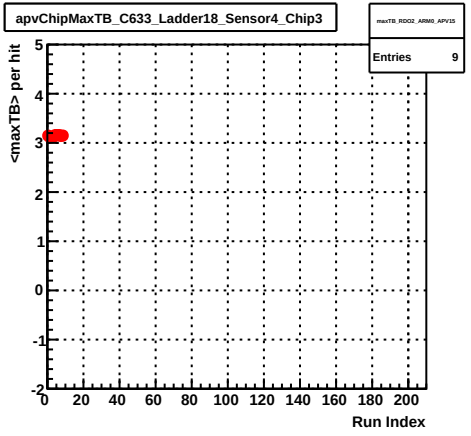

apvChipMaxTB_C628_Ladder18_Sensor3_Chip4
maxTB_RDO2_ARM0_APV20
Entries 9
<maxTB> per hit
Run Index

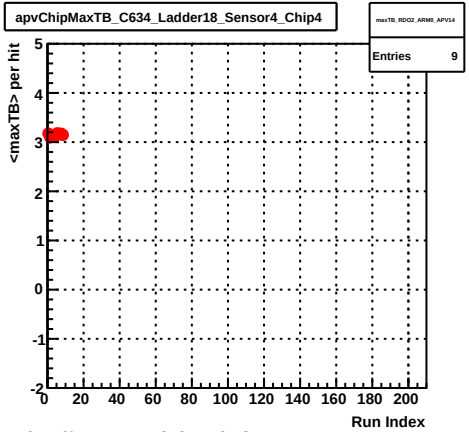

apvChipMaxTB_C629_Ladder18_Sensor3_Chip5
maxTB_RDO2_ARM0_APV19
Entries 9
<maxTB> per hit
Run Index

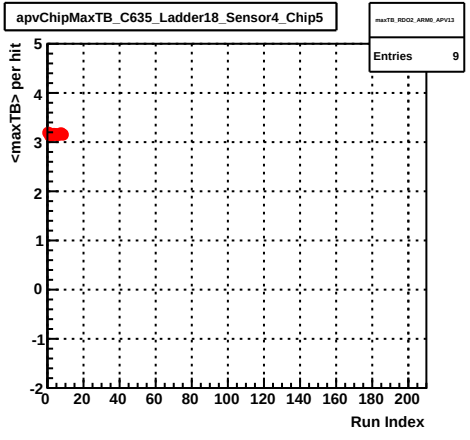

apvChipMaxTB_C630_Ladder18_Sensor3_Chip6
maxTB_RDO2_ARM0_APV18
Entries 9
<maxTB> per hit
Run Index

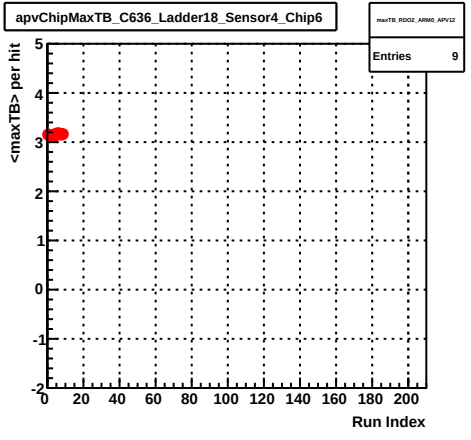

apvChipMaxTB_C631_Ladder18_Sensor4_Chip1
maxTB_RDO2_ARM0_APV17
Entries 9
<maxTB> per hit
Run Index

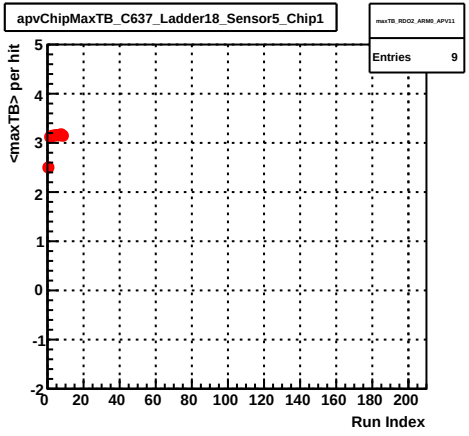

apvChipMaxTB_C632_Ladder18_Sensor4_Chip2
maxTB_RDO2_ARM0_APV16
Entries 9
<maxTB> per hit
Run Index

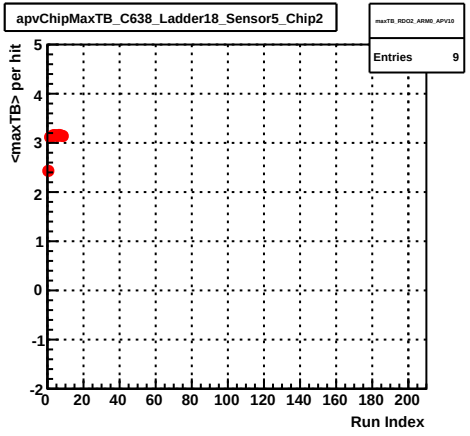

apvChipMaxTB_C633_Ladder18_Sensor4_Chip3
maxTB_RDO2_ARM0_APV15
Entries 9
<maxTB> per hit
Run Index

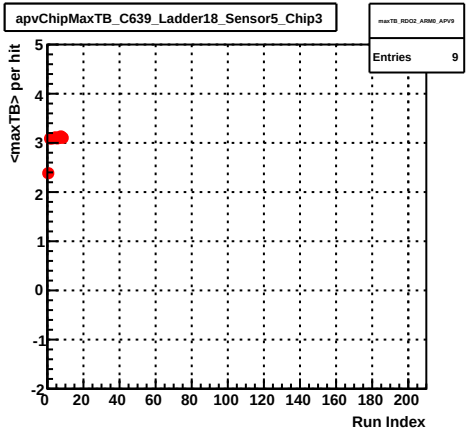

apvChipMaxTB_C634_Ladder18_Sensor4_Chip4
maxTB_RDO2_ARM0_APV14
Entries 9
<maxTB> per hit
Run Index

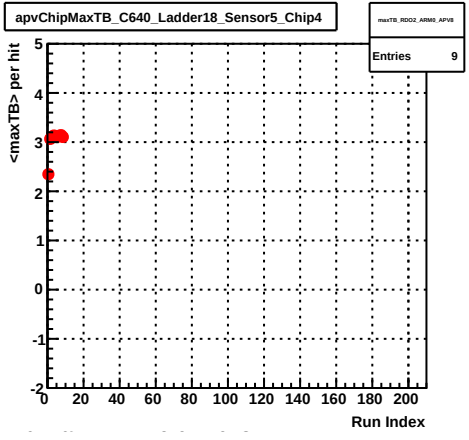

apvChipMaxTB_C635_Ladder18_Sensor4_Chip5
maxTB_RDO2_ARM0_APV13
Entries 9
<maxTB> per hit
Run Index

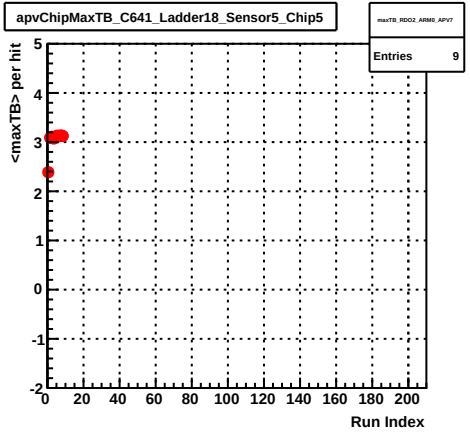

apvChipMaxTB_C636_Ladder18_Sensor4_Chip6
maxTB_RDO2_ARM0_APV12
Entries 9
<maxTB> per hit
Run Index

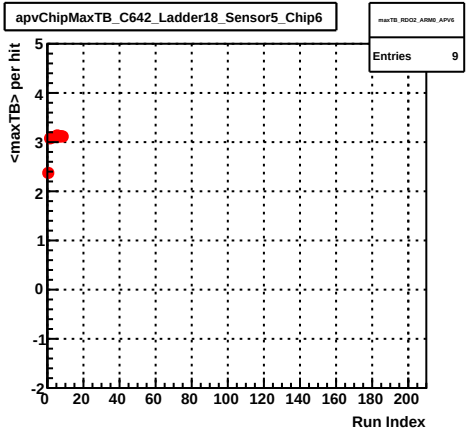

apvChipMaxTB_C637_Ladder18_Sensor5_Chip1
maxTB_RDO2_ARM0_APV11
Entries 9
<maxTB> per hit
Run Index

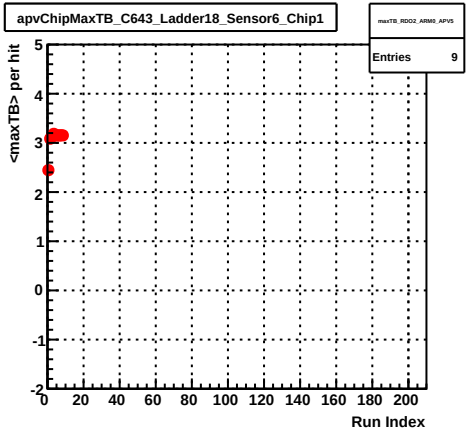

apvChipMaxTB_C638_Ladder18_Sensor5_Chip2
maxTB_RDO2_ARM0_APV10
Entries 9
<maxTB> per hit
Run Index

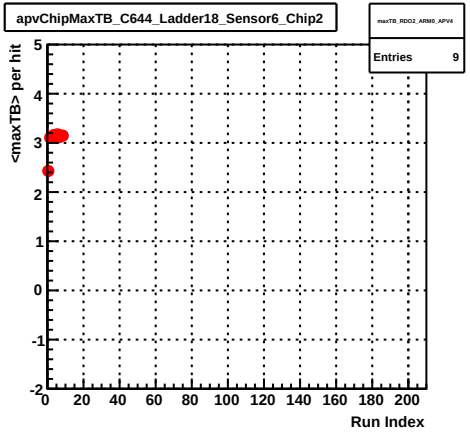

apvChipMaxTB_C639_Ladder18_Sensor5_Chip3
maxTB_RDO2_ARM0_APV9
Entries 9
<maxTB> per hit
Run Index

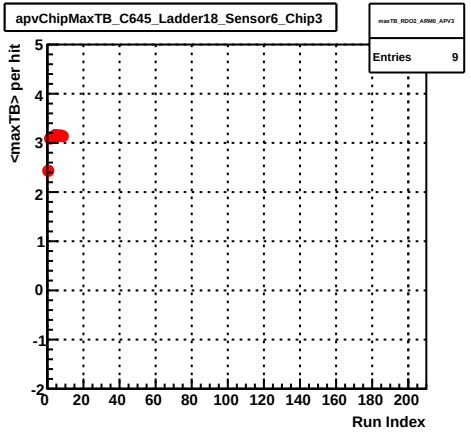

apvChipMaxTB_C640_Ladder18_Sensor5_Chip4
maxTB_RDO2_ARM0_APV8
Entries 9
<maxTB> per hit
Run Index

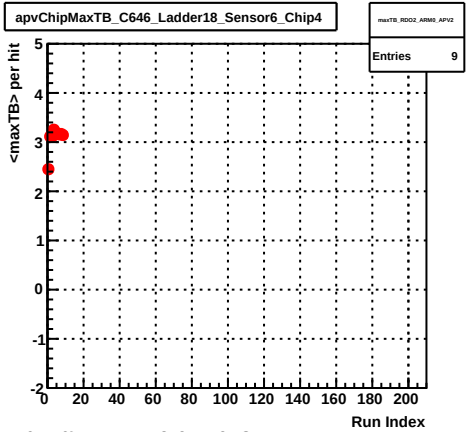

apvChipMaxTB_C641_Ladder18_Sensor5_Chip5
maxTB_RDO2_ARM0_APV7
Entries 9
<maxTB> per hit
Run Index

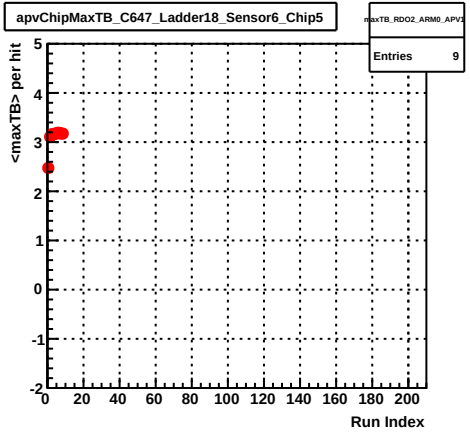

apvChipMaxTB_C642_Ladder18_Sensor5_Chip6
maxTB_RDO2_ARM0_APV6
Entries 9
<maxTB> per hit
Run Index

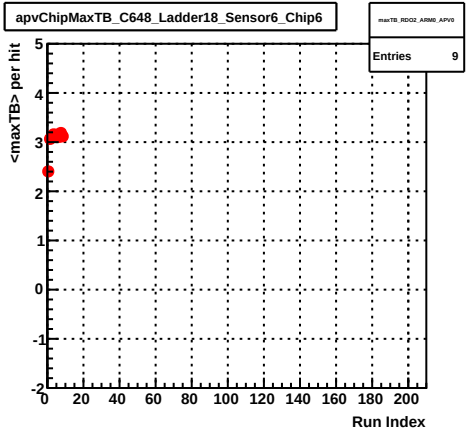

apvChipMaxTB_C643_Ladder18_Sensor6_Chip1
maxTB_RDO2_ARM0_APV5
Entries 9
<maxTB> per hit
Run Index

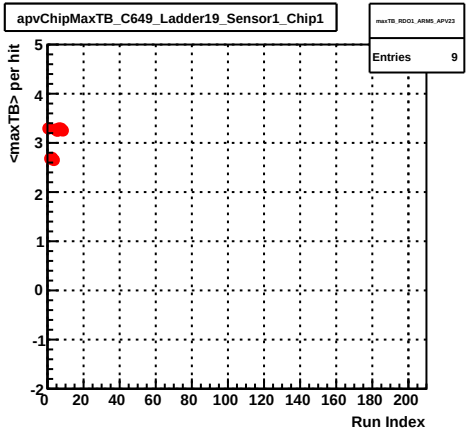

apvChipMaxTB_C644_Ladder18_Sensor6_Chip2
maxTB_RDO2_ARM0_APV4
Entries 9
<maxTB> per hit
Run Index

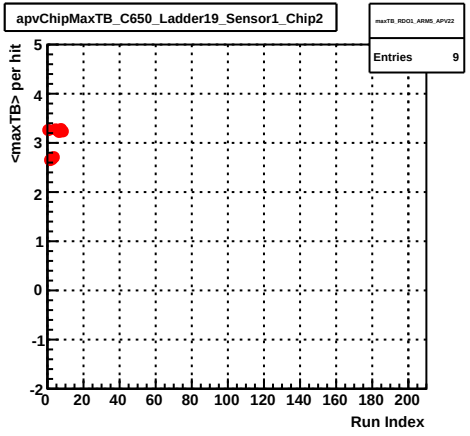

apvChipMaxTB_C645_Ladder18_Sensor6_Chip3
maxTB_RDO2_ARM0_APV3
Entries 9
<maxTB> per hit
Run Index

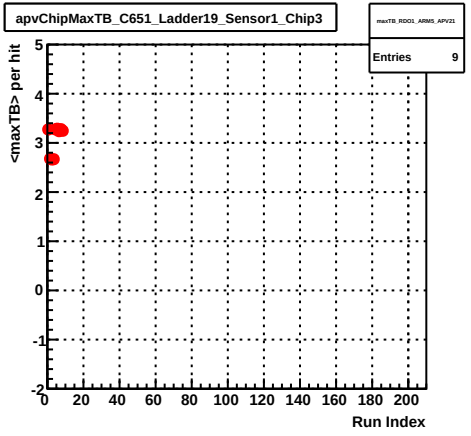

apvChipMaxTB_C646_Ladder18_Sensor6_Chip4
maxTB_RDO2_ARM0_APV2
Entries 9
<maxTB> per hit
Run Index

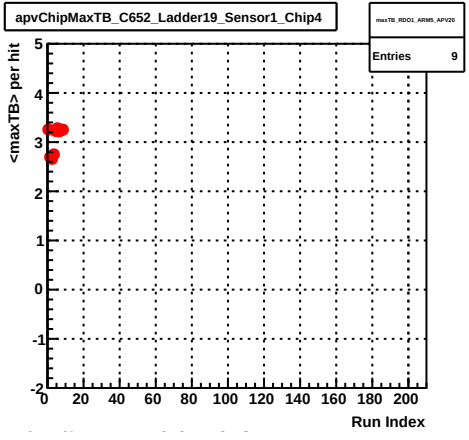

apvChipMaxTB_C647_Ladder18_Sensor6_Chip5
maxTB_RDO2_ARM0_APV1
Entries 9
<maxTB> per hit
Run Index

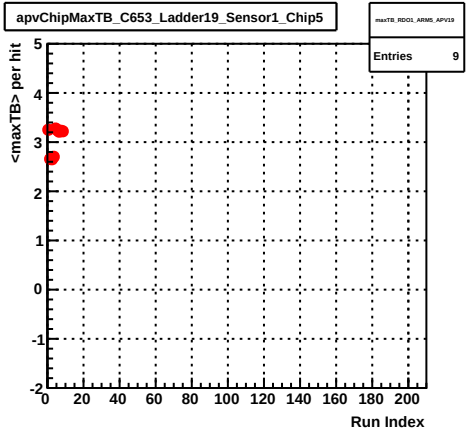

apvChipMaxTB_C648_Ladder18_Sensor6_Chip6
maxTB_RDO2_ARM0_APV0
Entries 9
<maxTB> per hit
Run Index

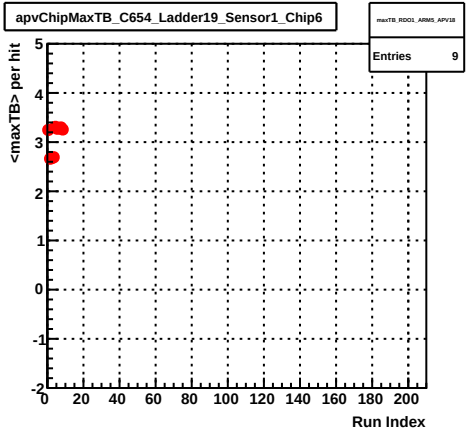

apvChipMaxTB_C649_Ladder19_Sensor1_Chip1
maxTB_RDO1_ARMS_APV23
Entries 9
<maxTB> per hit
Run Index

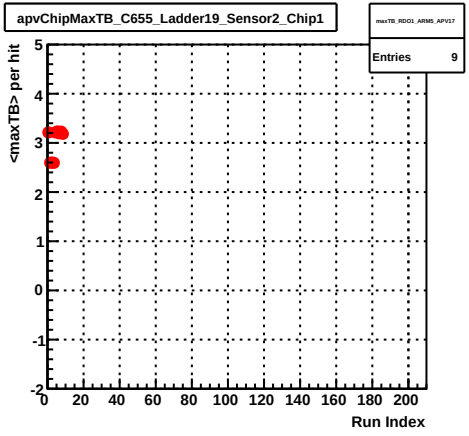

apvChipMaxTB_C650_Ladder19_Sensor1_Chip2
maxTB_RDO1_ARMS_APV22
Entries 9
<maxTB> per hit
Run Index

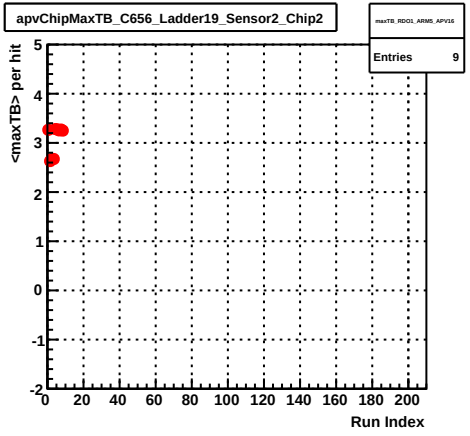

apvChipMaxTB_C651_Ladder19_Sensor1_Chip3
maxTB_RDO1_ARMS_APV21
Entries 9
<maxTB> per hit
Run Index

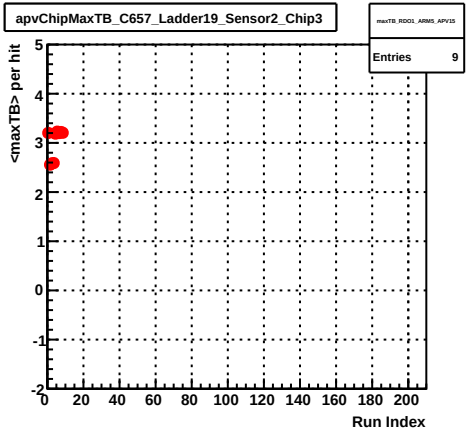

apvChipMaxTB_C652_Ladder19_Sensor1_Chip4
maxTB_RDO1_ARMS_APV20
Entries 9
<maxTB> per hit
Run Index

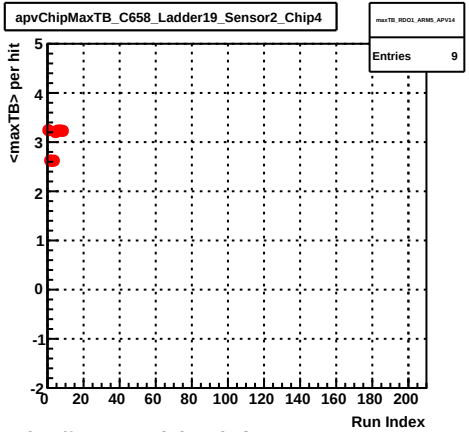

apvChipMaxTB_C653_Ladder19_Sensor1_Chip5
maxTB_RDO1_ARMS_APV19
Entries 9
<maxTB> per hit
Run Index

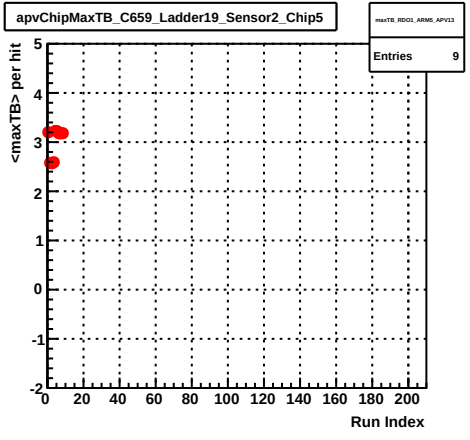

apvChipMaxTB_C654_Ladder19_Sensor1_Chip6
maxTB_RDO1_ARMS_APV18
Entries 9
<maxTB> per hit
Run Index

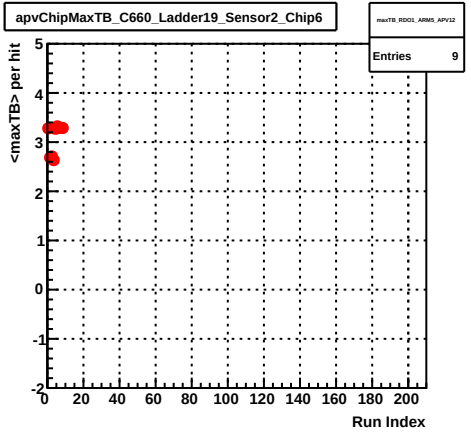

apvChipMaxTB_C655_Ladder19_Sensor2_Chip1
maxTB_RDO1_ARM5_APV17
Entries 9
<maxTB> per hit
Run Index

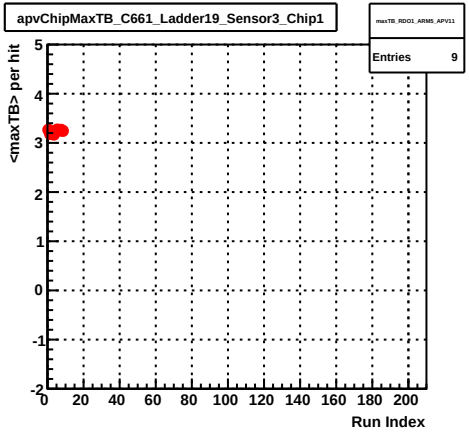

apvChipMaxTB_C656_Ladder19_Sensor2_Chip2
maxTB_RDO1_ARM5_APV16
Entries 9
<maxTB> per hit
Run Index

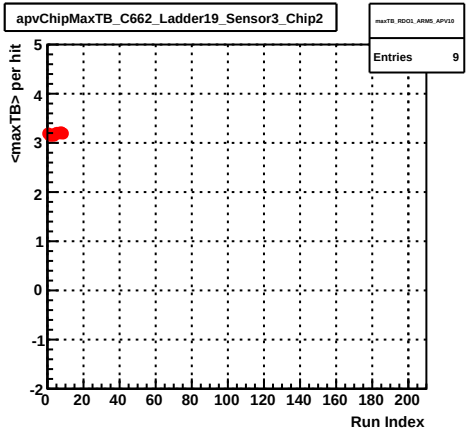

apvChipMaxTB_C657_Ladder19_Sensor2_Chip3
maxTB_RDO1_ARM5_APV15
Entries 9
<maxTB> per hit
Run Index

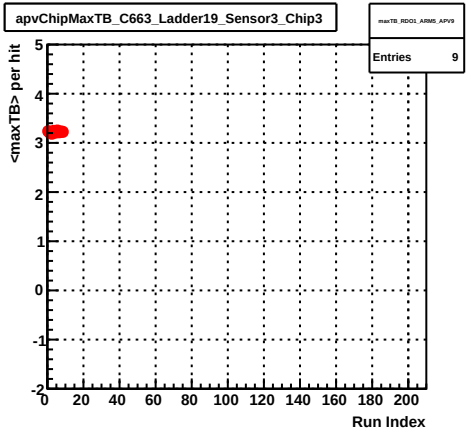

apvChipMaxTB_C658_Ladder19_Sensor2_Chip4
maxTB_RDO1_ARM5_APV14
Entries 9
<maxTB> per hit
Run Index

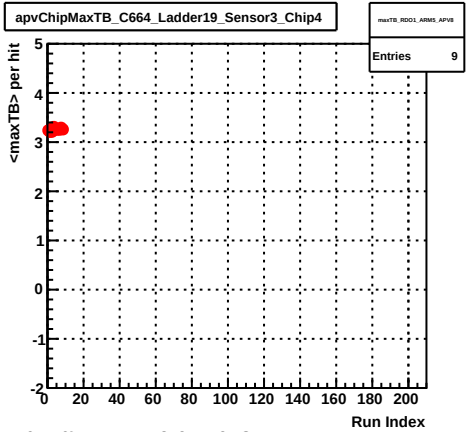

apvChipMaxTB_C659_Ladder19_Sensor2_Chip5
maxTB_RDO1_ARM5_APV13
Entries 9
<maxTB> per hit
Run Index

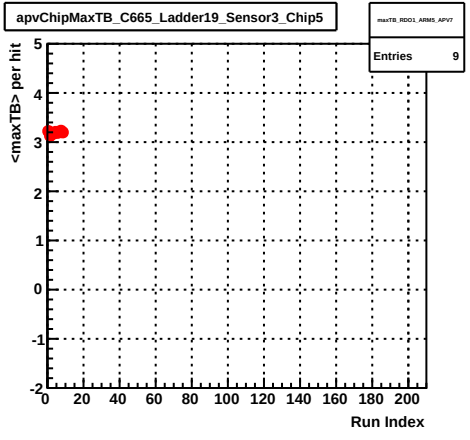

apvChipMaxTB_C660_Ladder19_Sensor2_Chip6
maxTB_RDO1_ARM5_APV12
Entries 9
<maxTB> per hit
Run Index

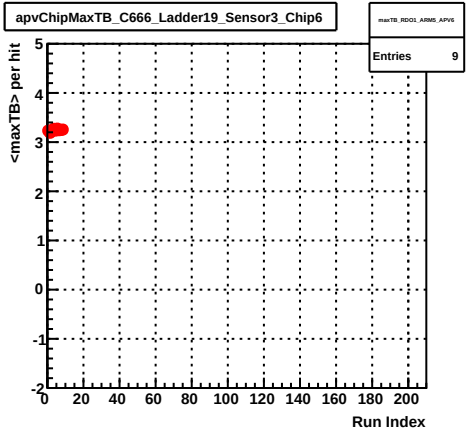

apvChipMaxTB_C661_Ladder19_Sensor3_Chip1
maxTB_RDO1_ARM5_APV11
Entries 9
<maxTB> per hit
Run Index

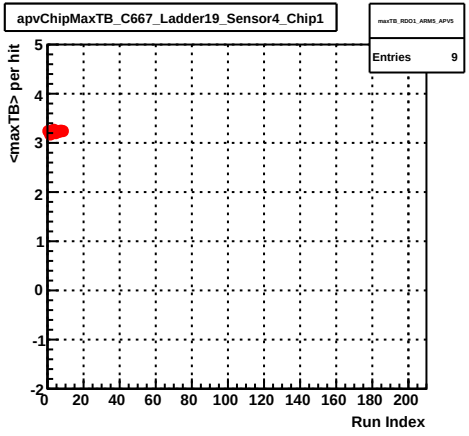

apvChipMaxTB_C662_Ladder19_Sensor3_Chip2
maxTB_RDO1_ARM5_APV10
Entries 9
<maxTB> per hit
Run Index

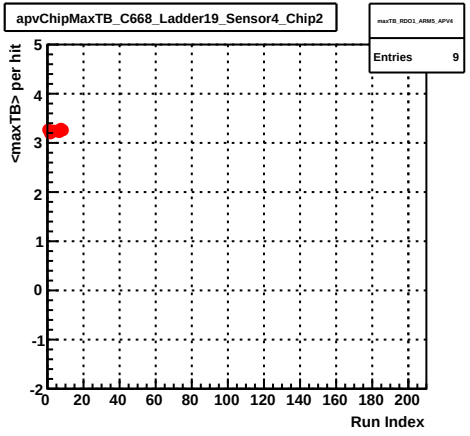

apvChipMaxTB_C663_Ladder19_Sensor3_Chip3
maxTB_RDO1_ARM5_APV9
Entries 9
<maxTB> per hit
Run Index

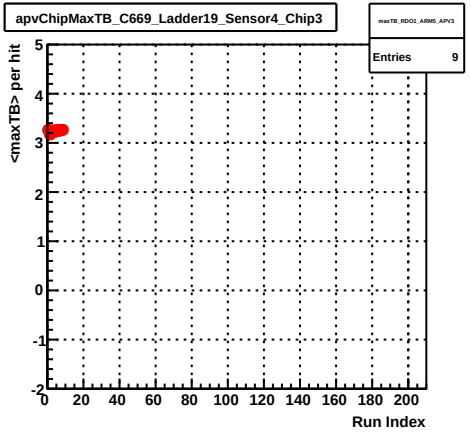

apvChipMaxTB_C664_Ladder19_Sensor3_Chip4
maxTB_RDO1_ARM5_APV8
Entries 9
<maxTB> per hit
Run Index

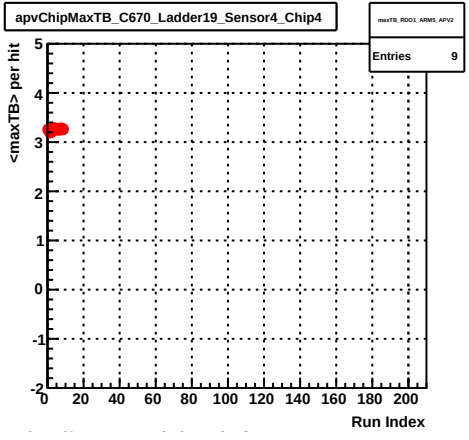

apvChipMaxTB_C665_Ladder19_Sensor3_Chip5
maxTB_RDO1_ARM5_APV7
Entries 9
<maxTB> per hit
Run Index

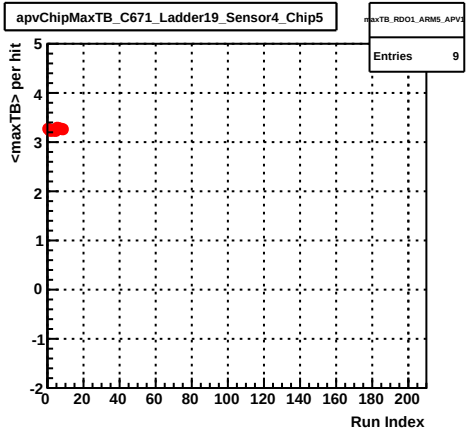

apvChipMaxTB_C666_Ladder19_Sensor3_Chip6
maxTB_RDO1_ARM5_APV6
Entries 9
<maxTB> per hit
Run Index

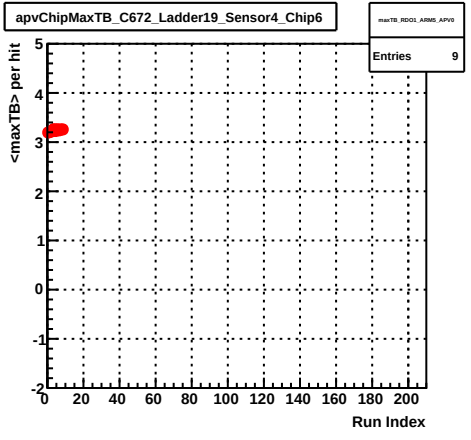

apvChipMaxTB_C667_Ladder19_Sensor4_Chip1
maxTB_RDO1_ARM5_APV5
Entries 9
<maxTB> per hit
Run Index

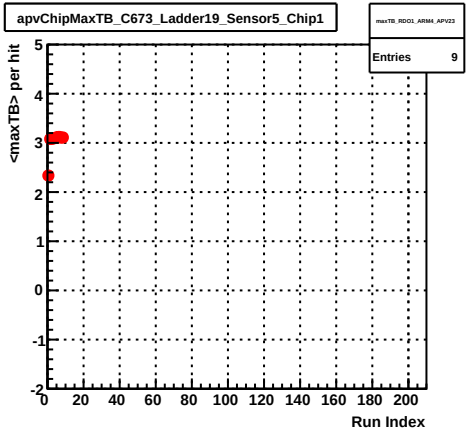

apvChipMaxTB_C668_Ladder19_Sensor4_Chip2
maxTB_RDO1_ARM5_APV4
Entries 9
<maxTB> per hit
Run Index

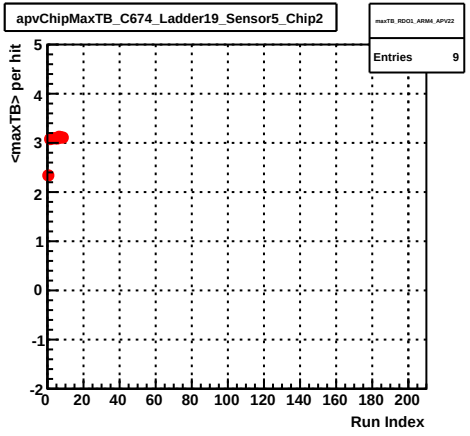

apvChipMaxTB_C669_Ladder19_Sensor4_Chip3
maxTB_RDO1_ARM5_APV3
Entries 9
<maxTB> per hit
Run Index

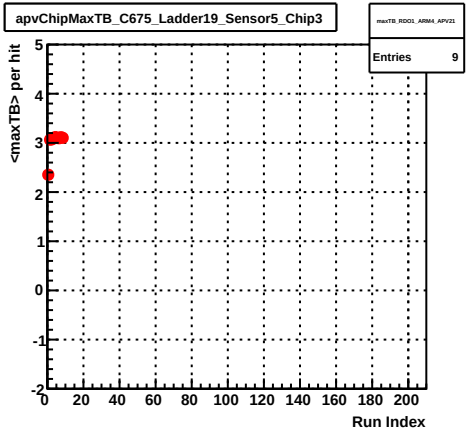

apvChipMaxTB_C670_Ladder19_Sensor4_Chip4
maxTB_RDO1_ARM5_APV2
Entries 9
<maxTB> per hit
Run Index

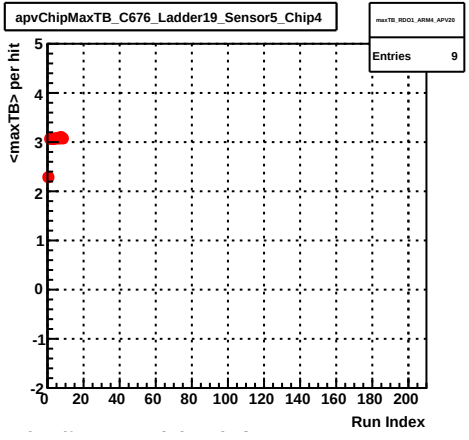

apvChipMaxTB_C671_Ladder19_Sensor4_Chip5
maxTB_RDO1_ARM5_APV1
Entries 9
<maxTB> per hit
Run Index

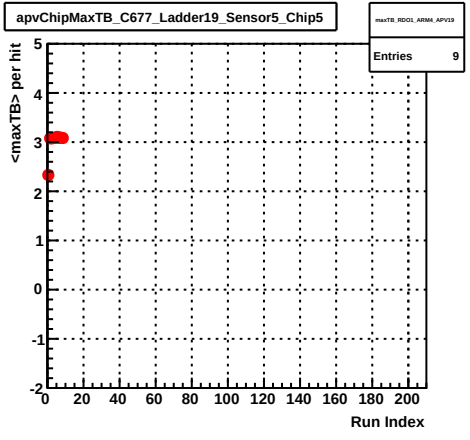

apvChipMaxTB_C672_Ladder19_Sensor4_Chip6
maxTB_RDO1_ARM5_APV0
Entries 9
<maxTB> per hit
Run Index

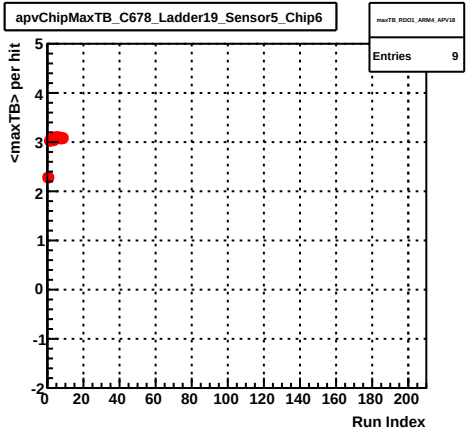

apvChipMaxTB_C673_Ladder19_Sensor5_Chip1
maxTB_RDO1_ARM4_APV23
Entries 9
<maxTB> per hit
Run Index

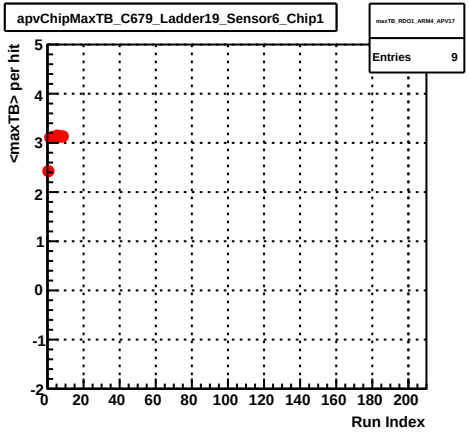

apvChipMaxTB_C674_Ladder19_Sensor5_Chip2
maxTB_RDO1_ARM4_APV22
Entries 9
<maxTB> per hit
Run Index

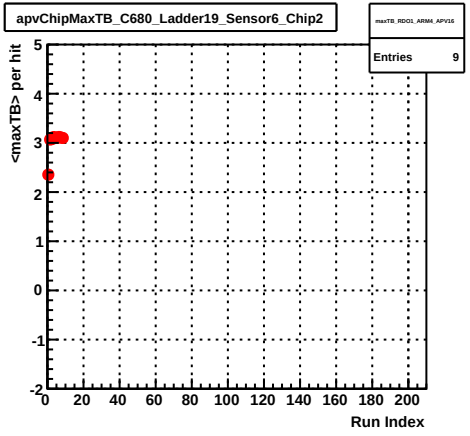

apvChipMaxTB_C675_Ladder19_Sensor5_Chip3
maxTB_RDO1_ARM4_APV21
Entries 9
<maxTB> per hit
Run Index

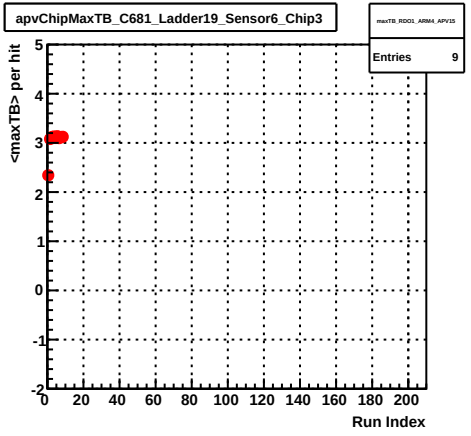

apvChipMaxTB_C676_Ladder19_Sensor5_Chip4
maxTB_RDO1_ARM4_APV20
Entries 9
<maxTB> per hit
Run Index

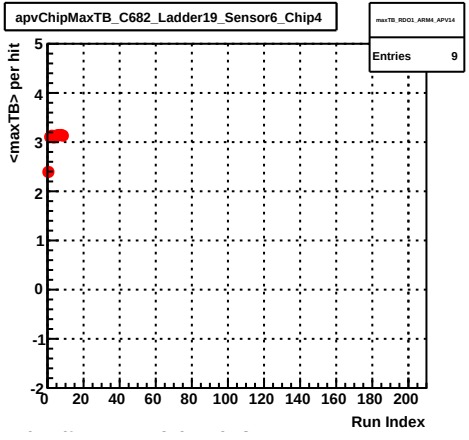

apvChipMaxTB_C677_Ladder19_Sensor5_Chip5
maxTB_RDO1_ARM4_APV19
Entries 9
<maxTB> per hit
Run Index

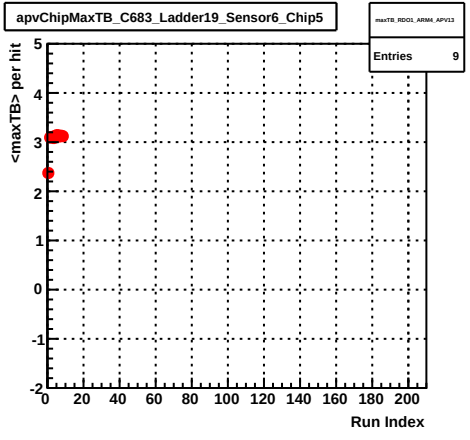

apvChipMaxTB_C678_Ladder19_Sensor5_Chip6
maxTB_RDO1_ARM4_APV18
Entries 9
<maxTB> per hit
Run Index

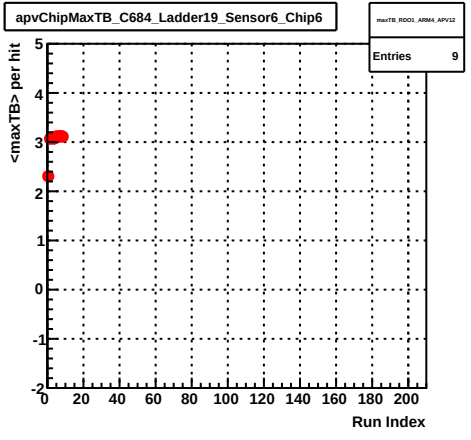

apvChipMaxTB_C679_Ladder19_Sensor6_Chip1
maxTB_RDO1_ARM4_APV17
Entries 9
<maxTB> per hit
Run Index

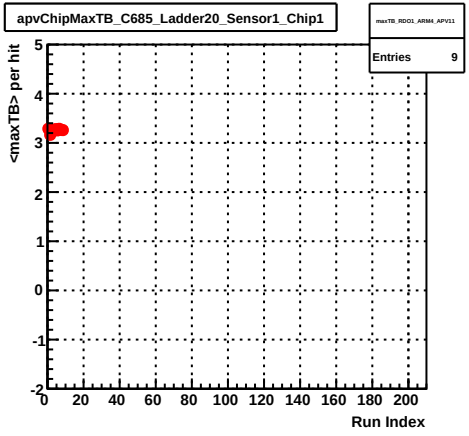

apvChipMaxTB_C680_Ladder19_Sensor6_Chip2
maxTB_RDO1_ARM4_APV16
Entries 9
<maxTB> per hit
Run Index

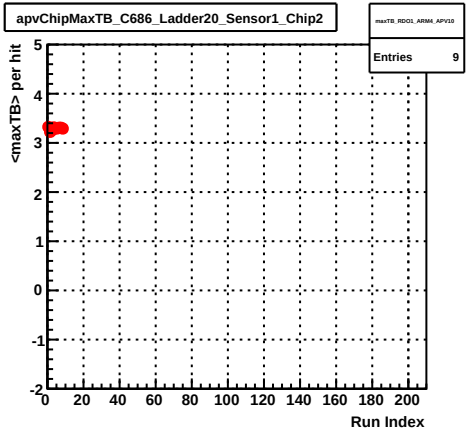

apvChipMaxTB_C681_Ladder19_Sensor6_Chip3
maxTB_RDO1_ARM4_APV15
Entries 9
<maxTB> per hit
Run Index

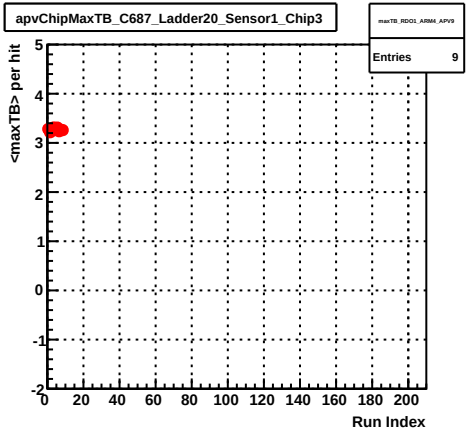

apvChipMaxTB_C682_Ladder19_Sensor6_Chip4
maxTB_RDO1_ARM4_APV14
Entries 9
<maxTB> per hit
Run Index

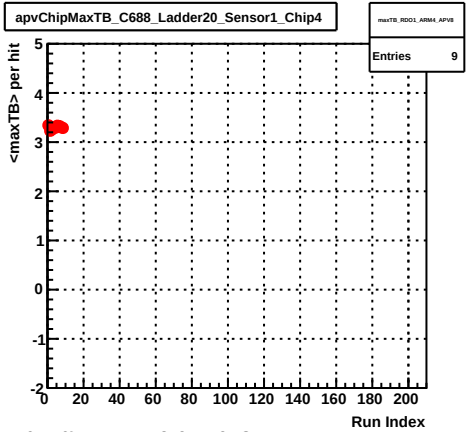

apvChipMaxTB_C683_Ladder19_Sensor6_Chip5
maxTB_RDO1_ARM4_APV13
Entries 9
<maxTB> per hit
Run Index

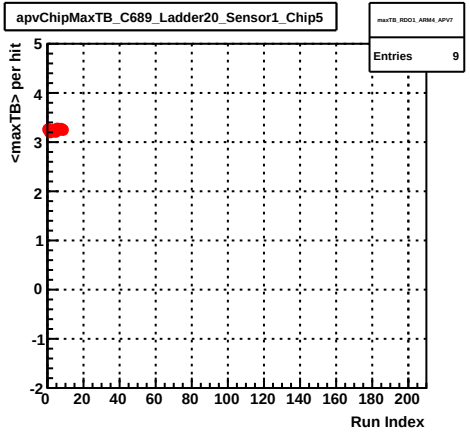

apvChipMaxTB_C684_Ladder19_Sensor6_Chip6
maxTB_RDO1_ARM4_APV12
Entries 9
<maxTB> per hit
Run Index

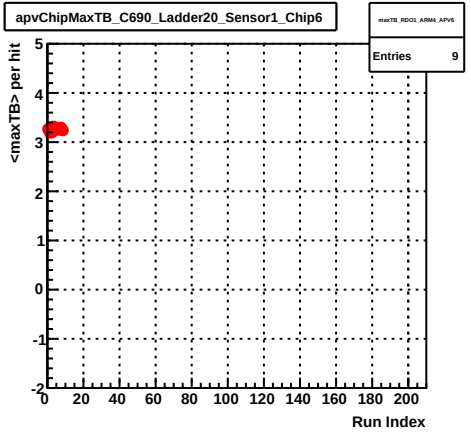

apvChipMaxTB_C685_Ladder20_Sensor1_Chip1
maxTB_RDO1_ARM4_APV11
Entries 9
<maxTB> per hit
Run Index

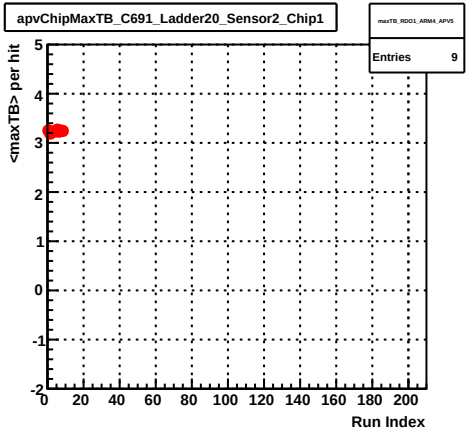

apvChipMaxTB_C686_Ladder20_Sensor1_Chip2
maxTB_RDO1_ARM4_APV10
Entries 9
<maxTB> per hit
Run Index

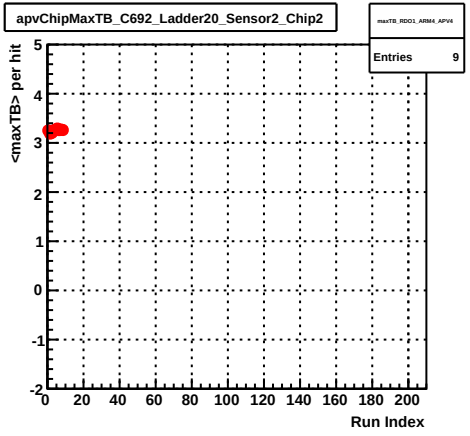

apvChipMaxTB_C687_Ladder20_Sensor1_Chip3
maxTB_RDO1_ARM4_APV9
Entries 9
<maxTB> per hit
Run Index

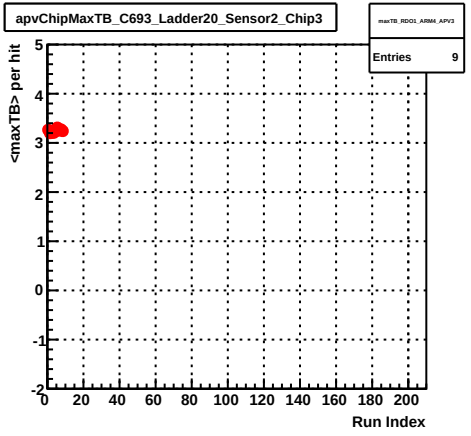

apvChipMaxTB_C688_Ladder20_Sensor1_Chip4
maxTB_RDO1_ARM4_APV8
Entries 9
<maxTB> per hit
Run Index

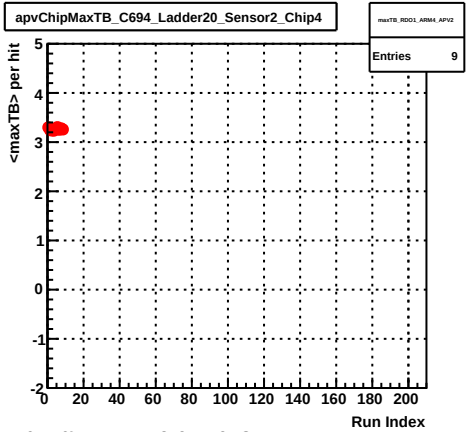

apvChipMaxTB_C689_Ladder20_Sensor1_Chip5
maxTB_RDO1_ARM4_APV7
Entries 9
<maxTB> per hit
Run Index

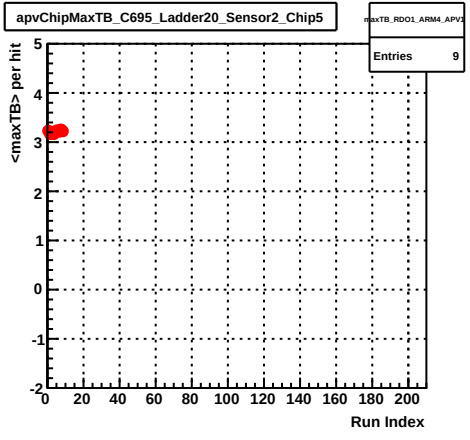

apvChipMaxTB_C690_Ladder20_Sensor1_Chip6
maxTB_RDO1_ARM4_APV6
Entries 9
<maxTB> per hit
Run Index

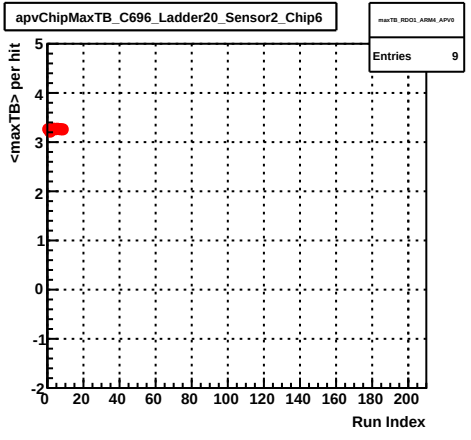

apvChipMaxTB_C691_Ladder20_Sensor2_Chip1
maxTB_RDO1_ARM4_APV5
Entries 9
<maxTB> per hit
Run Index

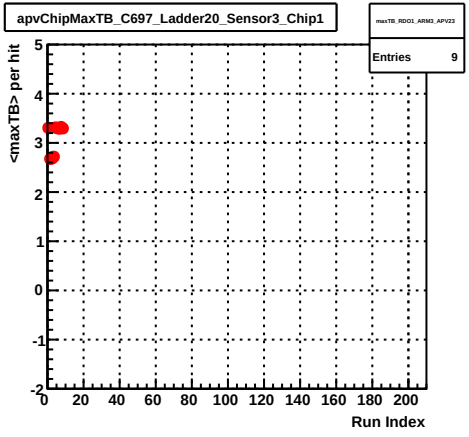

apvChipMaxTB_C692_Ladder20_Sensor2_Chip2
maxTB_RDO1_ARM4_APV4
Entries 9
<maxTB> per hit
Run Index

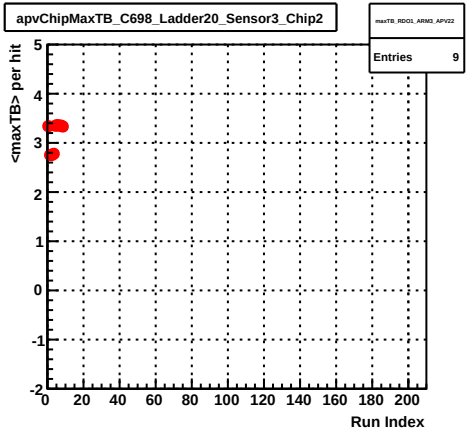

apvChipMaxTB_C693_Ladder20_Sensor2_Chip3
maxTB_RDO1_ARM4_APV3
Entries 9
<maxTB> per hit
Run Index

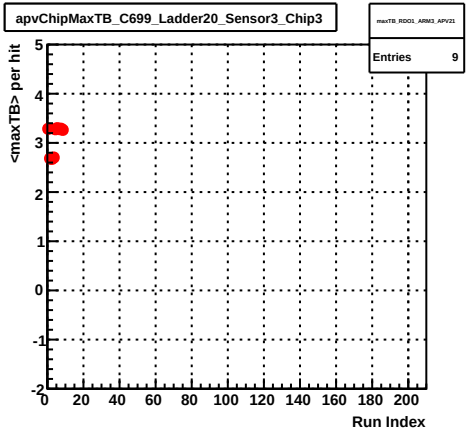

apvChipMaxTB_C694_Ladder20_Sensor2_Chip4
maxTB_RDO1_ARM4_APV2
Entries 9
<maxTB> per hit
Run Index

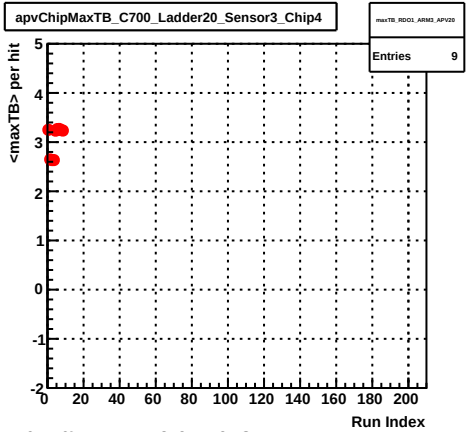

apvChipMaxTB_C695_Ladder20_Sensor2_Chip5
maxTB_RDO1_ARM4_APV1
Entries 9
<maxTB> per hit
Run Index

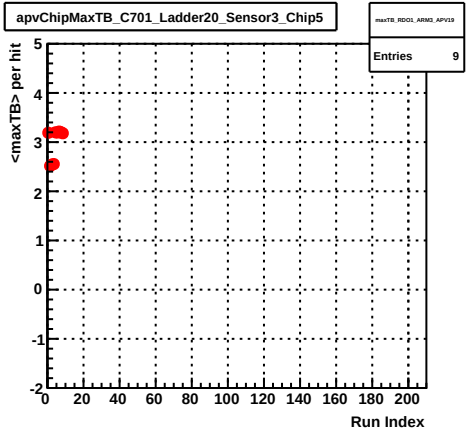

apvChipMaxTB_C696_Ladder20_Sensor2_Chip6
maxTB_RDO1_ARM4_APV0
Entries 9
<maxTB> per hit
Run Index

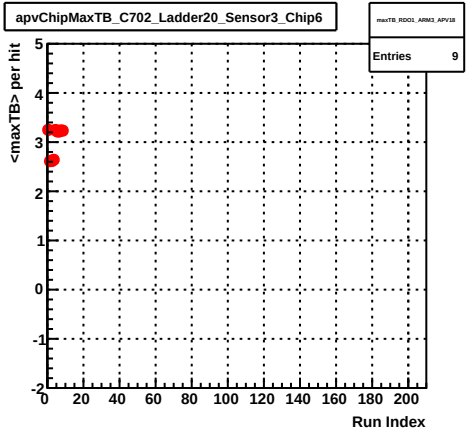

apvChipMaxTB_C697_Ladder20_Sensor3_Chip1
maxTB_RDO1_ARM3_APV23
Entries 9
<maxTB> per hit
Run Index

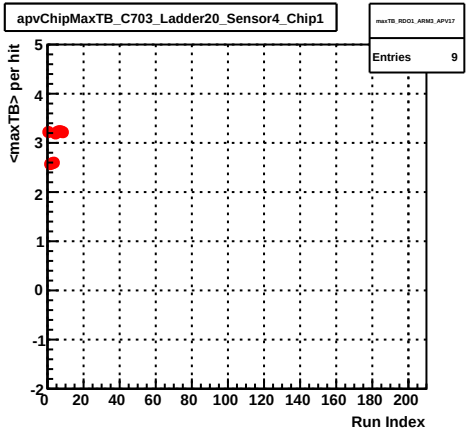

apvChipMaxTB_C698_Ladder20_Sensor3_Chip2
maxTB_RDO1_ARM3_APV22
Entries 9
<maxTB> per hit
Run Index

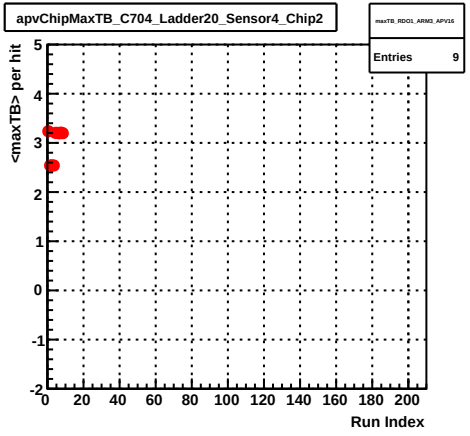

apvChipMaxTB_C699_Ladder20_Sensor3_Chip3
maxTB_RDO1_ARM3_APV21
Entries 9
<maxTB> per hit
Run Index

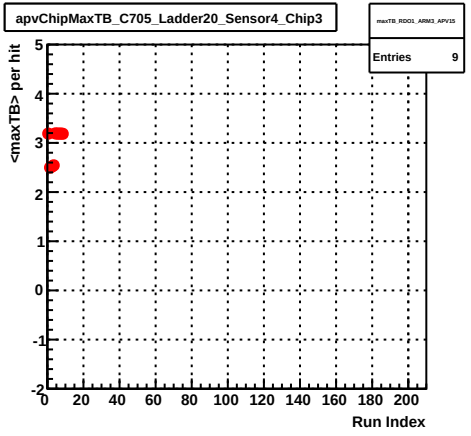

apvChipMaxTB_C700_Ladder20_Sensor3_Chip4
maxTB_RDO1_ARM3_APV20
Entries 9
<maxTB> per hit
Run Index

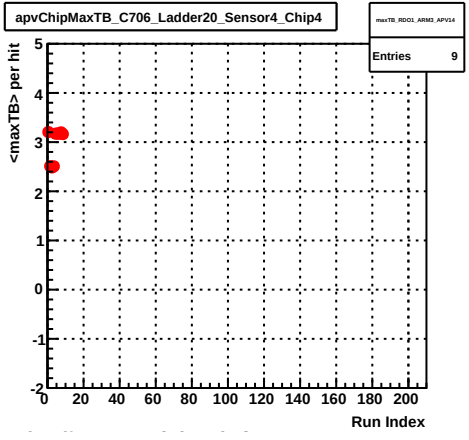

apvChipMaxTB_C701_Ladder20_Sensor3_Chip5
maxTB_RDO1_ARM3_APV19
Entries 9
<maxTB> per hit
Run Index

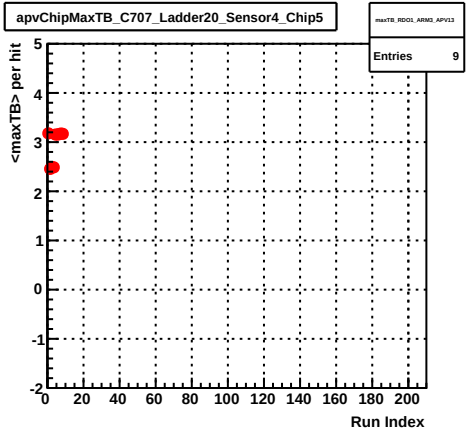

apvChipMaxTB_C702_Ladder20_Sensor3_Chip6
maxTB_RDO1_ARM3_APV18
Entries 9
<maxTB> per hit
Run Index

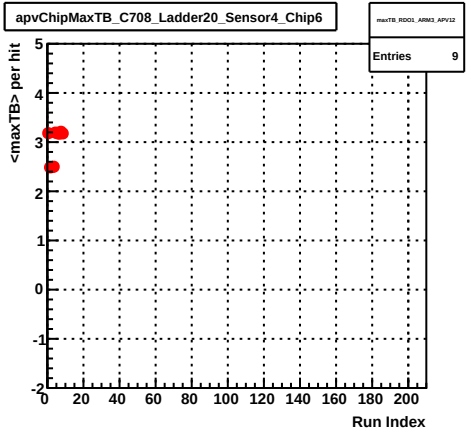

apvChipMaxTB_C703_Ladder20_Sensor4_Chip1
maxTB_RDO1_ARM3_APV17
Entries 9
<maxTB> per hit
Run Index

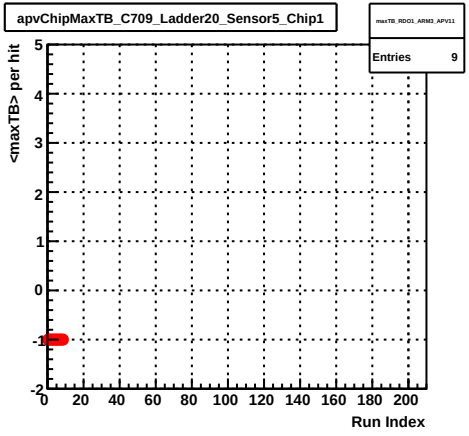

apvChipMaxTB_C704_Ladder20_Sensor4_Chip2
maxTB_RDO1_ARM3_APV16
Entries 9
<maxTB> per hit
Run Index

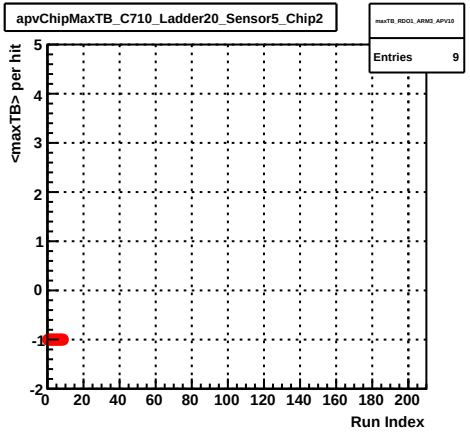

apvChipMaxTB_C705_Ladder20_Sensor4_Chip3
maxTB_RDO1_ARM3_APV15
Entries 9
<maxTB> per hit
Run Index

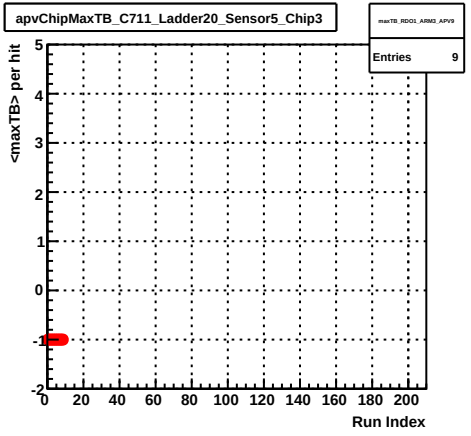

apvChipMaxTB_C706_Ladder20_Sensor4_Chip4
maxTB_RDO1_ARM3_APV14
Entries 9
<maxTB> per hit
Run Index

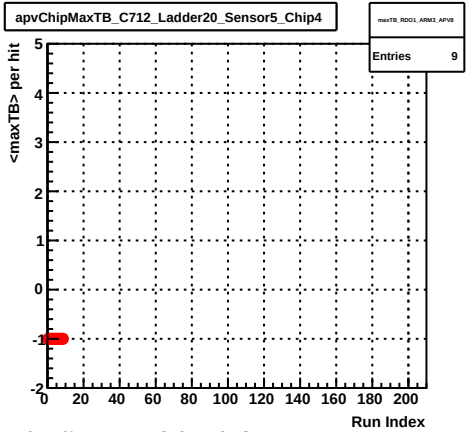

apvChipMaxTB_C707_Ladder20_Sensor4_Chip5
maxTB_RDO1_ARM3_APV13
Entries 9
<maxTB> per hit
Run Index

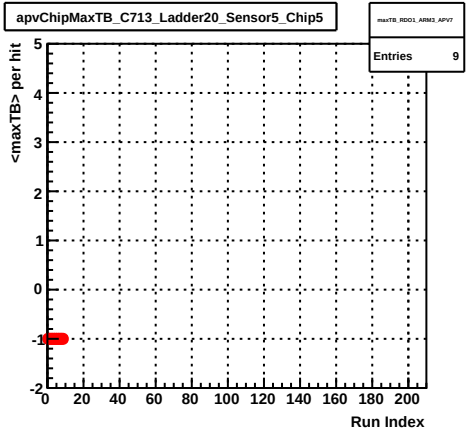

apvChipMaxTB_C708_Ladder20_Sensor4_Chip6
maxTB_RDO1_ARM3_APV12
Entries 9
<maxTB> per hit
Run Index

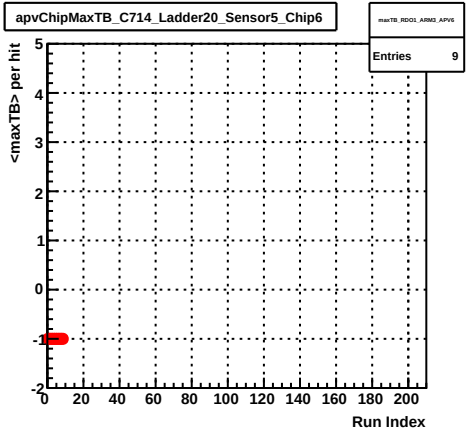

apvChipMaxTB_C709_Ladder20_Sensor5_Chip1
maxTB_RDO1_ARM3_APV11
Entries 9
<maxTB> per hit
Run Index

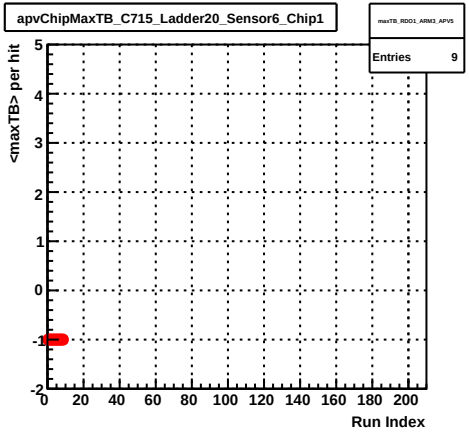

apvChipMaxTB_C710_Ladder20_Sensor5_Chip2
maxTB_RDO1_ARM3_APV10
Entries 9
<maxTB> per hit
Run Index

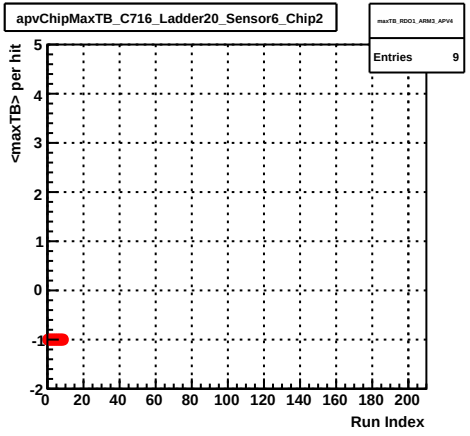

apvChipMaxTB_C711_Ladder20_Sensor5_Chip3
maxTB_RDO1_ARM3_APV9
Entries 9
<maxTB> per hit
Run Index

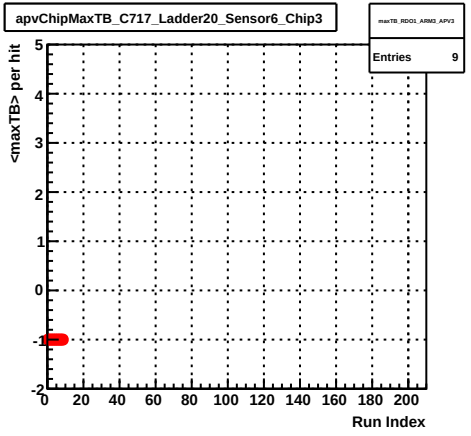

apvChipMaxTB_C712_Ladder20_Sensor5_Chip4
maxTB_RDO1_ARM3_APV8
Entries 9
<maxTB> per hit
Run Index

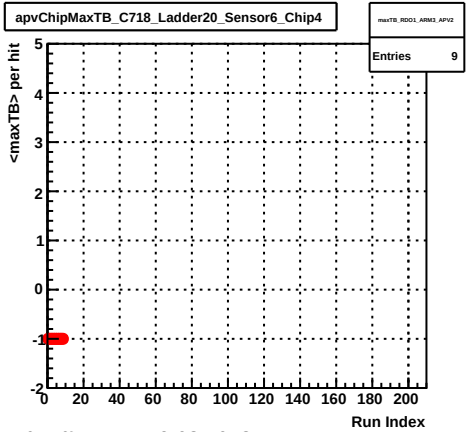

apvChipMaxTB_C713_Ladder20_Sensor5_Chip5
maxTB_RDO1_ARM3_APV7
Entries 9
<maxTB> per hit
Run Index

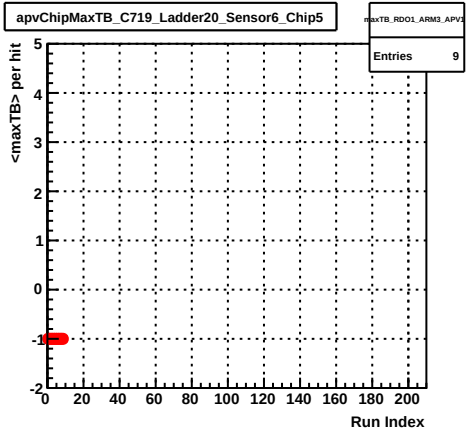

apvChipMaxTB_C714_Ladder20_Sensor5_Chip6
maxTB_RDO1_ARM3_APV6
Entries 9
<maxTB> per hit
Run Index

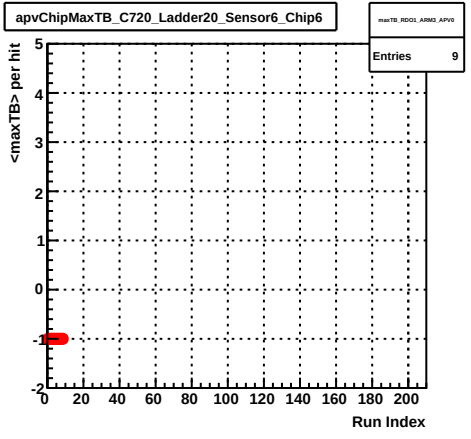

apvChipMaxTB_C715_Ladder20_Sensor6_Chip1
maxTB_RDO1_ARM3_APV5
Entries 9
<maxTB> per hit
Run Index

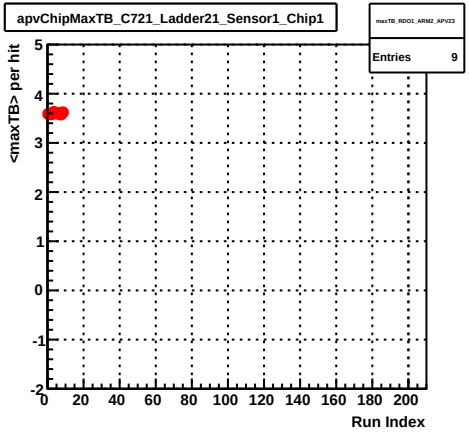

apvChipMaxTB_C716_Ladder20_Sensor6_Chip2
maxTB_RDO1_ARM3_APV4
Entries 9
<maxTB> per hit
Run Index

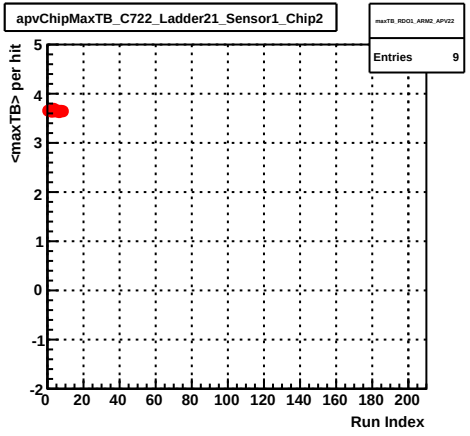

apvChipMaxTB_C717_Ladder20_Sensor6_Chip3
maxTB_RDO1_ARM3_APV3
Entries 9
<maxTB> per hit
Run Index

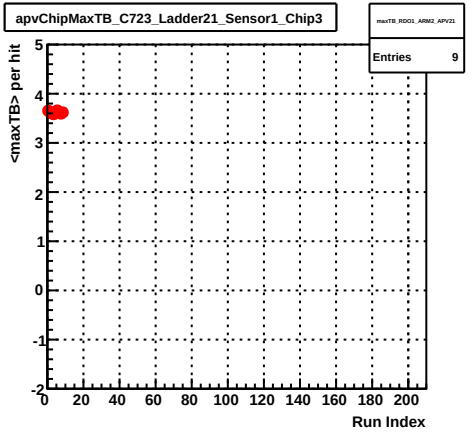

apvChipMaxTB_C718_Ladder20_Sensor6_Chip4
maxTB_RDO1_ARM3_APV2
Entries 9
<maxTB> per hit
Run Index

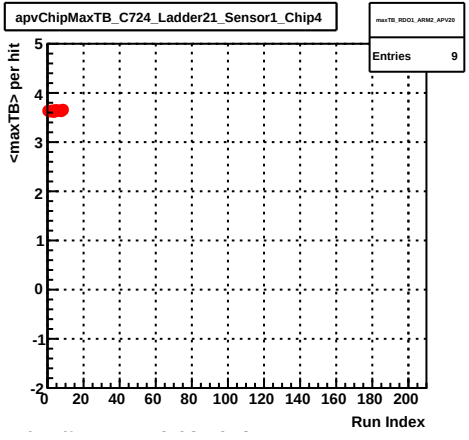

apvChipMaxTB_C719_Ladder20_Sensor6_Chip5
maxTB_RDO1_ARM3_APV1
Entries 9
<maxTB> per hit
Run Index

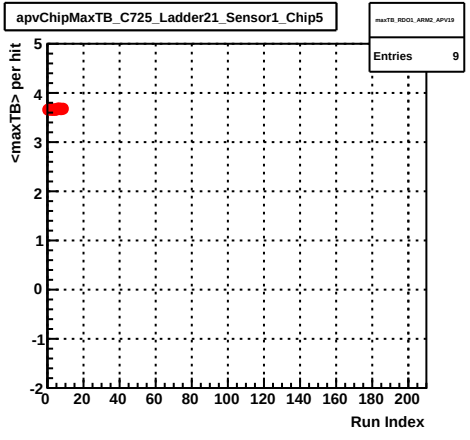

apvChipMaxTB_C720_Ladder20_Sensor6_Chip6
maxTB_RDO1_ARM3_APV0
Entries 9
<maxTB> per hit
Run Index

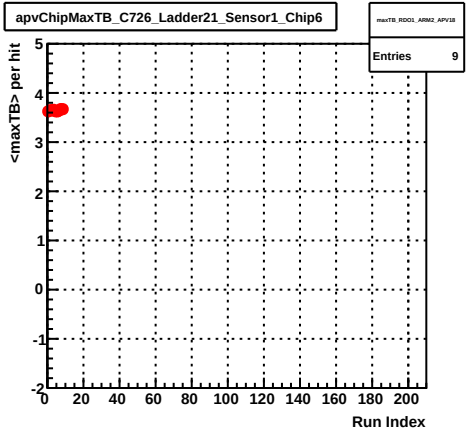

apvChipMaxTB_C721_Ladder21_Sensor1_Chip1
maxTB_RDO1_ARM2_APV23
Entries 9
<maxTB> per hit
Run Index

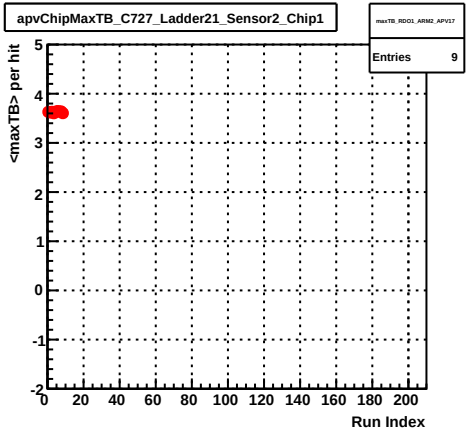

apvChipMaxTB_C722_Ladder21_Sensor1_Chip2
maxTB_RDO1_ARM2_APV22
Entries 9
<maxTB> per hit
Run Index

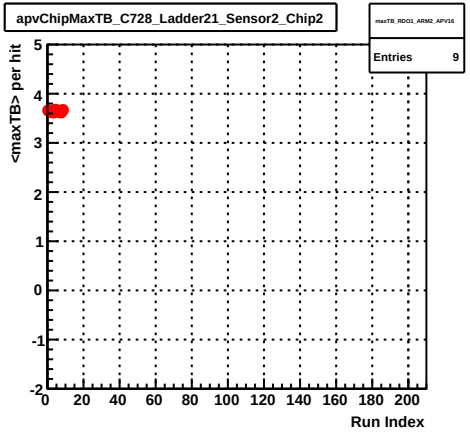

apvChipMaxTB_C723_Ladder21_Sensor1_Chip3
maxTB_RDO1_ARM2_APV21
Entries 9
<maxTB> per hit
Run Index

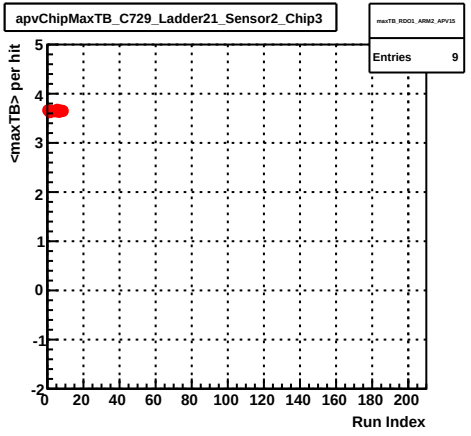

apvChipMaxTB_C724_Ladder21_Sensor1_Chip4
maxTB_RDO1_ARM2_APV20
Entries 9
<maxTB> per hit
Run Index

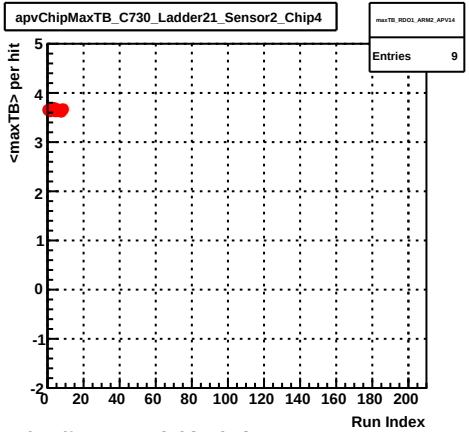

apvChipMaxTB_C725_Ladder21_Sensor1_Chip5
maxTB_RDO1_ARM2_APV19
Entries 9
<maxTB> per hit
Run Index

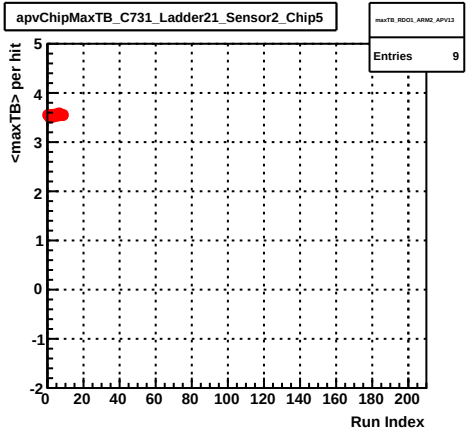

apvChipMaxTB_C726_Ladder21_Sensor1_Chip6
maxTB_RDO1_ARM2_APV18
Entries 9
<maxTB> per hit
Run Index

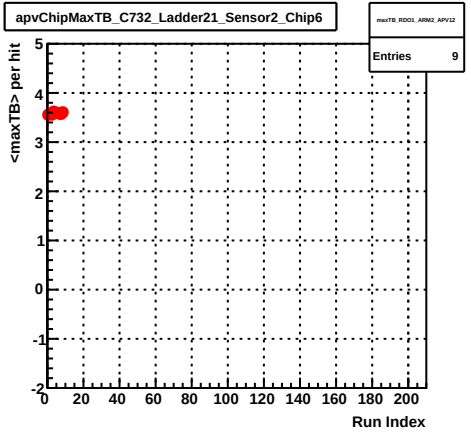

apvChipMaxTB_C727_Ladder21_Sensor2_Chip1
maxTB_RDO1_ARM2_APV17
Entries 9
<maxTB> per hit
Run Index

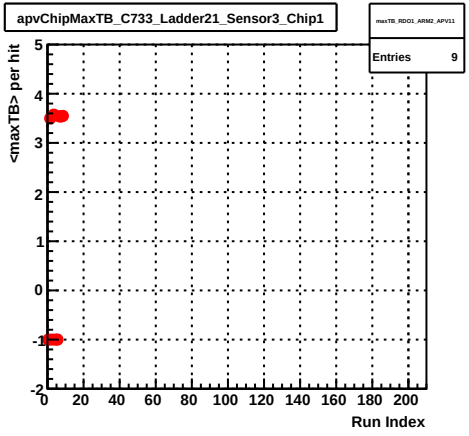

apvChipMaxTB_C728_Ladder21_Sensor2_Chip2
maxTB_RDO1_ARM2_APV16
Entries 9
<maxTB> per hit
Run Index

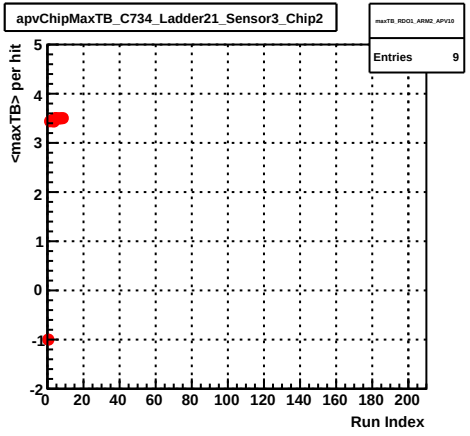

apvChipMaxTB_C729_Ladder21_Sensor2_Chip3
maxTB_RDO1_ARM2_APV15
Entries 9
<maxTB> per hit
Run Index

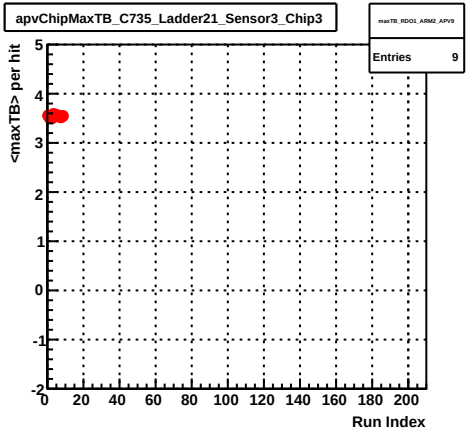

apvChipMaxTB_C730_Ladder21_Sensor2_Chip4
maxTB_RDO1_ARM2_APV14
Entries 9
<maxTB> per hit
Run Index

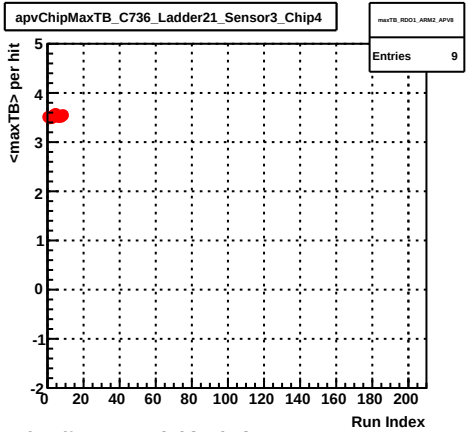

apvChipMaxTB_C731_Ladder21_Sensor2_Chip5
maxTB_RDO1_ARM2_APV13
Entries 9
<maxTB> per hit
Run Index

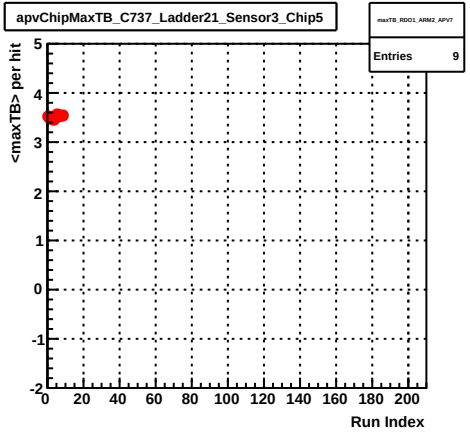

apvChipMaxTB_C732_Ladder21_Sensor2_Chip6
maxTB_RDO1_ARM2_APV12
Entries 9
<maxTB> per hit
Run Index

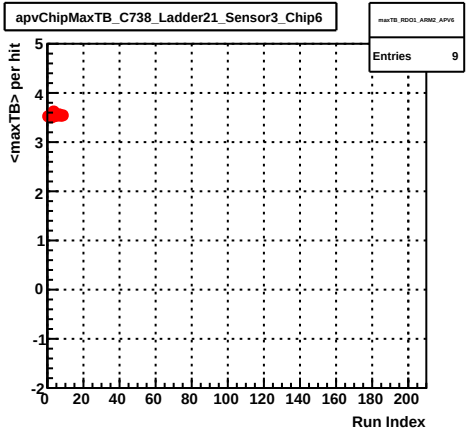

apvChipMaxTB_C733_Ladder21_Sensor3_Chip1
maxTB_RDO1_ARM2_APV11
Entries 9
<maxTB> per hit
Run Index

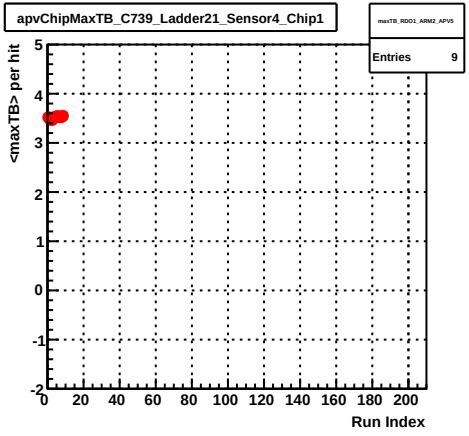

apvChipMaxTB_C734_Ladder21_Sensor3_Chip2
maxTB_RDO1_ARM2_APV10
Entries 9
<maxTB> per hit
Run Index

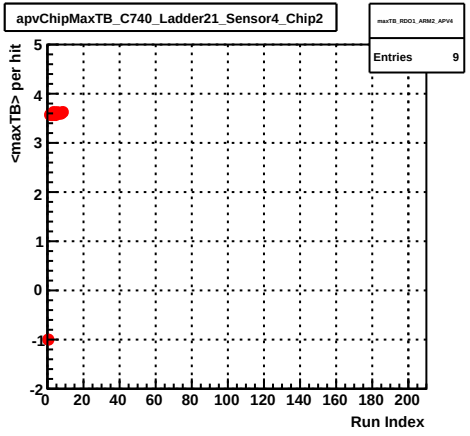

apvChipMaxTB_C735_Ladder21_Sensor3_Chip3
maxTB_RDO1_ARM2_APV9
Entries 9
<maxTB> per hit
Run Index

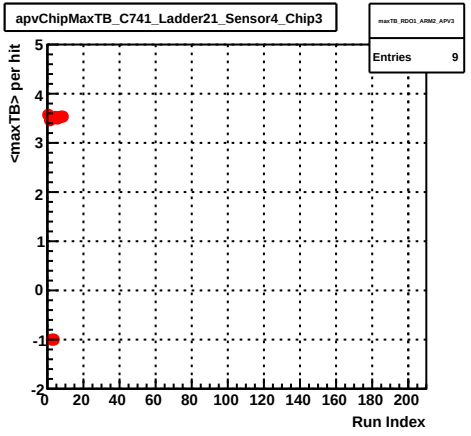

apvChipMaxTB_C736_Ladder21_Sensor3_Chip4
maxTB_RDO1_ARM2_APV8
Entries 9
<maxTB> per hit
Run Index

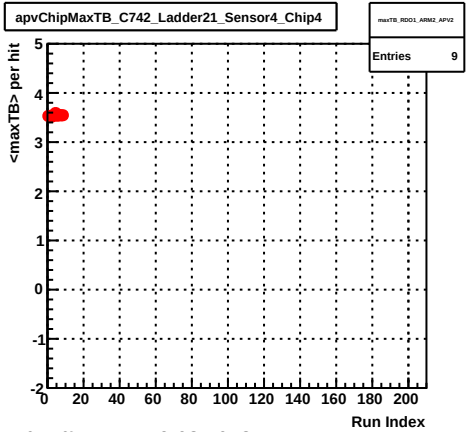

apvChipMaxTB_C737_Ladder21_Sensor3_Chip5
maxTB_RDO1_ARM2_APV7
Entries 9
<maxTB> per hit
Run Index

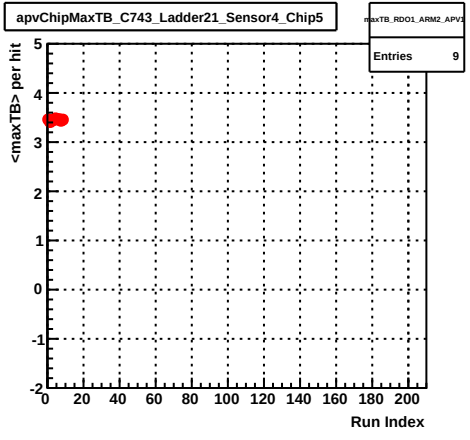

apvChipMaxTB_C738_Ladder21_Sensor3_Chip6
maxTB_RDO1_ARM2_APV6
Entries 9
<maxTB> per hit
Run Index

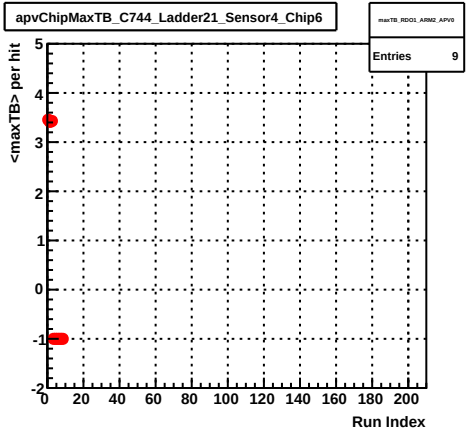

apvChipMaxTB_C739_Ladder21_Sensor4_Chip1
maxTB_RDO1_ARM2_APV5
Entries 9
<maxTB> per hit
Run Index

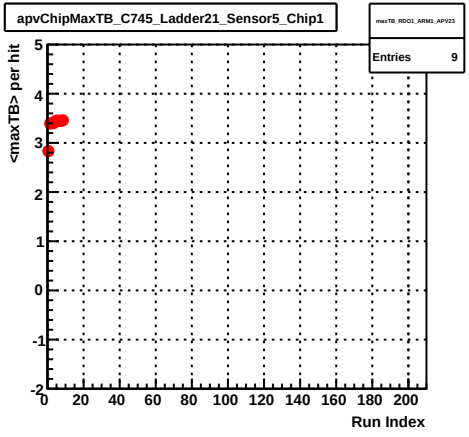

apvChipMaxTB_C740_Ladder21_Sensor4_Chip2
maxTB_RDO1_ARM2_APV4
Entries 9
<maxTB> per hit
Run Index

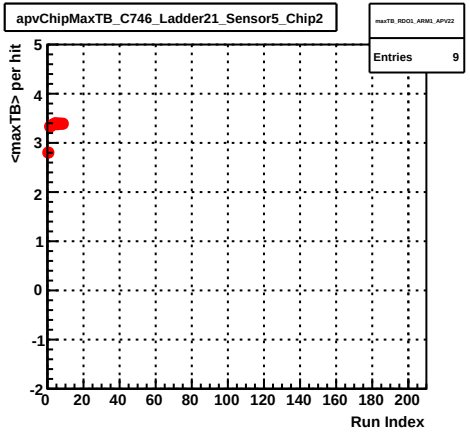

apvChipMaxTB_C741_Ladder21_Sensor4_Chip3
maxTB_RDO1_ARM2_APV3
Entries 9
<maxTB> per hit
Run Index

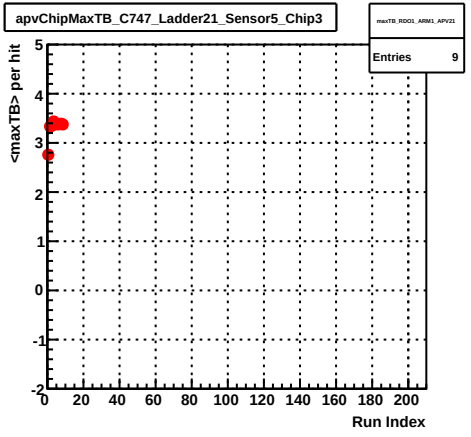

apvChipMaxTB_C742_Ladder21_Sensor4_Chip4
maxTB_RDO1_ARM2_APV2
Entries 9
<maxTB> per hit
Run Index

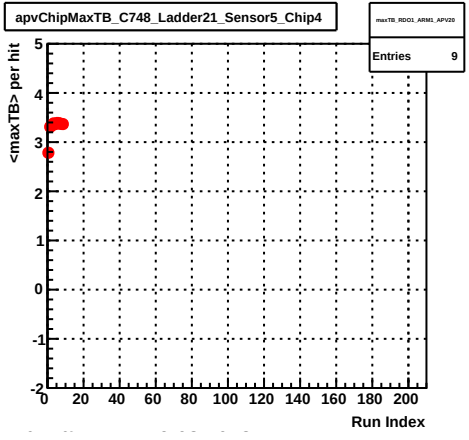

apvChipMaxTB_C743_Ladder21_Sensor4_Chip5
maxTB_RDO1_ARM2_APV1
Entries 9
<maxTB> per hit
Run Index

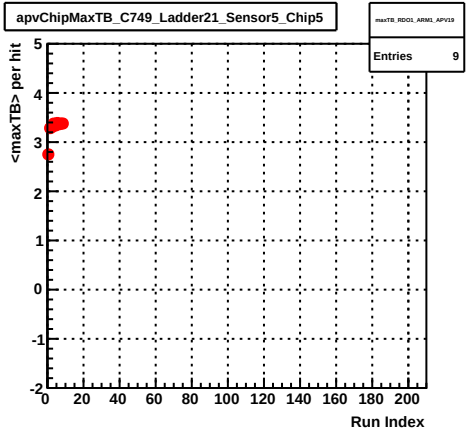

apvChipMaxTB_C744_Ladder21_Sensor4_Chip6
maxTB_RDO1_ARM2_APV0
Entries 9
<maxTB> per hit
Run Index

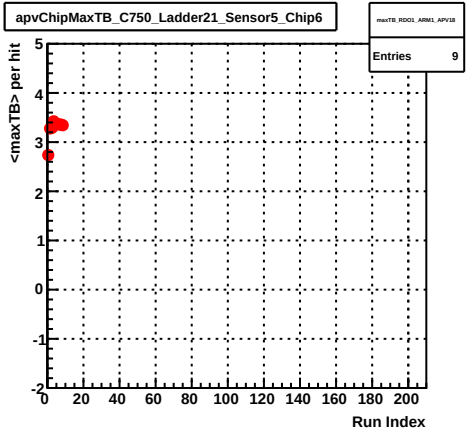

apvChipMaxTB_C745_Ladder21_Sensor5_Chip1
maxTB_RDO1_ARM1_APV23
Entries 9
<maxTB> per hit
Run Index

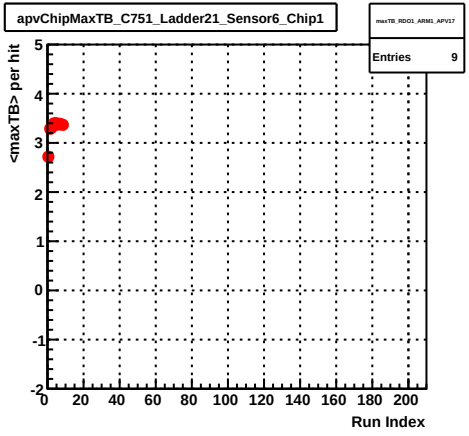

apvChipMaxTB_C746_Ladder21_Sensor5_Chip2
maxTB_RDO1_ARM1_APV22
Entries 9
<maxTB> per hit
Run Index

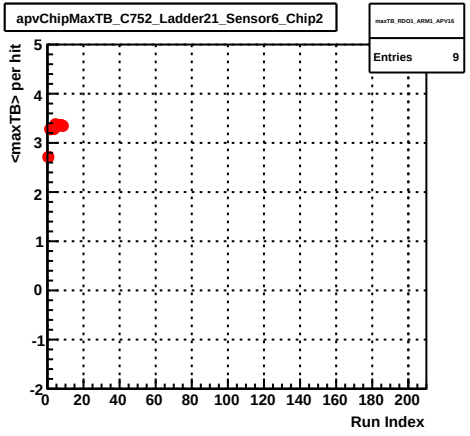

apvChipMaxTB_C747_Ladder21_Sensor5_Chip3
maxTB_RDO1_ARM1_APV21
Entries 9
<maxTB> per hit
Run Index

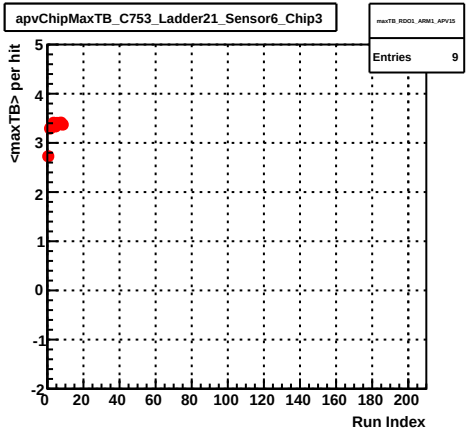

apvChipMaxTB_C748_Ladder21_Sensor5_Chip4
maxTB_RDO1_ARM1_APV20
Entries 9
<maxTB> per hit
Run Index

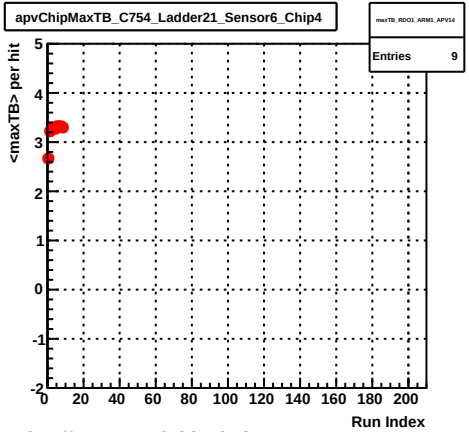

apvChipMaxTB_C749_Ladder21_Sensor5_Chip5
maxTB_RDO1_ARM1_APV19
Entries 9
<maxTB> per hit
Run Index

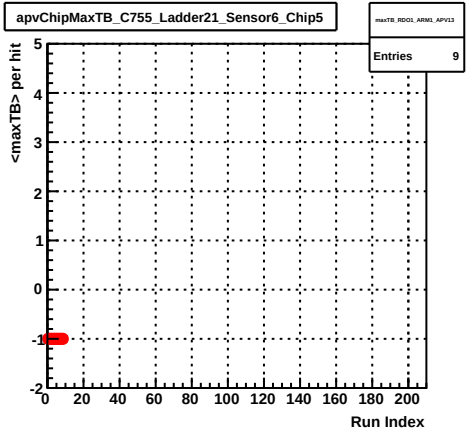

apvChipMaxTB_C750_Ladder21_Sensor5_Chip6
maxTB_RDO1_ARM1_APV18
Entries 9
<maxTB> per hit
Run Index

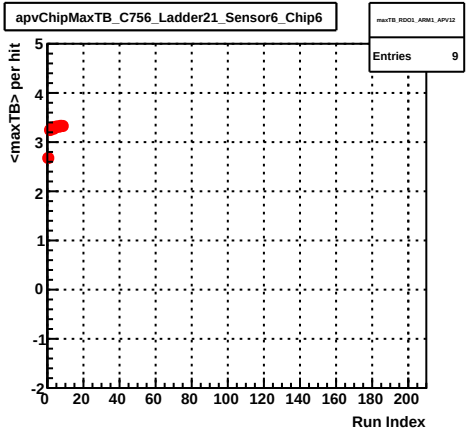

apvChipMaxTB_C751_Ladder21_Sensor6_Chip1
maxTB_RDO1_ARM1_APV17
Entries 9
<maxTB> per hit
Run Index

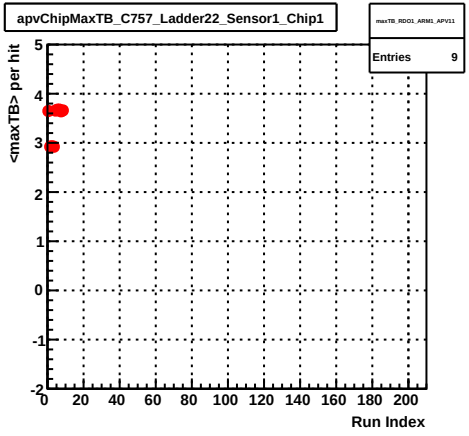

apvChipMaxTB_C752_Ladder21_Sensor6_Chip2
maxTB_RDO1_ARM1_APV16
Entries 9
<maxTB> per hit
Run Index

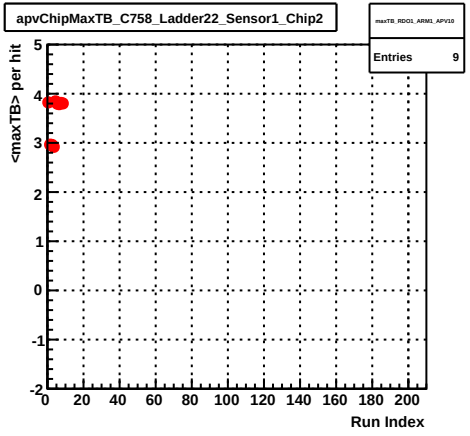

apvChipMaxTB_C753_Ladder21_Sensor6_Chip3
maxTB_RDO1_ARM1_APV15
Entries 9
<maxTB> per hit
Run Index

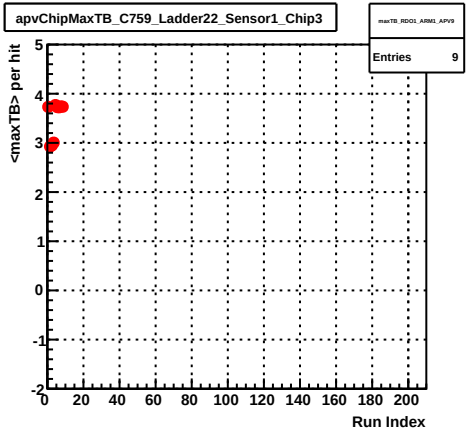

apvChipMaxTB_C754_Ladder21_Sensor6_Chip4
maxTB_RDO1_ARM1_APV14
Entries 9
<maxTB> per hit
Run Index

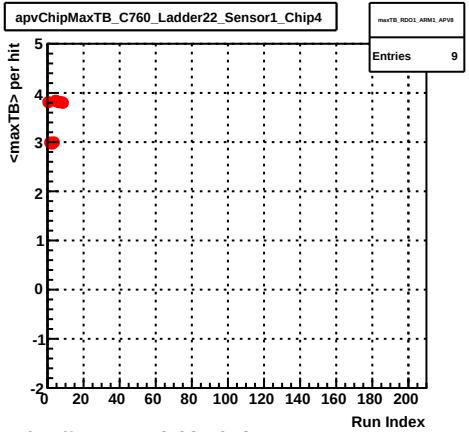

apvChipMaxTB_C755_Ladder21_Sensor6_Chip5
maxTB_RDO1_ARM1_APV13
Entries 9
<maxTB> per hit
Run Index

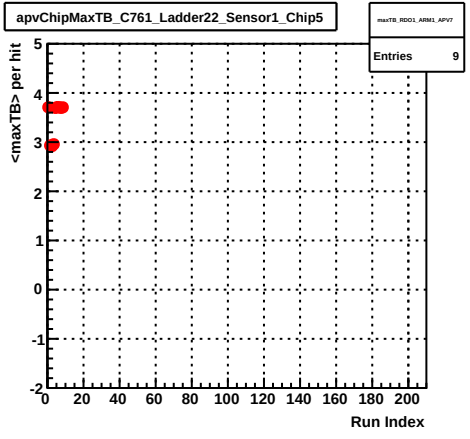

apvChipMaxTB_C756_Ladder21_Sensor6_Chip6
maxTB_RDO1_ARM1_APV12
Entries 9
<maxTB> per hit
Run Index

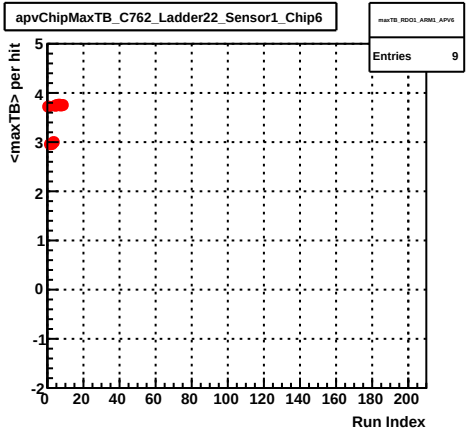

apvChipMaxTB_C757_Ladder22_Sensor1_Chip1
maxTB_RDO1_ARM1_APV11
Entries 9
<maxTB> per hit
Run Index

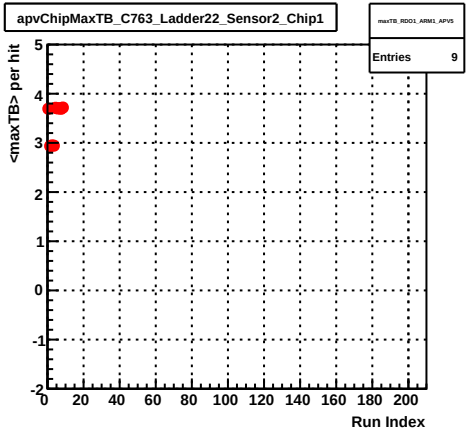

apvChipMaxTB_C758_Ladder22_Sensor1_Chip2
maxTB_RDO1_ARM1_APV10
Entries 9
<maxTB> per hit
Run Index

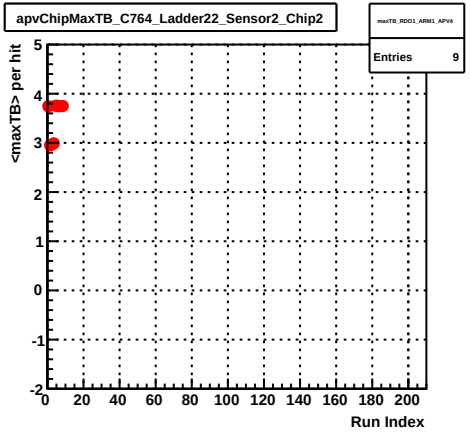

apvChipMaxTB_C759_Ladder22_Sensor1_Chip3
maxTB_RDO1_ARM1_APV9
Entries 9
<maxTB> per hit
Run Index

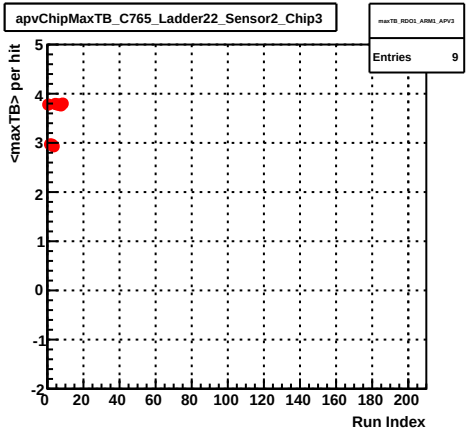

apvChipMaxTB_C760_Ladder22_Sensor1_Chip4
maxTB_RDO1_ARM1_APV8
Entries 9
<maxTB> per hit
Run Index

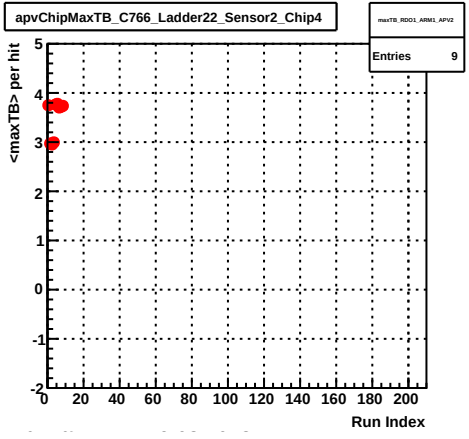

apvChipMaxTB_C761_Ladder22_Sensor1_Chip5
maxTB_RDO1_ARM1_APV7
Entries 9
<maxTB> per hit
Run Index

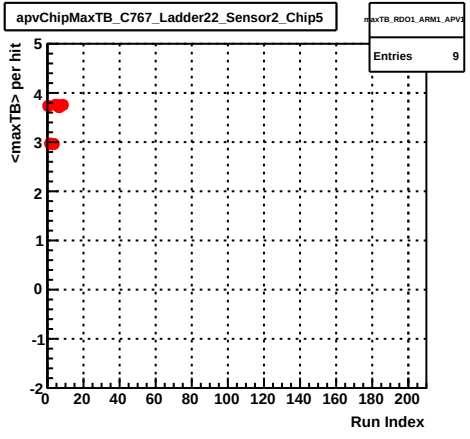

apvChipMaxTB_C762_Ladder22_Sensor1_Chip6
maxTB_RDO1_ARM1_APV6
Entries 9
<maxTB> per hit
Run Index

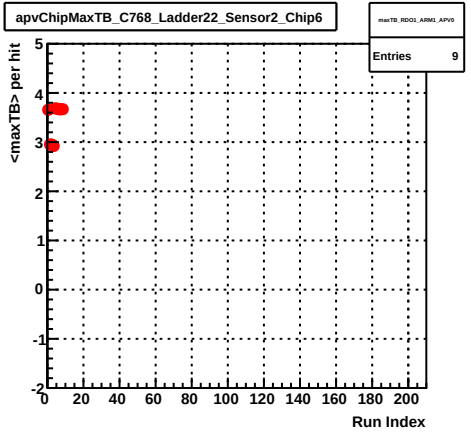

apvChipMaxTB_C763_Ladder22_Sensor2_Chip1
maxTB_RDO1_ARM1_APV5
Entries 9
<maxTB> per hit
Run Index

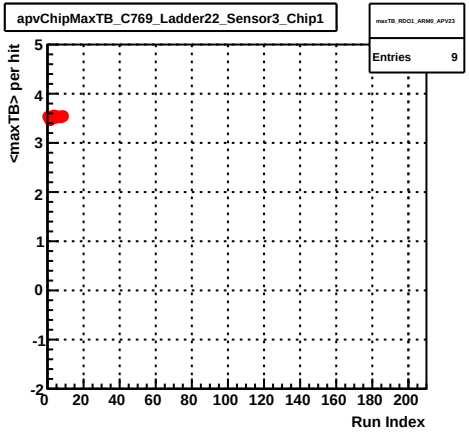

apvChipMaxTB_C764_Ladder22_Sensor2_Chip2
maxTB_RDO1_ARM1_APV4
Entries 9
<maxTB> per hit
Run Index

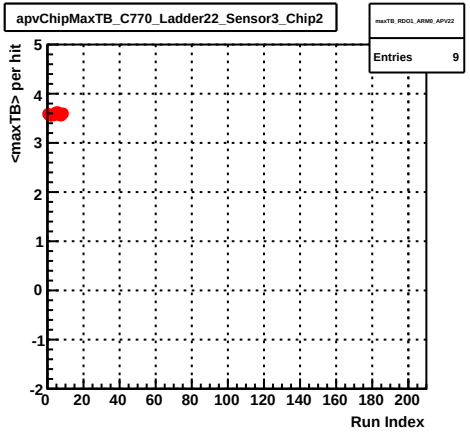

apvChipMaxTB_C765_Ladder22_Sensor2_Chip3
maxTB_RDO1_ARM1_APV3
Entries 9
<maxTB> per hit
Run Index

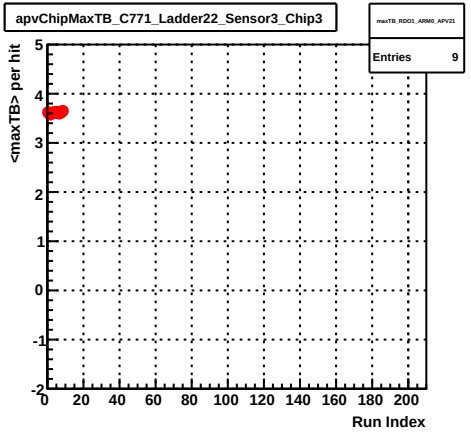

apvChipMaxTB_C766_Ladder22_Sensor2_Chip4
maxTB_RDO1_ARM1_APV2
Entries 9
<maxTB> per hit
Run Index

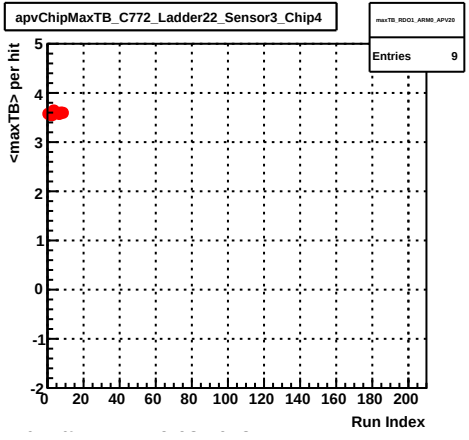

apvChipMaxTB_C767_Ladder22_Sensor2_Chip5
maxTB_RDO1_ARM1_APV1
Entries 9
<maxTB> per hit
Run Index

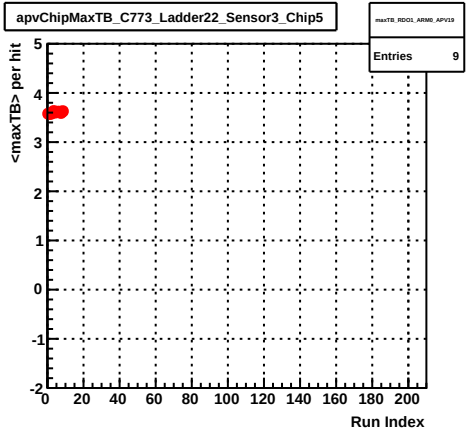

apvChipMaxTB_C768_Ladder22_Sensor2_Chip6
maxTB_RDO1_ARM1_APV0
Entries 9
<maxTB> per hit
Run Index

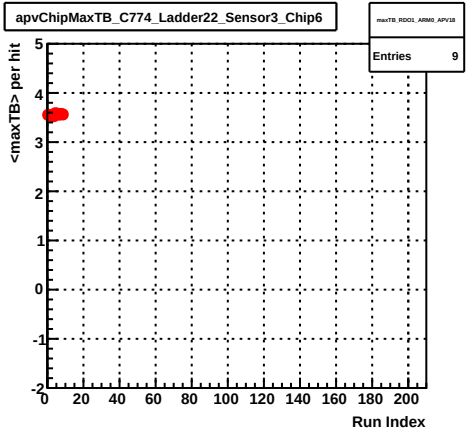

apvChipMaxTB_C769_Ladder22_Sensor3_Chip1
maxTB_RDO1_ARM0_APV23
Entries 9
<maxTB> per hit
Run Index

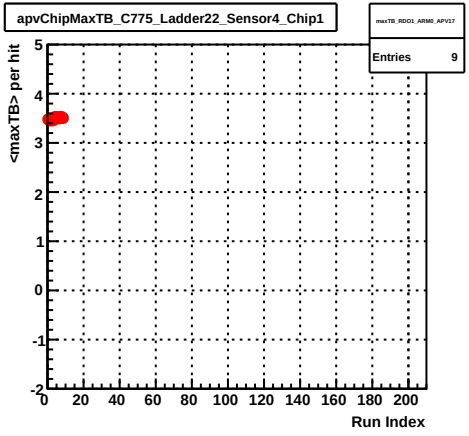

apvChipMaxTB_C770_Ladder22_Sensor3_Chip2
maxTB_RDO1_ARM0_APV22
Entries 9
<maxTB> per hit
Run Index

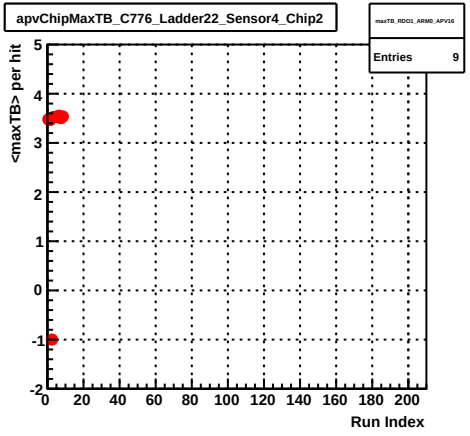

apvChipMaxTB_C771_Ladder22_Sensor3_Chip3
maxTB_RDO1_ARM0_APV21
Entries 9
<maxTB> per hit
Run Index

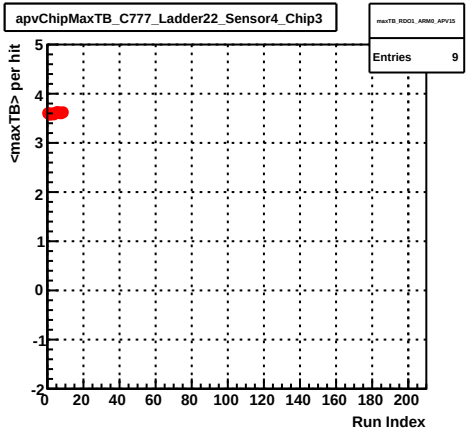

apvChipMaxTB_C772_Ladder22_Sensor3_Chip4
maxTB_RDO1_ARM0_APV20
Entries 9
<maxTB> per hit
Run Index

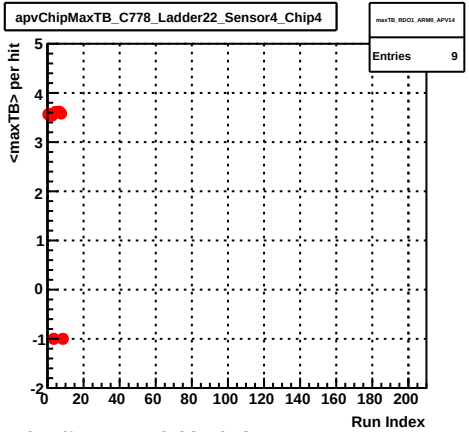

apvChipMaxTB_C773_Ladder22_Sensor3_Chip5
maxTB_RDO1_ARM0_APV19
Entries 9
<maxTB> per hit
Run Index

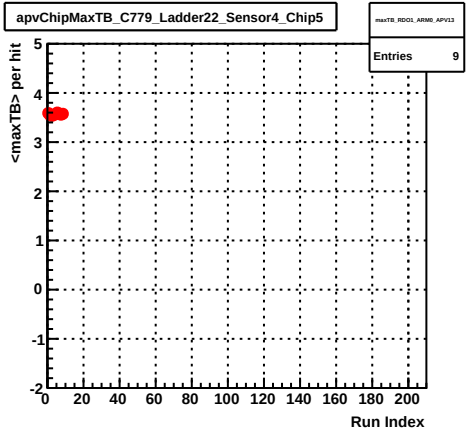

apvChipMaxTB_C774_Ladder22_Sensor3_Chip6
maxTB_RDO1_ARM0_APV18
Entries 9
<maxTB> per hit
Run Index

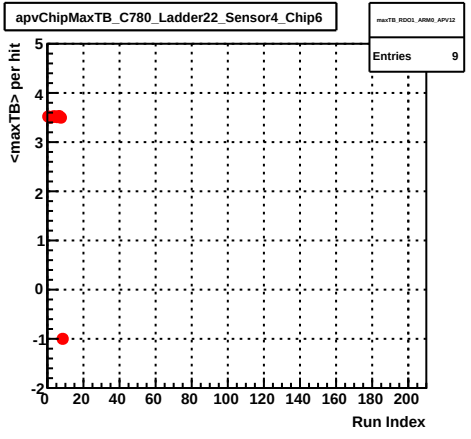

apvChipMaxTB_C775_Ladder22_Sensor4_Chip1
maxTB_RDO1_ARM0_APV17
Entries 9
<maxTB> per hit
Run Index

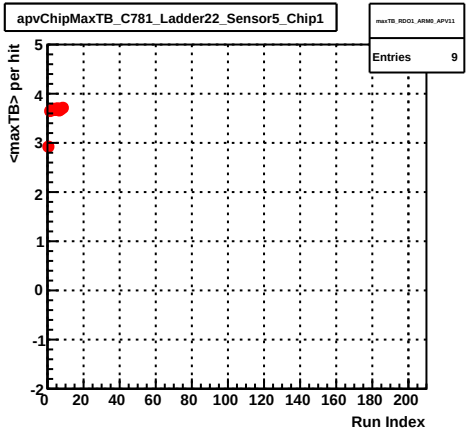

apvChipMaxTB_C776_Ladder22_Sensor4_Chip2
maxTB_RDO1_ARM0_APV16
Entries 9
<maxTB> per hit
Run Index

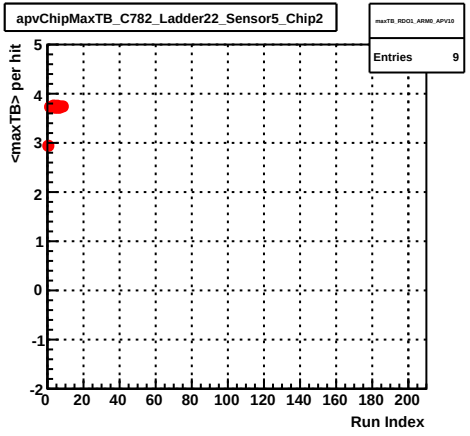

apvChipMaxTB_C777_Ladder22_Sensor4_Chip3
maxTB_RDO1_ARM0_APV15
Entries 9
<maxTB> per hit
Run Index

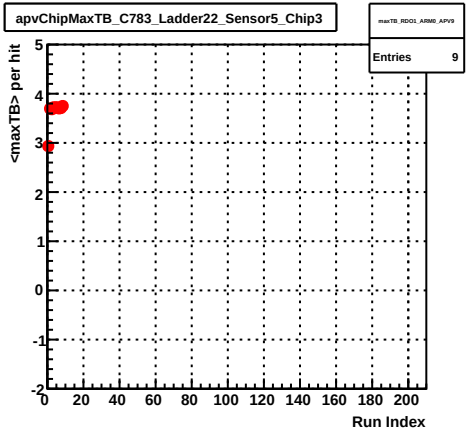

apvChipMaxTB_C778_Ladder22_Sensor4_Chip4
maxTB_RDO1_ARM0_APV14
Entries 9
<maxTB> per hit
Run Index

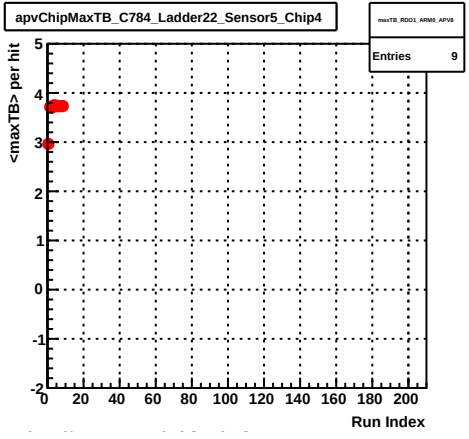

apvChipMaxTB_C779_Ladder22_Sensor4_Chip5
maxTB_RDO1_ARM0_APV13
Entries 9
<maxTB> per hit
Run Index

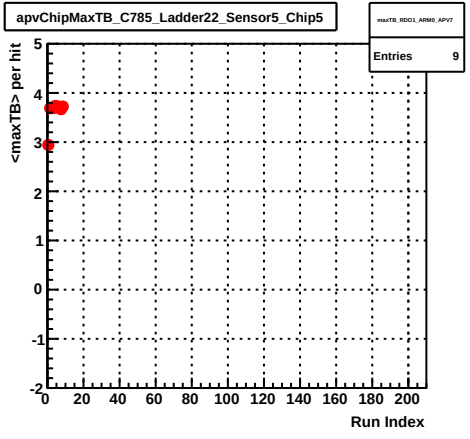

apvChipMaxTB_C780_Ladder22_Sensor4_Chip6
maxTB_RDO1_ARM0_APV12
Entries 9
<maxTB> per hit
Run Index

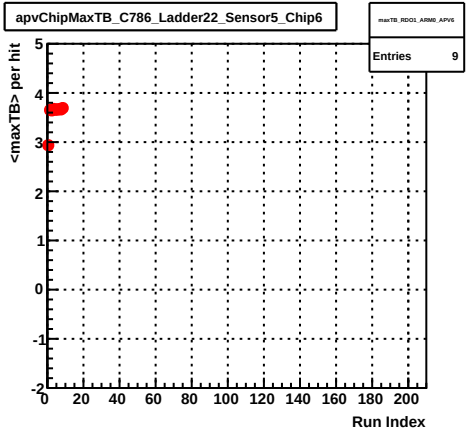

apvChipMaxTB_C781_Ladder22_Sensor5_Chip1
maxTB_RDO1_ARM0_APV11
Entries 9
<maxTB> per hit
Run Index

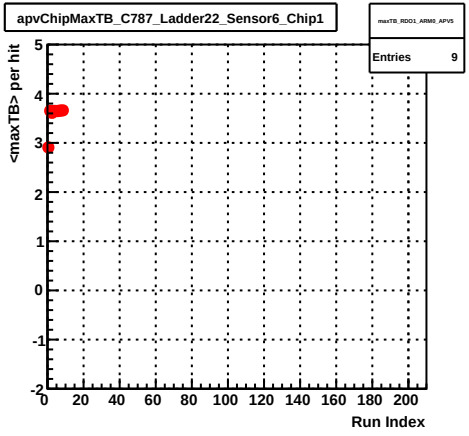

apvChipMaxTB_C782_Ladder22_Sensor5_Chip2
maxTB_RDO1_ARM0_APV10
Entries 9
<maxTB> per hit
Run Index

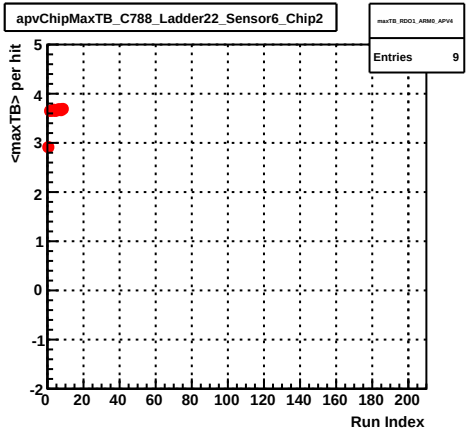

apvChipMaxTB_C783_Ladder22_Sensor5_Chip3
maxTB_RDO1_ARM0_APV9
Entries 9
<maxTB> per hit
Run Index

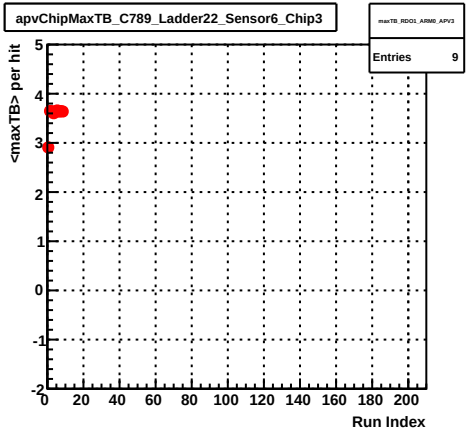

apvChipMaxTB_C784_Ladder22_Sensor5_Chip4
maxTB_RDO1_ARM0_APV8
Entries 9
<maxTB> per hit
Run Index

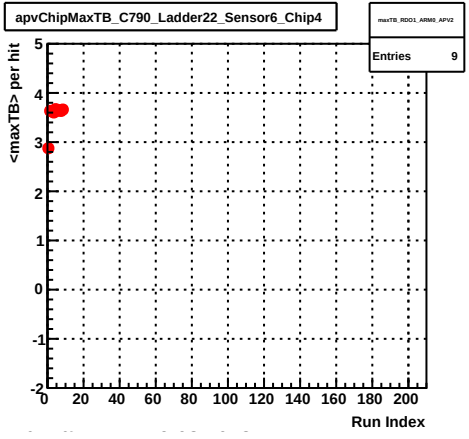

apvChipMaxTB_C785_Ladder22_Sensor5_Chip5
maxTB_RDO1_ARM0_APV7
Entries 9
<maxTB> per hit
Run Index

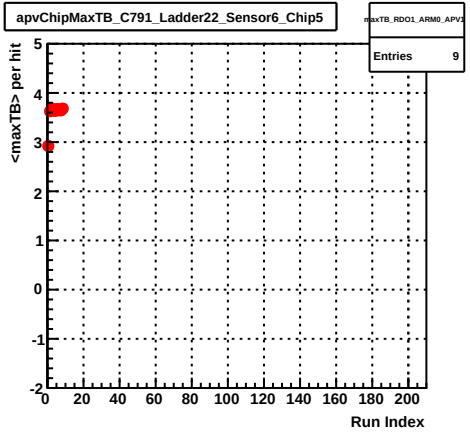

apvChipMaxTB_C786_Ladder22_Sensor5_Chip6
maxTB_RDO1_ARM0_APV6
Entries 9
<maxTB> per hit
Run Index

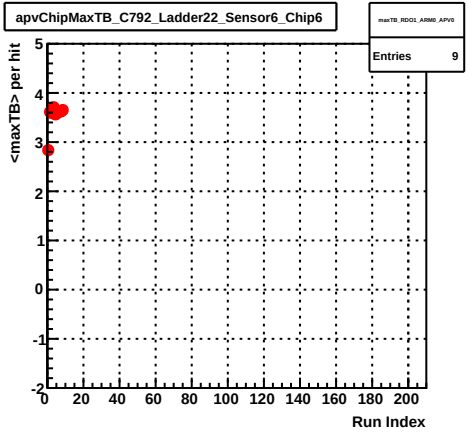

apvChipMaxTB_C787_Ladder22_Sensor6_Chip1
maxTB_RDO1_ARM0_APV5
Entries 9
<maxTB> per hit
Run Index

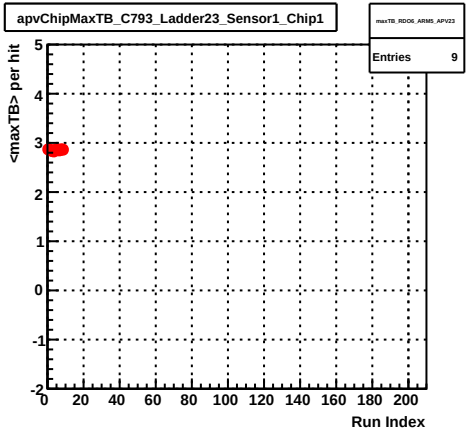

apvChipMaxTB_C788_Ladder22_Sensor6_Chip2
maxTB_RDO1_ARM0_APV4
Entries 9
<maxTB> per hit
Run Index

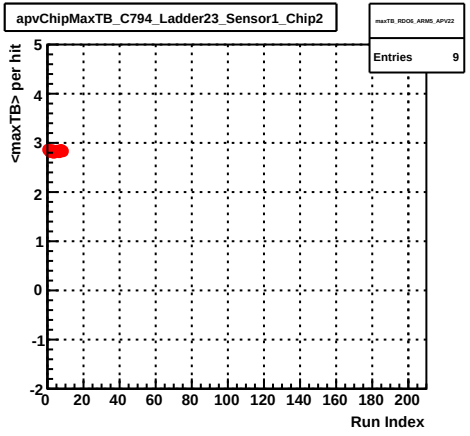

apvChipMaxTB_C789_Ladder22_Sensor6_Chip3
maxTB_RDO1_ARM0_APV3
Entries 9
<maxTB> per hit
Run Index

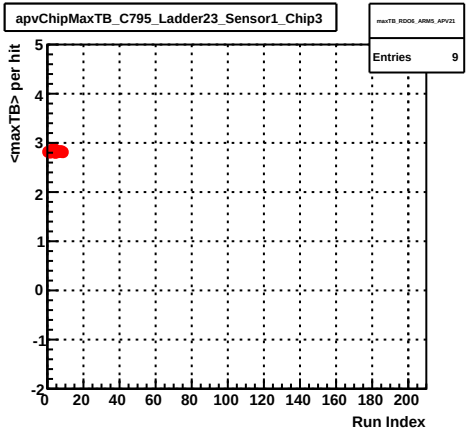

apvChipMaxTB_C790_Ladder22_Sensor6_Chip4
maxTB_RDO1_ARM0_APV2
Entries 9
<maxTB> per hit
Run Index

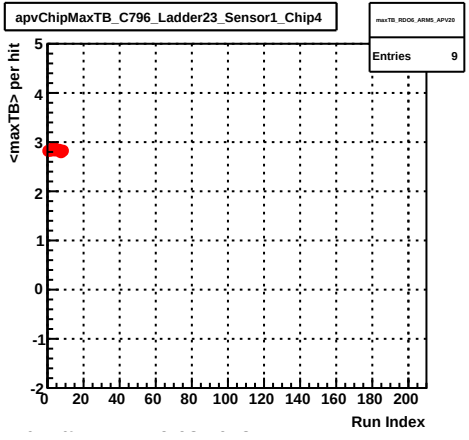

apvChipMaxTB_C791_Ladder22_Sensor6_Chip5
maxTB_RDO1_ARM0_APV1
Entries 9
<maxTB> per hit
Run Index

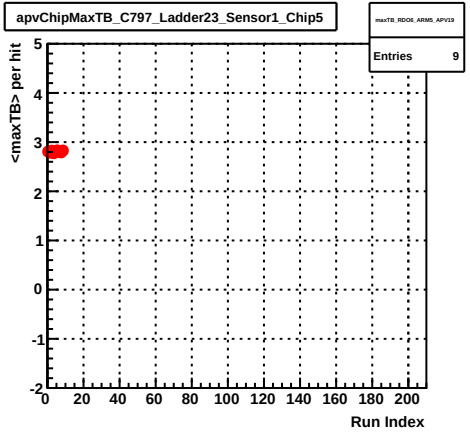

apvChipMaxTB_C792_Ladder22_Sensor6_Chip6
maxTB_RDO1_ARM0_APV0
Entries 9
<maxTB> per hit
Run Index

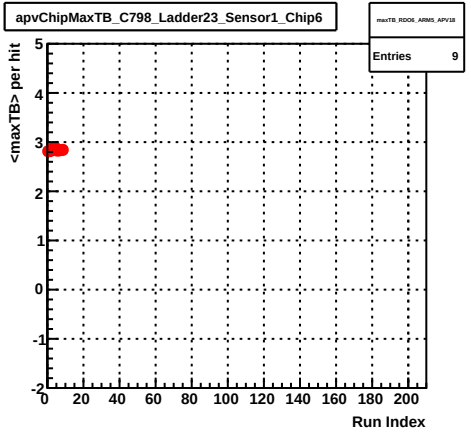

apvChipMaxTB_C793_Ladder23_Sensor1_Chip1
maxTB_RDO6_ARMS_APV23
Entries 9
<maxTB> per hit
Run Index

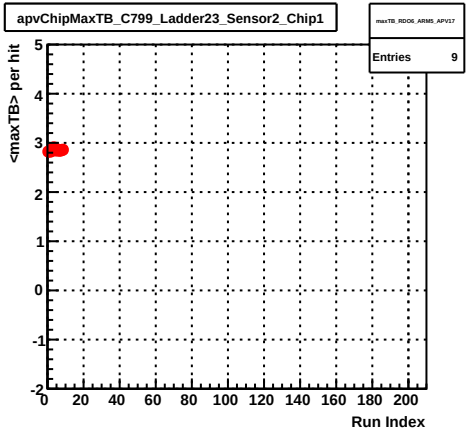

apvChipMaxTB_C794_Ladder23_Sensor1_Chip2
maxTB_RDO6_ARMS_APV22
Entries 9
<maxTB> per hit
Run Index

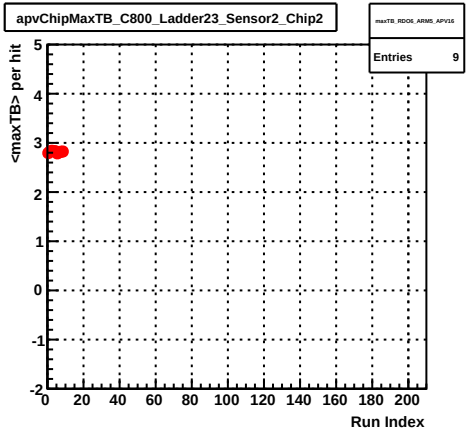

apvChipMaxTB_C795_Ladder23_Sensor1_Chip3
maxTB_RDO6_ARMS_APV21
Entries 9
<maxTB> per hit
Run Index

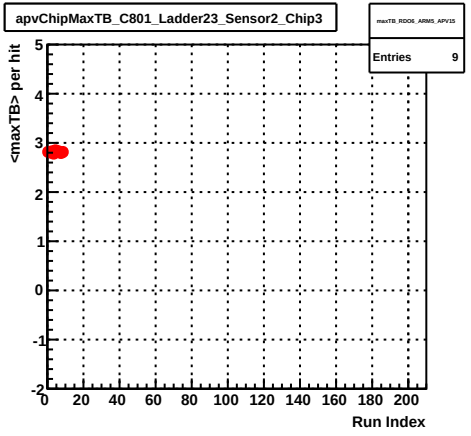

apvChipMaxTB_C796_Ladder23_Sensor1_Chip4
maxTB_RDO6_ARMS_APV20
Entries 9
<maxTB> per hit
Run Index

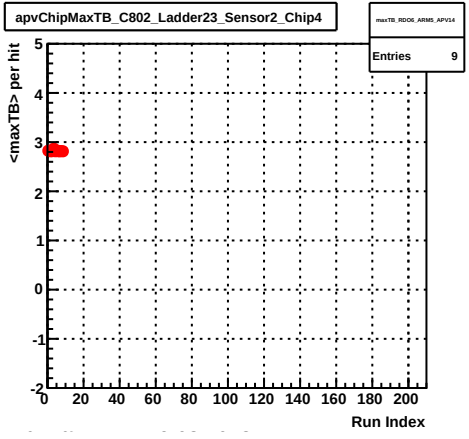

apvChipMaxTB_C797_Ladder23_Sensor1_Chip5
maxTB_RDO6_ARMS_APV19
Entries 9
<maxTB> per hit
Run Index

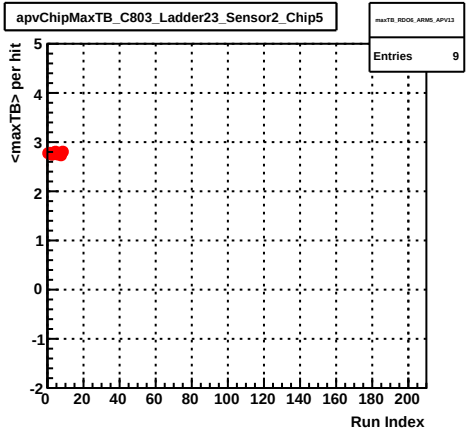

apvChipMaxTB_C798_Ladder23_Sensor1_Chip6
maxTB_RDO6_ARMS_APV18
Entries 9
<maxTB> per hit
Run Index

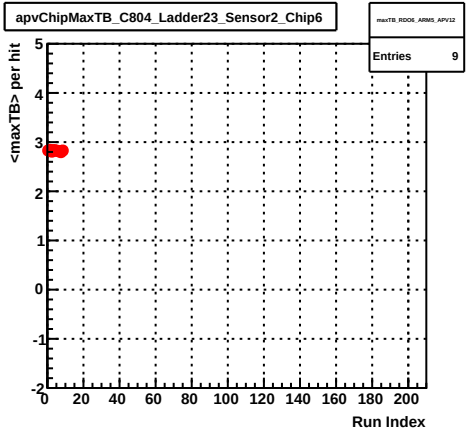

apvChipMaxTB_C799_Ladder23_Sensor2_Chip1
maxTB_RD06_ARM5_APV17
Entries 9
<maxTB> per hit
Run Index

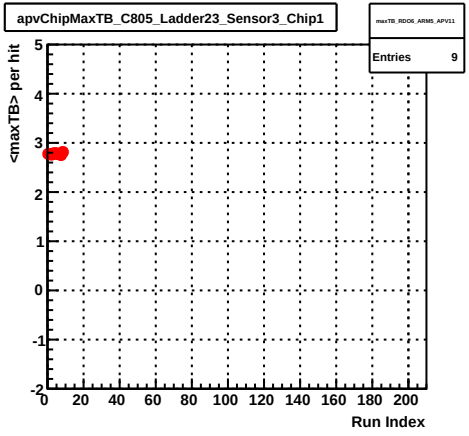

apvChipMaxTB_C800_Ladder23_Sensor2_Chip2
maxTB_RD06_ARM5_APV16
Entries 9
<maxTB> per hit
Run Index

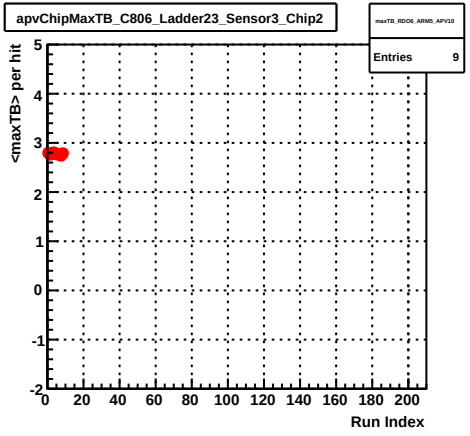

apvChipMaxTB_C801_Ladder23_Sensor2_Chip3
maxTB_RD06_ARM5_APV15
Entries 9
<maxTB> per hit
Run Index

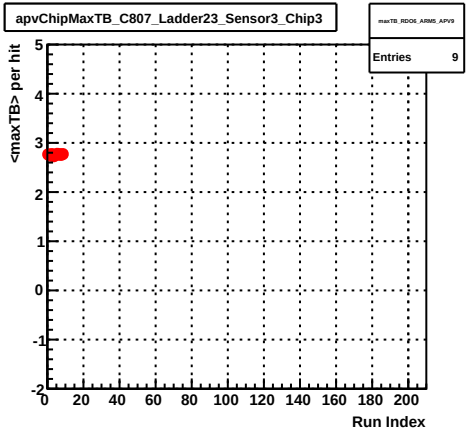

apvChipMaxTB_C802_Ladder23_Sensor2_Chip4
maxTB_RD06_ARM5_APV14
Entries 9
<maxTB> per hit
Run Index

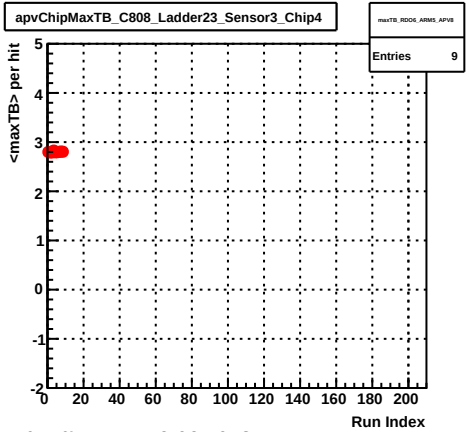

apvChipMaxTB_C803_Ladder23_Sensor2_Chip5
maxTB_RD06_ARM5_APV13
Entries 9
<maxTB> per hit
Run Index

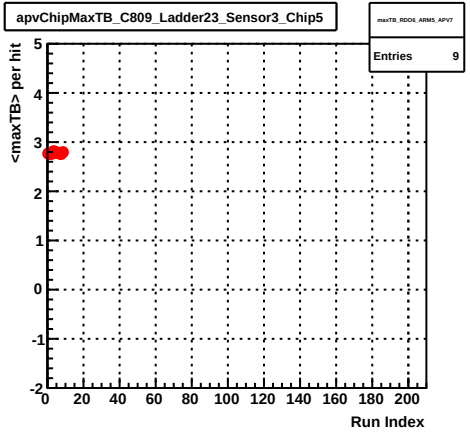

apvChipMaxTB_C804_Ladder23_Sensor2_Chip6
maxTB_RD06_ARM5_APV12
Entries 9
<maxTB> per hit
Run Index

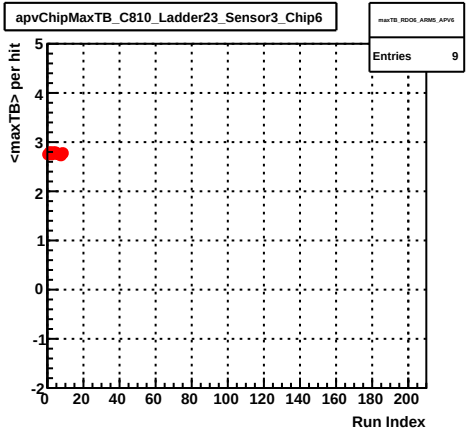

apvChipMaxTB_C805_Ladder23_Sensor3_Chip1
maxTB_RDO6_ARM5_APV11
Entries 9
<maxTB> per hit
Run Index

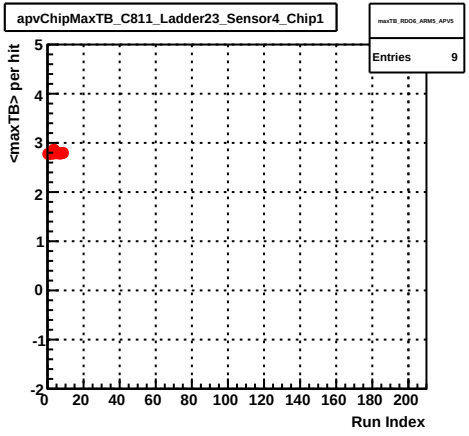

apvChipMaxTB_C806_Ladder23_Sensor3_Chip2
maxTB_RDO6_ARM5_APV10
Entries 9
<maxTB> per hit
Run Index

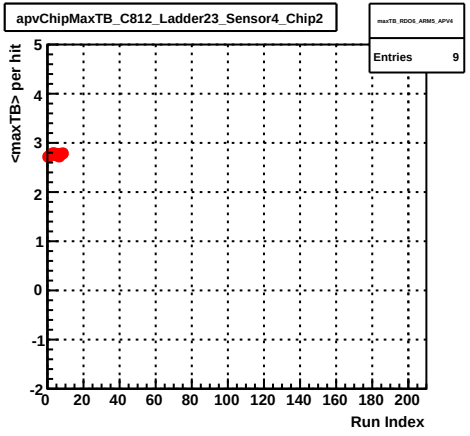

apvChipMaxTB_C807_Ladder23_Sensor3_Chip3
maxTB_RDO6_ARM5_APV9
Entries 9
<maxTB> per hit
Run Index

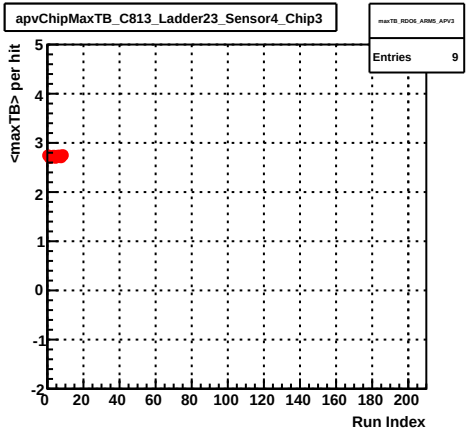

apvChipMaxTB_C808_Ladder23_Sensor3_Chip4
maxTB_RDO6_ARM5_APV8
Entries 9
<maxTB> per hit
Run Index

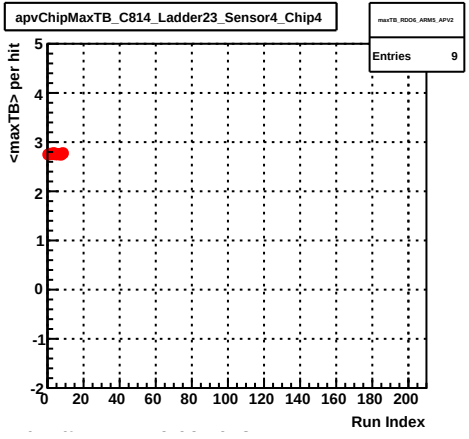

apvChipMaxTB_C809_Ladder23_Sensor3_Chip5
maxTB_RDO6_ARM5_APV7
Entries 9
<maxTB> per hit
Run Index

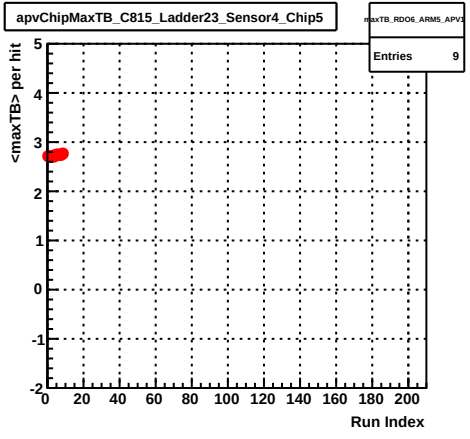

apvChipMaxTB_C810_Ladder23_Sensor3_Chip6
maxTB_RDO6_ARM5_APV6
Entries 9
<maxTB> per hit
Run Index

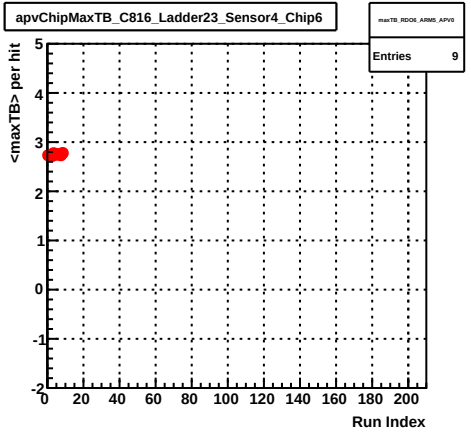

apvChipMaxTB_C811_Ladder23_Sensor4_Chip1
maxTB_RDO6_ARM5_APV5
Entries 9
<maxTB> per hit
Run Index

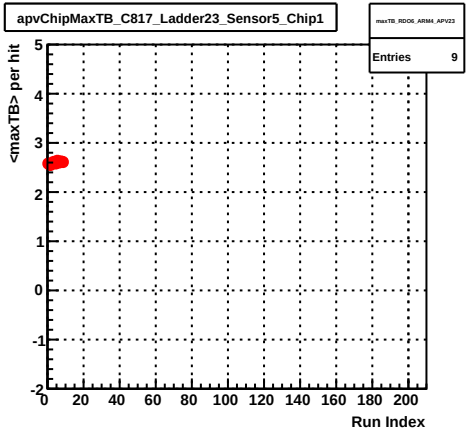

apvChipMaxTB_C812_Ladder23_Sensor4_Chip2
maxTB_RDO6_ARM5_APV4
Entries 9
<maxTB> per hit
Run Index

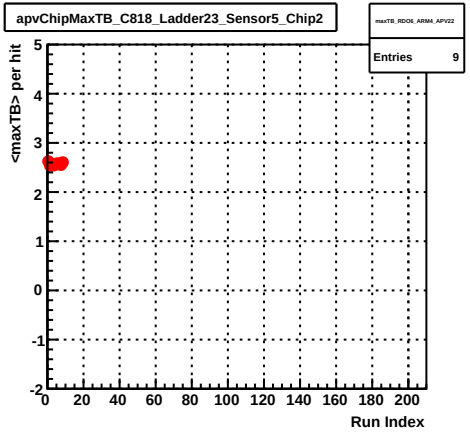

apvChipMaxTB_C813_Ladder23_Sensor4_Chip3
maxTB_RDO6_ARM5_APV3
Entries 9
<maxTB> per hit
Run Index

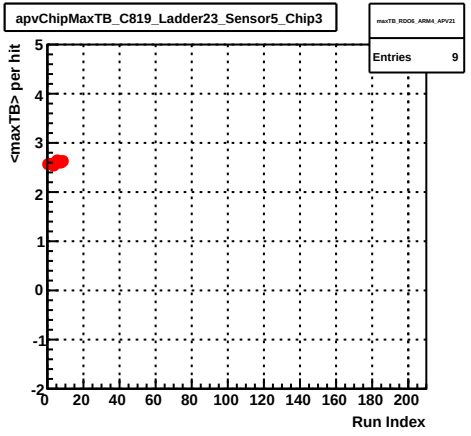

apvChipMaxTB_C814_Ladder23_Sensor4_Chip4
maxTB_RDO6_ARM5_APV2
Entries 9
<maxTB> per hit
Run Index

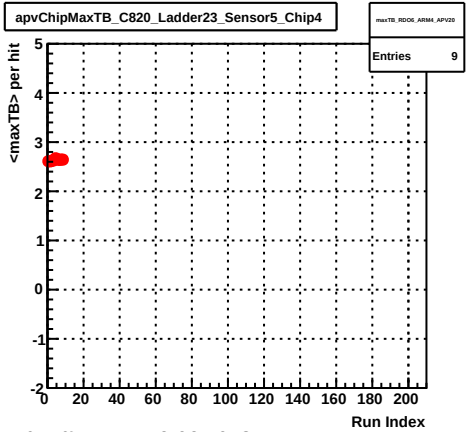

apvChipMaxTB_C815_Ladder23_Sensor4_Chip5
maxTB_RDO6_ARM5_APV1
Entries 9
<maxTB> per hit
Run Index

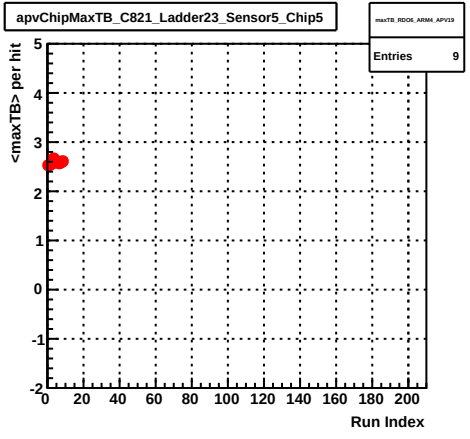

apvChipMaxTB_C816_Ladder23_Sensor4_Chip6
maxTB_RDO6_ARM5_APV0
Entries 9
<maxTB> per hit
Run Index

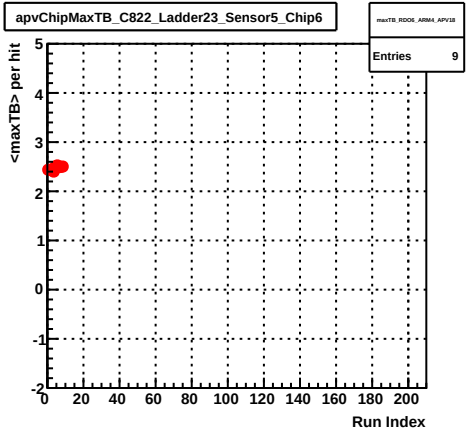

apvChipMaxTB_C817_Ladder23_Sensor5_Chip1
maxTB_RDO6_ARM4_APV23
Entries 9
<maxTB> per hit
Run Index

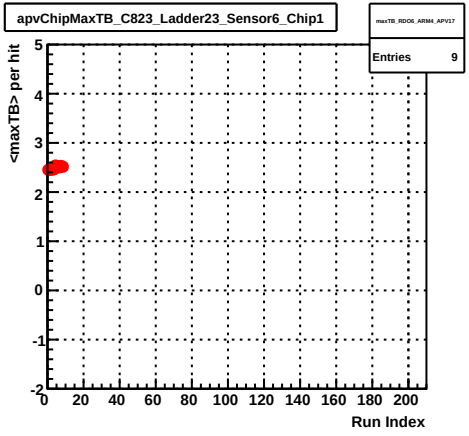

apvChipMaxTB_C818_Ladder23_Sensor5_Chip2
maxTB_RDO6_ARM4_APV22
Entries 9
<maxTB> per hit
Run Index

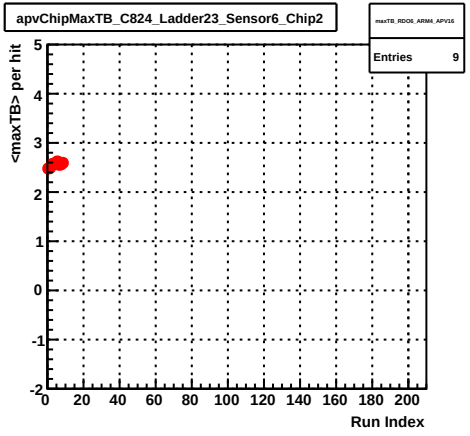

apvChipMaxTB_C819_Ladder23_Sensor5_Chip3
maxTB_RDO6_ARM4_APV21
Entries 9
<maxTB> per hit
Run Index

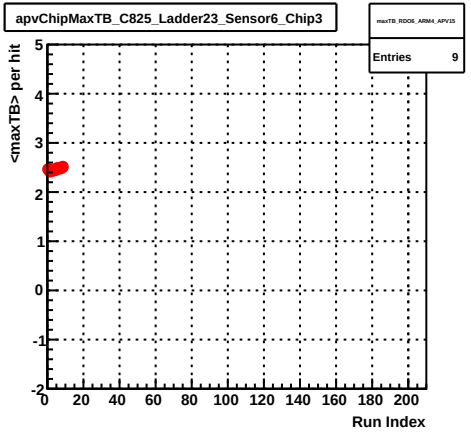

apvChipMaxTB_C820_Ladder23_Sensor5_Chip4
maxTB_RDO6_ARM4_APV20
Entries 9
<maxTB> per hit
Run Index

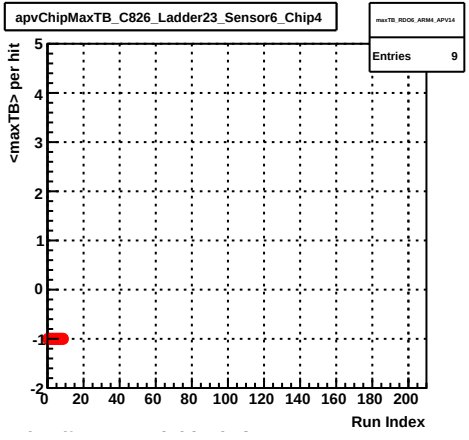

apvChipMaxTB_C821_Ladder23_Sensor5_Chip5
maxTB_RDO6_ARM4_APV19
Entries 9
<maxTB> per hit
Run Index

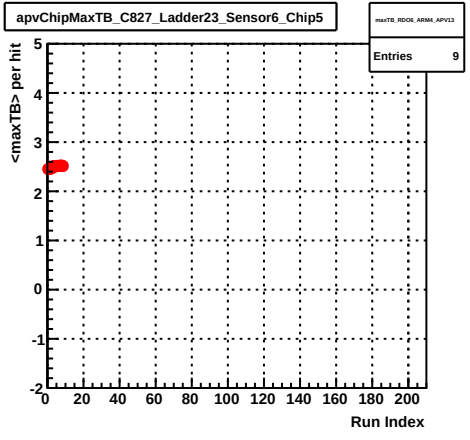

apvChipMaxTB_C822_Ladder23_Sensor5_Chip6
maxTB_RDO6_ARM4_APV18
Entries 9
<maxTB> per hit
Run Index

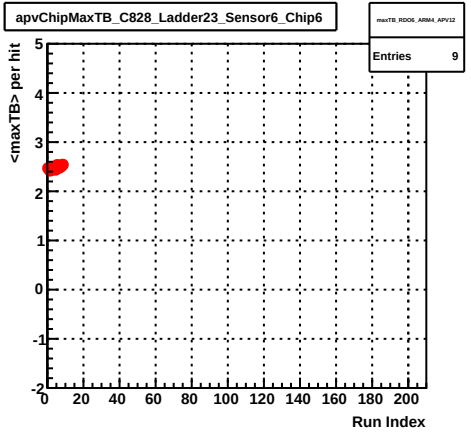

apvChipMaxTB_C823_Ladder23_Sensor6_Chip1
maxTB_RDO6_ARM4_APV17
Entries 9
<maxTB> per hit
Run Index

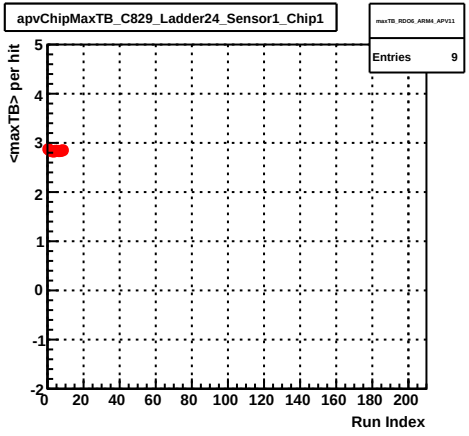

apvChipMaxTB_C824_Ladder23_Sensor6_Chip2
maxTB_RDO6_ARM4_APV16
Entries 9
<maxTB> per hit
Run Index

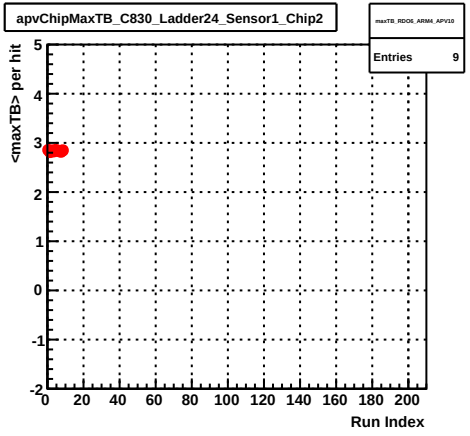

apvChipMaxTB_C825_Ladder23_Sensor6_Chip3
maxTB_RDO6_ARM4_APV15
Entries 9
<maxTB> per hit
Run Index

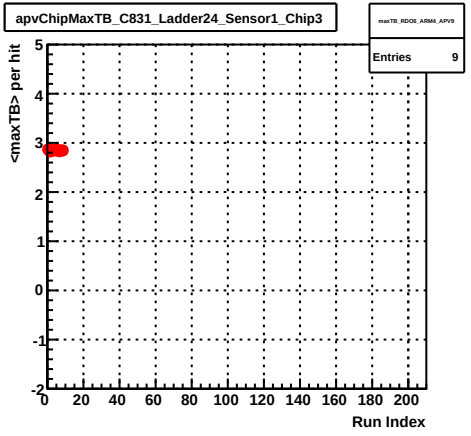

apvChipMaxTB_C826_Ladder23_Sensor6_Chip4
maxTB_RDO6_ARM4_APV14
Entries 9
<maxTB> per hit
Run Index

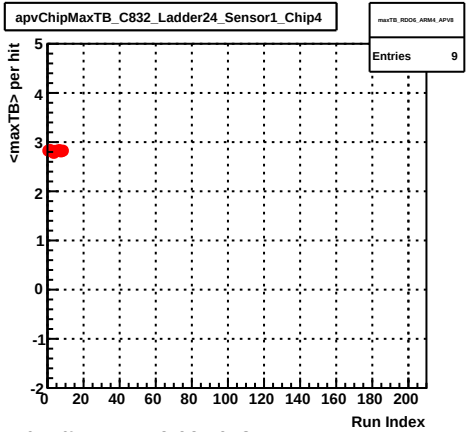

apvChipMaxTB_C827_Ladder23_Sensor6_Chip5
maxTB_RDO6_ARM4_APV13
Entries 9
<maxTB> per hit
Run Index

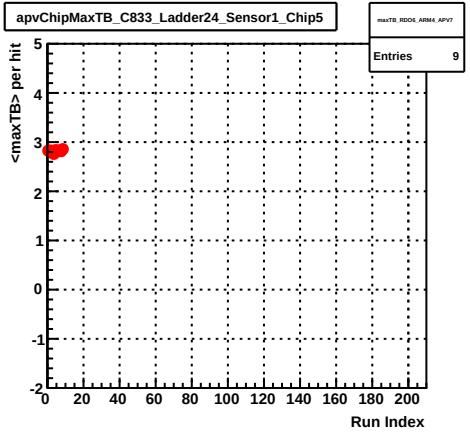

apvChipMaxTB_C828_Ladder23_Sensor6_Chip6
maxTB_RDO6_ARM4_APV12
Entries 9
<maxTB> per hit
Run Index

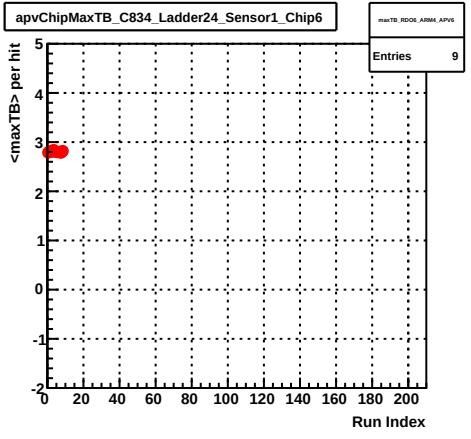

apvChipMaxTB_C829_Ladder24_Sensor1_Chip1
maxTB_RDO6_ARM4_APV11
Entries 9
<maxTB> per hit
Run Index

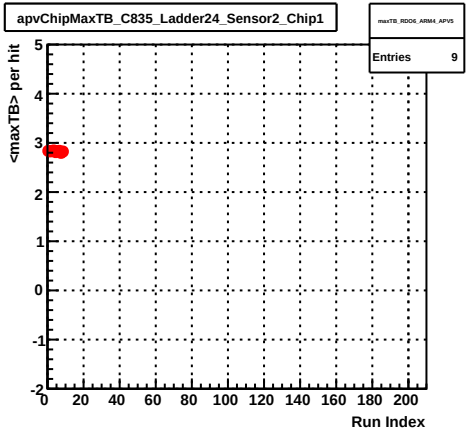

apvChipMaxTB_C830_Ladder24_Sensor1_Chip2
maxTB_RDO6_ARM4_APV10
Entries 9
<maxTB> per hit
Run Index

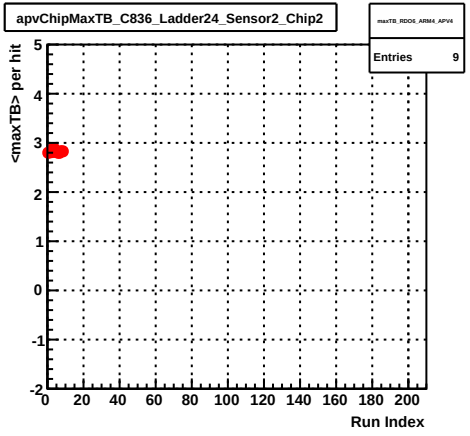

apvChipMaxTB_C831_Ladder24_Sensor1_Chip3
maxTB_RDO6_ARM4_APV9
Entries 9
<maxTB> per hit
Run Index

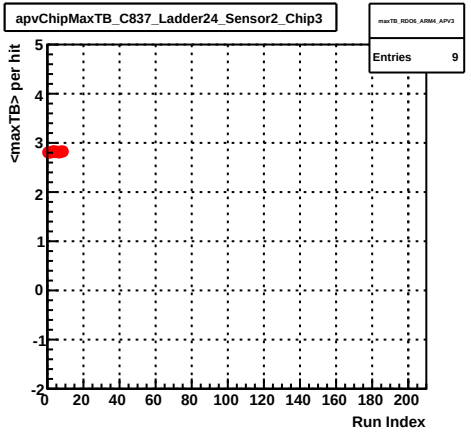

apvChipMaxTB_C832_Ladder24_Sensor1_Chip4
maxTB_RDO6_ARM4_APV8
Entries 9
<maxTB> per hit
Run Index

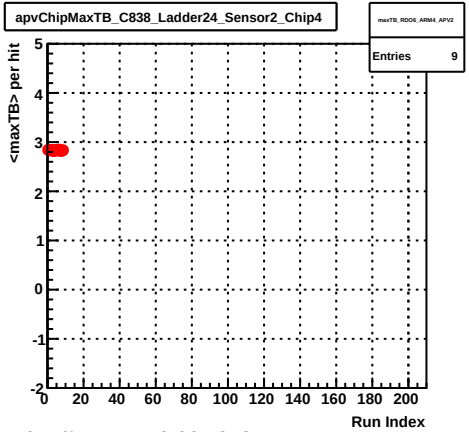

apvChipMaxTB_C833_Ladder24_Sensor1_Chip5
maxTB_RDO6_ARM4_APV7
Entries 9
<maxTB> per hit
Run Index

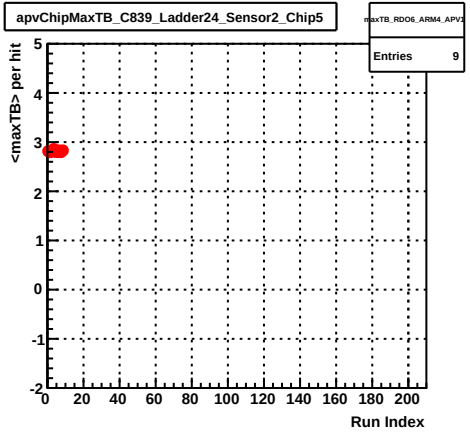

apvChipMaxTB_C834_Ladder24_Sensor1_Chip6
maxTB_RDO6_ARM4_APV6
Entries 9
<maxTB> per hit
Run Index

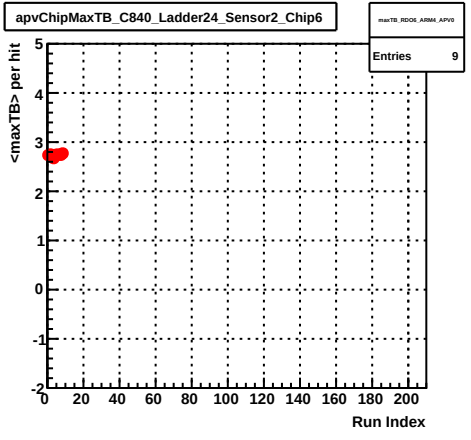

apvChipMaxTB_C835_Ladder24_Sensor2_Chip1
maxTB_RDO6_ARM4_APV5
Entries 9
<maxTB> per hit
Run Index

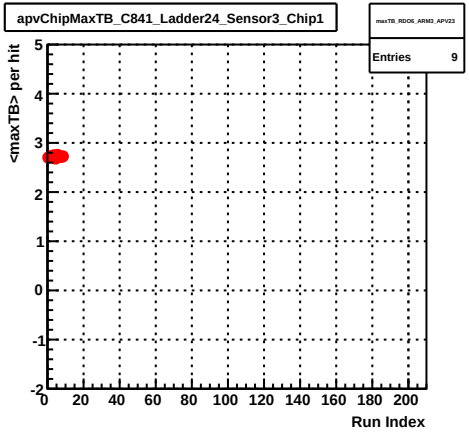

apvChipMaxTB_C836_Ladder24_Sensor2_Chip2
maxTB_RDO6_ARM4_APV4
Entries 9
<maxTB> per hit
Run Index

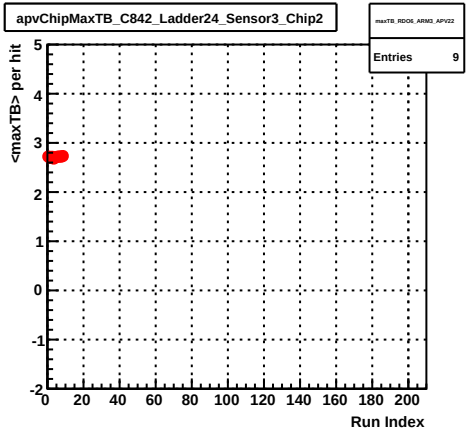

apvChipMaxTB_C837_Ladder24_Sensor2_Chip3
maxTB_RDO6_ARM4_APV3
Entries 9
<maxTB> per hit
Run Index

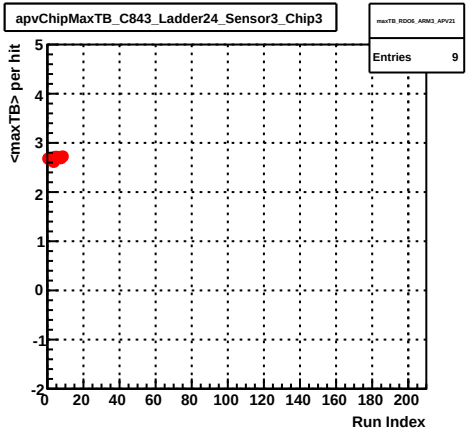

apvChipMaxTB_C838_Ladder24_Sensor2_Chip4
maxTB_RDO6_ARM4_APV2
Entries 9
<maxTB> per hit
Run Index

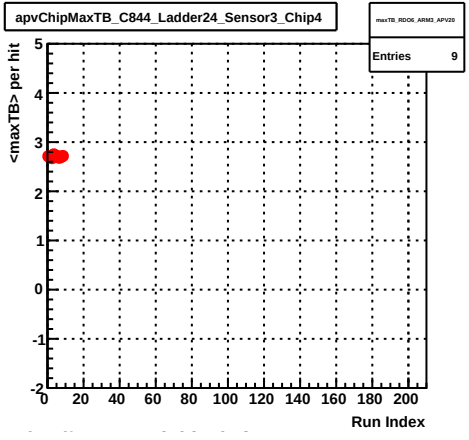

apvChipMaxTB_C839_Ladder24_Sensor2_Chip5
maxTB_RDO6_ARM4_APV1
Entries 9
<maxTB> per hit
Run Index

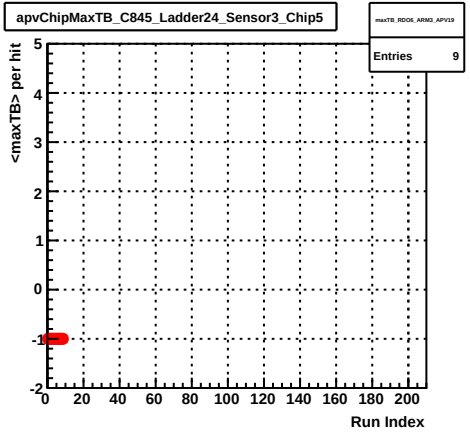

apvChipMaxTB_C840_Ladder24_Sensor2_Chip6
maxTB_RDO6_ARM4_APV0
Entries 9
<maxTB> per hit
Run Index

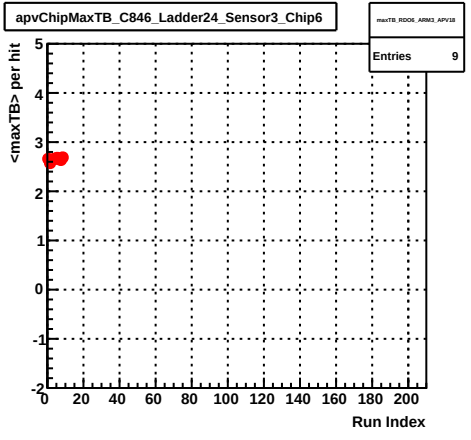

apvChipMaxTB_C841_Ladder24_Sensor3_Chip1
maxTB_RDO6_ARM3_APV23
Entries 9
<maxTB> per hit
Run Index

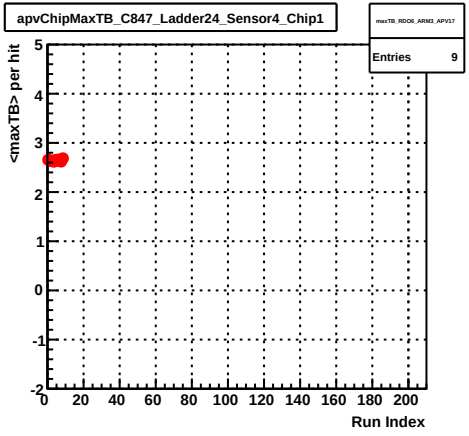

apvChipMaxTB_C842_Ladder24_Sensor3_Chip2
maxTB_RDO6_ARM3_APV22
Entries 9
<maxTB> per hit
Run Index

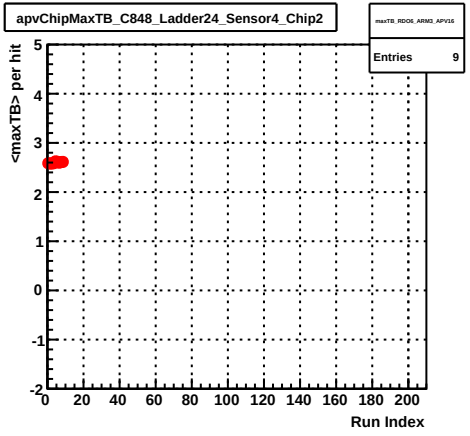

apvChipMaxTB_C843_Ladder24_Sensor3_Chip3
maxTB_RDO6_ARM3_APV21
Entries 9
<maxTB> per hit
Run Index

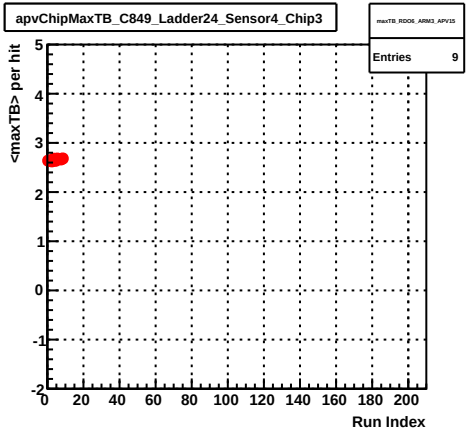

apvChipMaxTB_C844_Ladder24_Sensor3_Chip4
maxTB_RDO6_ARM3_APV20
Entries 9
<maxTB> per hit
Run Index

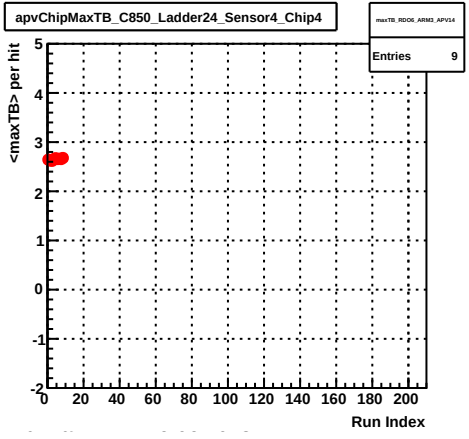

apvChipMaxTB_C845_Ladder24_Sensor3_Chip5
maxTB_RDO6_ARM3_APV19
Entries 9
<maxTB> per hit
Run Index

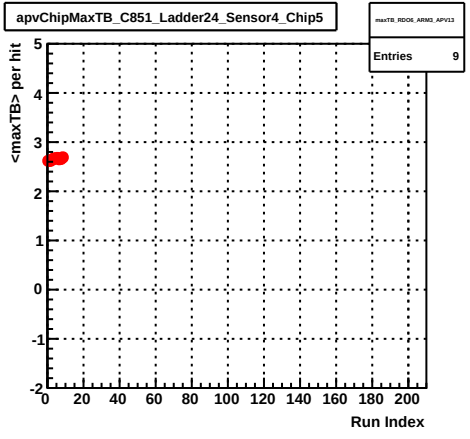

apvChipMaxTB_C846_Ladder24_Sensor3_Chip6
maxTB_RDO6_ARM3_APV18
Entries 9
<maxTB> per hit
Run Index

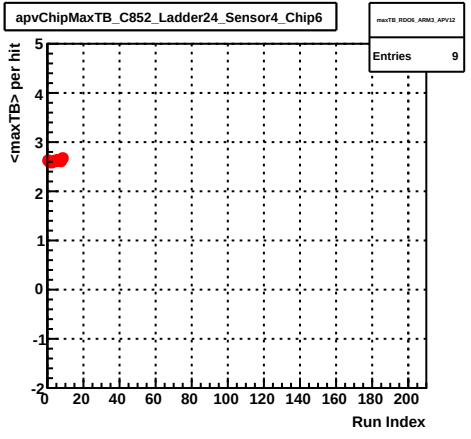

apvChipMaxTB_C847_Ladder24_Sensor4_Chip1
maxTB_RD06_ARM3_APV17
Entries 9
<maxTB> per hit
Run Index

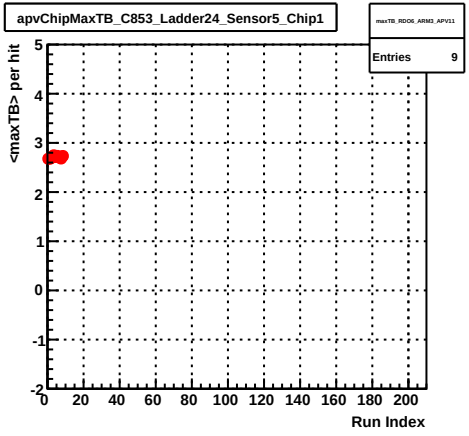

apvChipMaxTB_C848_Ladder24_Sensor4_Chip2
maxTB_RD06_ARM3_APV16
Entries 9
<maxTB> per hit
Run Index

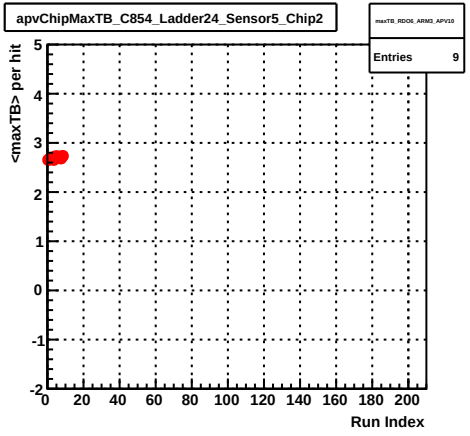

apvChipMaxTB_C849_Ladder24_Sensor4_Chip3
maxTB_RD06_ARM3_APV15
Entries 9
<maxTB> per hit
Run Index

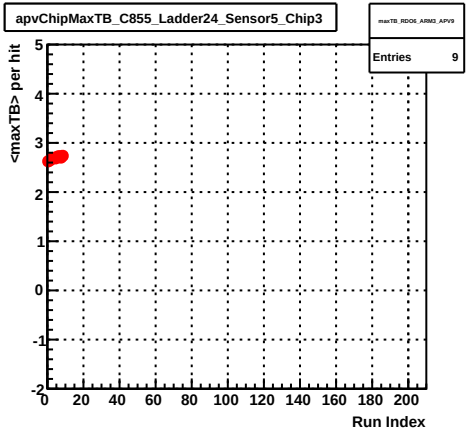

apvChipMaxTB_C850_Ladder24_Sensor4_Chip4
maxTB_RD06_ARM3_APV14
Entries 9
<maxTB> per hit
Run Index

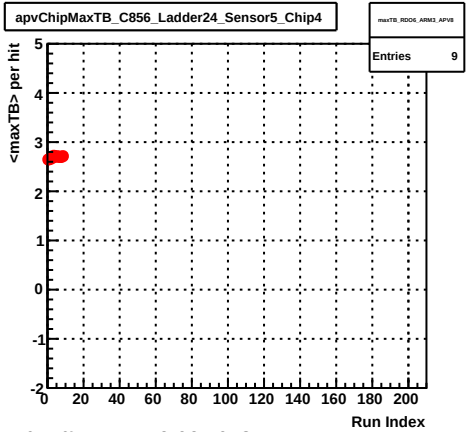

apvChipMaxTB_C851_Ladder24_Sensor4_Chip5
maxTB_RD06_ARM3_APV13
Entries 9
<maxTB> per hit
Run Index

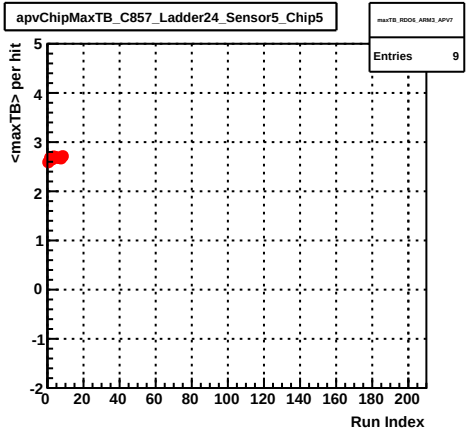

apvChipMaxTB_C852_Ladder24_Sensor4_Chip6
maxTB_RD06_ARM3_APV12
Entries 9
<maxTB> per hit
Run Index

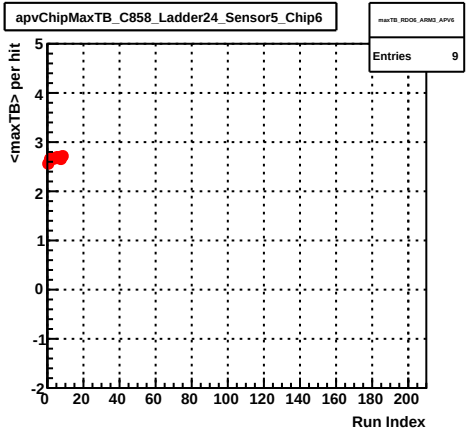

apvChipMaxTB_C853_Ladder24_Sensor5_Chip1
maxTB_RDO6_ARM3_APV11
Entries 9
<maxTB> per hit
Run Index

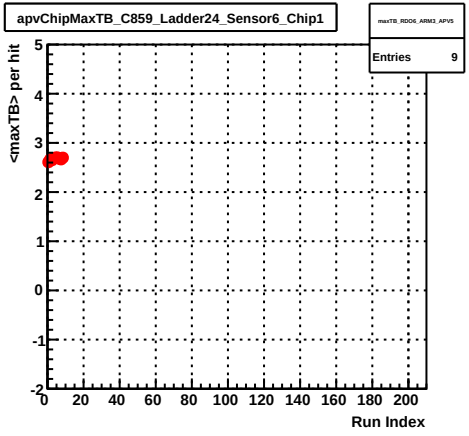

apvChipMaxTB_C854_Ladder24_Sensor5_Chip2
maxTB_RDO6_ARM3_APV10
Entries 9
<maxTB> per hit
Run Index

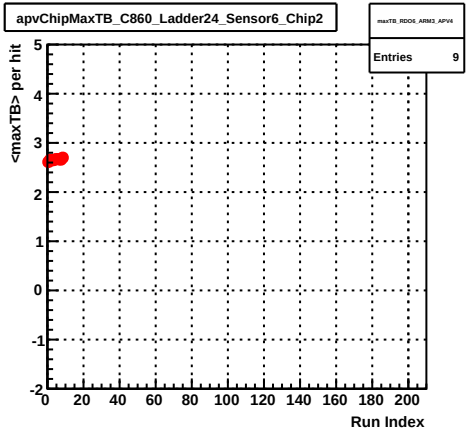

apvChipMaxTB_C855_Ladder24_Sensor5_Chip3
maxTB_RDO6_ARM3_APV9
Entries 9
<maxTB> per hit
Run Index

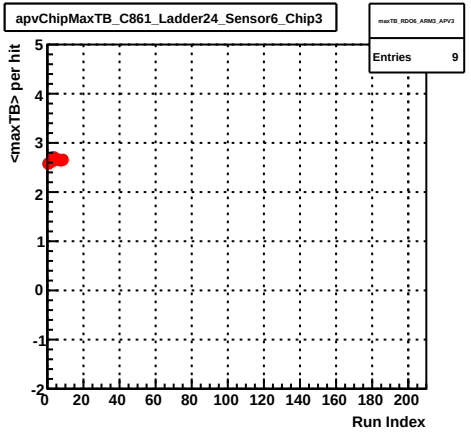

apvChipMaxTB_C856_Ladder24_Sensor5_Chip4
maxTB_RDO6_ARM3_APV8
Entries 9
<maxTB> per hit
Run Index

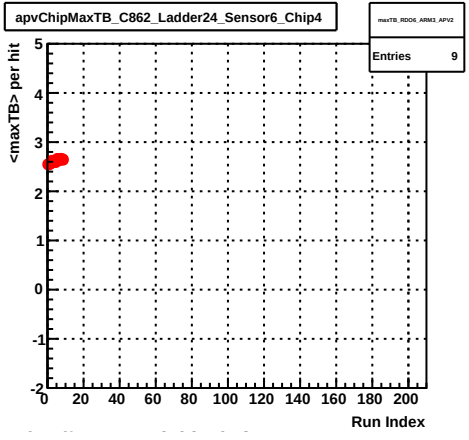

apvChipMaxTB_C857_Ladder24_Sensor5_Chip5
maxTB_RDO6_ARM3_APV7
Entries 9
<maxTB> per hit
Run Index

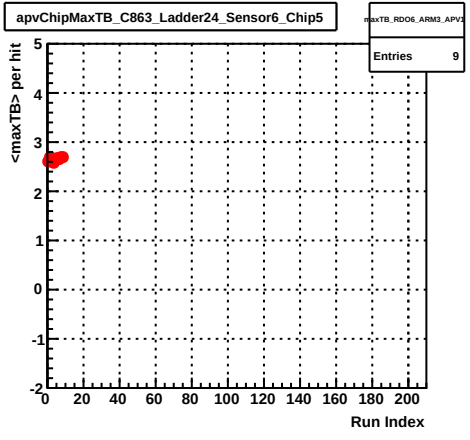

apvChipMaxTB_C858_Ladder24_Sensor5_Chip6
maxTB_RDO6_ARM3_APV6
Entries 9
<maxTB> per hit
Run Index

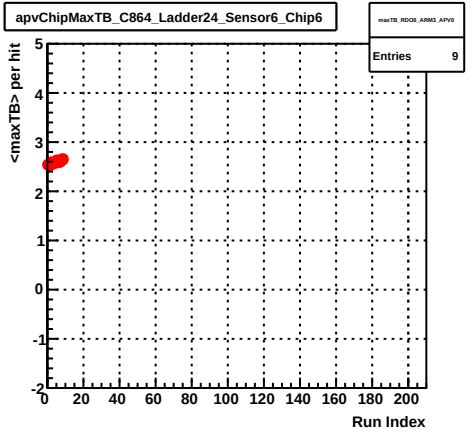

apvChipMaxTB_C859_Ladder24_Sensor6_Chip1
maxTB_RDO6_ARM3_APV5
Entries 9
<maxTB> per hit
Run Index


apvChipMaxTB_C860_Ladder24_Sensor6_Chip2
maxTB_RDO6_ARM3_APV4
Entries 9
<maxTB> per hit
Run Index


apvChipMaxTB_C861_Ladder24_Sensor6_Chip3
maxTB_RDO6_ARM3_APV3
Entries 9
<maxTB> per hit
Run Index


apvChipMaxTB_C862_Ladder24_Sensor6_Chip4
maxTB_RDO6_ARM3_APV2
Entries 9
<maxTB> per hit
Run Index


apvChipMaxTB_C863_Ladder24_Sensor6_Chip5
maxTB_RDO6_ARM3_APV1
Entries 9
<maxTB> per hit
Run Index


apvChipMaxTB_C864_Ladder24_Sensor6_Chip6
maxTB_RDO6_ARM3_APV0
Entries 9
<maxTB> per hit
Run Index